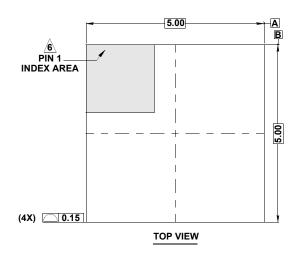
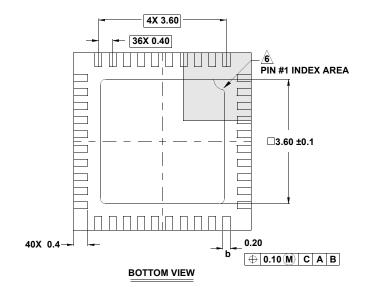
Plastic Packages for Integrated Circuits

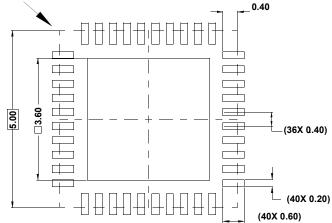
Package Outline Drawing

L40.5x5C 40 LEAD QUAD FLAT NO-LEAD PLASTIC PACKAGE Rev 0, 8/10

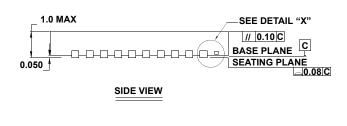


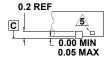


PACKAGE OUTLINE



TYPICAL RECOMMENDED LAND PATTERN





DETAIL "X"

NOTES:

- Dimensions are in millimeters.
 Dimensions in () for Reference Only.
- 2. Dimensioning and tolerancing conform to AMSE Y14.5m-1994.
- 3. Unless otherwise specified, tolerance : Decimal ± 0.05
- 4. Dimension applies to the metallized terminal and is measured between 0.15mm and 0.27mm from the terminal tip.
- 5. Tiebar shown (if present) is a non-functional feature.
- The configuration of the pin #1 identifier is optional, but must be located within the zone indicated. The pin #1 identifier may be either a mold or mark feature.

