μPD78F9200/78F9201/78F9202

IDD VS VDD(25°C/8MHz[Internal-OSC]/AD=OFF)

Prepared on July. 11th, 2008

μPD78F9200/78F9201/78F9202
IDD VS VDD
(Ta=25°C,fx=8MHz[Internal-OSC],A/D=OFF)

Legend:
- MAIN RUN(fcpu=fx,fxp=fx)
- MAIN RUN(fcpu=fx/2,fxp=fx/2)
- MAIN RUN(fcpu=fx/4,fxp=fx)
- MAIN RUN(fcpu=fx/4,fxp=fx/4)
- MAIN RUN(fcpu=fx/8,fxp=fx/2)
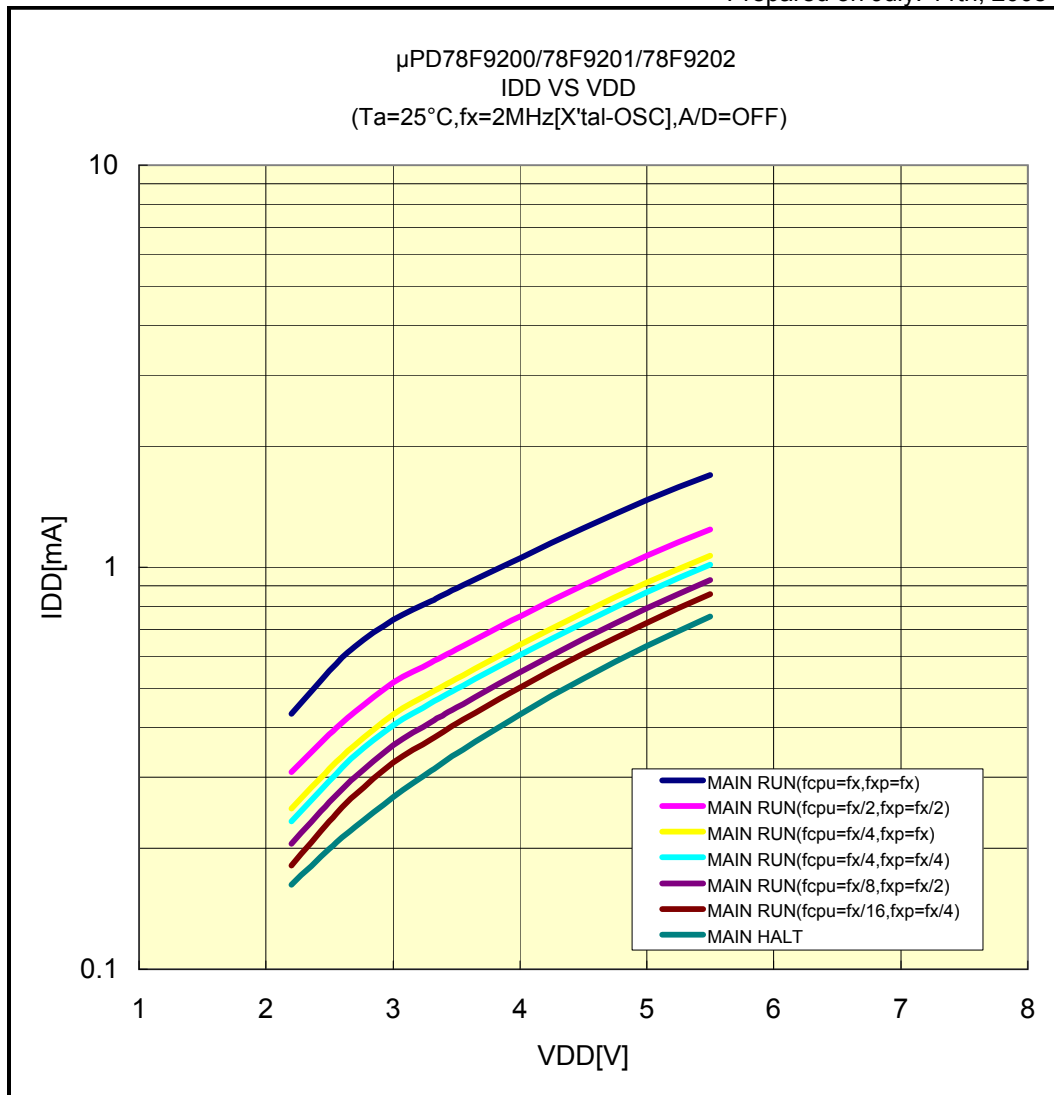- MAIN RUN(fcpu=fx/16,fxp=fx/4)
- MAIN HALT

Axes: IDD[mA] vs VDD[V]

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

μPD78F9200/78F9201/78F9202

IDD VS VDD(25°C/2MHz[X'tal-OSC]/AD=OFF)

μPD78F9200/78F9201/78F9202
IDD VS VDD
(Ta=25°C,fx=2MHz[X'tal-OSC],A/D=OFF)

Legend:
- MAIN RUN(fcpu=fx,fxp=fx)
- MAIN RUN(fcpu=fx/2,fxp=fx/2)
- MAIN RUN(fcpu=fx/4,fxp=fx)
- MAIN RUN(fcpu=fx/4,fxp=fx/4)
- MAIN RUN(fcpu=fx/8,fxp=fx/2)
- MAIN RUN(fcpu=fx/16,fxp=fx/4)
- MAIN HALT
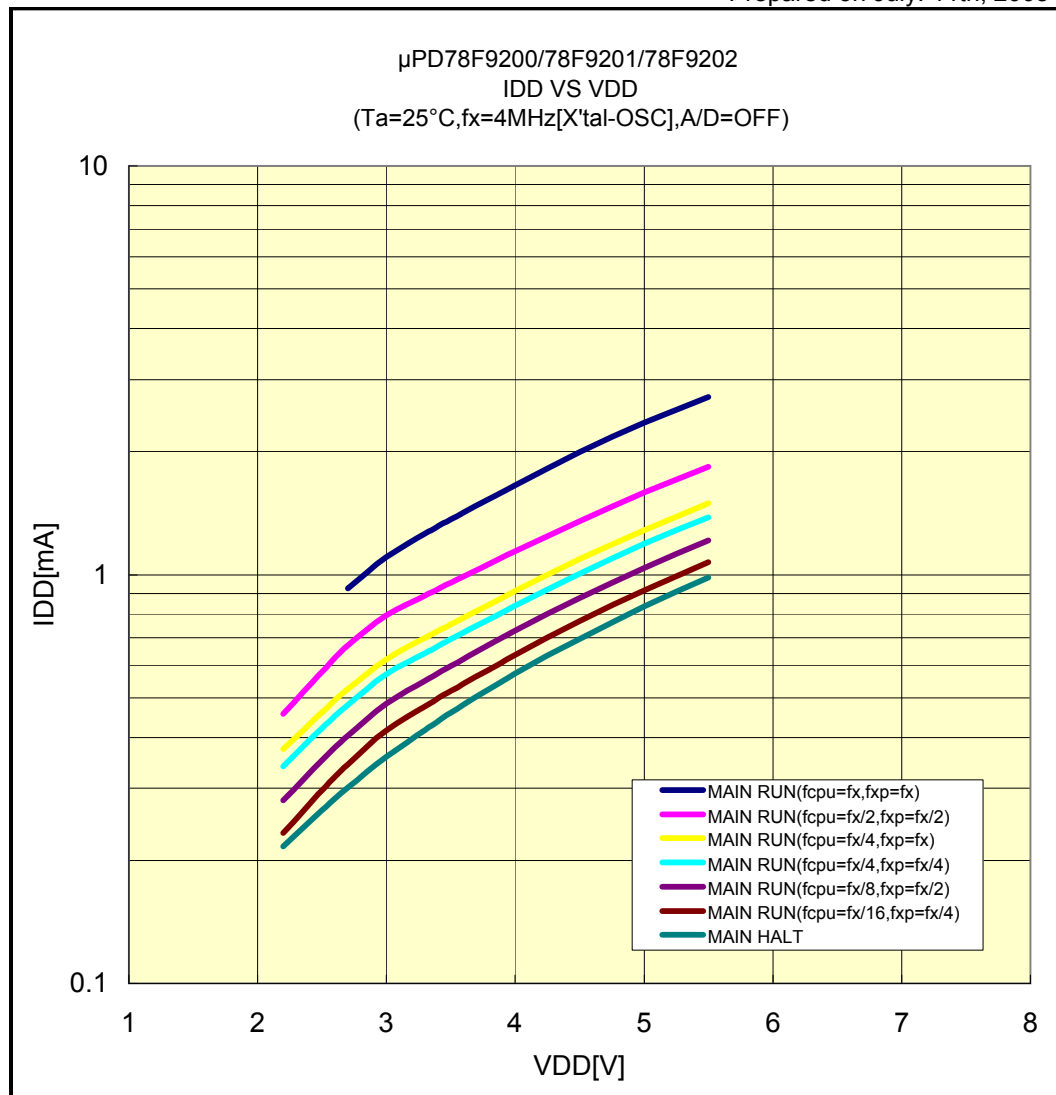
X-axis: VDD[V], Y-axis: IDD[mA]

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

μPD78F9200/78F9201/78F9202

IDD VS VDD(25°C/4MHz[X'tal-OSC]/AD=OFF)

μPD78F9200/78F9201/78F9202
IDD VS VDD
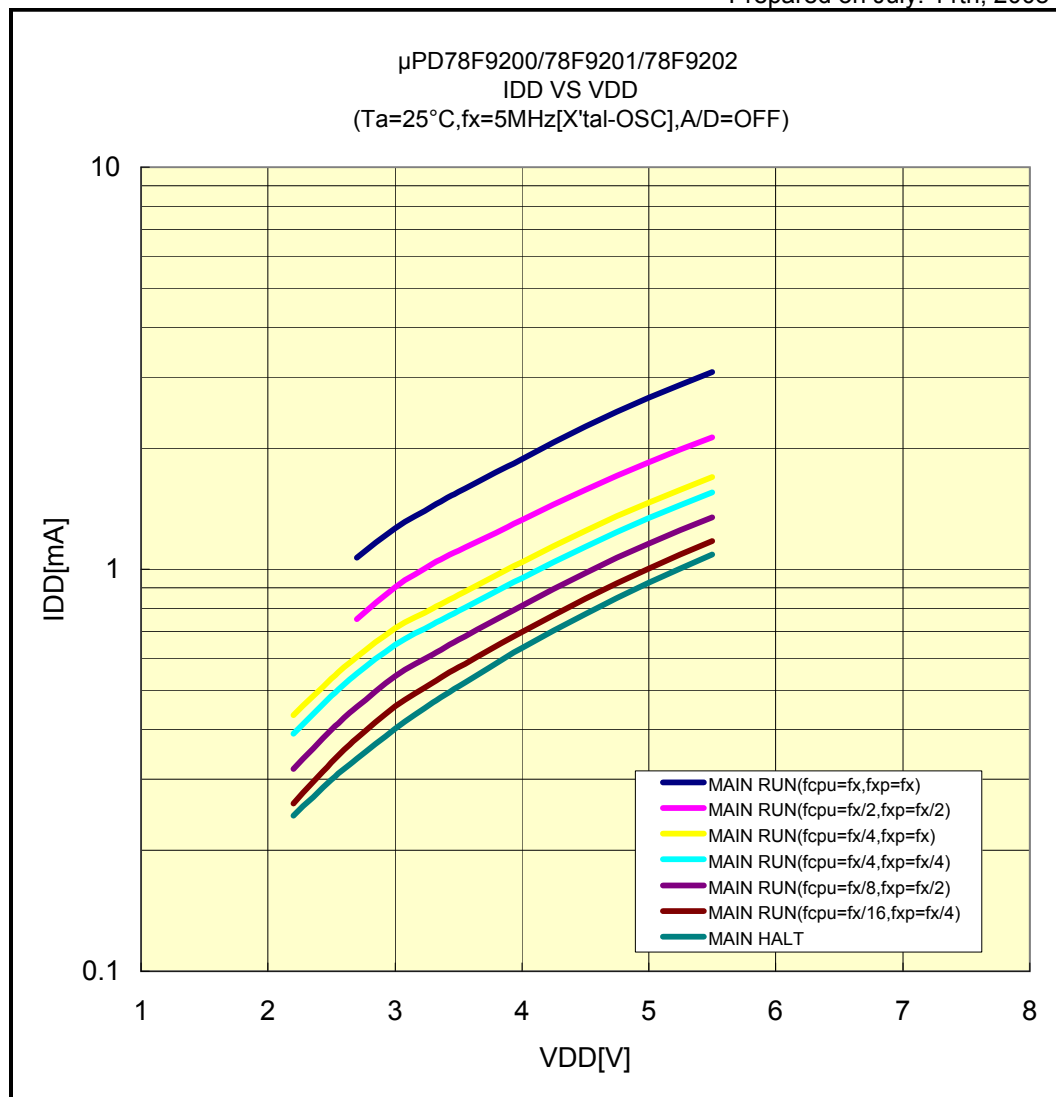(Ta=25°C,fx=4MHz[X'tal-OSC],A/D=OFF)

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

µPD78F9200/78F9201/78F9202

IDD VS VDD(25°C/5MHz[X'tal-OSC]/AD=OFF)

Prepared on July. 11th, 2008

µPD78F9200/78F9201/78F9202
IDD VS VDD
(Ta=25°C,fx=5MHz[X'tal-OSC],A/D=OFF)

Legend:
- MAIN RUN(fcpu=fx,fxp=fx)
- MAIN RUN(fcpu=fx/2,fxp=fx/2)
- MAIN RUN(fcpu=fx/4,fxp=fx)
- MAIN RUN(fcpu=fx/4,fxp=fx/4)
- MAIN RUN(fcpu=fx/8,fxp=fx/2)
- MAIN RUN(fcpu=fx/16,fxp=fx/4)
- MAIN HALT
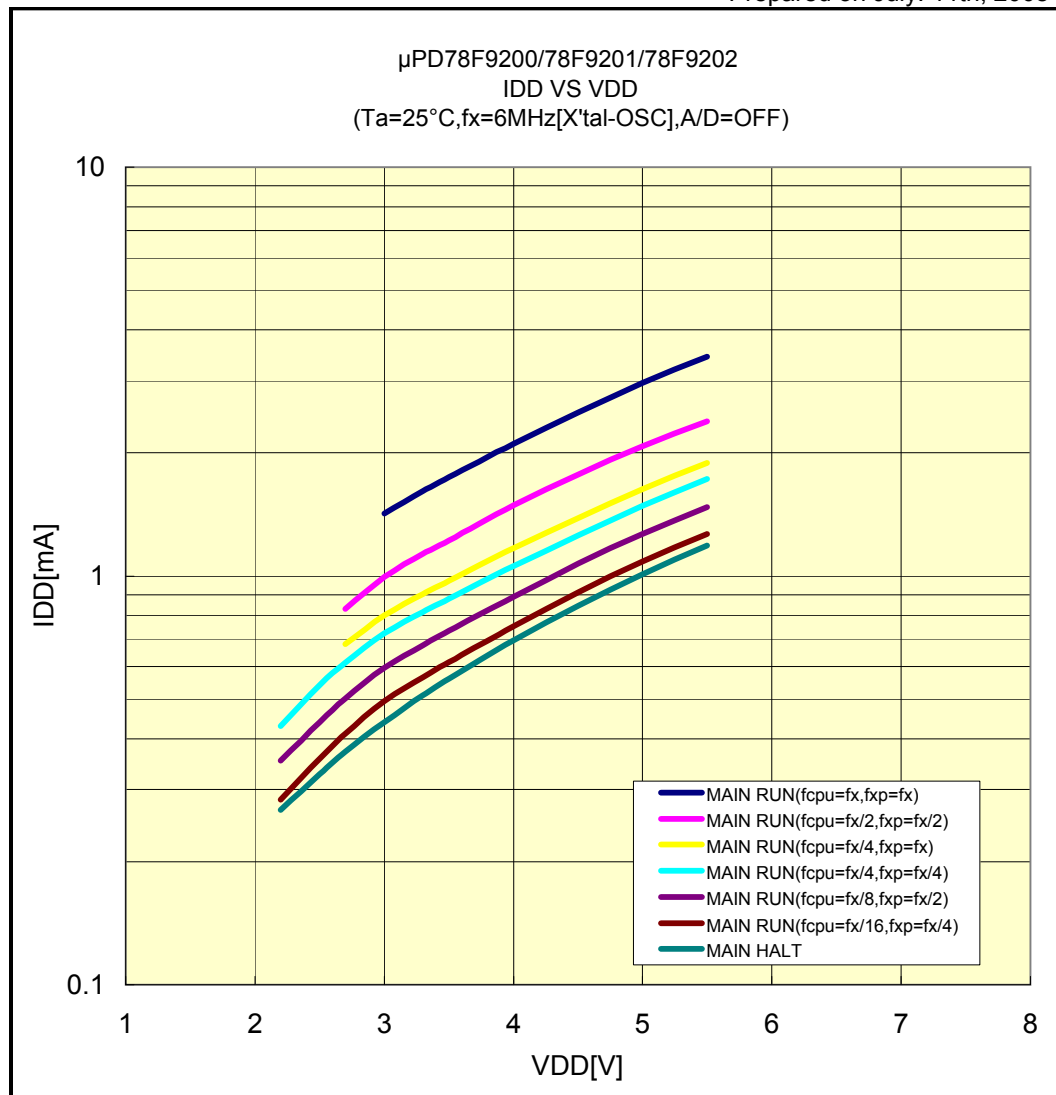
X-axis: VDD[V]
Y-axis: IDD[mA]

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

µPD78F9200/78F9201/78F9202

IDD VS VDD(25°C/6MHz[X'tal-OSC]/AD=OFF)

Prepared on July. 11th, 2008

µPD78F9200/78F9201/78F9202
IDD VS VDD
(Ta=25°C,fx=6MHz[X'tal-OSC],A/D=OFF)

Legend:
- MAIN RUN(fcpu=fx,fxp=fx)
- MAIN RUN(fcpu=fx/2,fxp=fx/2)
- MAIN RUN(fcpu=fx/4,fxp=fx)
- MAIN RUN(fcpu=fx/4,fxp=fx/4)
- MAIN RUN(fcpu=fx/8,fxp=fx/2)
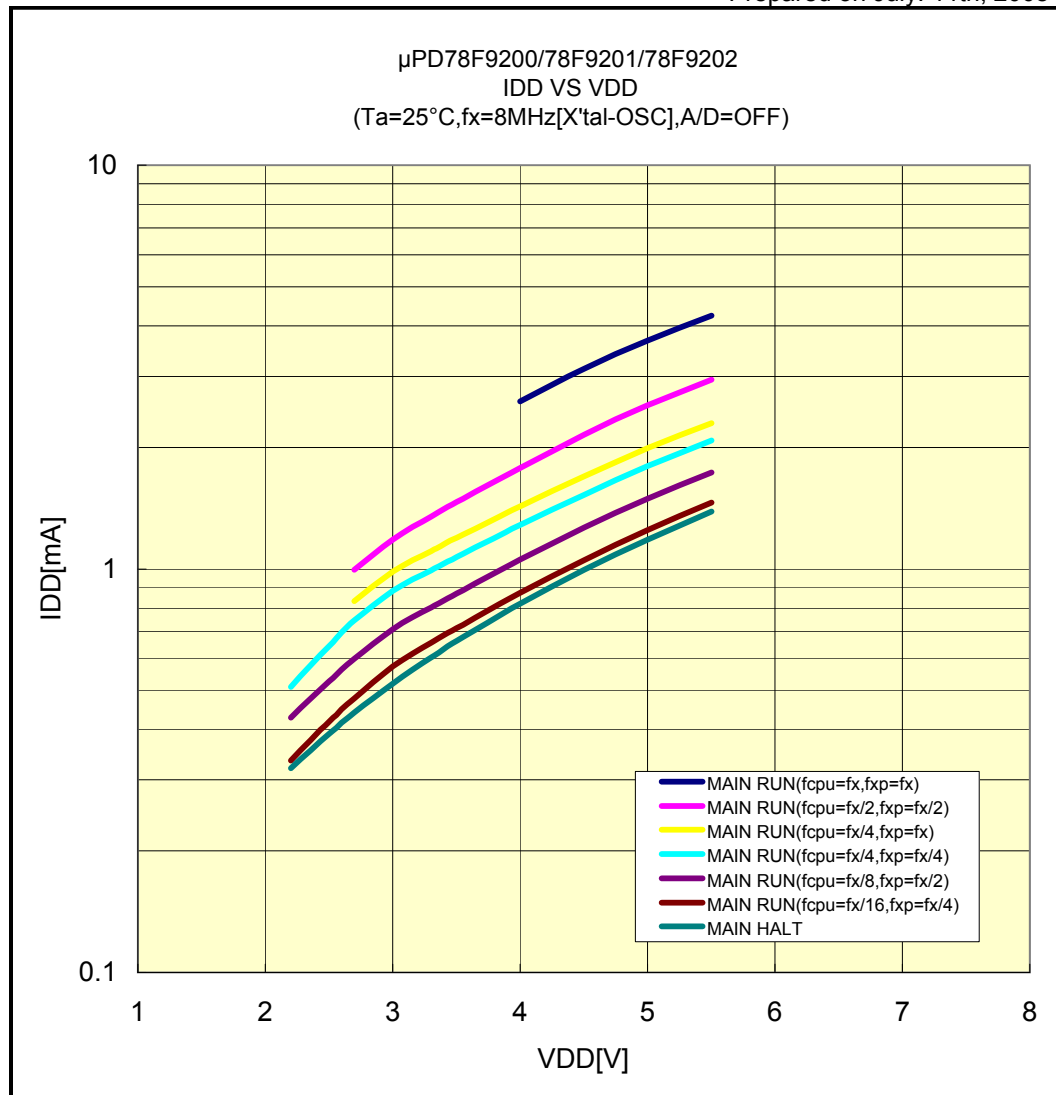- MAIN RUN(fcpu=fx/16,fxp=fx/4)
- MAIN HALT

Axes: IDD[mA] vs VDD[V]

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

µPD78F9200/78F9201/78F9202

IDD VS VDD(25°C/8MHz[X'tal-OSC]/AD=OFF)

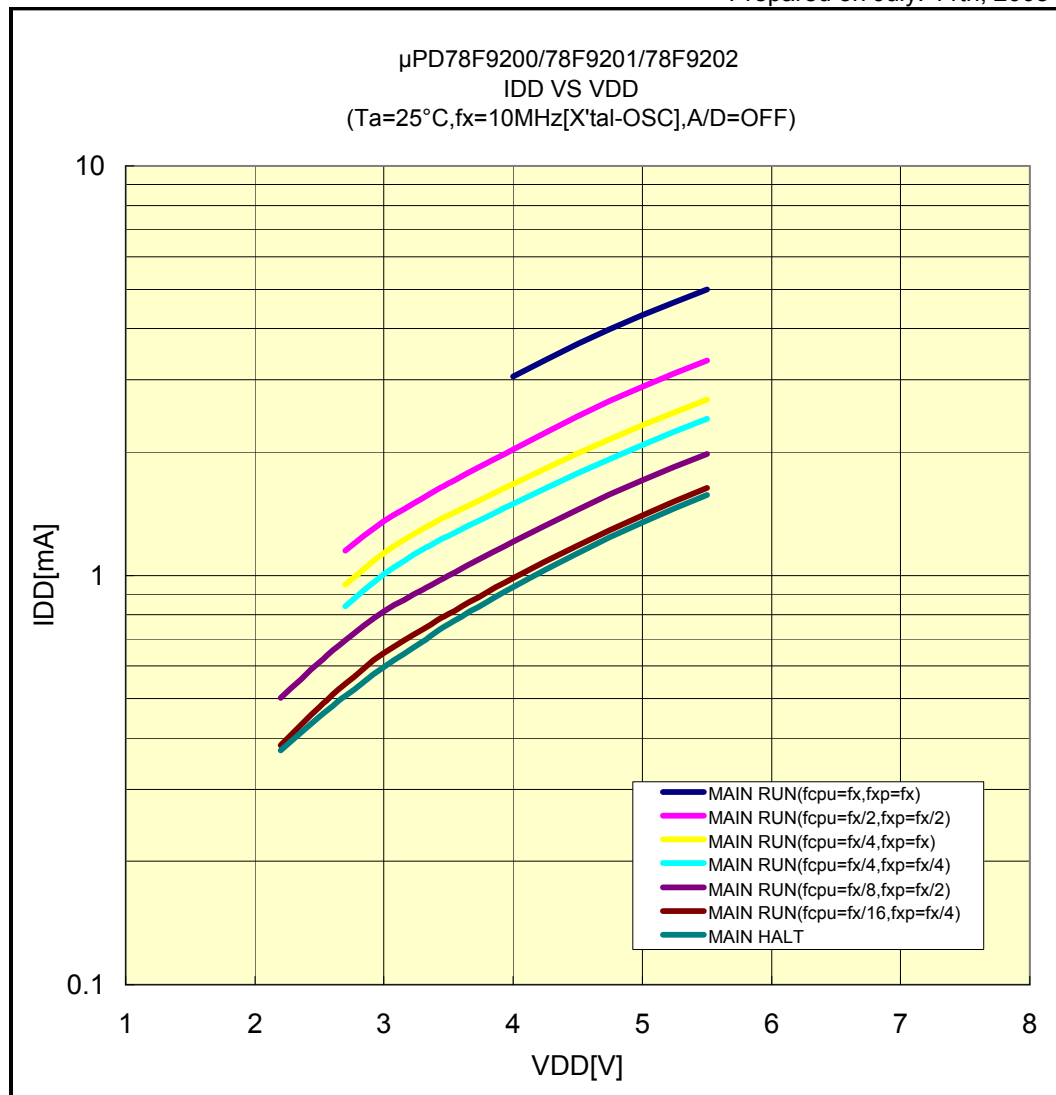Prepared on July. 11th, 2008

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

μPD78F9200/78F9201/78F9202

IDD VS VDD(25°C/10MHz[X'tal-OSC]/AD=OFF)

Prepared on July. 11th, 2008

μPD78F9200/78F9201/78F9202
IDD VS VDD
(Ta=25°C,fx=10MHz[X'tal-OSC],A/D=OFF)

IDD[mA]

VDD[V]

Legend:
- MAIN RUN(fcpu=fx,fxp=fx)
- MAIN RUN(fcpu=fx/2,fxp=fx/2)
- MAIN RUN(fcpu=fx/4,fxp=fx)
- MAIN RUN(fcpu=fx/4,fxp=fx/4)
- MAIN RUN(fcpu=fx/8,fxp=fx/2)
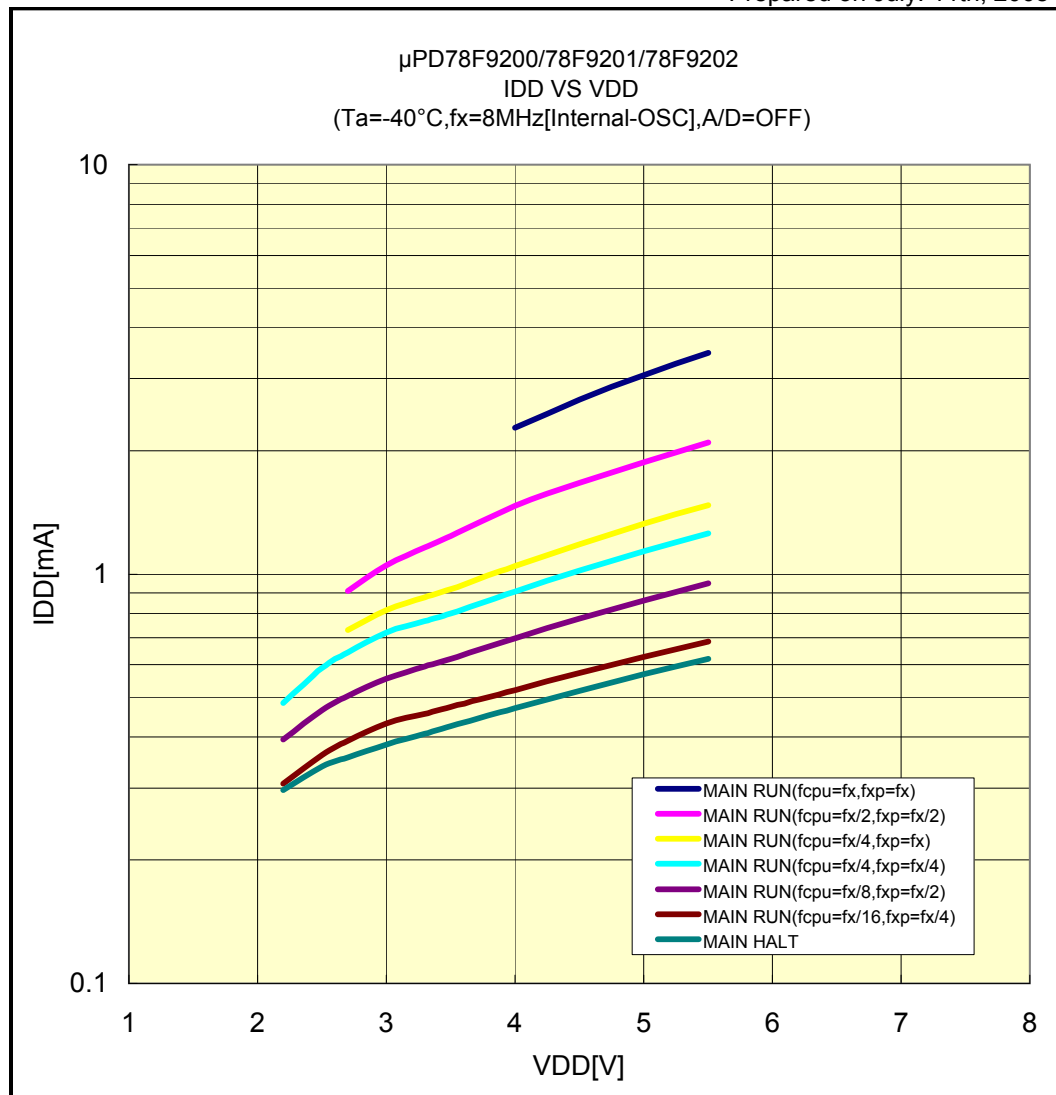- MAIN RUN(fcpu=fx/16,fxp=fx/4)
- MAIN HALT

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

µPD78F9200/78F9201/78F9202

IDD VS VDD(-40°C/8MHz[Internal-OSC]/AD=OFF)

Prepared on July. 11th, 2008

µPD78F9200/78F9201/78F9202
IDD VS VDD
(Ta=-40°C,fx=8MHz[Internal-OSC],A/D=OFF)

IDD[mA]

VDD[V]

MAIN RUN(fcpu=fx,fxp=fx)
MAIN RUN(fcpu=fx/2,fxp=fx/2)
MAIN RUN(fcpu=fx/4,fxp=fx)
MAIN RUN(fcpu=fx/4,fxp=fx/4)
MAIN RUN(fcpu=fx/8,fxp=fx/2)
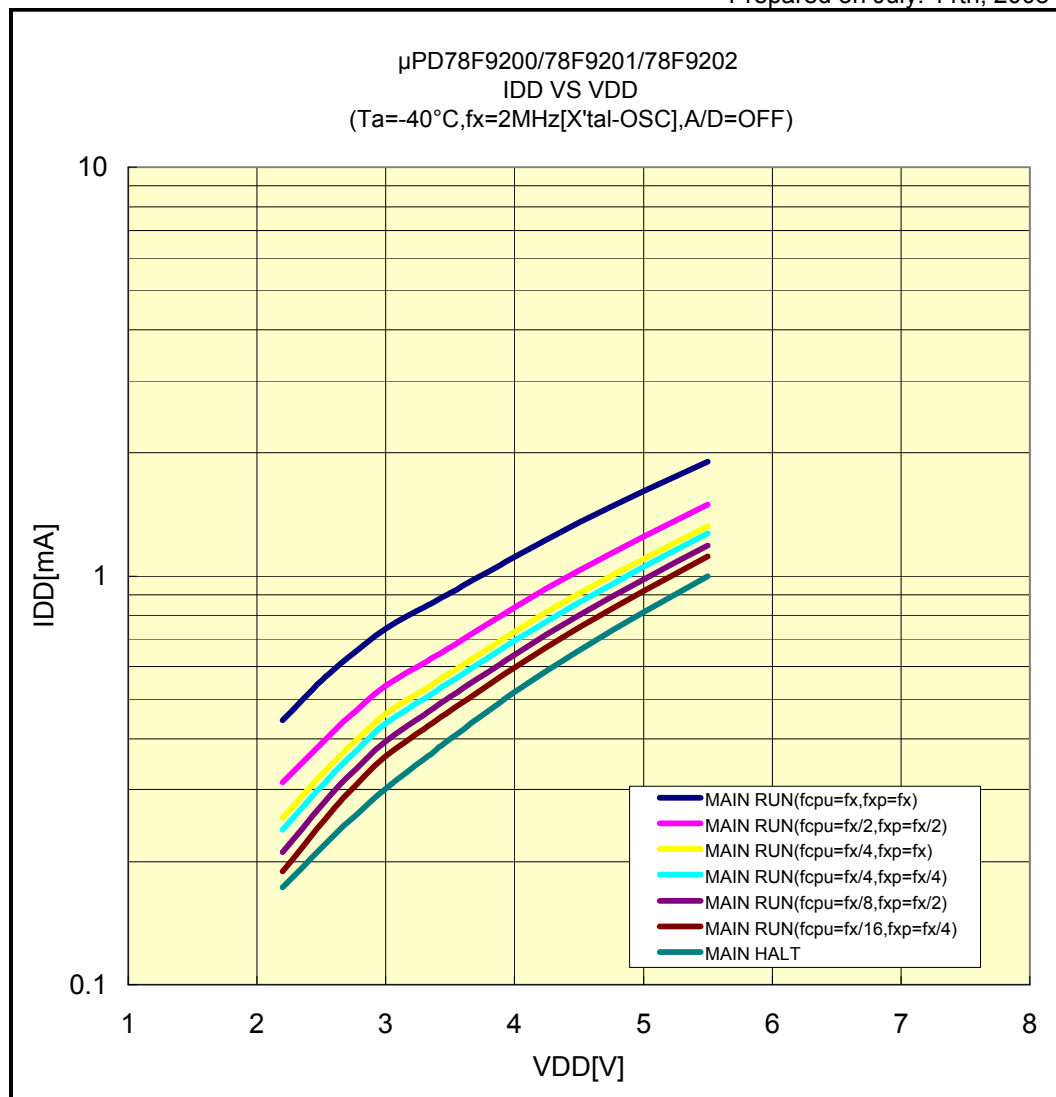MAIN RUN(fcpu=fx/16,fxp=fx/4)
MAIN HALT

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

µPD78F9200/78F9201/78F9202

IDD VS VDD(-40°C/2MHz[X'tal-OSC]/AD=OFF)

Prepared on July. 11th, 2008

µPD78F9200/78F9201/78F9202
IDD VS VDD
(Ta=-40°C,fx=2MHz[X'tal-OSC],A/D=OFF)

IDD[mA] vs VDD[V]

MAIN RUN(fcpu=fx,fxp=fx)
MAIN RUN(fcpu=fx/2,fxp=fx/2)
MAIN RUN(fcpu=fx/4,fxp=fx)
MAIN RUN(fcpu=fx/4,fxp=fx/4)
MAIN RUN(fcpu=fx/8,fxp=fx/2)
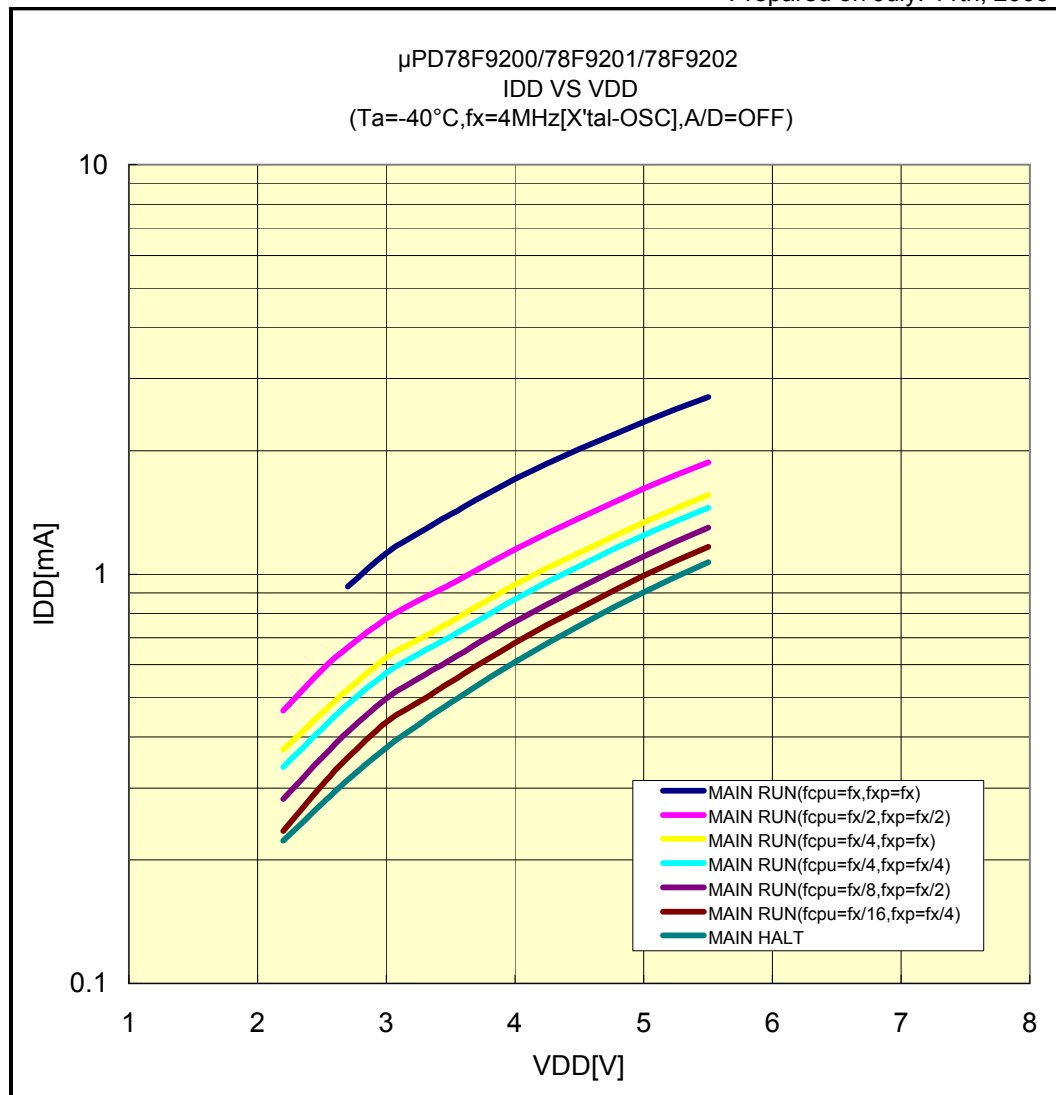MAIN RUN(fcpu=fx/16,fxp=fx/4)
MAIN HALT

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

µPD78F9200/78F9201/78F9202

IDD VS VDD(-40°C/4MHz[X'tal-OSC]/AD=OFF)

µPD78F9200/78F9201/78F9202
IDD VS VDD
(Ta=-40°C,fx=4MHz[X'tal-OSC],A/D=OFF)

Legend:
- MAIN RUN(fcpu=fx,fxp=fx)
- MAIN RUN(fcpu=fx/2,fxp=fx/2)
- MAIN RUN(fcpu=fx/4,fxp=fx)
- MAIN RUN(fcpu=fx/4,fxp=fx/4)
- MAIN RUN(fcpu=fx/8,fxp=fx/2)
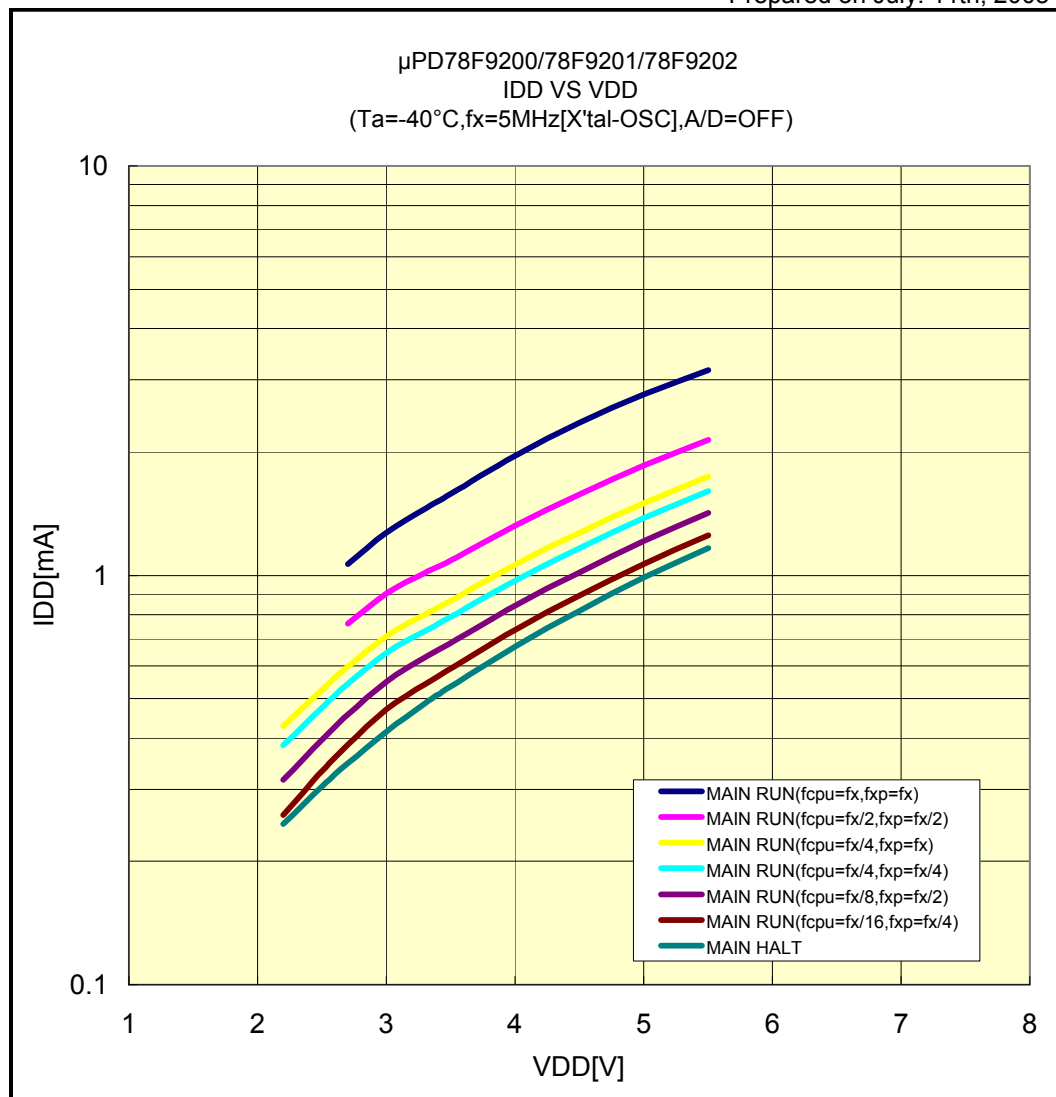- MAIN RUN(fcpu=fx/16,fxp=fx/4)
- MAIN HALT

Axes: IDD[mA] vs VDD[V]

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

µPD78F9200/78F9201/78F9202

IDD VS VDD(-40°C/5MHz[X'tal-OSC]/AD=OFF)

µPD78F9200/78F9201/78F9202
IDD VS VDD
(Ta=-40°C,fx=5MHz[X'tal-OSC],A/D=OFF)

Legend:
- MAIN RUN(fcpu=fx,fxp=fx)
- MAIN RUN(fcpu=fx/2,fxp=fx/2)
- MAIN RUN(fcpu=fx/4,fxp=fx)
- MAIN RUN(fcpu=fx/4,fxp=fx/4)
- MAIN RUN(fcpu=fx/8,fxp=fx/2)
- MAIN RUN(fcpu=fx/16,fxp=fx/4)
- MAIN HALT

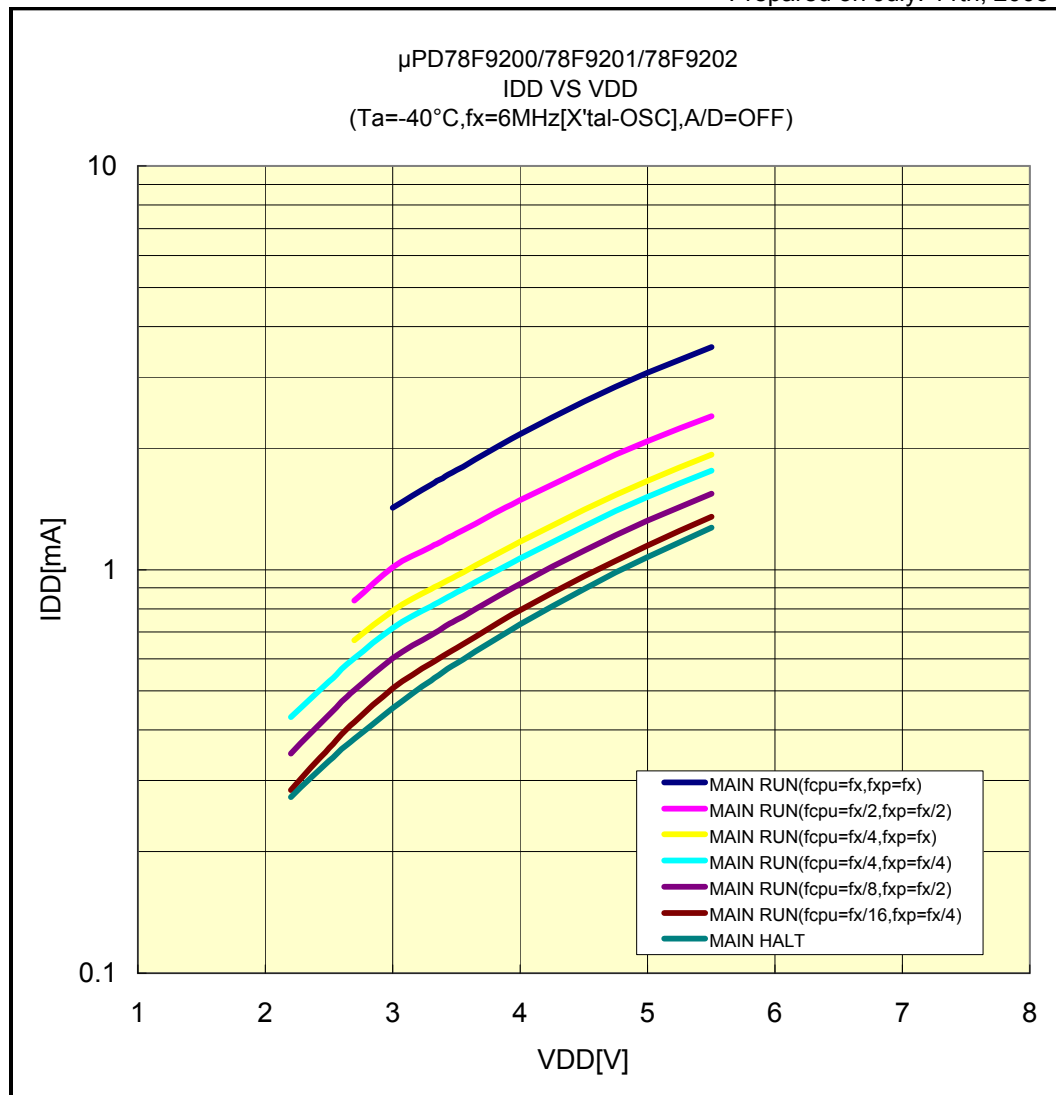Axis labels: IDD[mA] (vertical), VDD[V] (horizontal)

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

µPD78F9200/78F9201/78F9202

IDD VS VDD(-40°C/6MHz[X'tal-OSC]/AD=OFF)

µPD78F9200/78F9201/78F9202
IDD VS VDD
(Ta=-40°C,fx=6MHz[X'tal-OSC],A/D=OFF)

MAIN RUN(fcpu=fx,fxp=fx)
MAIN RUN(fcpu=fx/2,fxp=fx/2)
MAIN RUN(fcpu=fx/4,fxp=fx)
MAIN RUN(fcpu=fx/4,fxp=fx/4)
MAIN RUN(fcpu=fx/8,fxp=fx/2)
MAIN RUN(fcpu=fx/16,fxp=fx/4)
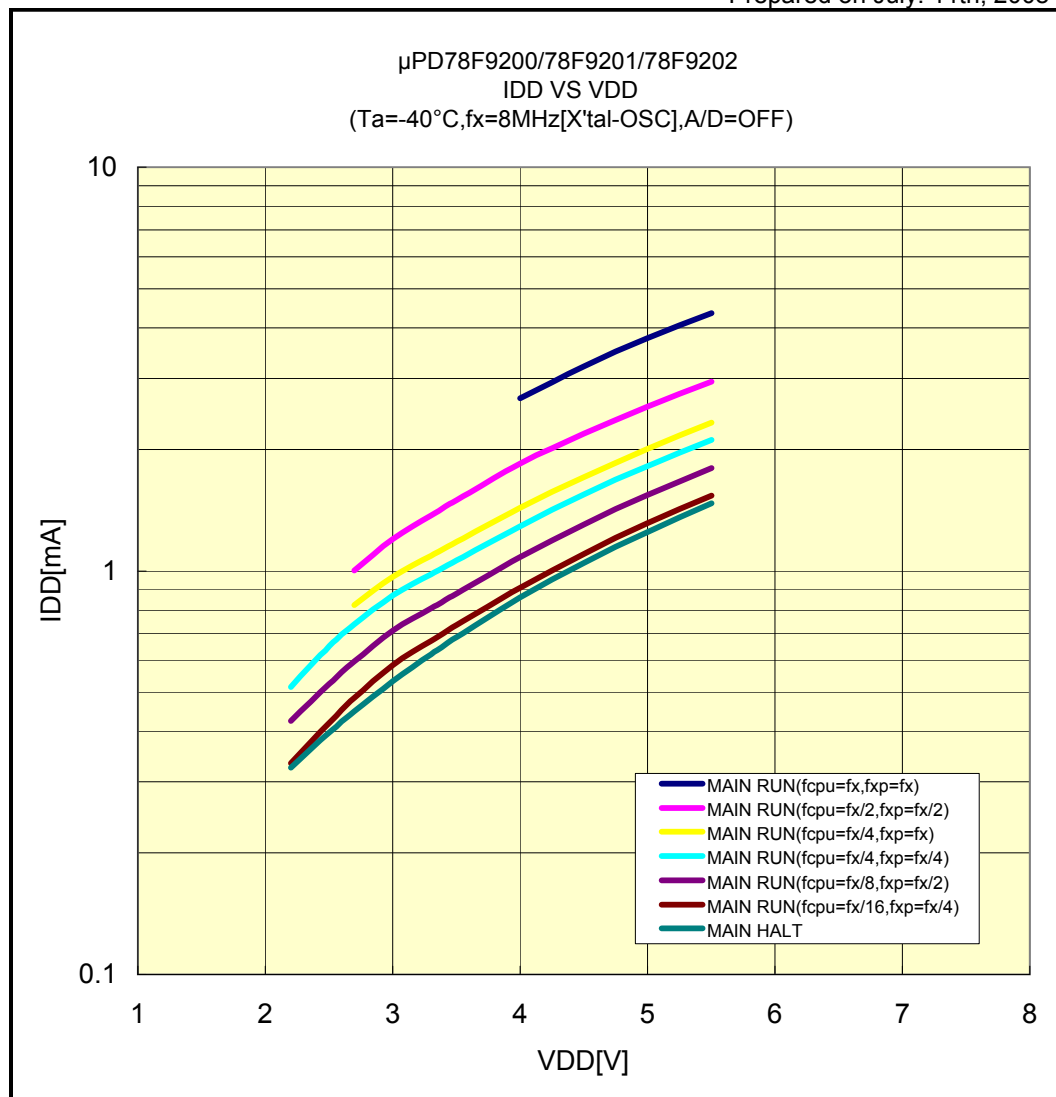MAIN HALT

IDD[mA]

VDD[V]

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

µPD78F9200/78F9201/78F9202

IDD VS VDD(-40°C/8MHz[X'tal-OSC]/AD=OFF)

µPD78F9200/78F9201/78F9202
IDD VS VDD
(Ta=-40°C,fx=8MHz[X'tal-OSC],A/D=OFF)

Legend:
- MAIN RUN(fcpu=fx,fxp=fx)
- MAIN RUN(fcpu=fx/2,fxp=fx/2)
- MAIN RUN(fcpu=fx/4,fxp=fx)
- MAIN RUN(fcpu=fx/4,fxp=fx/4)
- MAIN RUN(fcpu=fx/8,fxp=fx/2)
- MAIN RUN(fcpu=fx/16,fxp=fx/4)
- MAIN HALT
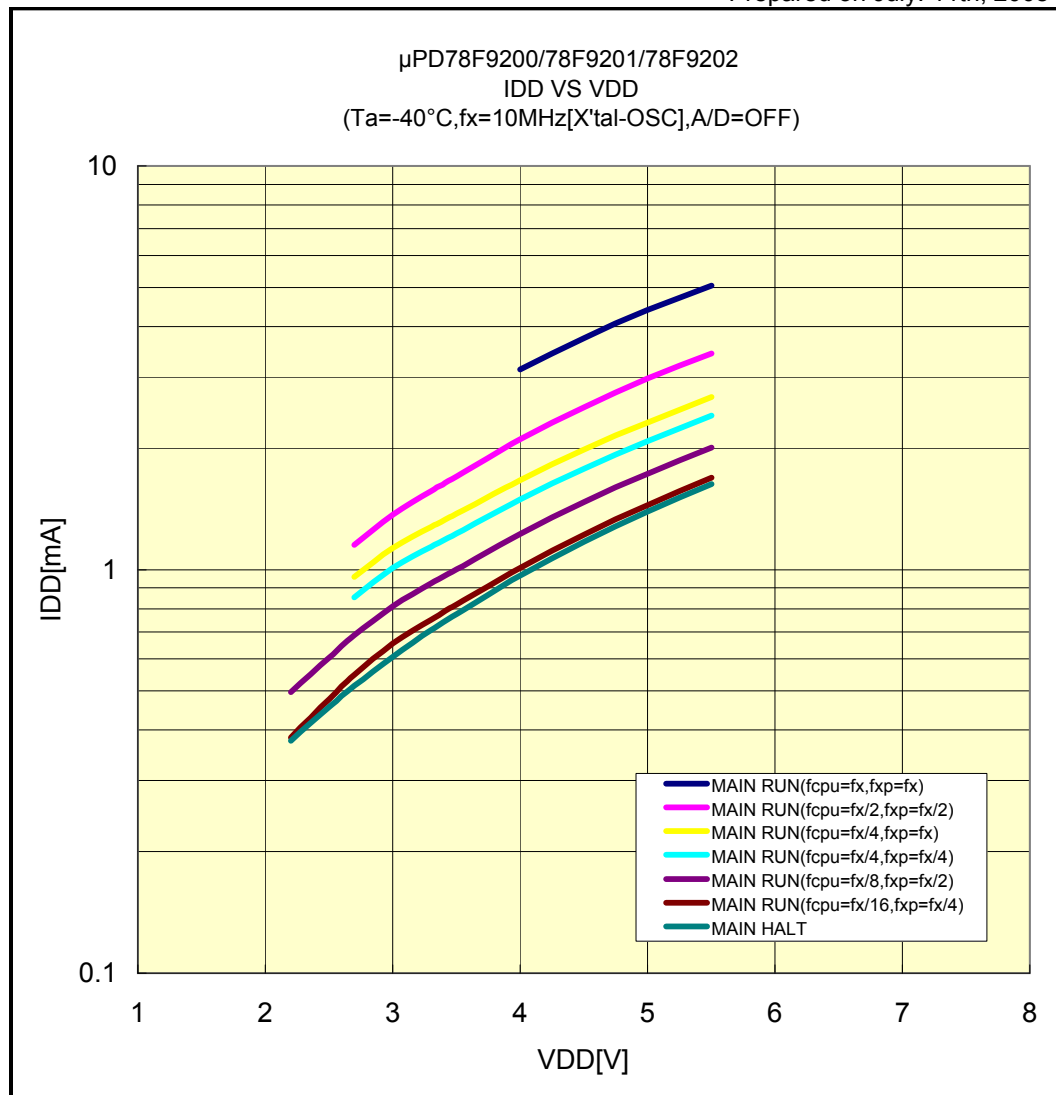
X-axis: VDD[V]
Y-axis: IDD[mA]

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

µPD78F9200/78F9201/78F9202

IDD VS VDD(-40°C/10MHz[X'tal-OSC]/AD=OFF)

µPD78F9200/78F9201/78F9202
IDD VS VDD
(Ta=-40°C,fx=10MHz[X'tal-OSC],A/D=OFF)

MAIN RUN(fcpu=fx,fxp=fx)
MAIN RUN(fcpu=fx/2,fxp=fx/2)
MAIN RUN(fcpu=fx/4,fxp=fx)
MAIN RUN(fcpu=fx/4,fxp=fx/4)
MAIN RUN(fcpu=fx/8,fxp=fx/2)
MAIN RUN(fcpu=fx/16,fxp=fx/4)
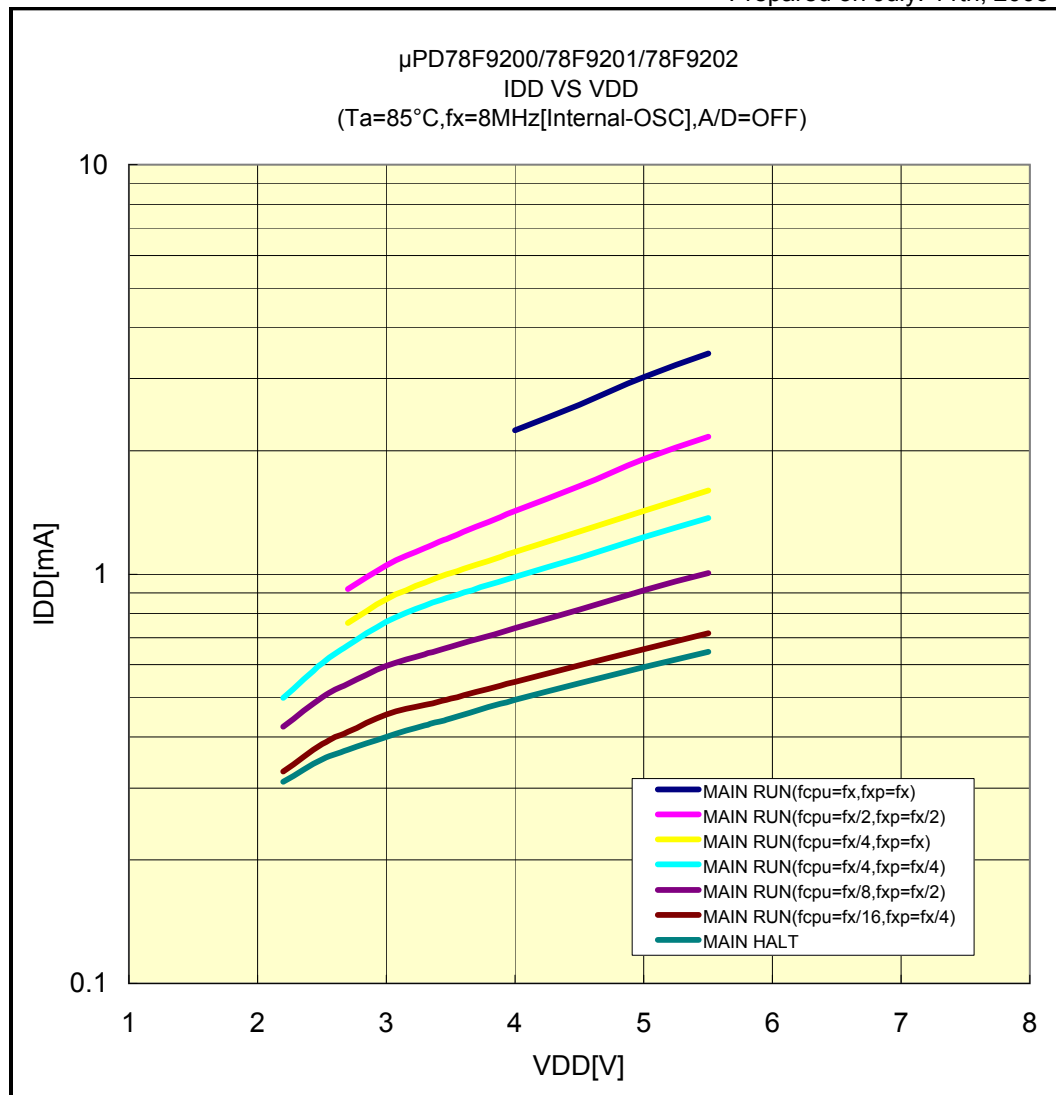MAIN HALT

IDD[mA]

VDD[V]

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

µPD78F9200/78F9201/78F9202

IDD VS VDD(85°C/8MHz[Internal-OSC]/AD=OFF)

µPD78F9200/78F9201/78F9202
IDD VS VDD
(Ta=85°C,fx=8MHz[Internal-OSC],A/D=OFF)

MAIN RUN(fcpu=fx,fxp=fx)
MAIN RUN(fcpu=fx/2,fxp=fx/2)
MAIN RUN(fcpu=fx/4,fxp=fx)
MAIN RUN(fcpu=fx/4,fxp=fx/4)
MAIN RUN(fcpu=fx/8,fxp=fx/2)
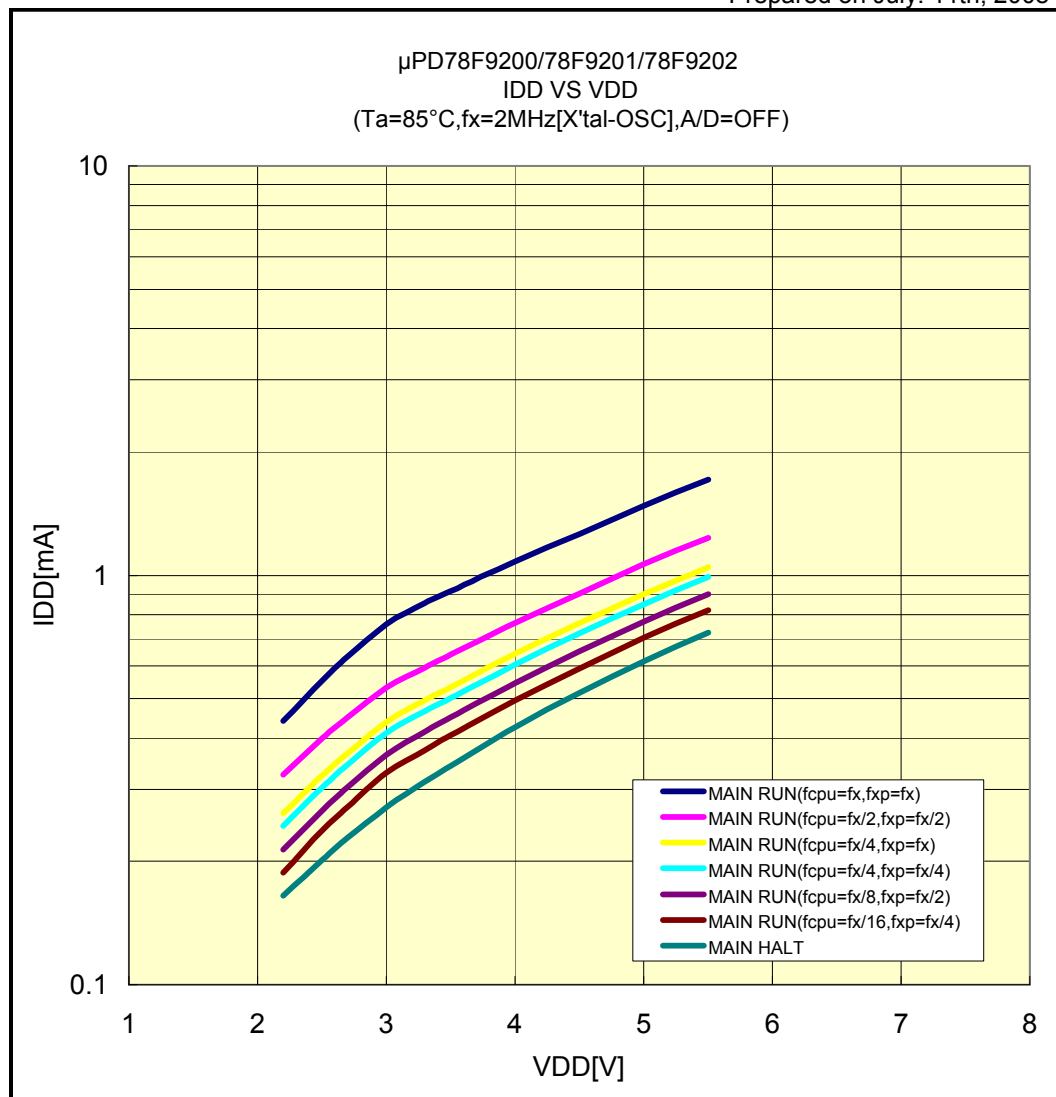MAIN RUN(fcpu=fx/16,fxp=fx/4)
MAIN HALT

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

µPD78F9200/78F9201/78F9202

IDD VS VDD(85°C/2MHz[X'tal-OSC]/AD=OFF)

Prepared on July. 11th, 2008

µPD78F9200/78F9201/78F9202
IDD VS VDD
(Ta=85°C,fx=2MHz[X'tal-OSC],A/D=OFF)

MAIN RUN(fcpu=fx,fxp=fx)
MAIN RUN(fcpu=fx/2,fxp=fx/2)
MAIN RUN(fcpu=fx/4,fxp=fx)
MAIN RUN(fcpu=fx/4,fxp=fx/4)
MAIN RUN(fcpu=fx/8,fxp=fx/2)
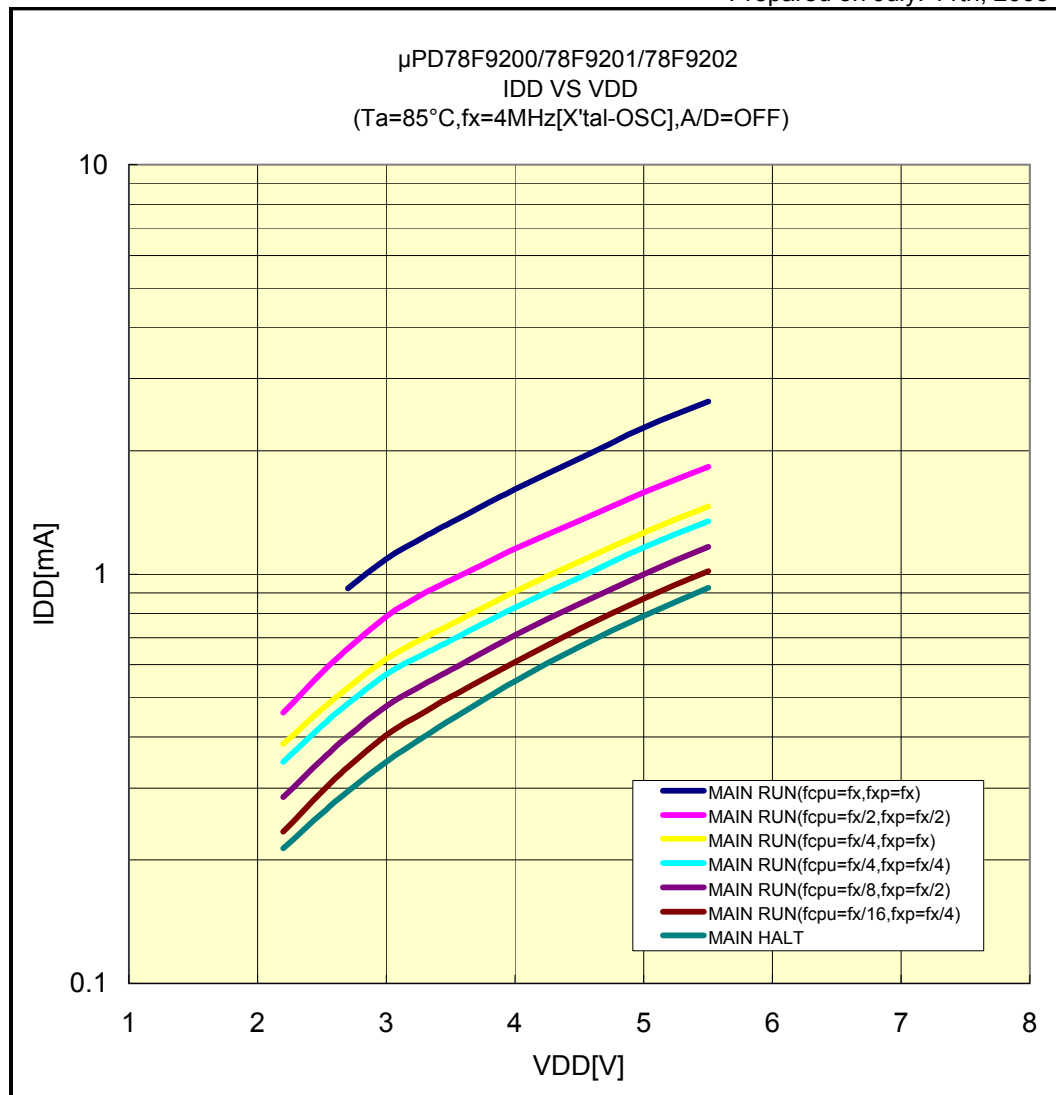MAIN RUN(fcpu=fx/16,fxp=fx/4)
MAIN HALT

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

µPD78F9200/78F9201/78F9202

IDD VS VDD(85°C/4MHz[X'tal-OSC]/AD=OFF)

µPD78F9200/78F9201/78F9202
IDD VS VDD
(Ta=85°C,fx=4MHz[X'tal-OSC],A/D=OFF)

MAIN RUN(fcpu=fx,fxp=fx)
MAIN RUN(fcpu=fx/2,fxp=fx/2)
MAIN RUN(fcpu=fx/4,fxp=fx)
MAIN RUN(fcpu=fx/4,fxp=fx/4)
MAIN RUN(fcpu=fx/8,fxp=fx/2)
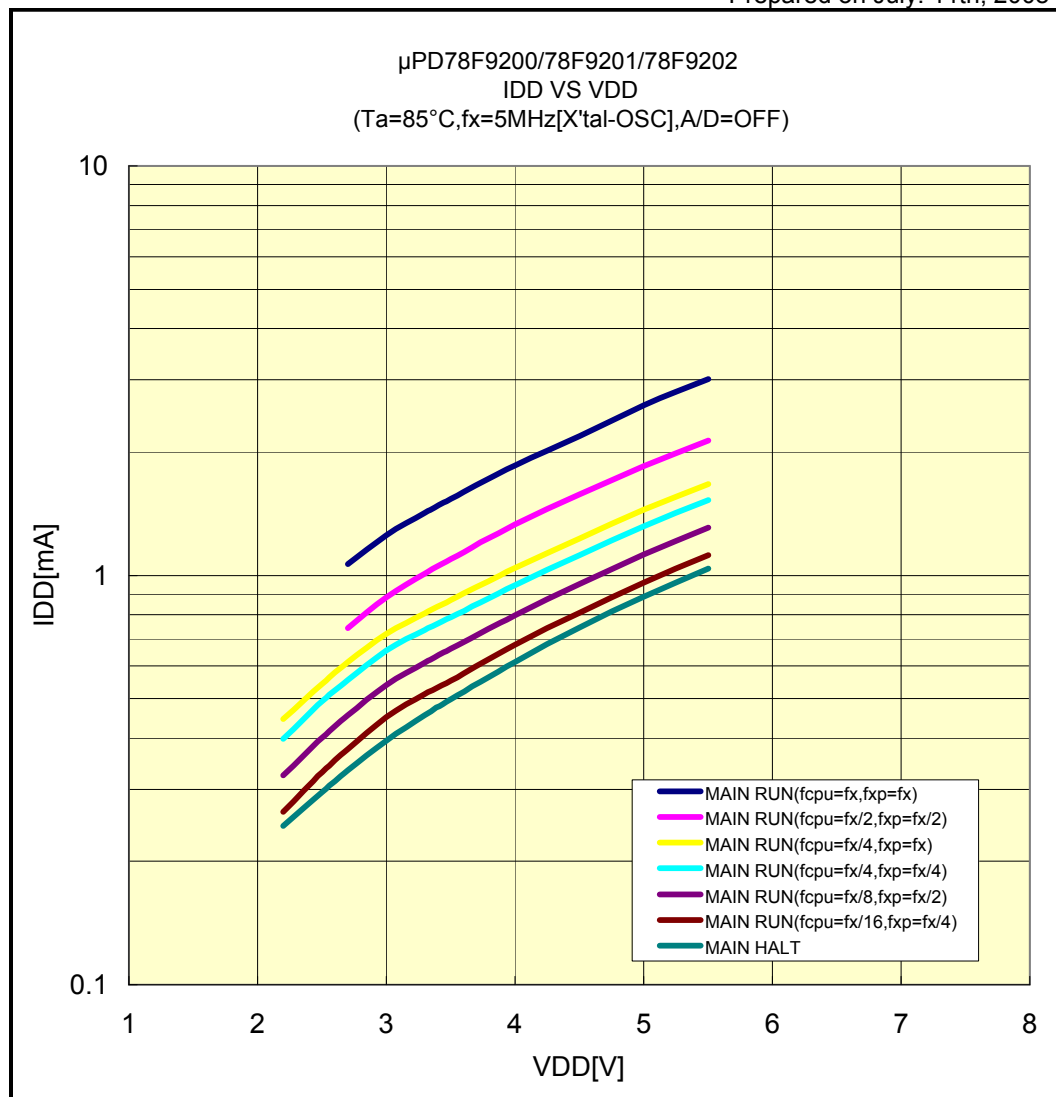MAIN RUN(fcpu=fx/16,fxp=fx/4)
MAIN HALT

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

µPD78F9200/78F9201/78F9202

IDD VS VDD(85°C/5MHz[X'tal-OSC]/AD=OFF)

µPD78F9200/78F9201/78F9202
IDD VS VDD
(Ta=85°C,fx=5MHz[X'tal-OSC],A/D=OFF)

Legend:
- MAIN RUN(fcpu=fx,fxp=fx)
- MAIN RUN(fcpu=fx/2,fxp=fx/2)
- MAIN RUN(fcpu=fx/4,fxp=fx)
- MAIN RUN(fcpu=fx/4,fxp=fx/4)
- MAIN RUN(fcpu=fx/8,fxp=fx/2)
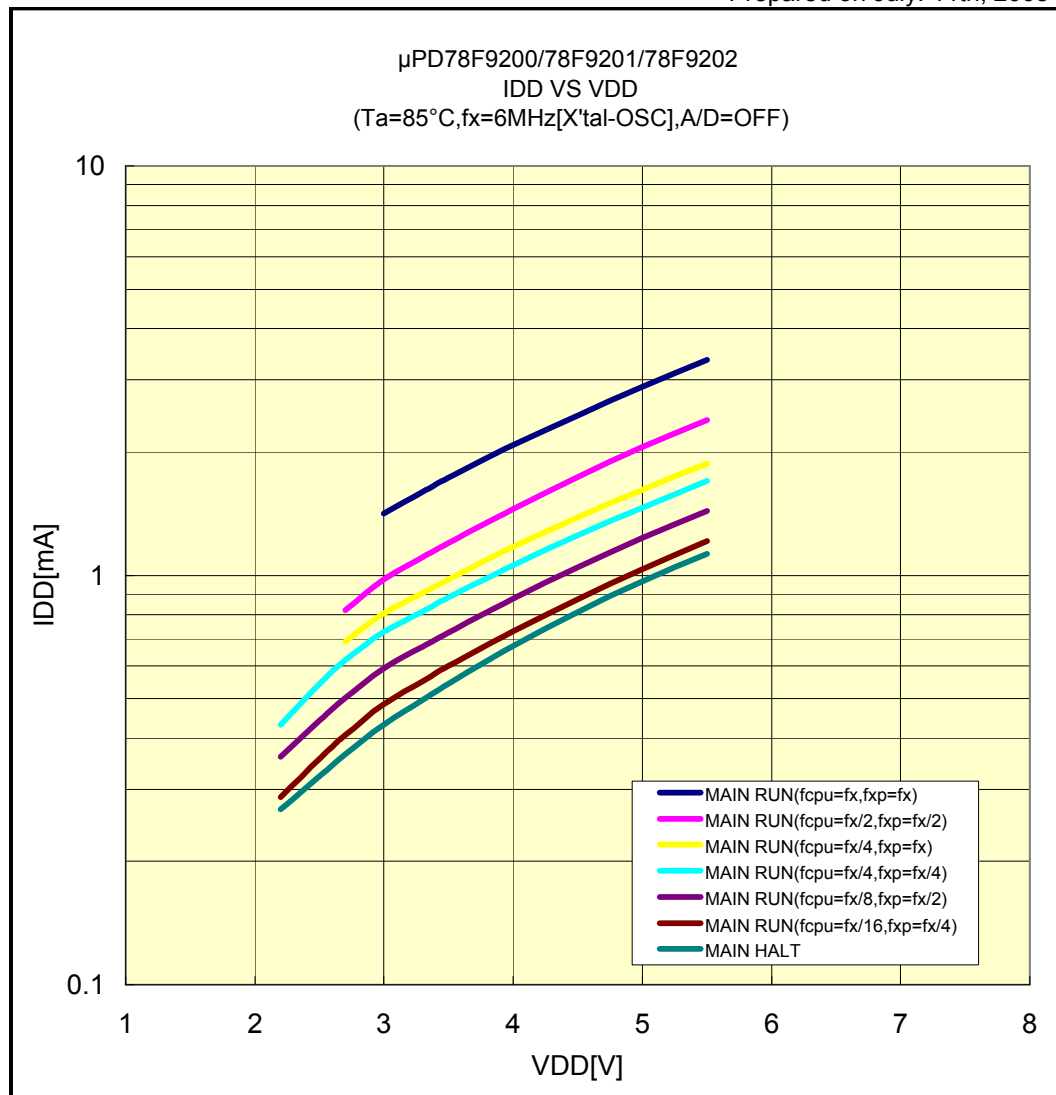- MAIN RUN(fcpu=fx/16,fxp=fx/4)
- MAIN HALT

Axes: IDD[mA] vs VDD[V]

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

µPD78F9200/78F9201/78F9202

IDD VS VDD(85°C/6MHz[X'tal-OSC]/AD=OFF)

µPD78F9200/78F9201/78F9202
IDD VS VDD
(Ta=85°C,fx=6MHz[X'tal-OSC],A/D=OFF)

IDD[mA]

VDD[V]

MAIN RUN(fcpu=fx,fxp=fx)
MAIN RUN(fcpu=fx/2,fxp=fx/2)
MAIN RUN(fcpu=fx/4,fxp=fx)
MAIN RUN(fcpu=fx/4,fxp=fx/4)
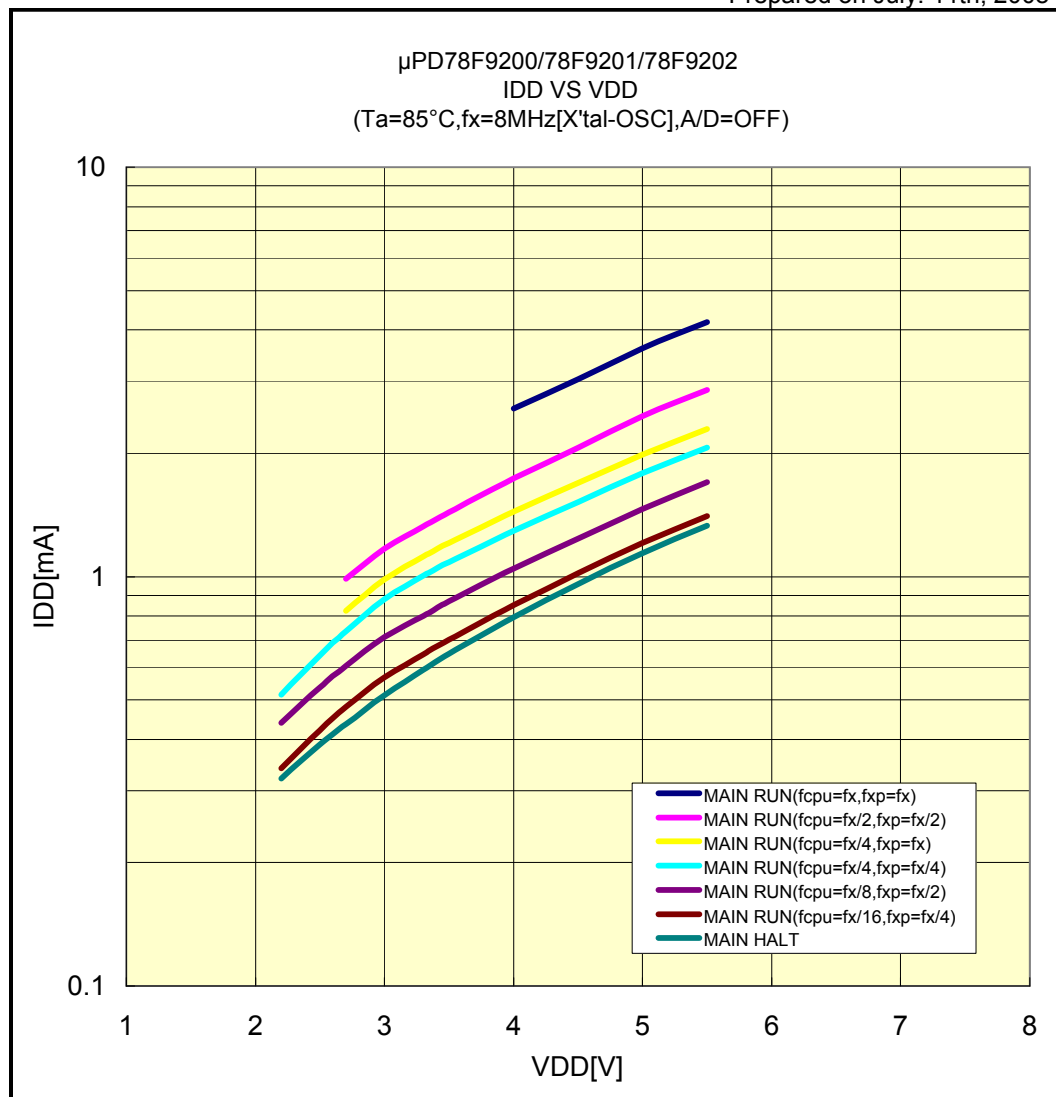MAIN RUN(fcpu=fx/8,fxp=fx/2)
MAIN RUN(fcpu=fx/16,fxp=fx/4)
MAIN HALT

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

µPD78F9200/78F9201/78F9202

IDD VS VDD(85°C/8MHz[X'tal-OSC]/AD=OFF)

µPD78F9200/78F9201/78F9202
IDD VS VDD
(Ta=85°C,fx=8MHz[X'tal-OSC],A/D=OFF)

Legend:
- MAIN RUN(fcpu=fx,fxp=fx)
- MAIN RUN(fcpu=fx/2,fxp=fx/2)
- MAIN RUN(fcpu=fx/4,fxp=fx)
- MAIN RUN(fcpu=fx/4,fxp=fx/4)
- MAIN RUN(fcpu=fx/8,fxp=fx/2)
- MAIN RUN(fcpu=fx/16,fxp=fx/4)
- MAIN HALT
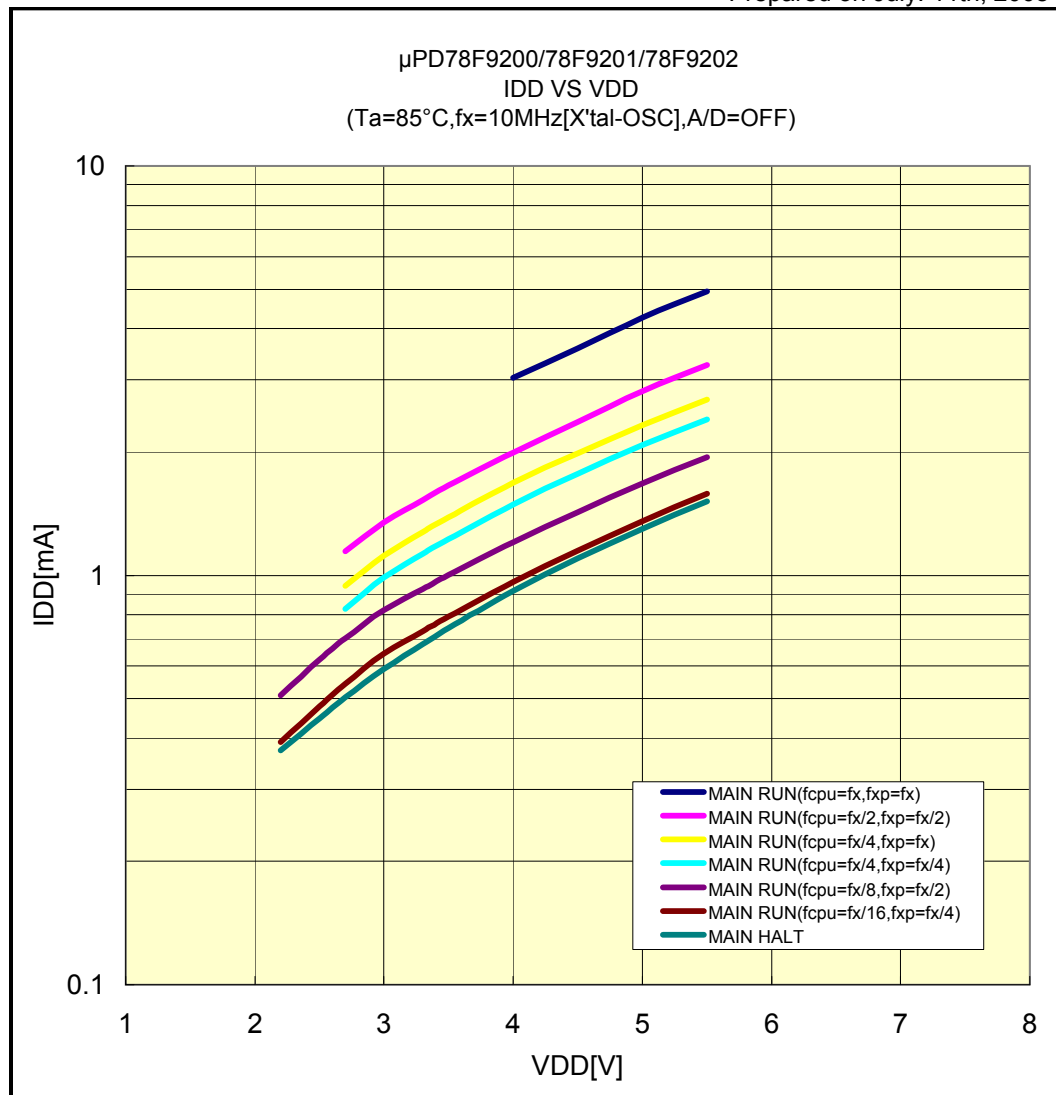
X-axis: VDD[V]
Y-axis: IDD[mA]

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

µPD78F9200/78F9201/78F9202

IDD VS VDD(85°C/10MHz[X'tal-OSC]/AD=OFF)

µPD78F9200/78F9201/78F9202
IDD VS VDD
(Ta=85°C,fx=10MHz[X'tal-OSC],A/D=OFF)

Legend:
- MAIN RUN(fcpu=fx,fxp=fx)
- MAIN RUN(fcpu=fx/2,fxp=fx/2)
- MAIN RUN(fcpu=fx/4,fxp=fx)
- MAIN RUN(fcpu=fx/4,fxp=fx/4)
- MAIN RUN(fcpu=fx/8,fxp=fx/2)
- MAIN RUN(fcpu=fx/16,fxp=fx/4)
- MAIN HALT

Axes: IDD[mA] (vertical), VDD[V] (horizontal)

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.