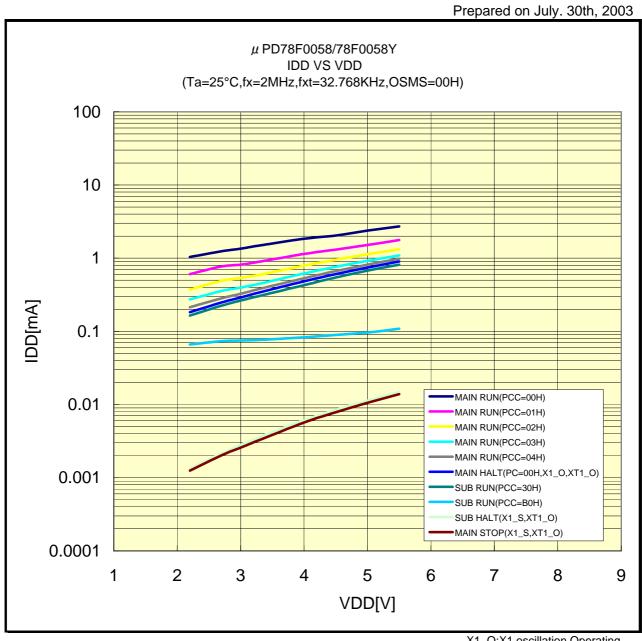
## µPD78F0058/78F0058Y

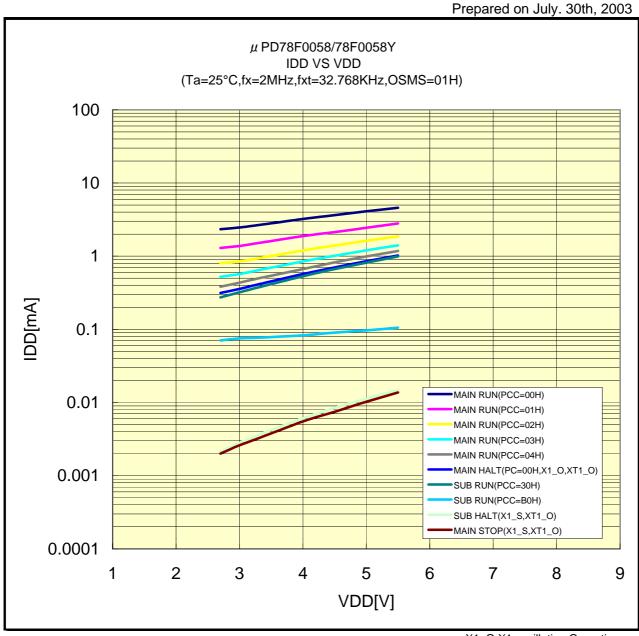
# IDD VS VDD(25°C/2MHz/OSMS=00H)



X1\_O:X1 oscillation Operating X1\_S:X1 oscillation Stop XT1\_O:XT1 oscillation Operating

## μPD78F0058/78F0058Y

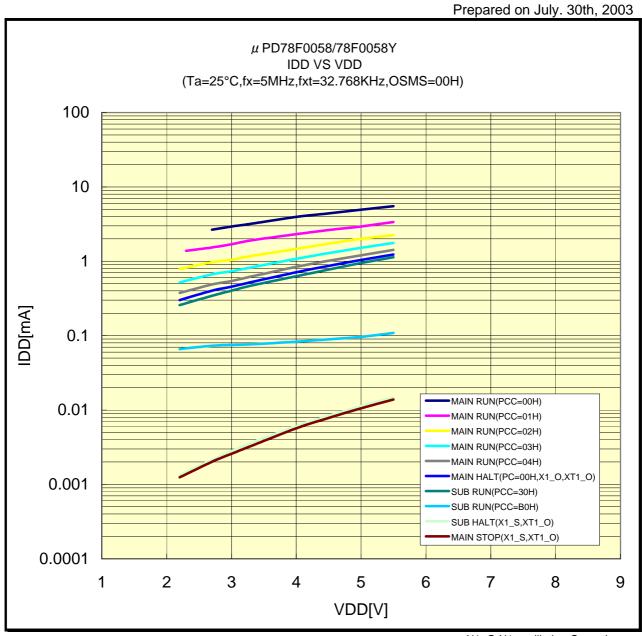
# IDD VS VDD(25°C/2MHz/OSMS=01H)



X1\_O:X1 oscillation Operating X1\_S:X1 oscillation Stop XT1\_O:XT1 oscillation Operating

## μPD78F0058/78F0058Y

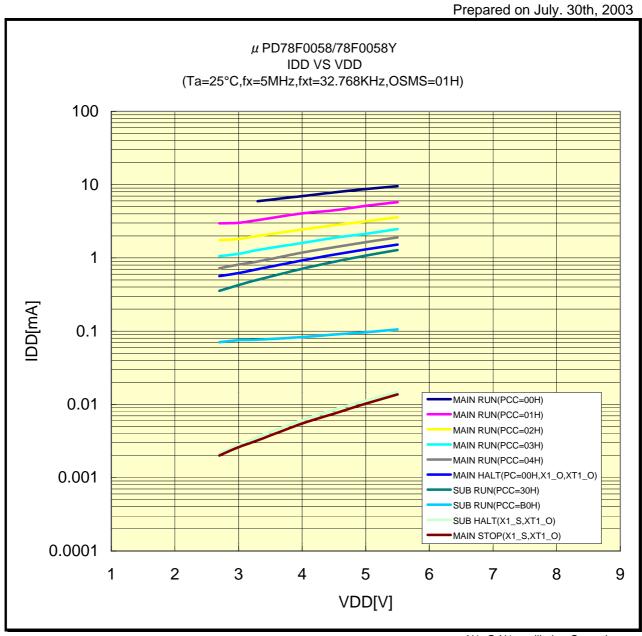
# IDD VS VDD(25°C/5MHz/OSMS=00H)



X1\_O:X1 oscillation Operating X1\_S:X1 oscillation Stop XT1\_O:XT1 oscillation Operating

## μPD78F0058/78F0058Y

# IDD VS VDD(25°C/5MHz/OSMS=01H)



X1\_O:X1 oscillation Operating X1\_S:X1 oscillation Stop XT1\_O:XT1 oscillation Operating