

To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: <http://www.renesas.com>

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

Send any inquiries to <http://www.renesas.com/inquiry>.

Notice

1. All information included in this document is current as of the date this document is issued. Such information, however, is subject to change without any prior notice. Before purchasing or using any Renesas Electronics products listed herein, please confirm the latest product information with a Renesas Electronics sales office. Also, please pay regular and careful attention to additional and different information to be disclosed by Renesas Electronics such as that disclosed through our website.
2. Renesas Electronics does not assume any liability for infringement of patents, copyrights, or other intellectual property rights of third parties by or arising from the use of Renesas Electronics products or technical information described in this document. No license, express, implied or otherwise, is granted hereby under any patents, copyrights or other intellectual property rights of Renesas Electronics or others.
3. You should not alter, modify, copy, or otherwise misappropriate any Renesas Electronics product, whether in whole or in part.
4. Descriptions of circuits, software and other related information in this document are provided only to illustrate the operation of semiconductor products and application examples. You are fully responsible for the incorporation of these circuits, software, and information in the design of your equipment. Renesas Electronics assumes no responsibility for any losses incurred by you or third parties arising from the use of these circuits, software, or information.
5. When exporting the products or technology described in this document, you should comply with the applicable export control laws and regulations and follow the procedures required by such laws and regulations. You should not use Renesas Electronics products or the technology described in this document for any purpose relating to military applications or use by the military, including but not limited to the development of weapons of mass destruction. Renesas Electronics products and technology may not be used for or incorporated into any products or systems whose manufacture, use, or sale is prohibited under any applicable domestic or foreign laws or regulations.
6. Renesas Electronics has used reasonable care in preparing the information included in this document, but Renesas Electronics does not warrant that such information is error free. Renesas Electronics assumes no liability whatsoever for any damages incurred by you resulting from errors in or omissions from the information included herein.
7. Renesas Electronics products are classified according to the following three quality grades: "Standard", "High Quality", and "Specific". The recommended applications for each Renesas Electronics product depends on the product's quality grade, as indicated below. You must check the quality grade of each Renesas Electronics product before using it in a particular application. You may not use any Renesas Electronics product for any application categorized as "Specific" without the prior written consent of Renesas Electronics. Further, you may not use any Renesas Electronics product for any application for which it is not intended without the prior written consent of Renesas Electronics. Renesas Electronics shall not be in any way liable for any damages or losses incurred by you or third parties arising from the use of any Renesas Electronics product for an application categorized as "Specific" or for which the product is not intended where you have failed to obtain the prior written consent of Renesas Electronics. The quality grade of each Renesas Electronics product is "Standard" unless otherwise expressly specified in a Renesas Electronics data sheets or data books, etc.

"Standard": Computers; office equipment; communications equipment; test and measurement equipment; audio and visual equipment; home electronic appliances; machine tools; personal electronic equipment; and industrial robots.

"High Quality": Transportation equipment (automobiles, trains, ships, etc.); traffic control systems; anti-disaster systems; anti-crime systems; safety equipment; and medical equipment not specifically designed for life support.

"Specific": Aircraft; aerospace equipment; submersible repeaters; nuclear reactor control systems; medical equipment or systems for life support (e.g. artificial life support devices or systems), surgical implantations, or healthcare intervention (e.g. excision, etc.), and any other applications or purposes that pose a direct threat to human life.
8. You should use the Renesas Electronics products described in this document within the range specified by Renesas Electronics, especially with respect to the maximum rating, operating supply voltage range, movement power voltage range, heat radiation characteristics, installation and other product characteristics. Renesas Electronics shall have no liability for malfunctions or damages arising out of the use of Renesas Electronics products beyond such specified ranges.
9. Although Renesas Electronics endeavors to improve the quality and reliability of its products, semiconductor products have specific characteristics such as the occurrence of failure at a certain rate and malfunctions under certain use conditions. Further, Renesas Electronics products are not subject to radiation resistance design. Please be sure to implement safety measures to guard them against the possibility of physical injury, and injury or damage caused by fire in the event of the failure of a Renesas Electronics product, such as safety design for hardware and software including but not limited to redundancy, fire control and malfunction prevention, appropriate treatment for aging degradation or any other appropriate measures. Because the evaluation of microcomputer software alone is very difficult, please evaluate the safety of the final products or system manufactured by you.
10. Please contact a Renesas Electronics sales office for details as to environmental matters such as the environmental compatibility of each Renesas Electronics product. Please use Renesas Electronics products in compliance with all applicable laws and regulations that regulate the inclusion or use of controlled substances, including without limitation, the EU RoHS Directive. Renesas Electronics assumes no liability for damages or losses occurring as a result of your noncompliance with applicable laws and regulations.
11. This document may not be reproduced or duplicated, in any form, in whole or in part, without prior written consent of Renesas Electronics.
12. Please contact a Renesas Electronics sales office if you have any questions regarding the information contained in this document or Renesas Electronics products, or if you have any other inquiries.

(Note 1) "Renesas Electronics" as used in this document means Renesas Electronics Corporation and also includes its majority-owned subsidiaries.

(Note 2) "Renesas Electronics product(s)" means any product developed or manufactured by or for Renesas Electronics.

To all our customers

Regarding the change of names mentioned in the document, such as Hitachi Electric and Hitachi XX, to Renesas Technology Corp.

The semiconductor operations of Mitsubishi Electric and Hitachi were transferred to Renesas Technology Corporation on April 1st 2003. These operations include microcomputer, logic, analog and discrete devices, and memory chips other than DRAMs (flash memory, SRAMs etc.) Accordingly, although Hitachi, Hitachi, Ltd., Hitachi Semiconductors, and other Hitachi brand names are mentioned in the document, these names have in fact all been changed to Renesas Technology Corp. Thank you for your understanding. Except for our corporate trademark, logo and corporate statement, no changes whatsoever have been made to the contents of the document, and these changes do not constitute any alteration to the contents of the document itself.

Renesas Technology Home Page: <http://www.renesas.com>

Renesas Technology Corp.
Customer Support Dept.
April 1, 2003

Cautions

Keep safety first in your circuit designs!

1. Renesas Technology Corporation puts the maximum effort into making semiconductor products better and more reliable, but there is always the possibility that trouble may occur with them. Trouble with semiconductors may lead to personal injury, fire or property damage.
Remember to give due consideration to safety when making your circuit designs, with appropriate measures such as (i) placement of substitutive, auxiliary circuits, (ii) use of nonflammable material or (iii) prevention against any malfunction or mishap.

Notes regarding these materials

1. These materials are intended as a reference to assist our customers in the selection of the Renesas Technology Corporation product best suited to the customer's application; they do not convey any license under any intellectual property rights, or any other rights, belonging to Renesas Technology Corporation or a third party.
2. Renesas Technology Corporation assumes no responsibility for any damage, or infringement of any third-party's rights, originating in the use of any product data, diagrams, charts, programs, algorithms, or circuit application examples contained in these materials.
3. All information contained in these materials, including product data, diagrams, charts, programs and algorithms represents information on products at the time of publication of these materials, and are subject to change by Renesas Technology Corporation without notice due to product improvements or other reasons. It is therefore recommended that customers contact Renesas Technology Corporation or an authorized Renesas Technology Corporation product distributor for the latest product information before purchasing a product listed herein.
The information described here may contain technical inaccuracies or typographical errors.
Renesas Technology Corporation assumes no responsibility for any damage, liability, or other loss rising from these inaccuracies or errors.
Please also pay attention to information published by Renesas Technology Corporation by various means, including the Renesas Technology Corporation Semiconductor home page (<http://www.renesas.com>).
4. When using any or all of the information contained in these materials, including product data, diagrams, charts, programs, and algorithms, please be sure to evaluate all information as a total system before making a final decision on the applicability of the information and products. Renesas Technology Corporation assumes no responsibility for any damage, liability or other loss resulting from the information contained herein.
5. Renesas Technology Corporation semiconductors are not designed or manufactured for use in a device or system that is used under circumstances in which human life is potentially at stake. Please contact Renesas Technology Corporation or an authorized Renesas Technology Corporation product distributor when considering the use of a product contained herein for any specific purposes, such as apparatus or systems for transportation, vehicular, medical, aerospace, nuclear, or undersea repeater use.
6. The prior written approval of Renesas Technology Corporation is necessary to reprint or reproduce in whole or in part these materials.
7. If these products or technologies are subject to the Japanese export control restrictions, they must be exported under a license from the Japanese government and cannot be imported into a country other than the approved destination.
Any diversion or reexport contrary to the export control laws and regulations of Japan and/or the country of destination is prohibited.
8. Please contact Renesas Technology Corporation for further details on these materials or the products contained therein.

SH7294 E10A Emulator

User's Manual

Cautions

1. Hitachi neither warrants nor grants licenses of any rights of Hitachi's or any third party's patent, copyright, trademark, or other intellectual property rights for information contained in this document. Hitachi bears no responsibility for problems that may arise with third party's rights, including intellectual property rights, in connection with use of the information contained in this document.
2. Products and product specifications may be subject to change without notice. Confirm that you have received the latest product standards or specifications before final design, purchase or use.
3. Hitachi makes every attempt to ensure that its products are of high quality and reliability. However, contact Hitachi's sales office before using the product in an application that demands especially high quality and reliability or where its failure or malfunction may directly threaten human life or cause risk of bodily injury, such as aerospace, aeronautics, nuclear power, combustion control, transportation, traffic, safety equipment or medical equipment for life support.
4. Design your application so that the product is used within the ranges guaranteed by Hitachi particularly for maximum rating, operating supply voltage range, heat radiation characteristics, installation conditions and other characteristics. Hitachi bears no responsibility for failure or damage when used beyond the guaranteed ranges. Even within the guaranteed ranges, consider normally foreseeable failure rates or failure modes in semiconductor devices and employ systemic measures such as fail-safes, so that the equipment incorporating Hitachi product does not cause bodily injury, fire or other consequential damage due to operation of the Hitachi product.
5. This product is not designed to be radiation resistant.
6. No one is permitted to reproduce or duplicate, in any form, the whole or part of this document without written approval from Hitachi.
7. Contact Hitachi's sales office for any questions regarding this document or Hitachi semiconductor products.

IMPORTANT INFORMATION

READ FIRST

- **READ** this user's manual before using this emulator product.
- **KEEP the user's manual handy for future reference.**

Do not attempt to use the emulator product until you fully understand its mechanism.

Emulator Product:

Throughout this document, the term "emulator product" shall be defined as the following products produced only by Hitachi, Ltd. excluding all subsidiary products.

- Emulator
- User system interface cable

The user system or a host computer is not included in this definition.

Purpose of the Emulator Product:

This emulator product is a software and hardware development tool for systems employing the Hitachi microcomputer. This emulator product must only be used for the above purpose.

Limited Applications:

This emulator product is not authorized for use in MEDICAL, atomic energy, aeronautical or space technology applications without consent of the appropriate officer of a Hitachi sales company. Such use includes, but is not limited to, use in life support systems. Buyers of this emulator product must notify the relevant Hitachi sales offices before planning to use the product in such applications.

Improvement Policy:

Hitachi, Ltd. (including its subsidiaries, hereafter collectively referred to as Hitachi) pursues a policy of continuing improvement in design, performance, and safety of the emulator product. Hitachi reserves the right to change, wholly or partially, the specifications, design, user's manual, and other documentation at any time without notice.

Target User of the Emulator Product:

This emulator product should only be used by those who have carefully read and thoroughly understood the information and restrictions contained in the user's manual. Do not attempt to use the emulator product until you fully understand its mechanism.

It is highly recommended that first-time users be instructed by users that are well versed in the operation of the emulator product.

LIMITED WARRANTY

Hitachi warrants its emulator products to be manufactured in accordance with published specifications and free from defects in material and/or workmanship. Hitachi, at its option, will replace any emulator products returned intact to the factory, transportation charges prepaid, which Hitachi, upon inspection, shall determine to be defective in material and/or workmanship. The foregoing shall constitute the sole remedy for any breach of Hitachi's warranty. See the Hitachi warranty booklet for details on the warranty period. This warranty extends only to you, the original Purchaser. It is not transferable to anyone who subsequently purchases the emulator product from you. Hitachi is not liable for any claim made by a third party or made by you for a third party.

DISCLAIMER

HITACHI MAKES NO WARRANTIES, EITHER EXPRESS OR IMPLIED, ORAL OR WRITTEN, EXCEPT AS PROVIDED HEREIN, INCLUDING WITHOUT LIMITATION THEREOF, WARRANTIES AS TO MARKETABILITY, MERCHANTABILITY, FITNESS FOR ANY PARTICULAR PURPOSE OR USE, OR AGAINST INFRINGEMENT OF ANY PATENT. IN NO EVENT SHALL HITACHI BE LIABLE FOR ANY DIRECT, INCIDENTAL OR CONSEQUENTIAL DAMAGES OF ANY NATURE, OR LOSSES OR EXPENSES RESULTING FROM ANY DEFECTIVE EMULATOR PRODUCT, THE USE OF ANY EMULATOR PRODUCT, OR ITS DOCUMENTATION, EVEN IF ADVISED OF THE POSSIBILITY OF SUCH DAMAGES. EXCEPT AS EXPRESSLY STATED OTHERWISE IN THIS WARRANTY, THIS EMULATOR PRODUCT IS SOLD "AS IS", AND YOU MUST ASSUME ALL RISK FOR THE USE AND RESULTS OBTAINED FROM THE EMULATOR PRODUCT.

State Law:

Some states do not allow the exclusion or limitation of implied warranties or liability for incidental or consequential damages, so the above limitation or exclusion may not apply to you. This warranty gives you specific legal rights, and you may have other rights which may vary from state to state.

The Warranty is Void in the Following Cases:

Hitachi shall have no liability or legal responsibility for any problems caused by misuse, abuse, misapplication, neglect, improper handling, installation, repair or modifications of the emulator product without Hitachi's prior written consent or any problems caused by the user system.

All Rights Reserved:

This user's manual and emulator product are copyrighted and all rights are reserved by Hitachi. No part of this user's manual, all or part, may be reproduced or duplicated in any form, in hard-copy or machine-readable form, by any means available without Hitachi's prior written consent.

Other Important Things to Keep in Mind:

1. Circuitry and other examples described herein are meant merely to indicate the characteristics and performance of Hitachi's semiconductor products. Hitachi assumes no responsibility for any intellectual property claims or other problems that may result from applications based on the examples described herein.
2. No license is granted by implication or otherwise under any patents or other rights of any third party or Hitachi.

Figures:

Some figures in this user's manual may show items different from your actual system.

Device names:

Sections 1 to 5 in this user's manual use SHxxxx as an example of the device names.

Limited Anticipation of Danger:

Hitachi cannot anticipate every possible circumstance that might involve a potential hazard. The warnings in this user's manual and on the emulator product are therefore not all inclusive. Therefore, you must use the emulator product safely at your own risk.

SAFETY PAGE

READ FIRST

- **READ** this user's manual before using this emulator product.
- **KEEP** the user's manual handy for future reference.

Do not attempt to use the emulator product until you fully understand its mechanism.

DEFINITION OF SIGNAL WORDS



This is the safety alert symbol. It is used to alert you to potential personal injury hazards. Obey all safety messages that follow this symbol to avoid possible injury or death.



DANGER indicates an imminently hazardous situation which, if not avoided, will result in death or serious injury.



WARNING indicates a potentially hazardous situation which, if not avoided, could result in death or serious injury.



CAUTION indicates a potentially hazardous situation which, if not avoided, may result in minor or moderate injury.



CAUTION used without the safety alert symbol indicates a potentially hazardous situation which, if not avoided, may result in property damage.

NOTE emphasizes essential information.

WARNING

Observe the precautions listed below. Failure to do so will result in a FIRE HAZARD and will damage the user system and the emulator product or will result in PERSONAL INJURY. The USER PROGRAM will be LOST.

- 1. Do not repair or remodel the emulator product by yourself for electric shock prevention and quality assurance.**
- 2. Always switch OFF the host computer and user system before connecting or disconnecting any CABLES or PARTS.**
- 3. Connect the connectors in the user system and in the user interface cable by confirming the correct direction.**
- 4. If the PCI interface board for the E6000 or E8000 emulator (HS6000EIC01H) and the E10A emulator PCI card are mounted on the same host computer, the connectors may be illegally connected.**

Warnings on Emulator Usage

Be sure to read and understand the warnings below before using this emulator. Note that these are the main warnings, not the complete list.



WARNING

Always switch OFF the host computer and user system before connecting or disconnecting any CABLES or PARTS.

Failure to do so will result in a FIRE HAZARD and will damage the user system and the emulator product or will result in PERSONAL INJURY. The USER PROGRAM will be LOST.

CAUTION

Place the host computer and user system so that no cable is bent or twisted. A bent or twisted cable will impose stress on the user interface leading to connection or contact failure.

Make sure that the host computer and the user system are placed in a secure position so that they do not move during use nor impose stress on the user interface.

Preface

Thank you for purchasing the E10A emulator.

CAUTION

READ section 2, Preparation before Use, of this User's Manual before using the emulator product. Incorrect operation will damage the user system and the emulator product.

This emulator is an efficient development tool for software and hardware of user systems based on Hitachi's original microprocessor. The emulator operates using the Hitachi debugging interface (hereafter referred to as the HDI), which is the interface program that runs on Microsoft® Windows® 98, Microsoft® Windows® Me, Microsoft® Windows® 2000, or Microsoft® Windows NT® operating system.

This manual describes the functions and operating procedures of the E10A emulator. Sections 1 to 5 describe common features of all types of E10A emulators. Section 6 describes supplements to the E10A emulator. Read section 1.1, Warnings, carefully before using the emulator.

This manual consists of six sections. The information contained in each section is summarized below:

- Section 1, Overview, gives the emulator overview.
- Section 2, Preparation before Use, gives instructions for first-time users, such as preparation before use and system connection.
- Section 3, Tutorial, describes HDI operating examples and functions.
- Section 4, Descriptions of Windows, describes HDI windows for operating the emulator.
- Section 5, Command-line Functions describes how to input HDI commands and command types.
- Section 6, SHxxxx E10A Emulator Specifications describes the detailed specifications and the features of the E10A emulator for each device. Section 7 describes the important information of the E10A emulator according to emulator products. Read these sections before using the E10A emulator.

The HDI installation disks are provided by the CD-R. Refer to the descriptions in the manuals of the host computer or operating system.

Related Manuals:

- SuperH™ RISC Engine C/C++ Compiler, Assembler, Optimizing Linkage Editor User's Manual
- Hitachi Debugging Interface User's Manual
- Hardware Manual for each device
- Programming Manual for each device

- Notes:**
- 1. IBM PC is a registered trademark of International Business Machines Corporation.**
 - 2. Microsoft®, Windows®, and Windows NT® are registered trademarks of Microsoft Corporation in the United States and/or other countries. Microsoft® Windows® 98 operating system is referred to as Windows® 98 in this user's manual. Microsoft® Windows® Millennium Edition operating system is referred to as Windows® Me in this user's manual. Microsoft® Windows® 2000 operating system is referred to as Windows® 2000 in this user's manual.**
 - 3. Adobe, Acrobat, and Acrobat Reader are registered trademarks of Adobe Systems Incorporated.**
 - 4. Other brand and product names are registered trademarks of each company.**

Contents

Section 1 Overview	1
1.1 Warnings	3
1.2 Environmental Conditions	4
1.3 Components	6
Section 2 Preparation before Use	7
2.1 Emulator Preparation	7
2.2 HDI Installation	8
2.2.1 Installing under Windows®98 and Windows®Me Operating Systems.....	8
2.2.2 Installing under Windows NT®4.0 Operating System	9
2.2.3 Installing under Windows®2000 Operating System	10
2.3 Connecting the Card Emulator to the Host Computer	11
2.4 Connecting the Card Emulator to the User System	12
2.5 System Check	15
2.6 Ending the HDI.....	21
2.7 Uninstalling the HDI.....	22
2.8 CD-R.....	23
2.8.1 Configuration of the CD-R	23
2.9 Support	23
Section 3 Tutorial	25
3.1 Introduction.....	25
3.2 Running the HDI.....	27
3.3 [HDI] Window	28
3.4 Setting up the Emulator	29
3.5 Setting the [Configuration] Dialog Box.....	30
3.6 Checking the Operation of the Target Memory for Downloading.....	31
3.7 Downloading the Tutorial Program	33
3.7.1 Downloading the Tutorial Program	33
3.7.2 Displaying the Source Program	34
3.8 Setting a Software Breakpoint	36
3.9 Setting Registers	37
3.10 Executing the Program.....	39
3.11 Reviewing Breakpoints.....	41
3.12 Viewing Memory	42
3.13 Watching Variables.....	43
3.14 Stepping Through a Program	46
3.14.1 Executing [Step In] Command.....	46
3.14.2 Executing [Step Out] Command	48

3.14.3	Executing [Step Over] Command	50
3.15	Forced Breaking of Program Executions	52
3.16	Displaying Local Variables.....	53
3.17	Break Function.....	54
3.17.1	Software Break Function	54
3.18	Hardware Break Function	60
3.18.1	Setting the Sequential Break Condition	68
3.19	Trace Functions.....	73
3.19.1	Internal Trace Function.....	75
3.19.2	AUD Trace Function.....	77
3.19.3	VP_MAP Translation.....	79
3.20	Stack Trace Function	82
3.21	Profiling Function	84
3.22	Download Function to the Flash Memory Area	89
3.23	What Next?	95
Section 4 Descriptions of Windows.....		97
4.1	HDI Windows	97
4.2	Descriptions of Each Window	100
4.2.1	[Configuration] Dialog Box	100
4.2.2	[Breakpoints] Window.....	108
4.2.3	[Break] Dialog Box.....	111
4.2.4	[Break Point] Dialog Box	117
4.2.5	[Break Condition] Dialog Box.....	119
4.2.6	[Break Condition] Dialog Box Pages.....	121
4.2.7	[Trace] Window.....	133
4.2.8	[Trace Acquisition] Dialog Box.....	135
4.2.9	[System Status] Window.....	138
Section 5 Command-line Functions.....		141
5.1	Table and Symbol Description.....	141
5.1.1	Format.....	141
5.1.2	Parameter Input.....	141
5.1.3	Examples.....	142
5.1.4	Related Items	142
5.2	Command Descriptions.....	143
5.2.1	AUD_CLOCK:AUCL	145
5.2.2	AUD_MODE:AUM.....	147
5.2.3	AUD_TRACE:AUT.....	149
5.2.4	BREAKCONDITION_CLEAR: BCC	151
5.2.5	BREAKCONDITION_DISPLAY: BCD	152
5.2.6	BREAKCONDITION_ENABLE: BCE.....	153
5.2.7	BREAKCONDITION_SET: BCS	154

5.2.8	BREAKPOINT: BP	158
5.2.9	BREAKPOINT_CLEAR: BC	160
5.2.10	BREAKPOINT_DISPLAY: BD	162
5.2.11	BREAKPOINT_ENABLE: BE	163
5.2.12	DEVICE_TYPE: DE	165
5.2.13	GO_OPTION: GP	166
5.2.14	JTAG_CLOCK: JCK	168
5.2.15	MEMORYAREA_SET: MAS	170
5.2.16	REFRESH: RF	172
5.2.17	RESTART: RST	173
5.2.18	STATUS: STS	174
5.2.19	STEP_INTERRUPT: SI	175
5.2.20	TRACE_DISPLAY: TD	176
5.2.21	UBC_MODE:UM	178
5.2.22	VPMAP_CLEAR: VC	179
5.2.23	VPMAP_DISPLAY: VD	180
5.2.24	VPMAP_ENABLE: VE	181
5.2.25	VPMAP_SET: VS	182

Section 6	SH7294 E10A Emulator Specifications	183
6.1	Components of the Emulator	183
6.2	Pin Arrangement of the Hitachi-UDI Port Connector	186
6.3	User System Interface Circuit	190
6.4	Differences between the SH7294 and the Emulator	196
6.5	Specific Functions for the SH7294 E10A Emulator	199
6.5.1	Emulator Driver Selection	199
6.5.2	Break Condition Functions	200
6.5.3	Trace Functions	201
6.5.4	Notes on Using the JTAG Clock (TCK) and AUD Clock (AUDCK)	208
6.5.5	Notes on Setting the [Breakpoint] Dialog Box	208
6.5.6	Notes on Setting the [Break Condition] Dialog Box and the BREAKCONDITION_SET Command	210
6.5.7	Notes on Setting the [Trace] Window	210
6.5.8	Notes on Setting the UBC_MODE Command	210
6.5.9	Performance Measurement Function	211
6.5.10	Notes on U Standby State	216
6.5.11	Downloading to the Flash Memory	219
6.5.12	Notes on HDI	221

Figures

Figure 1.1	System Configuration with the Emulator (PCMCIA Card Emulator Used).....	1
Figure 1.2	System Configuration with the Emulator (PCI Card Emulator Used).....	2
Figure 2.1	Emulator Preparation Flow Chart.....	7
Figure 2.2	Inserting the PCMCIA Card Emulator in the Host Computer's Slot	11
Figure 2.3	Inserting the PCI Card Emulator in the Host Computer's Slot	11
Figure 2.4	Connecting the User System Interface Cable to the User System when the 14-pin Straight Type Connector is Used	13
Figure 2.5	User System Example	14
Figure 2.6	[Start] Menu	15
Figure 2.7	[Select Session] Dialog Box.....	16
Figure 2.8	[E10A Driver Details] Dialog Box.....	17
Figure 2.9	Dialog Box of the RESET Signal Input Request Message.....	18
Figure 2.10	[HDI] Status Bar.....	18
Figure 2.11	[JTAG Connector Disconnected] Dialog Box.....	18
Figure 2.12	[Can not find /RESET signal] Dialog Box	19
Figure 2.13	[Check the connection] Dialog Box	19
Figure 2.14	[COMMUNICATION TIMEOUT ERROR] Dialog Box	19
Figure 2.15	[INVALID ASERAM FIRMWARE!] Dialog Box	20
Figure 2.16	[Error JTAG boot] Dialog Box	20
Figure 2.17	[Unable to restore the previous driver settings] Dialog Box	20
Figure 2.18	[Exit HDI] Dialog Box	21
Figure 2.19	[Save session] Dialog Box	21
Figure 3.1	[Start] Menu	27
Figure 3.2	[HDI] Window	28
Figure 3.3	[Configuration] Dialog Box	30
Figure 3.4	[Open Memory Window] Dialog Box.....	31
Figure 3.5	[Memory] Window.....	31
Figure 3.6	[Load Program] Dialog Box.....	33
Figure 3.7	[HDI] Dialog Box.....	33
Figure 3.8	[Open] Dialog Box.....	34
Figure 3.9	[Source] Window (Displaying the Source Program).....	35
Figure 3.10	[Source] Window (Setting a Software Breakpoint).....	36
Figure 3.11	[Registers] Window.....	37
Figure 3.12	[Register] Dialog Box (PC).....	38
Figure 3.13	[Go] Button	39
Figure 3.14	[Source] Window (Break Status).....	39
Figure 3.15	[System Status] Window	40
Figure 3.16	[Breakpoints] Window	41
Figure 3.17	[Open Memory Window] Dialog Box.....	42
Figure 3.18	[Memory] Window.....	42
Figure 3.19	[Instant Watch] Dialog Box	43
Figure 3.20	[Watch] Window (Displaying the Array).....	44

Figure 3.21	[Add Watch] Dialog Box	44
Figure 3.22	[Watch] Window (Displaying the Variable)	45
Figure 3.23	[Watch] Window (Displaying Array Elements)	45
Figure 3.24	[Step In] Button	46
Figure 3.25	[Source] Window (Step In)	47
Figure 3.26	[Step Out] Button	48
Figure 3.27	[HDI] Window (Step Out)	48
Figure 3.28	[HDI] Window (Step In → Step In)	49
Figure 3.29	[Source] Window (Before Step Over Execution)	50
Figure 3.30	[Step Over] Button	50
Figure 3.31	[HDI] Window (Step Over)	51
Figure 3.32	[Go] Button	52
Figure 3.33	[Stop] Button	52
Figure 3.34	[Locals] Window	53
Figure 3.35	[Breakpoints] Window (Before Software Breakpoint Setting)	54
Figure 3.36	[Point] Page ([Break] Dialog Box)	55
Figure 3.37	[Break Point] Dialog Box	56
Figure 3.38	[Point] Page ([Break] Dialog Box) (After Software Breakpoint Setting)	57
Figure 3.39	[Breakpoints] Window (Software Breakpoint Setting)	58
Figure 3.40	[Source] Window at Execution Stop (Software Break)	58
Figure 3.41	Displayed Contents of the [System Status] Window (Software Break)	59
Figure 3.42	[Breakpoints] Window (Before Hardware Break Condition Setting)	60
Figure 3.43	[Condition] Page ([Break] Dialog Box)	61
Figure 3.44	[Address] Page ([Break Condition 1] Dialog Box)	62
Figure 3.45	[Bus State] Page ([Break Condition 1] Dialog Box)	63
Figure 3.46	[Break] Dialog Box (After Hardware Break Condition Setting)	64
Figure 3.47	[Breakpoints] Window ([Break Condition 1] Setting)	65
Figure 3.48	[Source] Window at Execution Stop (Break Condition 1)	66
Figure 3.49	Displayed Contents of the [System Status] Window (Break Condition 1)	67
Figure 3.50	[Configuration] Dialog Box (Sequential Break Setting)	69
Figure 3.51	[Breakpoints] Window (After Sequential Break Condition Setting)	70
Figure 3.52	[Source] Window at Execution Stop (Sequential Break)	71
Figure 3.53	Displayed Contents of the [System Status] Window (Sequential Break)	72
Figure 3.54	[Trace mode] Window	75
Figure 3.55	[Trace] Window	76
Figure 3.56	[Trace mode] Window	77
Figure 3.57	[Trace] Window in the SH7751 E10A Emulator	78
Figure 3.58	Address Translation according to VP_MAP Tables	80
Figure 3.59	[Source] Window (Software Breakpoint Setting)	82
Figure 3.60	[Stack Trace] Window	83
Figure 3.61	[Profile-List] Window	84
Figure 3.62	Selection of [Enable Profiler]	85
Figure 3.63	[Select Data] Dialog Box	86

Figure 3.64	[Source] Window (Software Break Setting).....	87
Figure 3.65	[Profile-List] Window	87
Figure 3.66	[Profile-Tree] Window	88
Figure 3.67	[Profile-Chart] Window	88
Figure 3.68	[Loading flash memory] Page.....	90
Figure 3.69	Flash Memory Wiring	92
Figure 3.70	[Loading flash memory] Page.....	93
Figure 4.1	[Configuration] Dialog Box	100
Figure 4.2	[General] Page ([Configuration] Dialog Box).....	102
Figure 4.3	Warning Message Box	104
Figure 4.4	[E10A Driver Details] Dialog Box.....	105
Figure 4.5	[Loading flash memory] Page ([Configuration] Dialog Box)	106
Figure 4.6	[Breakpoints] Window	108
Figure 4.7	[Break] Dialog Box	111
Figure 4.8	[Point] Page ([Break] Dialog Box).....	113
Figure 4.9	[Condition] Page ([Break] Dialog Box)	115
Figure 4.10	[Break Point] Dialog Box.....	117
Figure 4.11	[Break Condition] Dialog Box	119
Figure 4.12	[Address] Page ([Break Condition 1] Dialog Box)	123
Figure 4.13	[Data] Page ([Break Condition 1] Dialog Box).....	125
Figure 4.14	[ASID] Page ([Break Condition] Dialog Box)	127
Figure 4.15	[Bus State] Page ([Break Condition] Dialog Box)	128
Figure 4.16	[Count] Page ([Break Condition] Dialog Box)	130
Figure 4.17	[General] Page ([Break Condition] Dialog Box).....	131
Figure 4.18	[Trace] Window	133
Figure 4.19	[Trace mode] Page ([Trace Acquisition] Dialog Box)	136
Figure 4.20	[System Status] Window	138
Figure 5.1	TLB Error Message Dialog	176
Figure 6.1	Connecting Ferrite Core	185
Figure 6.2	Pin Arrangement of the Hitachi-UDI Port Connector (14 Pins).....	186
Figure 6.3	Pin Arrangement of the Hitachi-UDI Port Connector (36 Pins).....	188
Figure 6.4	User System Interface Circuit (HS7294KCM01H) (Model Name: HS0005KCM07H).....	190
Figure 6.5	User System Interface Circuit of the Hitachi-UDI Pin (HS7294KCM02H) (Model Name: HS0005KCM08H).....	191
Figure 6.6	User System Interface Circuit of the AUD Pin (HS7294KCM02H) (Model Name: HS0005KCM08H).....	192
Figure 6.7	User System Interface Circuit (HS7294KCI01H) (Model Name: HS0005KCI07H).....	193
Figure 6.8	User System Interface Circuit of the Hitachi-UDI Pin (HS7294KCI02H) (Model Name: HS0005KCI08H).....	194
Figure 6.9	User System Interface Circuit of the AUD Pin (HS7294KCI02H) (Model Name: HS0005KCI08H).....	195

Figure 6.10	[Trace mode] Page	203
Figure 6.11	[AUD Branch trace] Page.....	204
Figure 6.12	[Window trace] Page.....	205
Figure 6.13	[Trace] Window	206
Figure 6.14	[HDI] Message Box.....	210
Figure 6.15	[Performance 1] Dialog Box	212
Figure 6.16	[Performance Analysis] Window	216
Figure 6.17	[Recovery Information] Page	217
Figure 6.18	Message Box for Entering the U Standby State	218
Figure 6.19	Example of Connecting the Flash Memory	220

Tables

Table 1.1	Environmental Conditions	4
Table 1.2	Operating Environments	5
Table 2.1	Recommended Hitachi-UDI Port Connector	12
Table 2.2	Contents of the CD-R Directories	23
Table 3.1	Tutorial Program: Configuration and Parts	25
Table 3.2	Step Option	46
Table 3.3	Sequential Break Conditions	68
Table 3.4	AUD Trace Functions	74
Table 3.5	Address Translation Tables	81
Table 3.6	Module Interface	89
Table 3.7	[Loading flash memory] Page Options	91
Table 3.8	Example of Board Specifications	92
Table 3.9	Sample Program Specifications	93
Table 4.1	HDI Window Menus and Related Manual Entries	97
Table 4.2	[Configuration] Dialog Box Page	101
Table 4.3	[General] Page Options	103
Table 4.4	Options for the [E10A Driver Details] Dialog Box	105
Table 4.5	[Loading flash memory] Page Options	107
Table 4.6	[Breakpoints] Window Display Items	109
Table 4.7	[Breakpoints] Window Pop-up Menu Operation	110
Table 4.8	[Break] Dialog Box Pages	112
Table 4.9	[Point] Page Options	114
Table 4.10	[Condition] Page Options	116
Table 4.11	[Address] Page Options	118
Table 4.12	Setting Conditions in [Break Condition] Dialog Boxes	121
Table 4.13	[Break Condition] Dialog Box Pages	122
Table 4.14	[Address] Page Options	124
Table 4.15	Address Options	124
Table 4.16	[Data] Page Options	126
Table 4.17	[ASID] Page Options	127
Table 4.18	[Bus State] Page Options	129
Table 4.19	[Count] Page Options	130
Table 4.20	[General] Page Options	132
Table 4.21	[Trace] Window Display Items	134
Table 4.22	[Trace Acquisition] Dialog Box Page Options	135
Table 4.23	[Trace mode] Page Options	137
Table 4.24	[System Status] Window Display Items	139
Table 5.1	E10A HDI Commands	143
Table 5.2	AUD_CLOCK Command Parameter	145
Table 5.3	AUD_MODE Command Parameter	147
Table 5.4	AUD_TRACE Command Parameter	149
Table 5.5	BREAKCONDITION_CLEAR Command Parameter	151

Table 5.6	BREAKCONDITION_DISPLAY Command Parameter	152
Table 5.7	BREAKCONDITION_ENABLE Command Parameters	153
Table 5.8	BREAKCONDITION_SET Command Parameters	155
Table 5.9	BREAKPOINT Command Parameters	158
Table 5.10	BREAKPOINT_CLEAR Command Parameters	160
Table 5.11	BREAKPOINT_DISPLAY Command Parameter	162
Table 5.12	BREAKPOINT_ENABLE Command Parameters	163
Table 5.13	DEVICE_TYPE Command Parameter	165
Table 5.14	GO_OPTION Command Parameter	166
Table 5.15	JTAG_CLOCK Command Parameter	168
Table 5.16	MEMORYAREA_SET Command Parameters	170
Table 5.17	REFRESH Command Parameter	172
Table 5.18	RESTART Command Parameter	173
Table 5.19	STATUS Command Parameter	174
Table 5.20	STEP_INTERRUPT Command Parameter	175
Table 5.21	TRACE_DISPLAY Command Parameter	176
Table 5.22	UBC_MODE Command Parameter	178
Table 5.23	VPMAP_CLEAR Command Parameter	179
Table 5.24	VPMAP_DISPLAY Command Parameter	180
Table 5.25	VPMAP_ENABLE Command Parameter	181
Table 5.26	VPMAP_SET Command Parameters	182
Table 6.1	Components of the Emulator (HS7294KCM01H, HS7294KCM02H, HS7294KCI01H, or HS7294KCI02H)	184
Table 6.2	Register Initial Values at Emulator Power-On	196
Table 6.3	Multiplexed Functions	198
Table 6.4	Type Number and Driver	199
Table 6.5	Types of Break Conditions	200
Table 6.6	Dialog Boxes for Setting Break Conditions	201
Table 6.7	Trace Functions	201
Table 6.8	Type Number and AUD Function	201
Table 6.9	AUD Trace Acquisition Mode	202
Table 6.10	[Trace] Window Display Contents	207
Table 6.11	Conditions Specified in [Mode]	211
Table 6.12	Measurement Item	214
Table 6.13	Performance Measurement Conditions to be Counted	215
Table 6.14	Selected Items in the [Recovery Information] Page	217
Table 6.15	File Configuration of the Sample Program	219
Table 6.16	Specifications of the Sample Program	219
Table 6.17	Operating Conditions	220
Table 6.18	Watchdog Timer Register	224

Section 1 Overview

The E10A emulator (hereafter referred to as the emulator) is a software and hardware development support tool for application systems using the microprocessor developed by Hitachi, Ltd.

The PCMCIA card emulator or PCI card emulator (hereafter referred to as the card emulator), which is the main unit of the emulator, is connected, through the Hitachi-UDI (user debug interface) port*, to the user system. The user system can be debugged under the conditions similar to the actual application conditions. The emulator enables debugging anywhere indoors or out. The host computer for controlling the emulator must be an IBM PC compatible machine with a PCMCIA type II or PCI slot.

Figures 1.1 and 1.2 show the system configuration using the emulator.

Note: The Hitachi-UDI is an interface compatible with the Joint Test Action Group (JTAG) specifications.

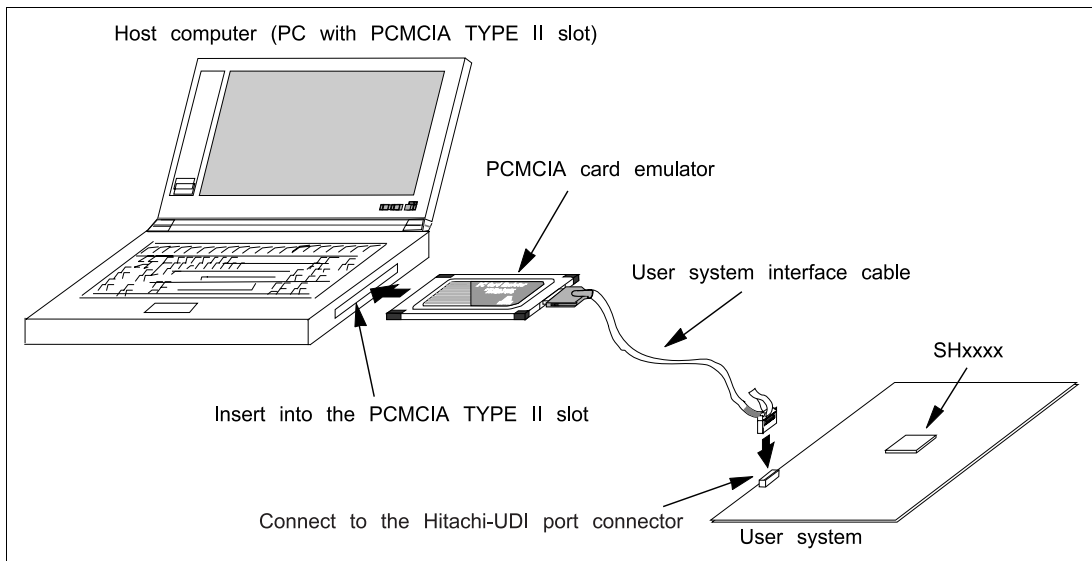


Figure 1.1 System Configuration with the Emulator (PCMCIA Card Emulator Used)

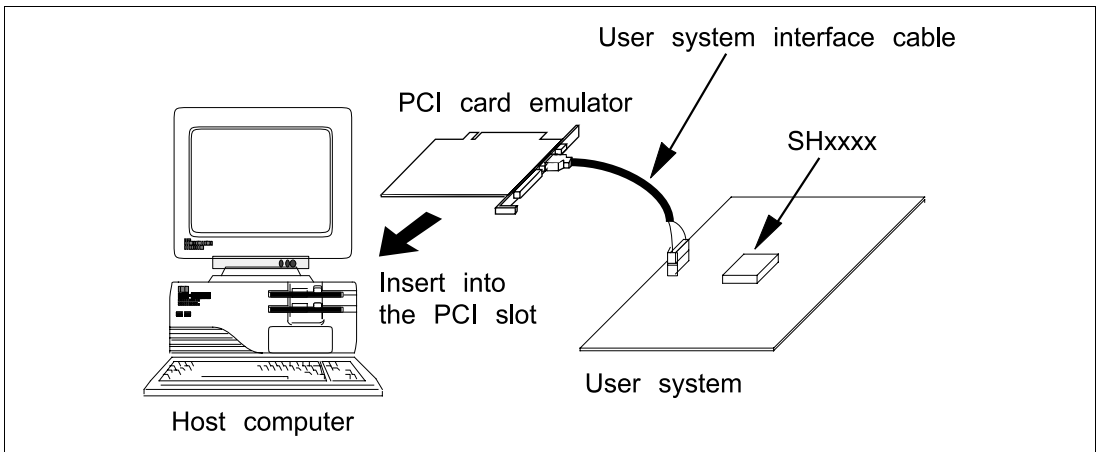


Figure 1.2 System Configuration with the Emulator (PCI Card Emulator Used)

The emulator provides the following features:

- **Excellent cost-performance card emulator**
Compactness and low price are implemented using the PCMCIA interface or the PCI interface.
- **Realtime emulation**
Realtime emulation of the user system is enabled at the maximum operating frequency of the CPU.
- **Excellent operability**
Using the Hitachi Debugging Interface (HDI) on the Microsoft® Windows® 98, Microsoft® Windows® Me, Microsoft® Windows® 2000, and Microsoft® Windows NT® operating systems enables user program debugging using a pointing device such as a mouse. The HDI enables high-speed downloading of load module files.
- **Various debugging functions**
Various break and trace functions enable efficient debugging. Breakpoints and break conditions can be set by the specific window, trace information can be displayed on a window, and command-line functions can be used.
- **Memory access during emulation**
During emulation, the memory contents can be read and modified.
- **Debugging of the user system in the final development stage**
The user system can be debugged under conditions similar to the actual application conditions.
- **Compact debugging environment**
When the card emulator specific to the PCMCIA interface is used, a laptop computer can be used as a host computer, creating a debugging environment in any place.

- AUD trace function*

The AUD trace function enables realtime trace.

Note: The AUD is an abbreviation of the Advanced User Debugger. Support for the AUD varies with the product.

1.1 Warnings

CAUTION

READ the following warnings before using the emulator product. Incorrect operation will damage the user system and the emulator product. The USER PROGRAM will be LOST.

1. Check all components against the component list after unpacking the emulator.
2. Never place heavy objects on the casing.
3. Protect the emulator from excessive impacts and stresses. For details, refer to section 1.2, Environmental Conditions.
4. Do not insert the emulator into any slot (PCMCIA TYPE II slot or PCI slot) other than the specified one.
5. When moving the host computer or user system, take care not to vibrate or damage it.
6. After connecting the cable, check that it is connected correctly. For details, refer to section 2, Preparation before Use.
7. Supply power to the connected equipment after connecting all cables. Cables must not be connected or removed while the power is on.

1.2 Environmental Conditions

CAUTION

Observe the conditions listed in tables 1.1 and 1.2 when using the emulator. Failure to do so will cause illegal operation in the user system, the emulator product, and the user program.

Table 1.1 Environmental Conditions

Item	Specifications
Temperature	Operating: +10°C to +35°C Storage: -10°C to +50°C
Humidity	Operating: 35% RH to 80% RH, no condensation Storage: 35% RH to 80% RH, no condensation
Vibration	Operating: 2.45 m/s ² max. Storage: 4.9 m/s ² max. Transportation: 14.7 m/s ² max.
Ambient gases	No corrosive gases may be present

Table 1.2 lists the acceptable operating environments.

Table 1.2 Operating Environments

Item	Description
Host computer	Built-in Pentium or higher-performance CPU (200 MHz or higher recommended); IBM PC or compatible machine with the PCMCIA TYPE II slot or the PCI slot.
OS	Windows® 98, Windows® Me, Windows® 2000, or Windows NT®
Minimum memory capacity	32 Mbytes or more (double of the load module size recommended)
Hard-disk capacity	Installation disk capacity: 10 Mbytes or more. (Prepare an area at least double the memory capacity (four-times or more recommended) as the swap area.)
Pointing device such as mouse	Connectable to the host computer; compatible with Windows® 98, Windows® Me, Windows® 2000, and Windows NT®.
Power voltage	5.0 ± 0.25 V
Current consumption	HSxxxxKCM01H: 110 mA (max) HSxxxxKCM02H: 230 mA (max) HSxxxxKCI01H: 340 mA (max) HSxxxxKCI02H: 600 mA (max)
CD-ROM drive	Required to install the HDI for the emulator or refer to the emulator user's manual.

1.3 Components

Check all the components unpacking. For details on the E10A emulator components, refer to section 6.1, Components of the Emulator. If the components are not complete, contact a Hitachi sales agency.

Section 2 Preparation before Use

2.1 Emulator Preparation



WARNING

READ the reference sections shaded in figure 2.1 before using the emulator product. Incorrect operation will damage the user system and the emulator product. The USER PROGRAM will be LOST.

Unpack the emulator and prepare it for use as follows:

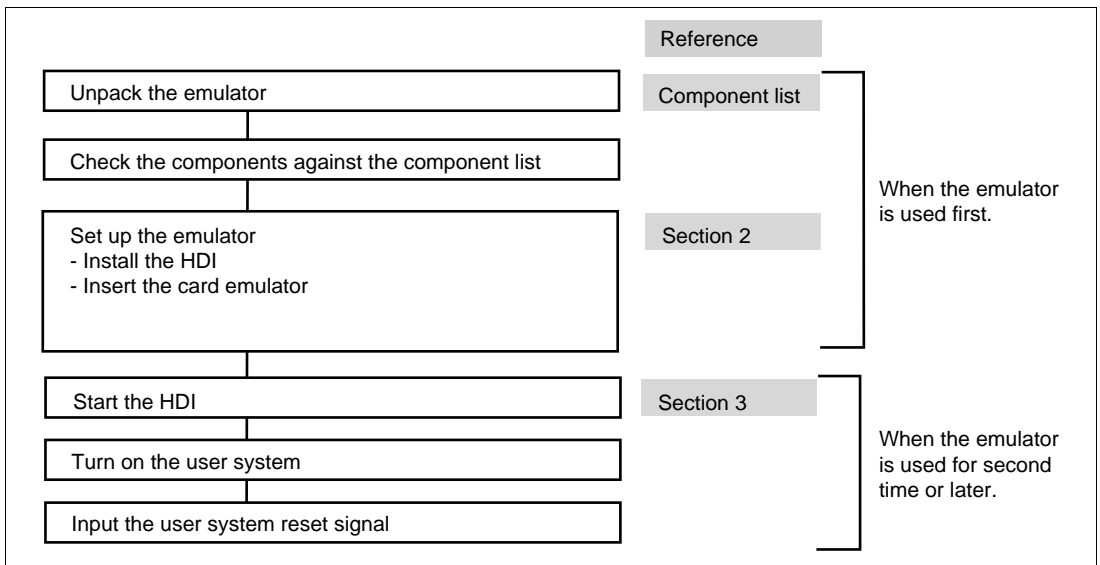


Figure 2.1 Emulator Preparation Flow Chart

2.2 HDI Installation

When the CD-R is inserted in the host computer's CD-ROM drive, the HDI installation wizard is automatically activated (holding the Shift key down while the CD-R is inserted cancels this automatic activation). To run the installation wizard when it has not been automatically activated, execute Setup.exe from the root directory of the CD-R.

Follow the cues given by the installation wizard to install the HDI.

Since hardware settings are also made during installation, the installation procedure differs according to the operating system or interface (PCI or PCMCIA) being used. Follow the installation steps carefully according to the environment you are using.

2.2.1 Installing under Windows®98 and Windows®Me Operating Systems

(1) When the emulator is a PCI card:

1. Install the HDI (when the component type has to be selected during installation, be sure to select [PCI Card Driver]).
2. Shut the operating system down and turn off the power to the host computer.
3. Insert the PCI-card emulator in a slot on the host computer. Refer to section 2.3, Connecting the Card Emulator to the Host Computer.
4. Restart the host computer. The hardware is now recognized and the driver is automatically installed.*

(2) When the emulator is a PCMCIA card:

1. Install the HDI (when the component type has to be selected during installation, be sure to select [PC Card Driver (PCMCIA)]).
2. Insert the PCMCIA-card emulator in the host computer's slot. Refer to section 2.3, Connecting the Card Emulator to the Host Computer.
3. The hardware is now recognized and the driver is automatically installed.*

Note: When [Add New Hardware Wizard] is displayed, select the [Search for the best driver for your device. (Recommended)] radio button and then the [Specify a location] check box to select the path to be searched for drivers. The location must be specified according to the emulator type, as indicated below:

When using the PCI-card emulator: <Drive>:\DRIVERS\PCI\95

When using the PCMCIA-card emulator: <Drive>:\DRIVERS\PCMCIA\95

(<Drive> is the CD-ROM drive name.)

2.2.2 Installing under Windows NT®4.0 Operating System

(1) When the emulator is a PCI card:

1. Shut the operating system down and turn off the power to the host computer.
2. Insert the PCI-card emulator in a slot on the host computer. Refer to section 2.3, Connecting the Card Emulator to the Host Computer.
3. Start the host computer and log-on with an administrator-level user name.
4. Install the HDI. (For a component, be sure to select [PCI Card Driver]. There is a check box for selecting the type name of the product under the [PCI Card Driver] component. Select the appropriate type name. If the correct name is not selected, the correct driver will not be installed, and the emulator will not operate.)
5. Restart the host computer.

(2) When the emulator is a PCMCIA card:

1. Shut the operating system down and turn off the power to the host computer.
2. Insert the PCMCIA-card emulator in the host computer's slot. Refer to section 2.3, Connecting the Card Emulator to the Host Computer.
3. Start the host computer and log-on with an administrator-level user name.
4. During HDI installation, the setting value should be checked beforehand because inquiries are made about the resource used by the PCMCIA-card emulator.
Start the [Start] menu -> [Programs] -> [Administrative Tools (Common)] -> [Windows NT Diagnostics], check the status of the IRQ, I/O port, and memory from the resource panel, and determine the setting values that do not conflict with other devices. (The following resources are used: IRQ: one channel, I/O port: H'F byte, and memory: H'4000 byte.)
5. Install the HDI. (For a component, be sure to select [PC Card Driver (PCMCIA)]. There is a check box for selecting the type name of each product under the [PC Card Driver (PCMCIA)] component. Select the appropriate type name. If the correct name is not selected, the correct driver will not be installed and the emulator will not operate.)
6. Restart the host computer.

- Notes:
1. For the SH7729, SH7729R, and SH7622 E10A emulators, there is a check box for selecting the MODEL name that appears on the component list. Select the correct type name.
 2. The driver that has been selected in the [Drivers] component starts after the host computer is initiated. If the host computer is initiated with the card disconnected or with the incorrect driver installed, the driver cannot initiate and the service control manager informs the system of an error. This, however, is not a problem.

2.2.3 Installing under Windows®2000 Operating System

(1) When the emulator is a PCI card:

1. Log-on with an administrator-level user name.
2. Install the HDI. (When a component is selected, be sure to select [PCI Card Driver].)
3. Shut the operating system down and turn off the power to the host computer.
4. Insert the PCI-card emulator in a slot on the host computer. Refer to section 2.3, Connecting the Card Emulator to the Host Computer.
5. Restart the host computer and log-on with an administrator-level user name. The hardware is now recognized and the driver is automatically installed.*

(2) When the emulator is a PCMCIA card:

1. Log-on with an administrator-level user name.
2. Install the HDI. (When a component is selected, be sure to select [PC Card Driver (PCMCIA)].)
3. Insert the PCMCIA-card emulator in the host computer's slot. Refer to section 2.3, Connecting the Card Emulator to the Host Computer.
4. The hardware is now recognized and the driver is automatically installed.*

Note: When [Found New Hardware Wizard] is displayed, select the [Search for a suitable driver for my device (recommended).] radio button and then the [Specify a location] check box to select the path to be searched for drivers. The location must be specified according to the emulator type, as indicated below:

When using the PCI-card emulator: <Drive>:\DRIVERS\PCI\2000

When using the PCMCIA-card emulator: <Drive>:\DRIVERS\PCMCIA\2000

(<Drive> is the CD-ROM drive name.)

2.3 Connecting the Card Emulator to the Host Computer

Insert the card emulator, according to its type, in a PCMCIA TYPE II slot or PCI slot on the host computer (figures 2.2 and 2.3).

Note: When using Windows®98, Windows®Me, or Windows®2000, be sure to install the HDI before putting the card emulator in place.

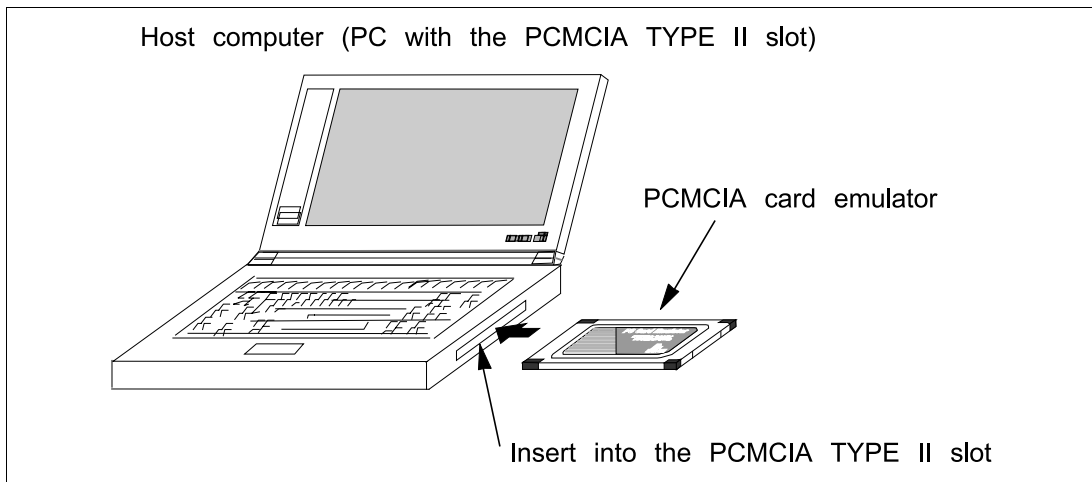


Figure 2.2 Inserting the PCMCIA Card Emulator in the Host Computer's Slot

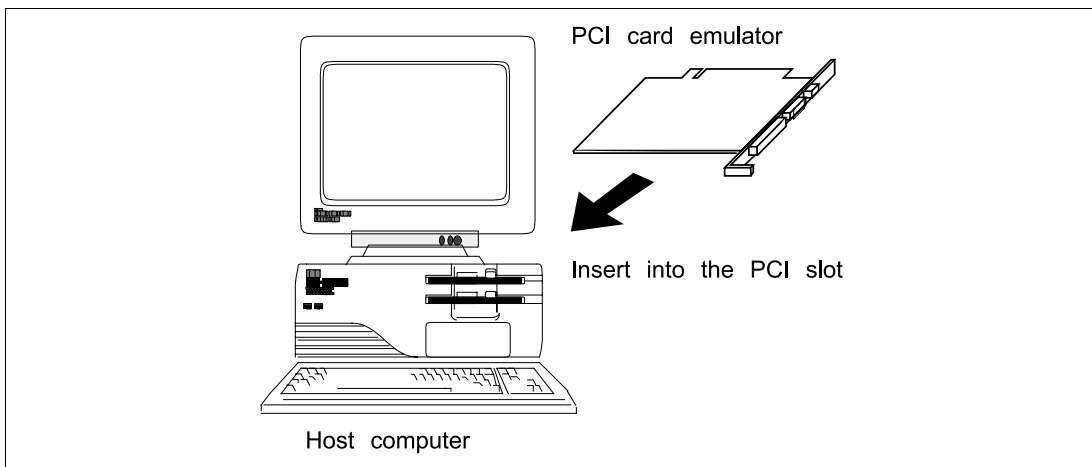


Figure 2.3 Inserting the PCI Card Emulator in the Host Computer's Slot

Use the procedure, described in section 2.4, to connect the emulator to the user system with the user system interface cable, or to disconnect them when moving the emulator or the user system.



WARNING

When inserting the PCI-card emulator, note the following. Failure to do so will damage the host computer.

- 1. Turn off the host computer.**
- 2. Insert the emulator into the PCI slot in parallel.**
- 3. Screw in the emulator after checking the connector and cable positions.**

2.4 Connecting the Card Emulator to the User System

(1) The Hitachi-UDI port connector must be installed to the user system. Table 2.1 shows the recommended Hitachi-UDI port connector for the emulator.

Table 2.1 Recommended Hitachi-UDI Port Connector

Connector	Type Number	Manufacturer	Specifications
14-pin connector	2514-6002	Minnesota Mining & Manufacturing Ltd.	14-pin straight type
36-pin connector	DX10M-36S	Hirose Electric Co., Ltd.	Screw type
	DX10M-36SE, DX10GM-36SE		Lock-pin type

Note: When the 14-pin connector is used, do not install any components within 3 mm of the Hitachi-UDI port connector.

When the 36-pin connector is used, do not connect other signal lines to the Hitachi-UDI port connector.

- (2) Note that the TDO signal of the user system interface cable connector must be connected to the TDI pin of the Hitachi-UDI port connector and the TDI signal of the user system interface cable connector must be connected to the TDO pin of the Hitachi-UDI port connector. Section 6.2 shows the pin arrangement of the Hitachi-UDI port connector.
- (3) Figure 2.4 shows how to connect the user system interface cable to the user system when the 14-pin straight type connector is used. Connect the ground line of the cable to the user system ground. The end of the ground line has a hole having a diameter of 3 mm, and therefore, when the ground line is screwed to the user system, the screw diameter must be 3 mm.

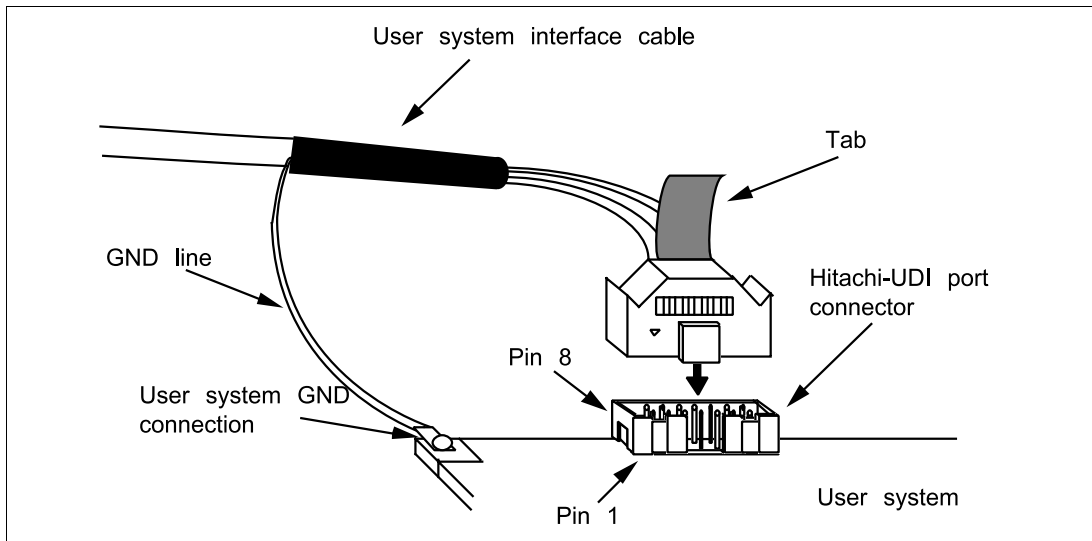
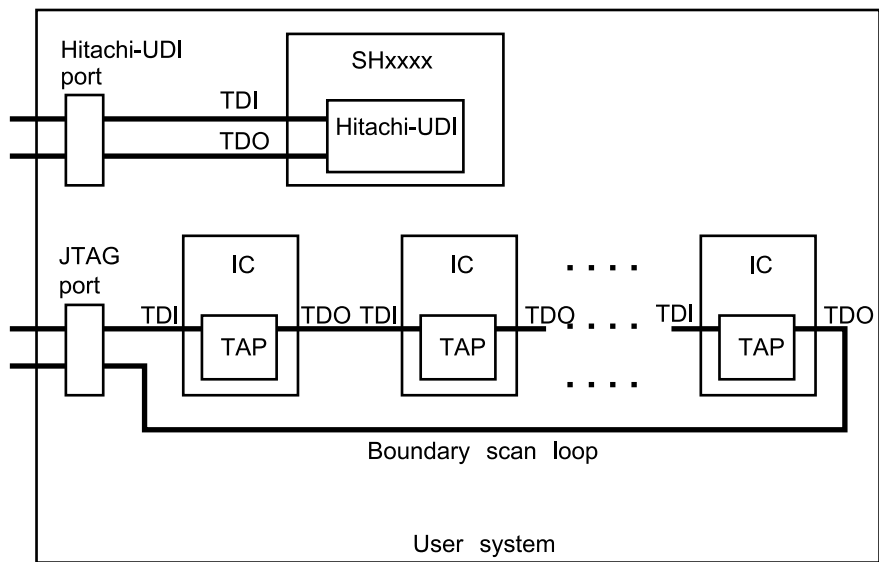


Figure 2.4 Connecting the User System Interface Cable to the User System when the 14-pin Straight Type Connector is Used

- Notes:**
1. To connect the signals output from the Hitachi-UDI port connector, refer to the device pin alignment.
 2. To remove the user system interface cable from the user system, pull the tab on the connector upward.
 3. The range of frequencies that the Hitachi-UDI operates at is different according to the devices used. For details, refer to section 6.5.4, Notes on Using the JTAG Clock (TCK) and AUD Clock (AUDCK).
 4. Connect the Hitachi-UDI signals from the Hitachi-UDI port connector directly to the device.
 5. When developing user systems, do not connect the TDI and TDO signals of the device to the boundary scan loop, or separate them by using a switch (figure 2.5).



TDI: Test data input
TDO: Test data output
TAP: Test access port

Figure 2.5 User System Example

2.5 System Check

When the HDI program is executed, use the procedure below to check that the emulator is operating correctly.

1. Check that the emulator card is inserted in the host computer's slot.
2. Connect the user system interface cable to the connector of the card emulator.
3. Connect the user system interface cable to the Hitachi-UDI port connector.
4. Supply power to the host computer and select [HDI for E10A SHxxxx] -> [Hitachi Debugging Interface] from the [Start] menu.

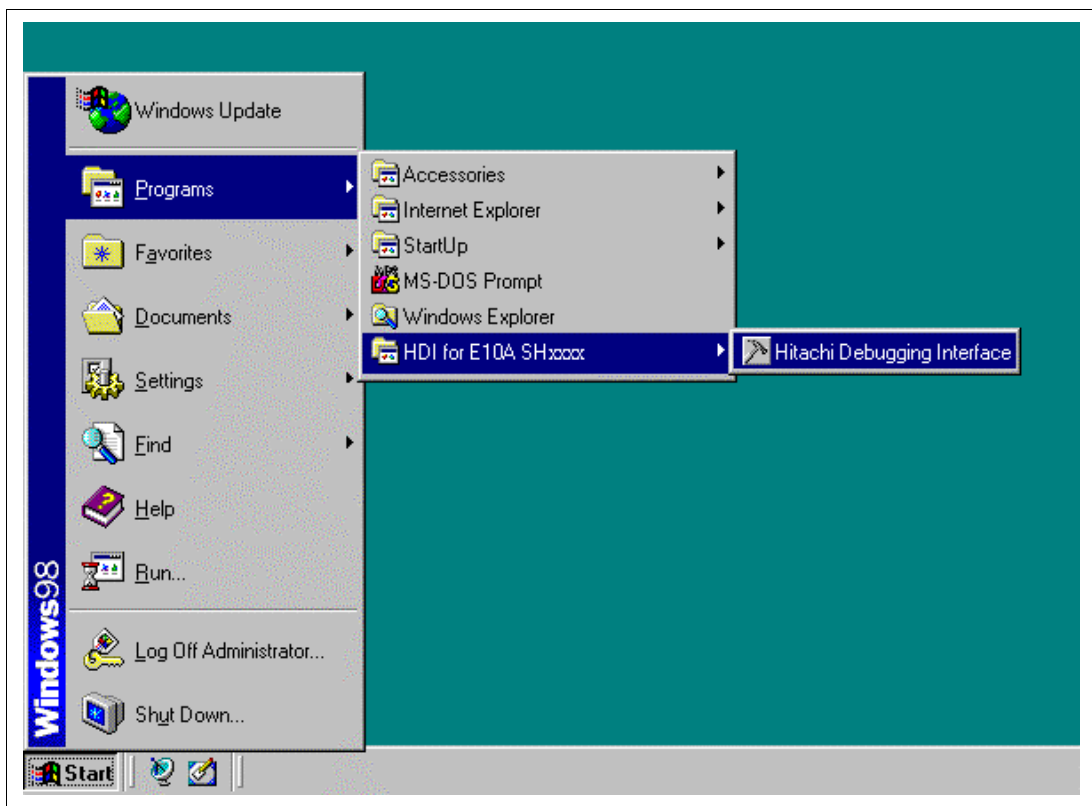


Figure 2.6 [Start] Menu

5. Select the setting to be used.

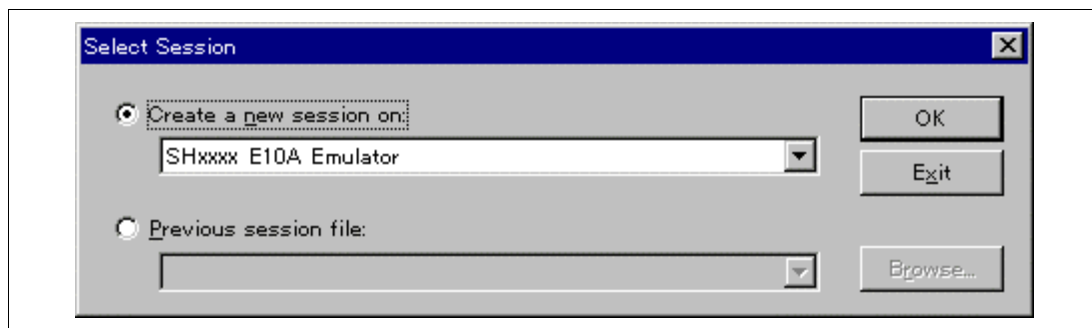


Figure 2.7 [Select Session] Dialog Box

6. The [E10A Driver Details] dialog box is displayed. With the [Driver] combo box, select the driver to connect the HDI with the emulator. [Interface] displays the interface name of the PC interface board to be connected, and [Channel] displays the interface to which the board is connected. Once the driver is selected in the [E10A Driver Details] dialog box, this dialog box is not displayed when the HDI is run next time. (This procedure will not be executed by target devices.)

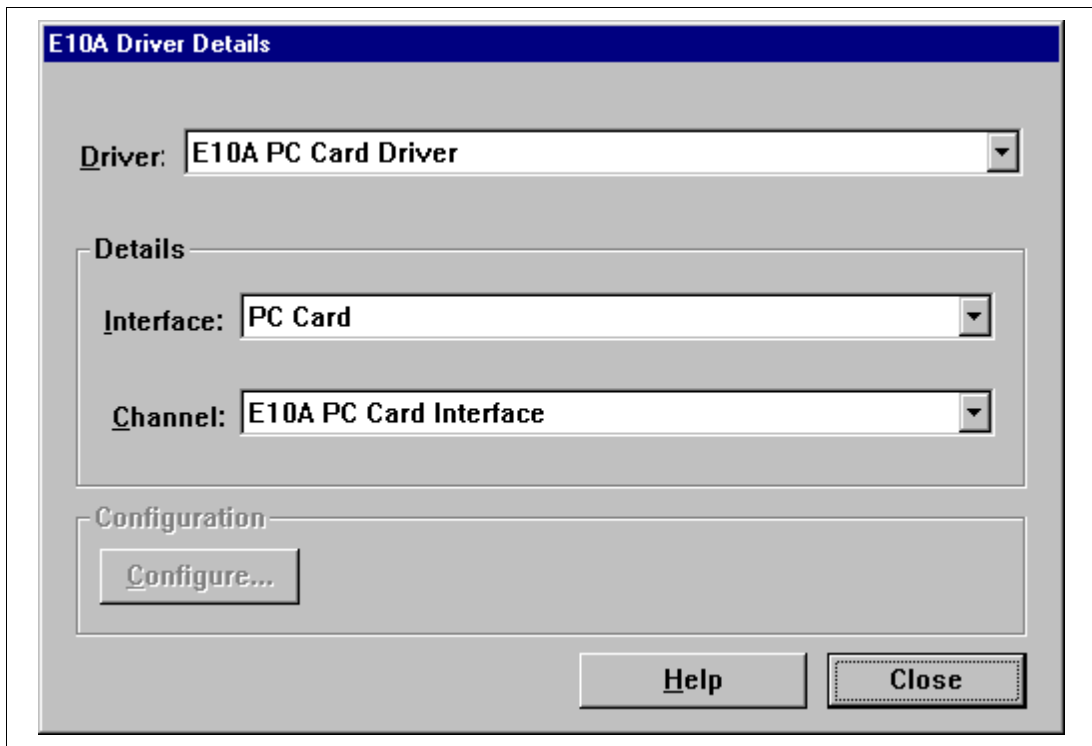


Figure 2.8 [E10A Driver Details] Dialog Box

- With the [Driver] combo box, select the driver to connect the HDI with the emulator.
- [Interface] displays the interface name of the card emulator to be connected, and [Channel] displays the interface to which the board is connected.

[Driver] combo box: Select [E10A PC Card Driver] to use the PCMCIA card emulator.

Select [E10A PCI Card Driver] to use the PCI card emulator. For details, refer to table 6.3 in section 6.5.1, Emulator Driver Selection.

[Interface] combo box: Select [PC Card] to use the PCMCIA card emulator.

[PCI] is displayed to use the PCI card emulator. (If the driver is not installed, the [PC Card] or [PCI] is not displayed.)

- Click the [Close] button.

7. The HDI window is displayed, and the dialog box is displayed as shown in figure 2.9.

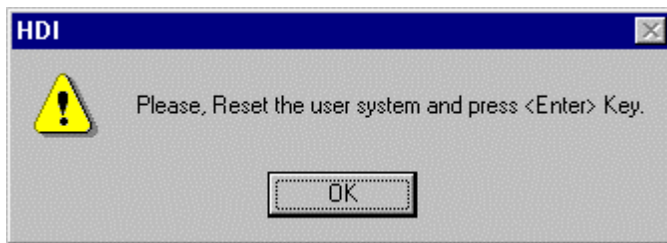


Figure 2.9 Dialog Box of the RESET Signal Input Request Message

8. Power on the user system.

9. Input the reset signal from the user system, and click the [OK] button.

10. When "Link Up" is displayed on the status bar, the HDI initiation is completed.

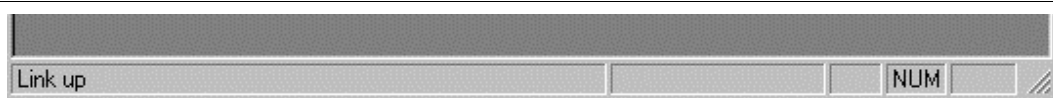


Figure 2.10 [HDI] Status Bar

Notes: 1. When the HDI is not linked up even if the above procedure has been executed, the driver will not be set correctly. Install drivers provided under the /DRIVERS directory in the CD-R according to the screen instructions. For instructions on how to check the driver has been correctly set, refer to the OS manual for your host computer or the following URL:

http://www.hitachi.co.jp/Sicd/English/Products/micom/dev_env/tool/eml/e10a/she10aq.htm

2. If the user system interface cable is disconnected to the Hitachi-UDI port connector on the user system during user program execution, the following dialog box will be displayed.

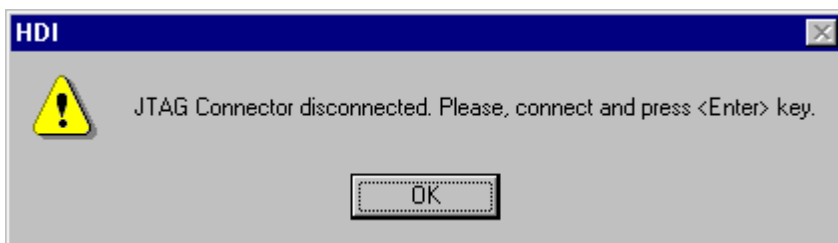


Figure 2.11 [JTAG Connector Disconnected] Dialog Box

3. If the emulator is not initiated, the following dialog boxes shown in figures 2.12 through 2.16 will be displayed.
- (a) If the following dialog box is displayed, the power of the user system may not be input or the RESET signal may not be input to the device. Check the input circuits for the power of the user system and the reset pin.

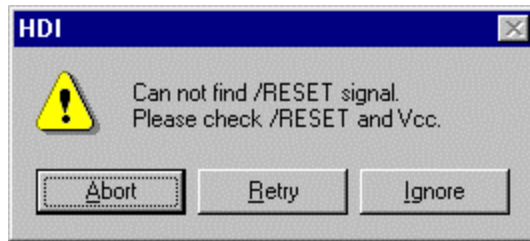


Figure 2.12 [Can not find /RESET signal] Dialog Box

- (b) If the following dialog box is displayed, check that the Hitachi-UDI port connector on the user system is correctly connected.

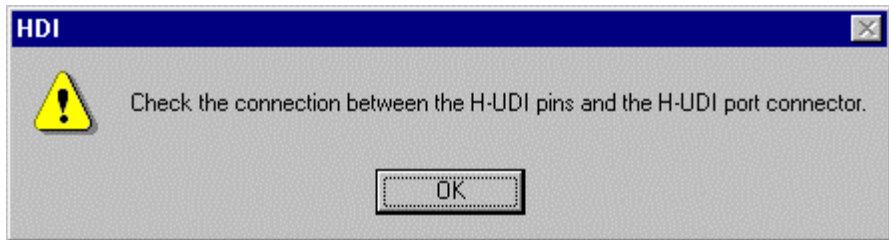


Figure 2.13 [Check the connection] Dialog Box

- (c) If the following dialog box is displayed, the device may not correctly operate. Check if there are reasons for illegal device operation.

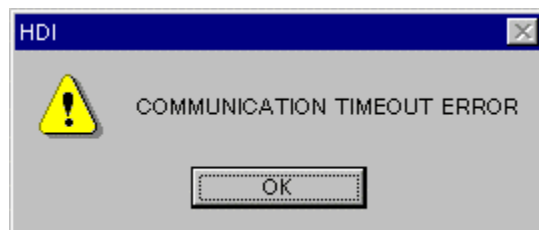


Figure 2.14 [COMMUNICATION TIMEOUT ERROR] Dialog Box



Figure 2.15 [INVALID ASERAM FIRMWARE!] Dialog Box



Figure 2.16 [Error JTAG boot] Dialog Box

4. If the driver is not correctly connected, the following dialog box will be displayed.

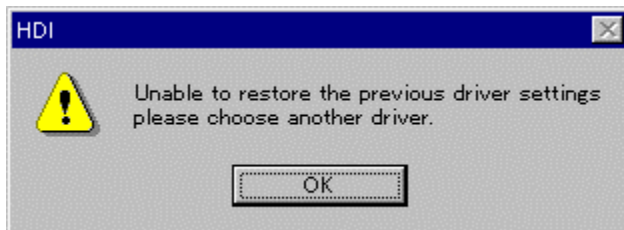


Figure 2.17 [Unable to restore the previous driver settings] Dialog Box

The [E10A Driver Details] dialog box is displayed when the [OK] button is clicked. Select the correct driver. For details, refer to section 6.5.1, Emulator Driver Selection.

2.6 Ending the HDI

Exit the HDI by using the following procedure:

1. Select [Exit] from the [File] menu to end the HDI. When the [Exit HDI] dialog box is displayed, click the [Yes] button.



Figure 2.18 [Exit HDI] Dialog Box

2. Then, the [Save session] dialog box is displayed. If necessary, click the [Yes] button to save session. After saving session, the HDI ends. If not necessary, click the [No] button to end the HDI.

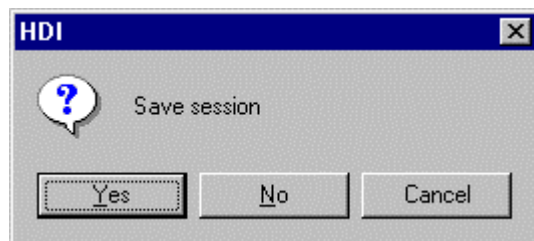


Figure 2.19 [Save session] Dialog Box

3. Turn the user system off.

2.7 Uninstalling the HDI

Follow this procedure to remove the installed HDI from the user's host computer.

1. Open [Add/Remove Programs Properties] from the control panel. Select the HDI program from the list and click the [Add/Remove...] button.
2. The setup program is executed again and the installed application can be changed, modified, or removed. When the application is to be uninstalled, select removal.

CAUTION

A shared file may be detected while the program is being removed. If another HDI may be using the shared file, do not remove the file. When Microsoft® Windows NT® 4.0 operating system is used, the removal of the registry information on the driver may be asked. If other HDI may use the target driver, do not remove the registry information. If another HDI does not start up after the removal process, re-install that HDI.

2.8 CD-R

2.8.1 Configuration of the CD-R

The root directory of the CD-R contains a setup program for HDI installation. The folders contain the files and programs listed below.

Table 2.2 Contents of the CD-R Directories

Directory Name	Contents	Description
Dlls	Microsoft® runtime library	A runtime library for the HDI. The version is checked at installation and this library is copied to the hard disk as part of the installation process.
Drivers	E10A emulator driver	The E10A emulator drivers.
Help	Online help for the E10A emulator	An online help file. This is copied to the hard disk as part of the installation process.
Manual	E10A emulator manual	Precautions on Using the E10A Emulator and the user's manual. These are provided as PDF files.
Pdf_read	Adobe® Acrobat® Reader setup program	Adobe® Acrobat® Reader is an application for displaying, viewing, and printing PDF files.

2.9 Support

Information on the latest version of the HDI and other supporting information for the emulator can be found on the web site. Access the following URL:

http://www.hitachi.co.jp/Sicd/English/Products/micom/dev_env/tool/eml/e10a/e10atop.htm

Section 3 Tutorial

3.1 Introduction

The following describes the main functions of the HDI by using a tutorial program.

The tutorial program is based on the C program that sorts ten random data items in ascending or descending order. The tutorial program performs the following actions:

- The `main` function generates random data to be sorted.
- The `sort` function sorts the generated random data in ascending order.
- The `change` function then sorts the data in descending order.

The file `tutorial.c` contains source code for the tutorial program. The file `tutorial.abs` is a compiled load module in the Dwarf2 format.

Table 3.1 is a list of the parts of the tutorial program and an outline of their configuration on the hard disk.

Table 3.1 Tutorial Program: Configuration and Parts

Item	Contents
Workspace for HEW V1.2	[Installation directory]\tutorial\tutorial.hws
Load module	[Installation directory]\tutorial\tutorial\Debug\tutorial.abs
Main program (source file)	[Installation directory]\tutorial\tutorial\tutorial.c
Stack information file	[Installation directory]\tutorial\tutorial\Debug\tutorial.sni

Use area 3 (CS3 space) as the operating environment. The MMU function is not used.

- Notes:**
1. Operation of `tutorial.abs` is big endian. For little-endian operation, `tutorial.abs` must be recompiled. After recompilation, the addresses may differ from those given in this section.
 2. This program was created by using Hitachi Embedded Workshop (hereafter referred to as HEW) V1.2. Older versions of HEW will not open the workspace included with the package, so create a new workspace in such situations.
 3. This program was compiled without optimization for the SH2 CPU. If recompiled with different settings, the addresses may differ from those given in this section.
 4. `tutorial.abs` is a load module in the Dwarf2 format. If a load module is recreated in the Sysrof format, the amount of information displayed on the HDI screen during the program's execution will be reduced.
 5. This section describes general usage examples for the emulator. For the specifications of particular products, refer to section 6 or the online help file.

3.2 Running the HDI

To run the HDI, select the [HDI for E10A SHxxxx] -> [Hitachi Debugging Interface] from the [Start] menu.

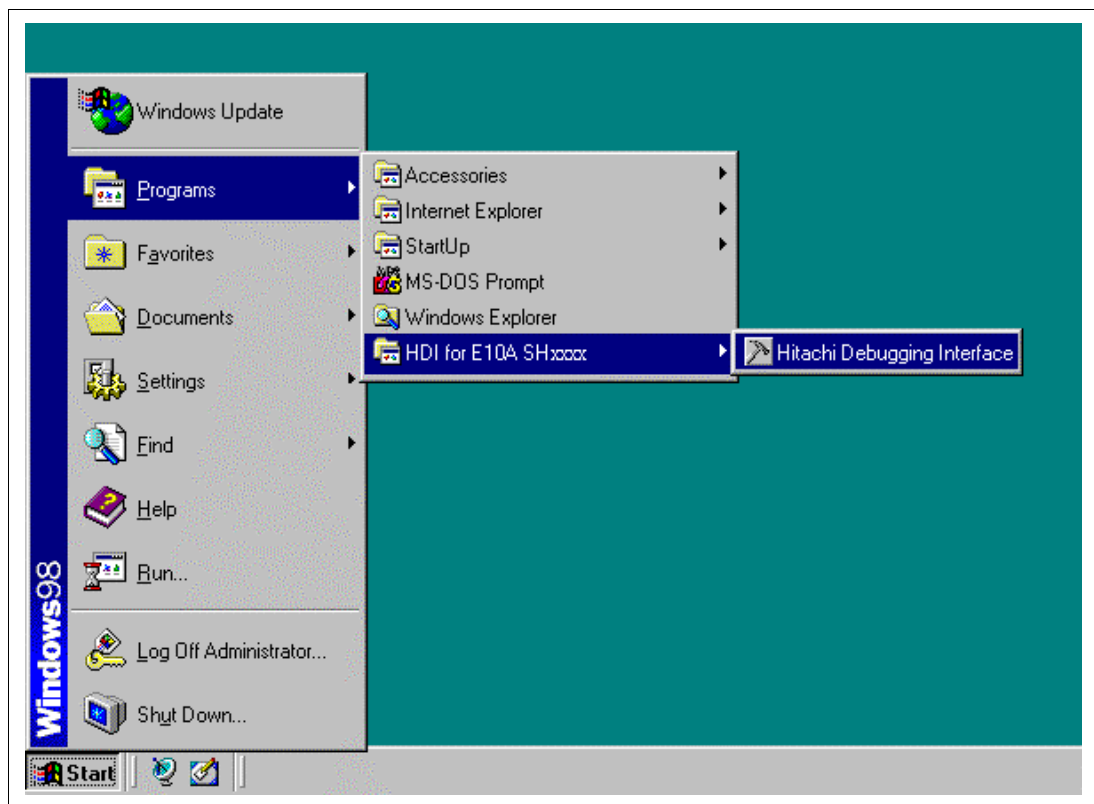


Figure 3.1 [Start] Menu

For the procedure of running the HDI, refer to section 2.5, System Check.

3.3 [HDI] Window

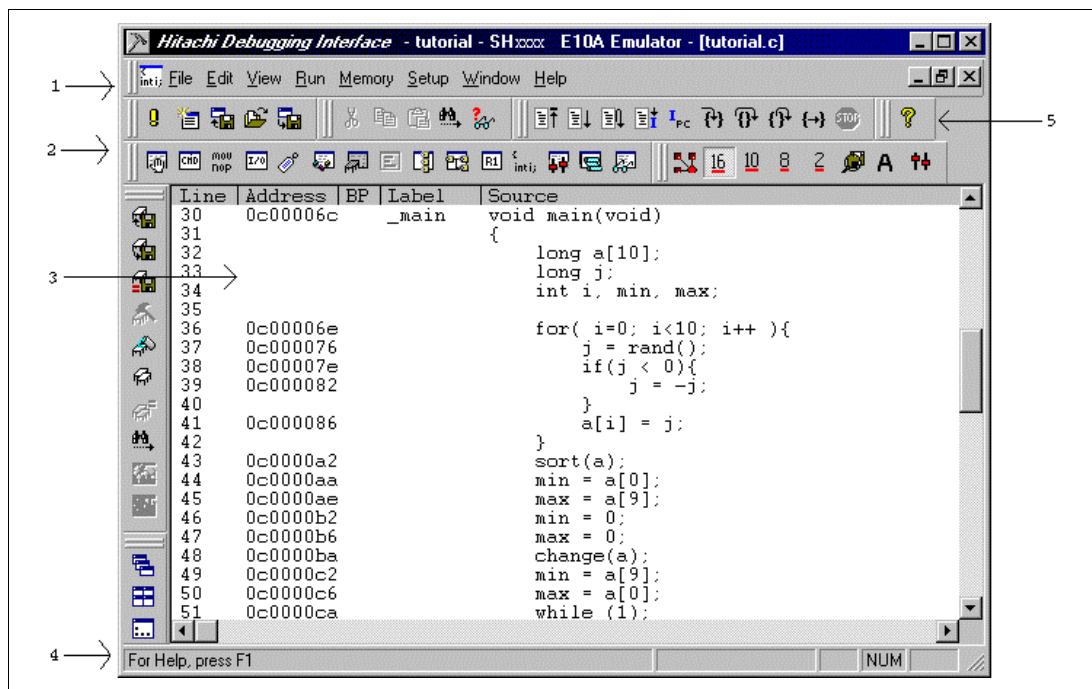


Figure 3.2 [HDI] Window

The key functions of the HDI are described in section 4, Descriptions of Windows. Numbers in figure 3.2 indicate the following:

1. Menu bar: Gives the user access to the HDI commands for using the HDI debugger.
2. Toolbar: Provides convenient buttons as shortcuts for the most frequently used menu commands.
3. Source window: Displays the source program being debugged.
4. Status bar: Displays the status of the emulator, and progress information about downloading.
5. [Help] button: Activates online help about any features of the HDI user interface.

3.4 Setting up the Emulator

The clocks which are used for data communications must be set up on the emulator before the program is downloaded.

- AUD clock

A clock used in acquiring AUD traces.

If its frequency is set too low, complete data may not be acquired during realtime tracing.

If the frequency is set too high, the upper limit for the device's AUD clock may be exceeded.

The AUD clock is only needed for emulators that have an AUD trace function.

- JTAG clock (TCK)

A communication clock for downloading data to the emulator except for acquiring AUD trace.

If its frequency is set too low, the speed of downloading will be lowered.

If its frequency is set too high, the upper limit for the device's TCK clock may be exceeded.

For details of the limitations on both clocks, refer to section 6.5.4, Notes on Using the JTAG Clock (TCK) and AUD Clock (AUDCK). The following is a description of the procedure used to set the clocks.

3.5 Setting the [Configuration] Dialog Box

- Select [Configure Platform...] from the [Setup] menu to set a communication clock. The [Configuration] dialog box is displayed.

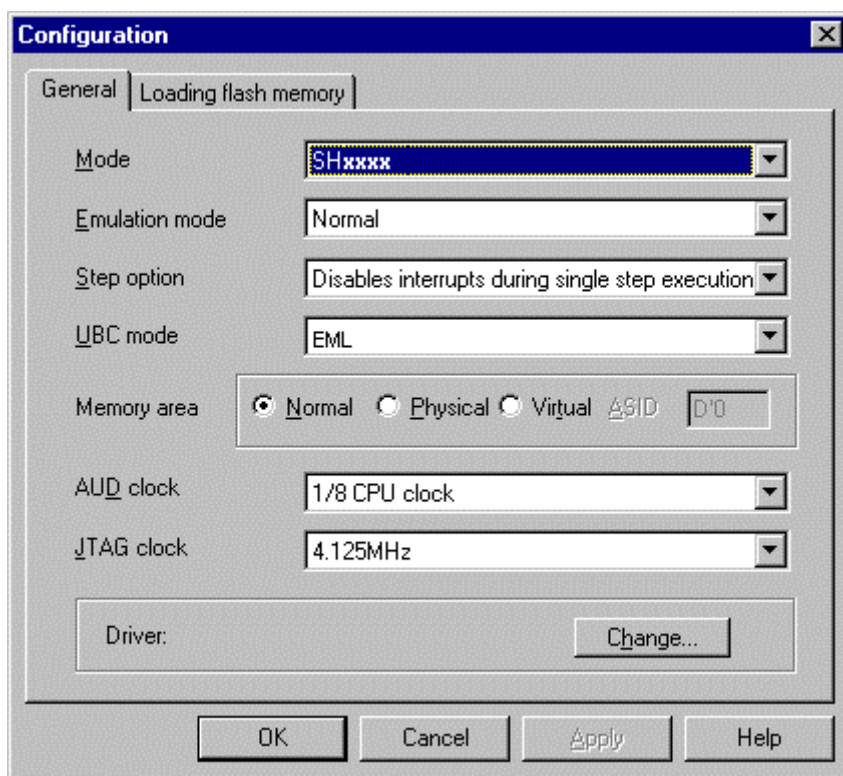


Figure 3.3 [Configuration] Dialog Box

- Set any value in the [AUD clock] and [JTAG clock] combo boxes. The clock also operates with the default value.

Note: The items that can be set in this window differ according to the product. For the settings for each product, refer to the online help.

- Click the [OK] button to set a configuration.

3.6 Checking the Operation of the Target Memory for Downloading

Check that the destination memory area for downloading is operating correctly.

When the destination memory is SDRAM or DRAM, a register in the bus controller must be set before downloading. Set the bus controller correctly in the [I/O Registers] window according to the memory type. For details, refer to section 8.6, I/O Register Display, in the Hitachi Debugging Interface User's Manual.

When the required settings, such as the settings for the bus controller, have been completed, display and edit the contents of the destination memory in the [Memory] window to check that the memory is operating correctly.

Note: The above way of checking the operation of memory may be inadequate. It is recommended that a program for checking the memory be created.

- Select [Memory...] from the [View] menu, enter H'0c000000 in the [Address] edit box, and set the format in the [Format] combo box to Byte.

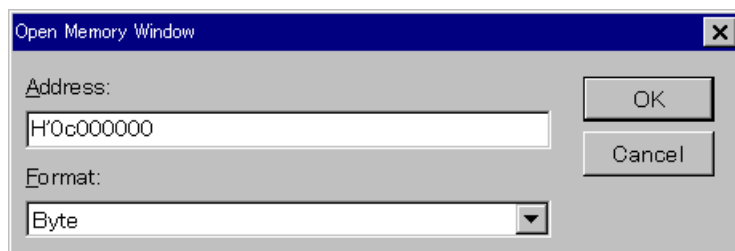


Figure 3.4 [Open Memory Window] Dialog Box

- Click the [OK] button. The [Memory] window is displayed and shows the specified memory area.

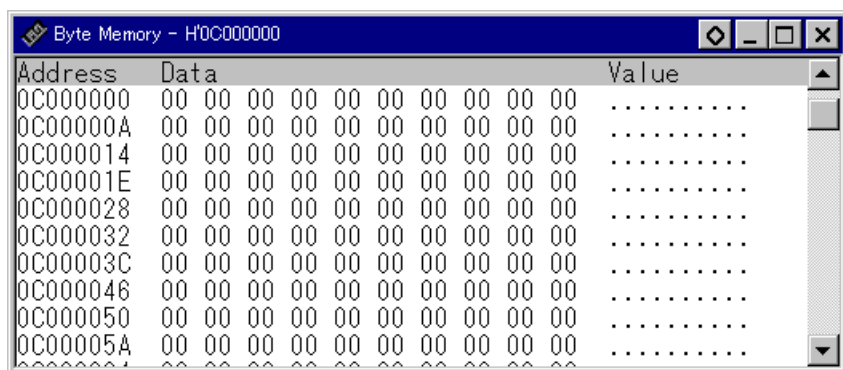


Figure 3.5 [Memory] Window

- Placing the mouse cursor on a point in the display of data in the [Memory] window and double clicking allows the values at that point to be changed. Data can also be directly edited around the current position of the text cursor.

3.7 Downloading the Tutorial Program

3.7.1 Downloading the Tutorial Program

Download the object program to be debugged.

- Select [Load Program...] from the [File] menu. The [Load Program] dialog box is displayed. Enter '[installation directory]\tutorial\tutorial\Debug\tutorial.abs' in the [File name] list box as shown in figure 3.6, then click the [Open] button.

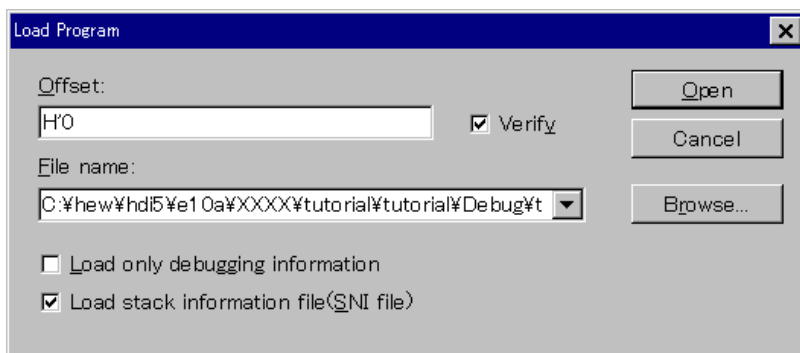


Figure 3.6 [Load Program] Dialog Box

- Notes:
1. When installing the emulator, if no directory is specified and the HEW is in use, the program is installed under '\Hew\hdi5\e10a'. When the HEW is not in use, the program is installed under '\root directory\E10A'.
 2. The SNI file is required so that the profiler function can be used.
 3. The [Verify] check box is disabled in this product.

After the file has been loaded, the following dialog box displays information about the memory areas to which the program code has been transferred.

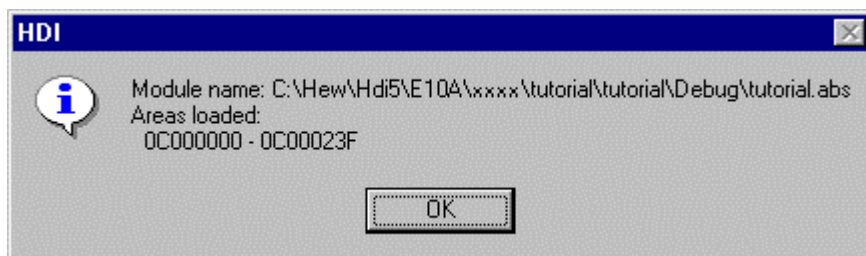


Figure 3.7 [HDI] Dialog Box

- Click the [OK] button to continue.

3.7.2 Displaying the Source Program

The HDI allows the user to debug a program at the source level.

- Select [Source...] from the [View] menu. The [Open] dialog box is displayed.
- Select the C source file that corresponds to the object file the user has loaded.

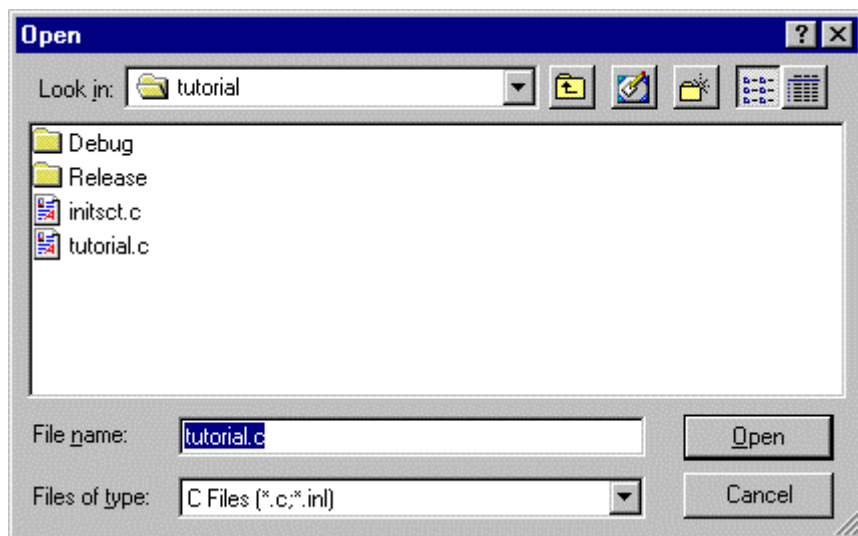


Figure 3.8 [Open] Dialog Box

- Select [tutorial.c] and click the [Open] button. The [Source] window is displayed.

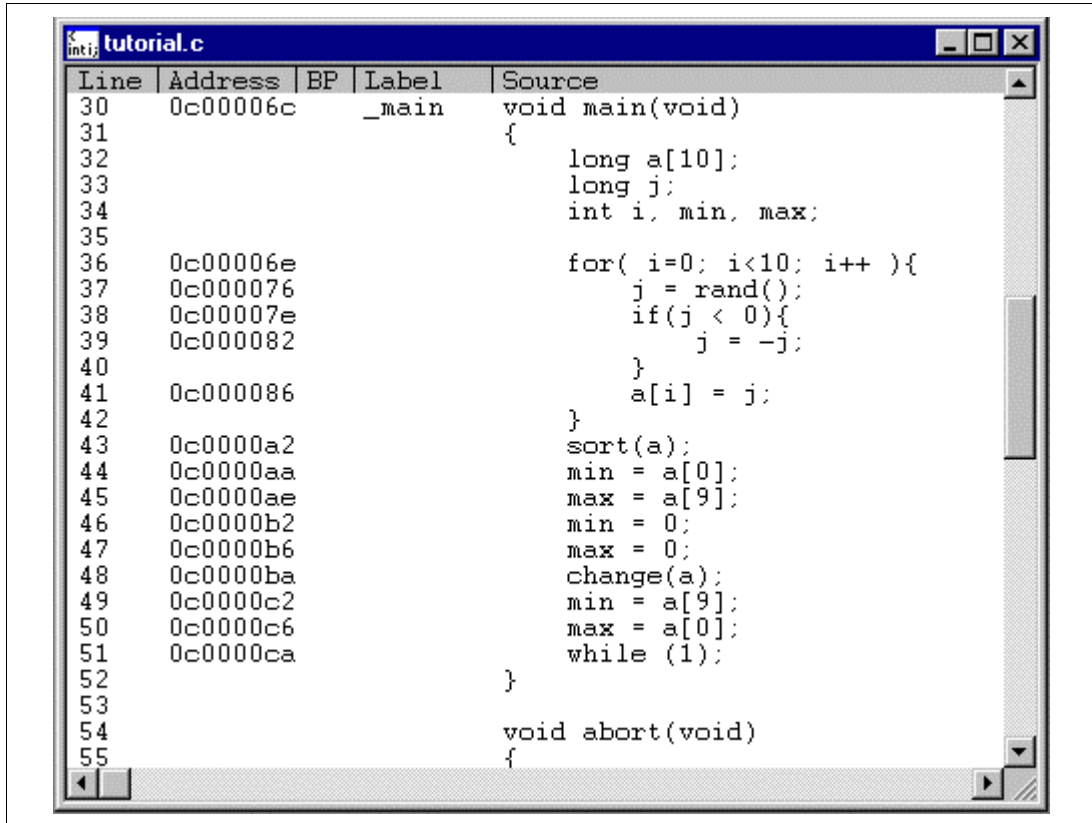


Figure 3.9 [Source] Window (Displaying the Source Program)

- If necessary, select the [Font] option from the [Customise] submenu on the [Setup] menu to select a font and size that are legible.

Initially the [Source] window shows the start of the main program, but the user can use the scroll bar to scroll through the program and look at the other statements.

3.8 Setting a Software Breakpoint

A breakpoint is a simple debugging function.

The [Source] window provides a very simple way of setting a software breakpoint at any point in a program. For example, to set a breakpoint at the `sort` function call:

- Select by double-clicking the [BP] column on the line containing the `sort` function call.

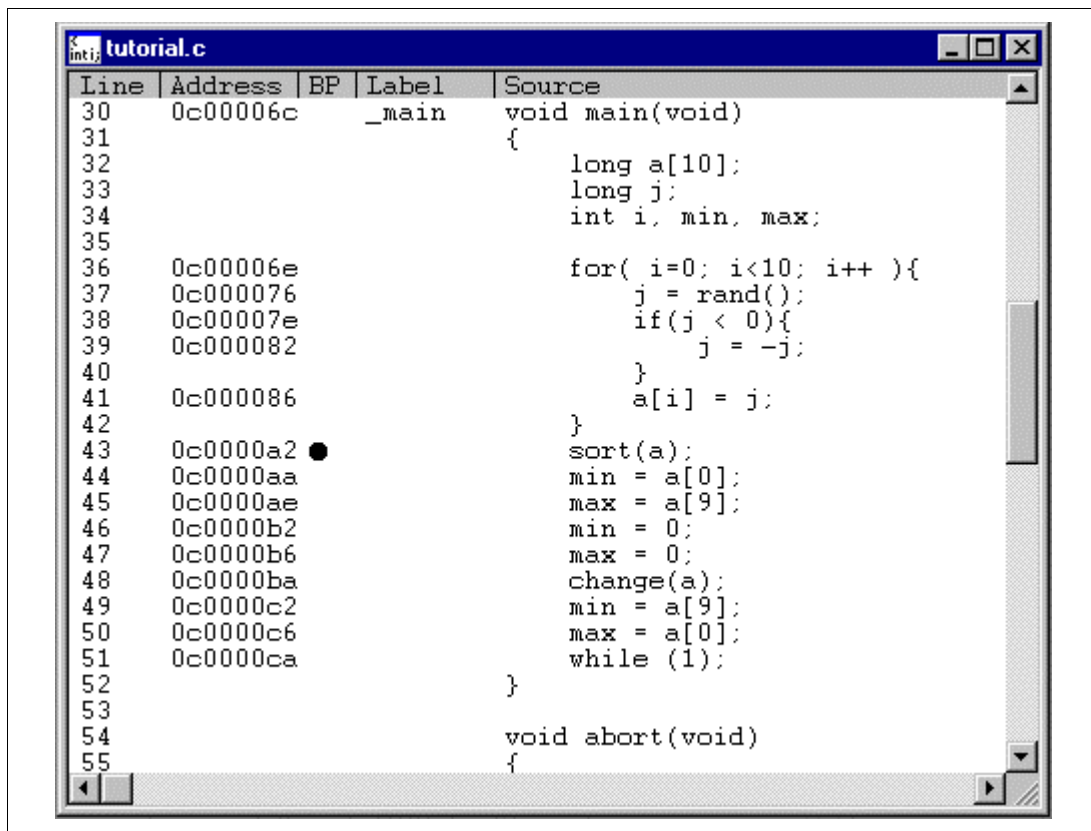


Figure 3.10 [Source] Window (Setting a Software Breakpoint)

The symbol ● will appear on the line containing the `sort` function, and the word ● Break will appear when the [BP] column is extended. This shows that a software breakpoint has been set.

Note: The software breakpoint cannot be set in the ROM area.

3.9 Setting Registers

Set values of the program counter and the stack pointer before executing the program.

- Select [Registers] from the [View] menu. The [Registers] window is displayed.

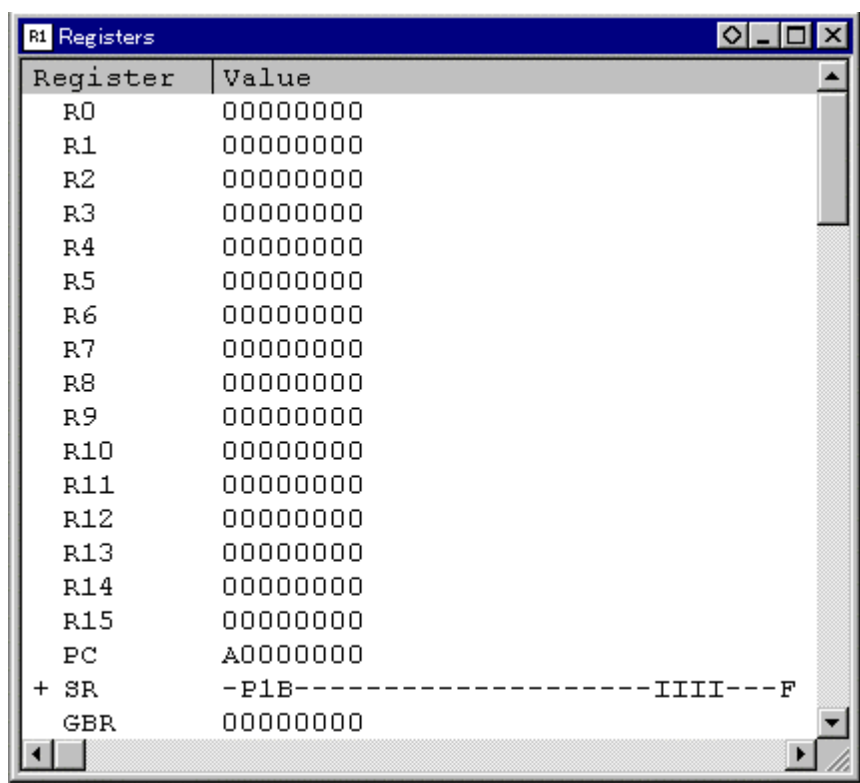


Figure 3.11 [Registers] Window

- To change the value of the program counter (PC), double-click the value area in the [Registers] window with the mouse. The following dialog box is then displayed, and the value can be changed. Set the program counter to H'0c00006c in this tutorial program, and click the [OK] button.
- Move the mouse pointer on the value to be changed in the [PC] value area and enter the new value by the keyboard.

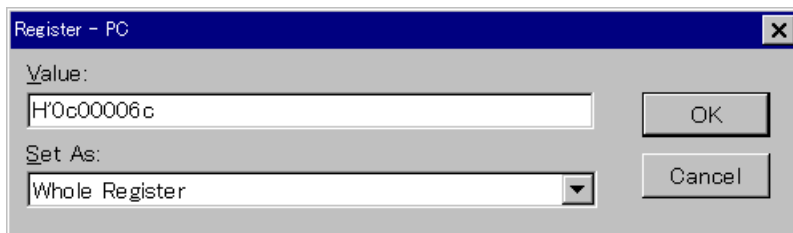


Figure 3.12 [Register] Dialog Box (PC)

- Change the value of the stack pointer (SP) in the same way. Set H'0c000c00 for the value of the stack pointer in this tutorial program.

3.10 Executing the Program

Execute the program as described in the following:

- To execute the program, select [Go] from the [Run] menu, or click the [Go] button on the toolbar.



Figure 3.13 [Go] Button

The program will be executed up to the breakpoint that has been inserted, and a statement will be highlighted in the [Source] window to show the position that the program has halted, with the message [Break=BREAKPOINT] in the status bar.

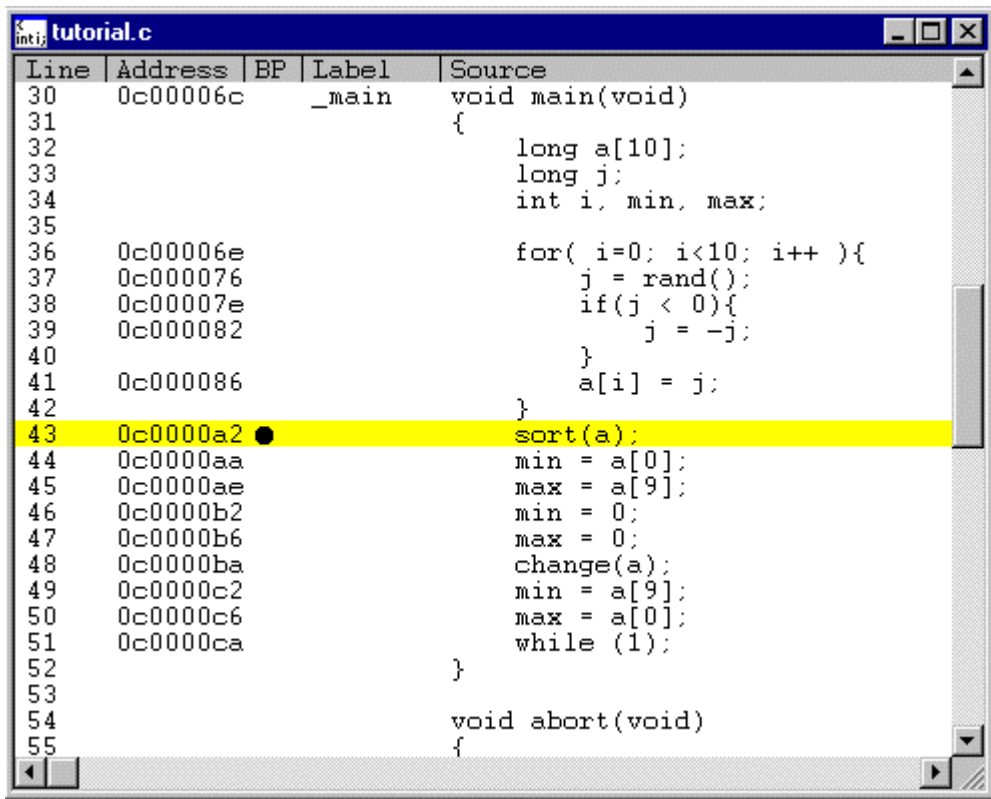


Figure 3.14 [Source] Window (Break Status)

The user can see the cause of the break that occurred last time in the [System Status] window.

- Select [Status] from the [View] menu. After the [System Status] window is displayed, open the [Platform] page, and check the status of Cause of last break.

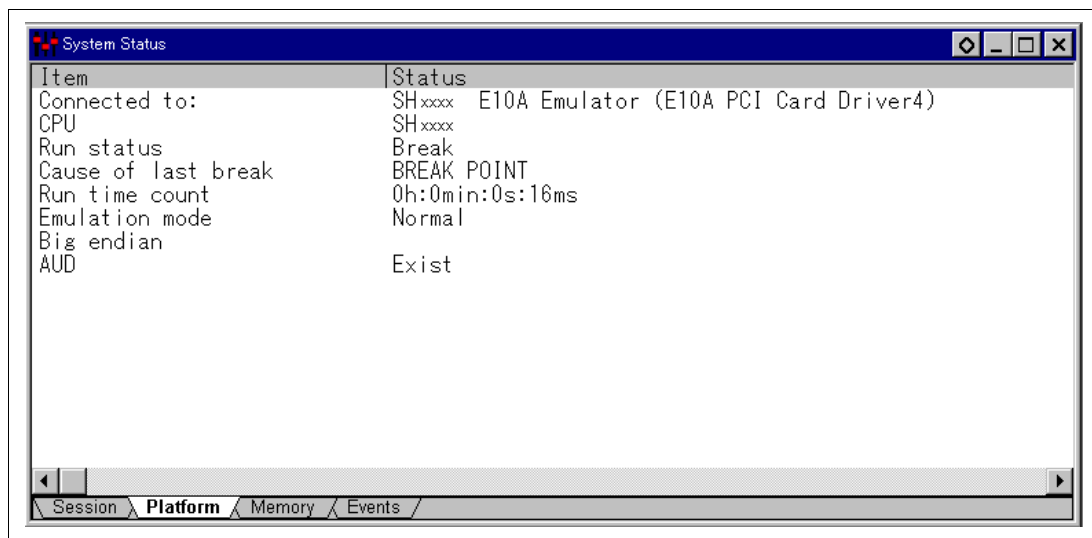


Figure 3.15 [System Status] Window

Note: The items that can be displayed in this window differ according to the product. For the items that can be displayed, refer to the online help.

3.11 Reviewing Breakpoints

The user can see all the breakpoints set in the program in the [Breakpoints] window.

- Select [Breakpoints] from the [View] menu.

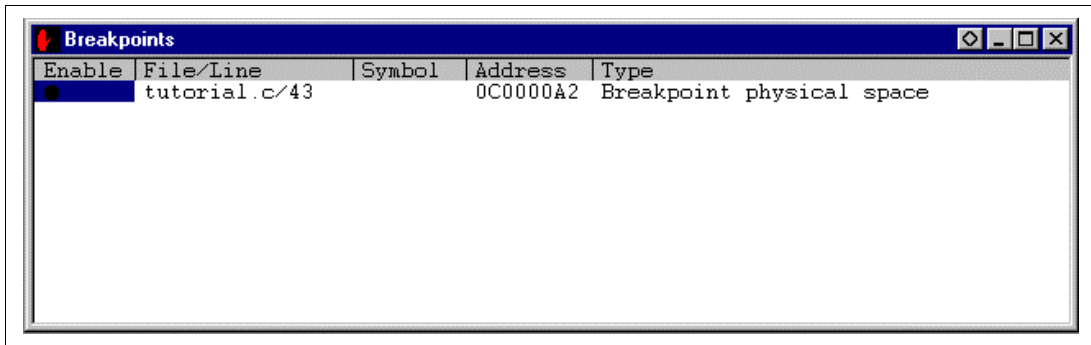


Figure 3.16 [Breakpoints] Window

The pop-up menu, opened by clicking the [Breakpoints] window with the right mouse button, also allows the user to set or change breakpoints, define new breakpoints, and delete, enable, or disable breakpoints.

3.12 Viewing Memory

When the label name is specified, the user can view the memory contents that the label has been registered in the [Memory] window. For example, to view the memory contents corresponding to the `_main` in word size:

- Select [Memory ...] from the [View] menu, enter `_main` in the [Address] edit box, and set Word in the [Format] combo box.

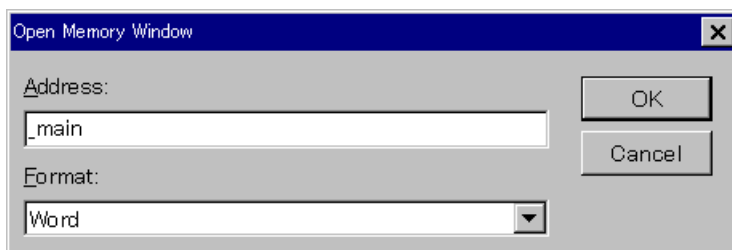
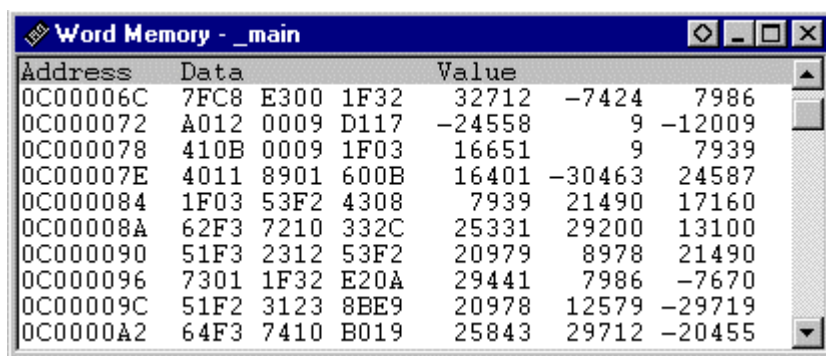


Figure 3.17 [Open Memory Window] Dialog Box

- Click the [OK] button. The [Memory] window showing the specified area of memory is displayed.

A screenshot of the 'Word Memory - _main' window. It displays a table of memory addresses, data, and values. The title bar shows 'Word Memory - _main' with standard window controls. The table has three columns: 'Address', 'Data', and 'Value'. The data is organized into three groups of three columns each, representing different data formats (hex, decimal, and signed decimal).

Address	Data			Value		
0C00006C	7FC8	E300	1F32	32712	-7424	7986
0C000072	A012	0009	D117	-24558	9	-12009
0C000078	410B	0009	1F03	16651	9	7939
0C00007E	4011	8901	600B	16401	-30463	24587
0C000084	1F03	53F2	4308	7939	21490	17160
0C00008A	62F3	7210	332C	25331	29200	13100
0C000090	51F3	2312	53F2	20979	8978	21490
0C000096	7301	1F32	E20A	29441	7986	-7670
0C00009C	51F2	3123	8BE9	20978	12579	-29719
0C0000A2	64F3	7410	B019	25843	29712	-20455

Figure 3.18 [Memory] Window

3.13 Watching Variables

As the user steps through a program, it is possible to watch that the values of variables used in the user program are changed. For example, set a watch on the long-type array `a` declared at the beginning of the program, by using the following procedure:

- Click the left of displayed array `a` in the [Source] window to position the cursor.
- Click the [Source] window with the right mouse button and select [Instant Watch...] from a pop-up menu.

The following dialog box will be displayed.

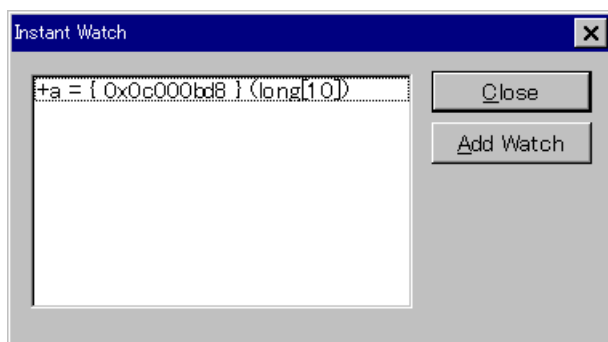


Figure 3.19 [Instant Watch] Dialog Box

- Click [Add Watch] button to add a variable to the [Watch] window.

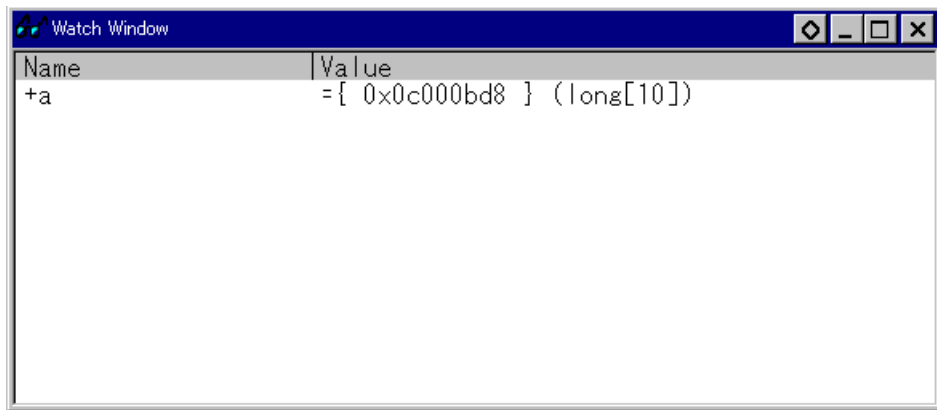


Figure 3.20 [Watch] Window (Displaying the Array)

The user can also add a variable to the [Watch] window by specifying its name.

- Click the [Watch] window with the right mouse button and select [Add Watch...] from the pop-up menu.

The following dialog box will be displayed.

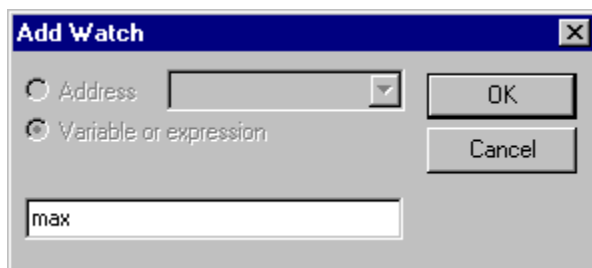


Figure 3.21 [Add Watch] Dialog Box

- Input variable **max** and click the [OK] button.

The [Watch] window will now also show the int-type variable max.

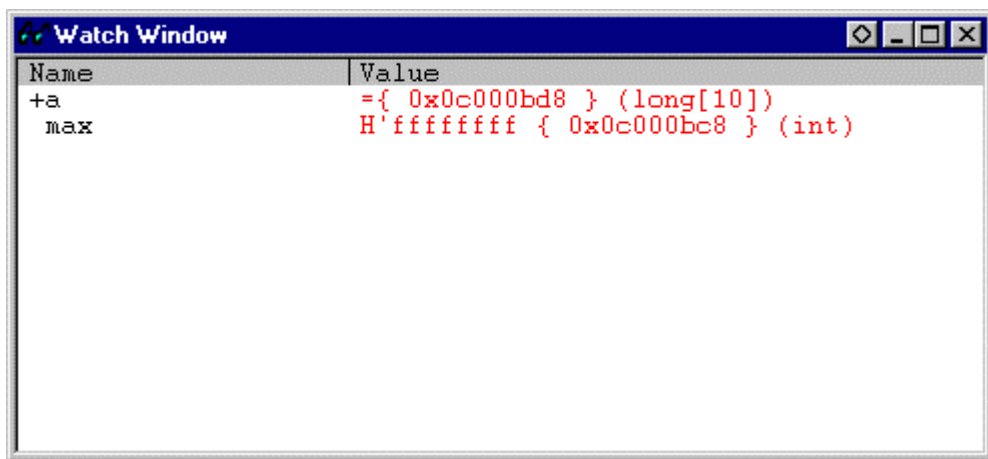


Figure 3.22 [Watch] Window (Displaying the Variable)

The user can double-click the + symbol to the left of any variable in the [Watch] window to watch the all elements in array a.

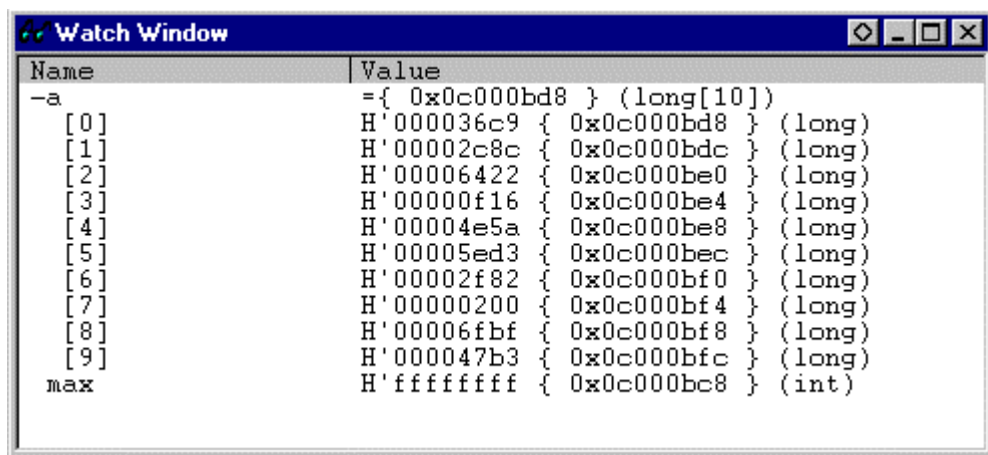


Figure 3.23 [Watch] Window (Displaying Array Elements)

3.14 Stepping Through a Program

The HDI provides a range of step menu commands that allow efficient program debugging.

Table 3.2 Step Option

Menu Command	Description
Step In	Executes each statement, including statements within functions.
Step Over	Executes a function call in a single step.
Step Out	Steps out of a function, and stops at the statement following the statement in the program that called the function.
Step...	Steps the specified times repeatedly at a specified rate.

3.14.1 Executing [Step In] Command

The [Step In] steps into the called function and stops at the first statement of the called function.

- To step through the `sort` function, select [Step In] from the [Run] menu, or click the [Step In] button in the toolbar.



Figure 3.24 [Step In] Button

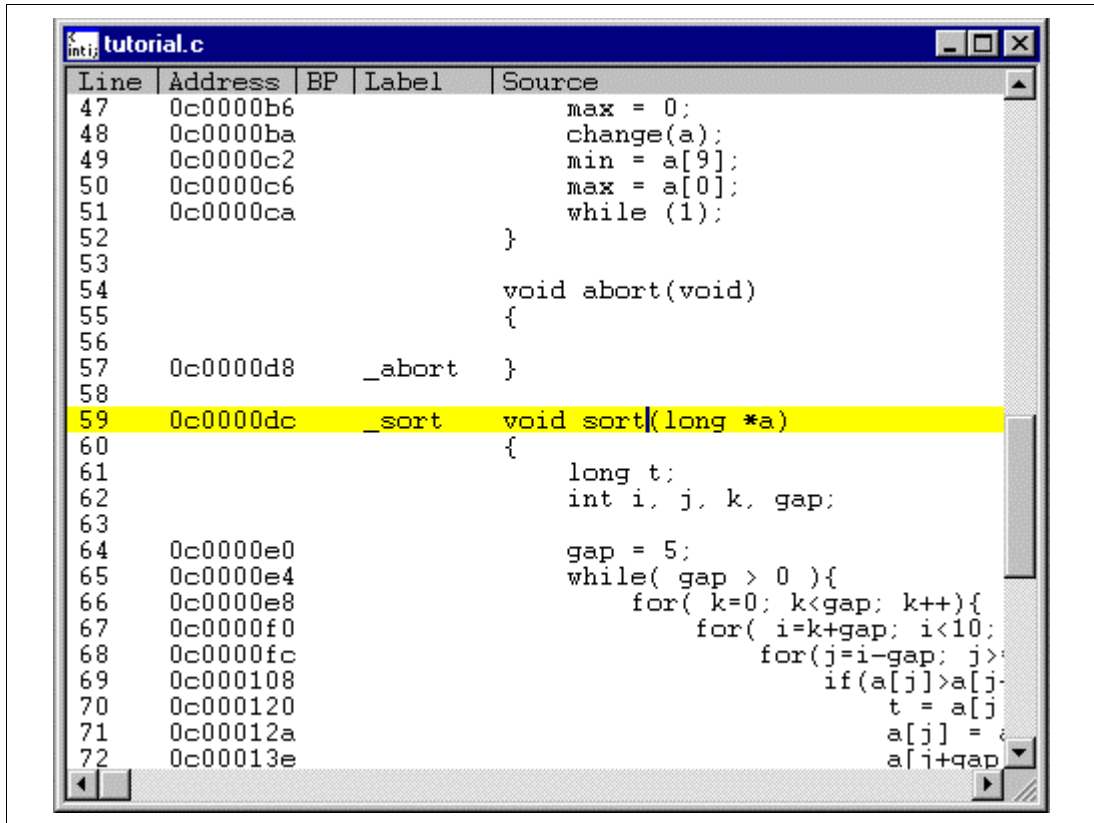


Figure 3.25 [Source] Window (Step In)

- The highlighted line moves to the first statement of the sort function in the [Source] window.

3.14.2 Executing [Step Out] Command

The [Step Out] steps out of the called function and stops at the next statement of the calling statement in the main function.

- To step out of the `sort` function, select [Step Out] from the [Run] menu, or click the [Step Out] button in the toolbar.

Note: It takes time to execute this function.



Figure 3.26 [Step Out] Button

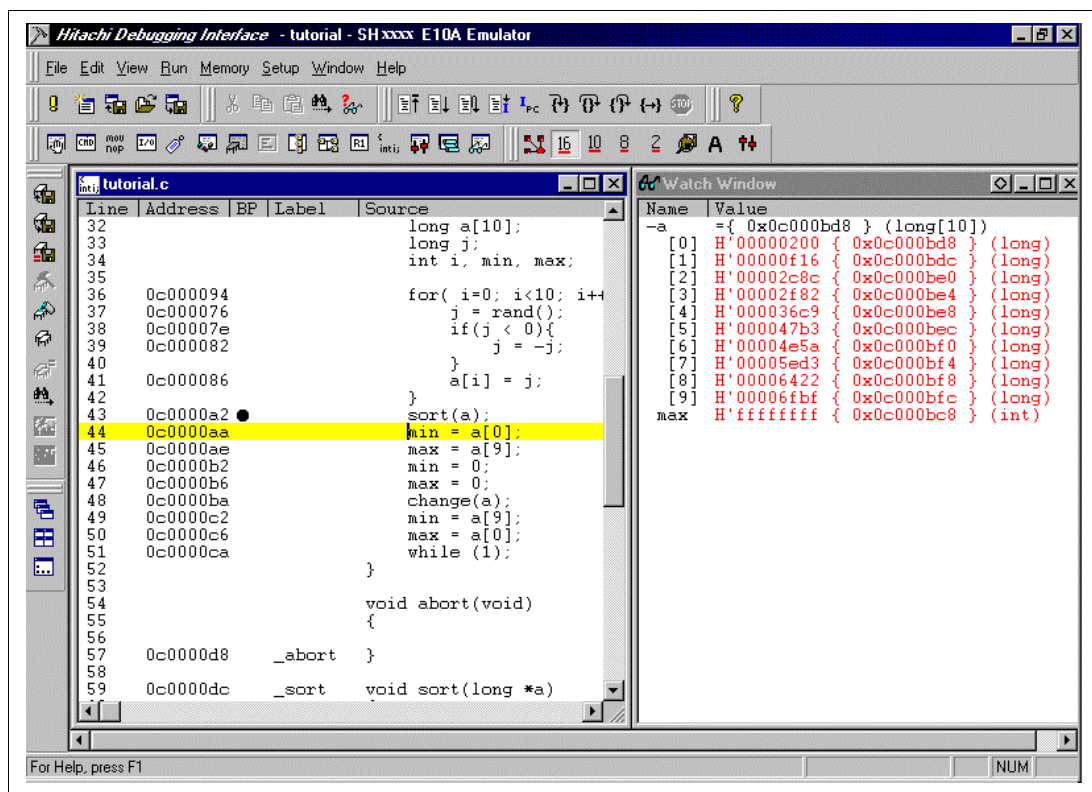


Figure 3.27 [HDI] Window (Step Out)

- The data of variable `a` displayed in the [Watch] window is sorted in ascending order.

- To execute two steps, use [Step In] twice.

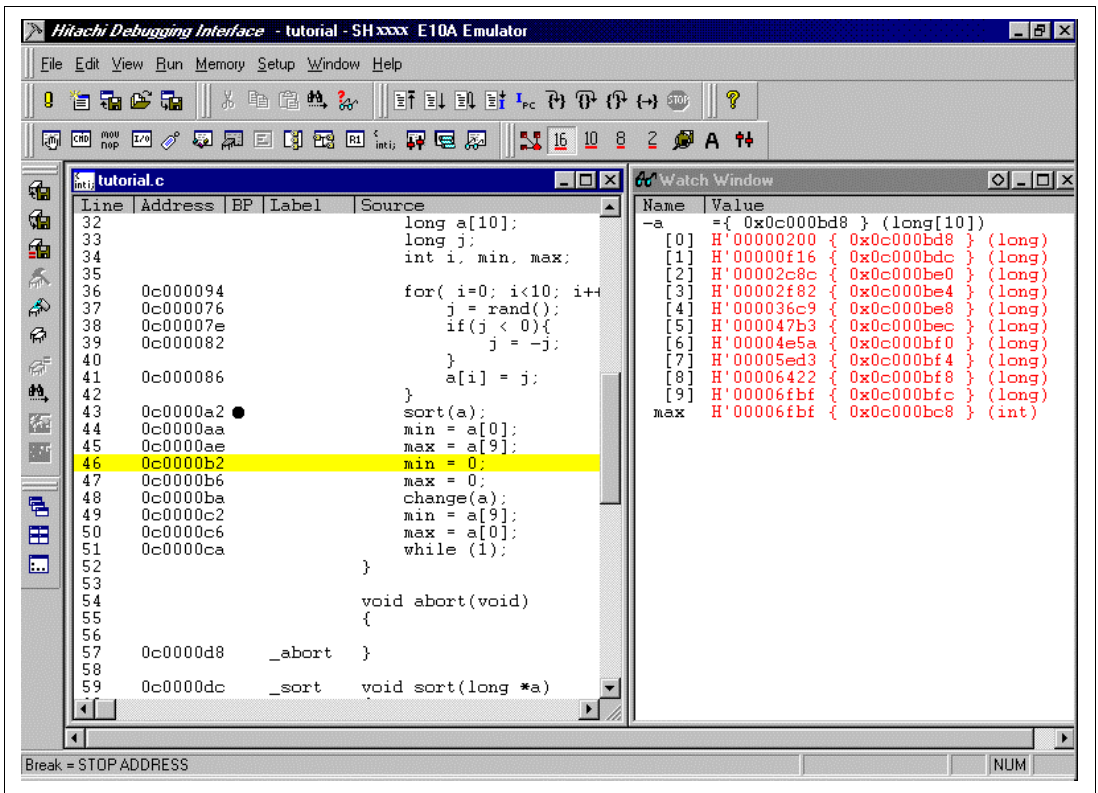


Figure 3.28 [HDI] Window (Step In → Step In)

- The value of `max` displayed in the [Watch] window is changed to the maximum data value.

3.14.3 Executing [Step Over] Command

The [Step Over] executes a function call as a single step and stops at the next statement of the main program.

- Using [Step Over], execute two steps to reach the change function statement.

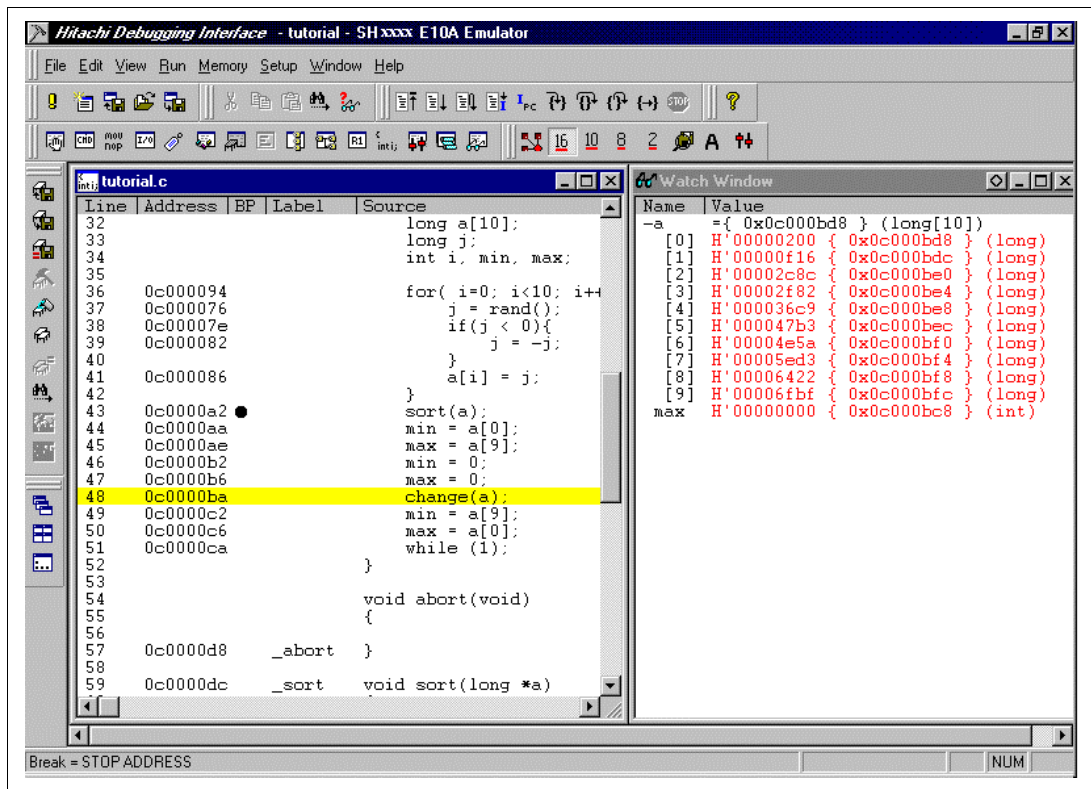


Figure 3.29 [Source] Window (Before Step Over Execution)

- To step through all statements in the change function at a single step, select [Step Over] from the [Run] menu, or click the [Step Over] button in the toolbar.



Figure 3.30 [Step Over] Button

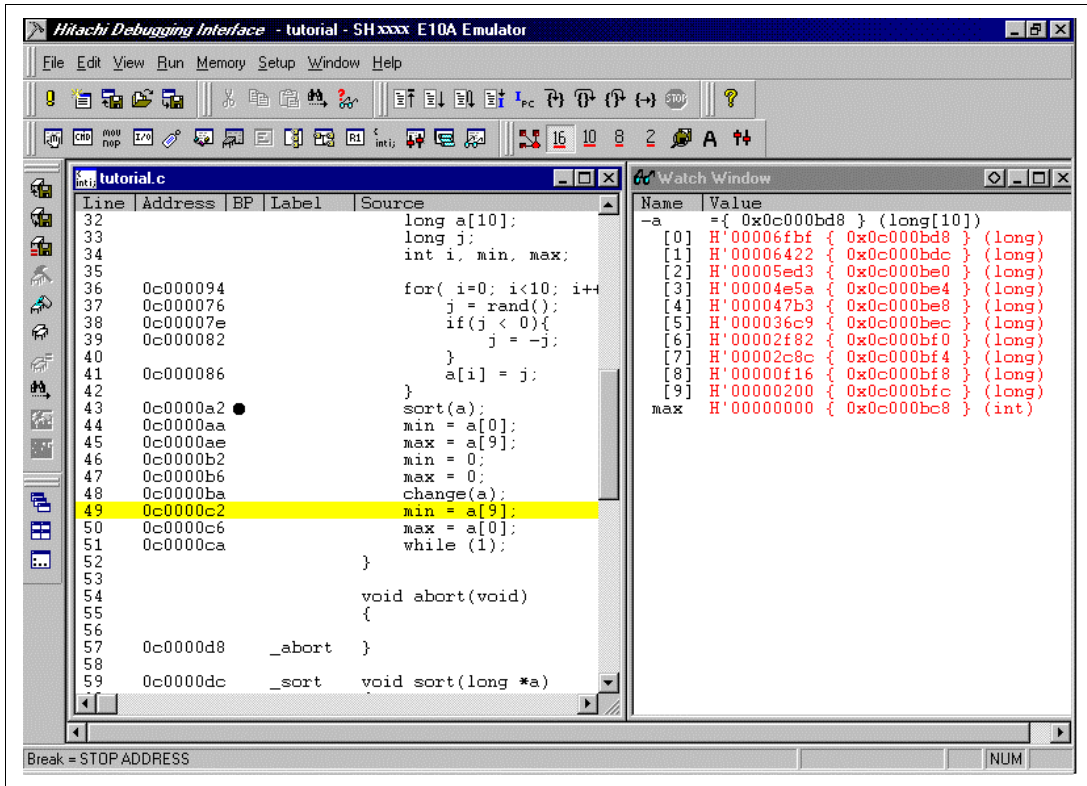


Figure 3.31 [HDI] Window (Step Over)

3.15 Forced Breaking of Program Executions

The HDI can force a break in the execution of a program.

- To execute the remaining sections of the main function, select [Go] from the [Run] menu or the [Go] button on the toolbar.



Figure 3.32 [Go] Button

- The program goes into an endless loop. To force a break in execution, select [Halt] from the [Run] menu or the [Stop] button on the toolbar.



Figure 3.33 [Stop] Button

- The highlighted line of the [Program] window moves to the `while` statement, and the value of `max` displayed in the [Watch] window is updated to the most recent value.

3.16 Displaying Local Variables

The user can display local variables in a function using the [Locals] window. For example, we will examine the local variables in the `main` function, which declares five local variables: `a`, `j`, `i`, `min`, and `max`.

- Select [Locals] from the [View] menu. The [Locals] window is displayed.

Initially, the [Locals] window is empty because local variables have not yet been declared.

The [Locals] window will now show the local variables and their values.

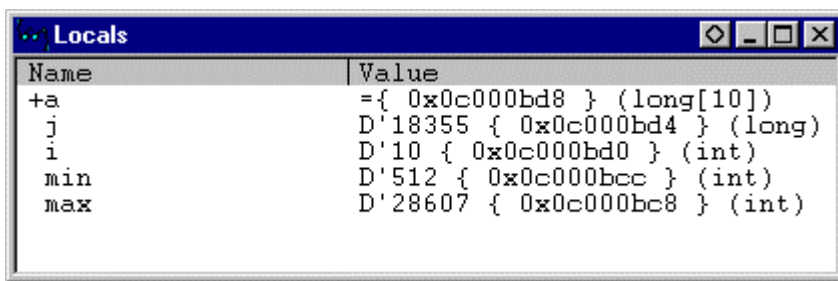


Figure 3.34 [Locals] Window

- Double-click the + symbol in front of array `a` in the [Locals] window to display the elements of array `a`.
- Refer to the elements of array `a` before and after the execution of the `sort` function, and confirm that random data is sorted in descending order.

3.17 Break Function

The emulator has software and hardware break functions. With the HDI, a software breakpoint can be set using the [Breakpoints] window, and a hardware break condition can be set using the [Break Condition] dialog box.

An overview and setting of the break function are described below.

3.17.1 Software Break Function

The emulator can set up to 255 software breakpoints. Other methods for setting a software breakpoint than in section 3.8 are described below.

- Select [Breakpoints] from the [View] menu. The [Breakpoints] window is displayed.
- Click the [Breakpoints] window with the right mouse button and select [Delete All] from the pop-up menu to cancel all the breakpoints that have been set.

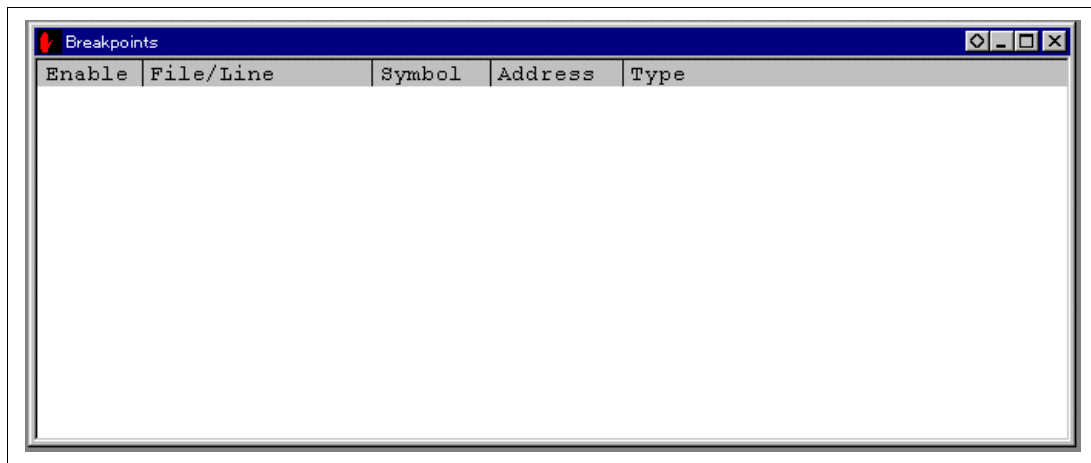


Figure 3.35 [Breakpoints] Window (Before Software Breakpoint Setting)

- Click the [Breakpoints] window with the right mouse button and select [Add] from the pop-up menu.

The [Break] dialog box is displayed. The [Point] page is displayed as a default.

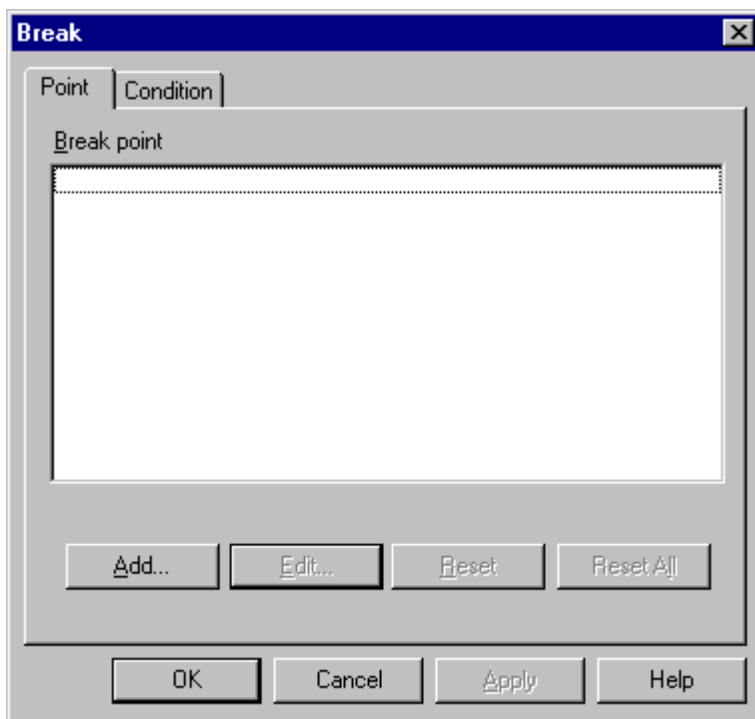


Figure 3.36 [Point] Page ([Break] Dialog Box)

- Click the [Add...] button to display the [Breakpoint] dialog box.
- Enter **H'0c0000c2** to the [Value] edit box.

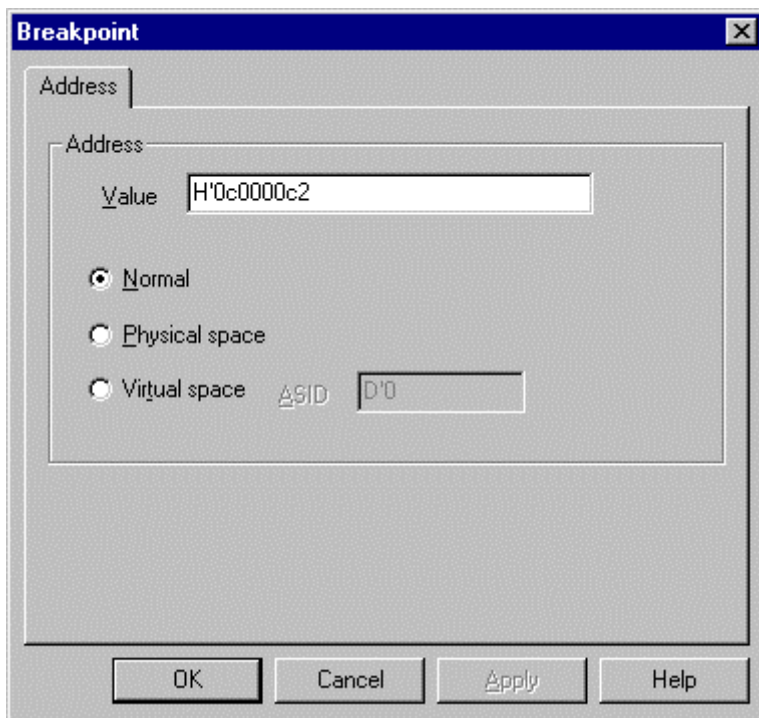


Figure 3.37 [Break Point] Dialog Box

- Click the [OK] button.

The [Break] dialog box is displayed. The address set in the value field of [Breakpoint] and the memory space are displayed.

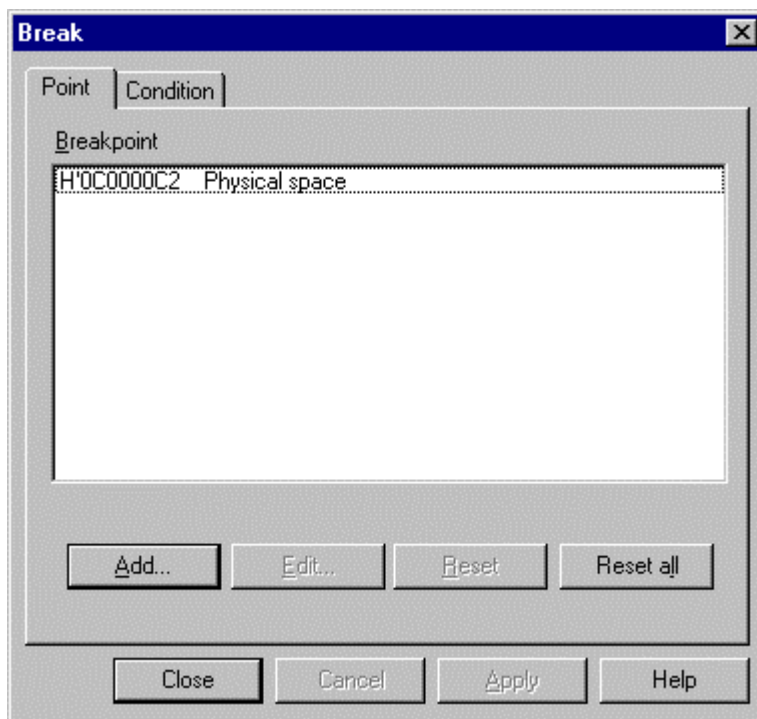


Figure 3.38 [Point] Page ([Break] Dialog Box) (After Software Breakpoint Setting)

- Click the [Close] button (or [OK] button in some emulator products).

The software breakpoint that has been set is displayed in the [Breakpoints] window.

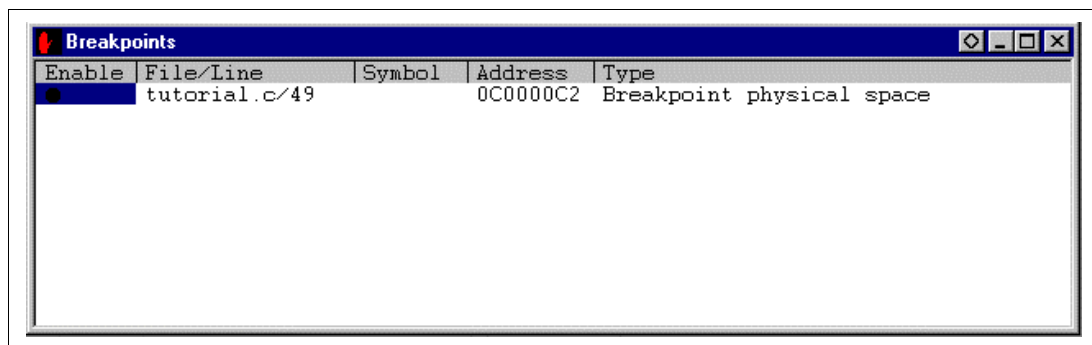


Figure 3.39 [Breakpoints] Window (Software Breakpoint Setting)

To stop the tutorial program at the breakpoint, the following procedure must be executed:

- Close the [Breakpoints] window.
- Set the program counter and stack pointer values (PC = H'0c00006c and R15 = H'0c000c00) that have been set in section 3.9, Setting Registers, in the [Registers] window. Click the [Go] button.

The program runs, and stops at the set breakpoint.

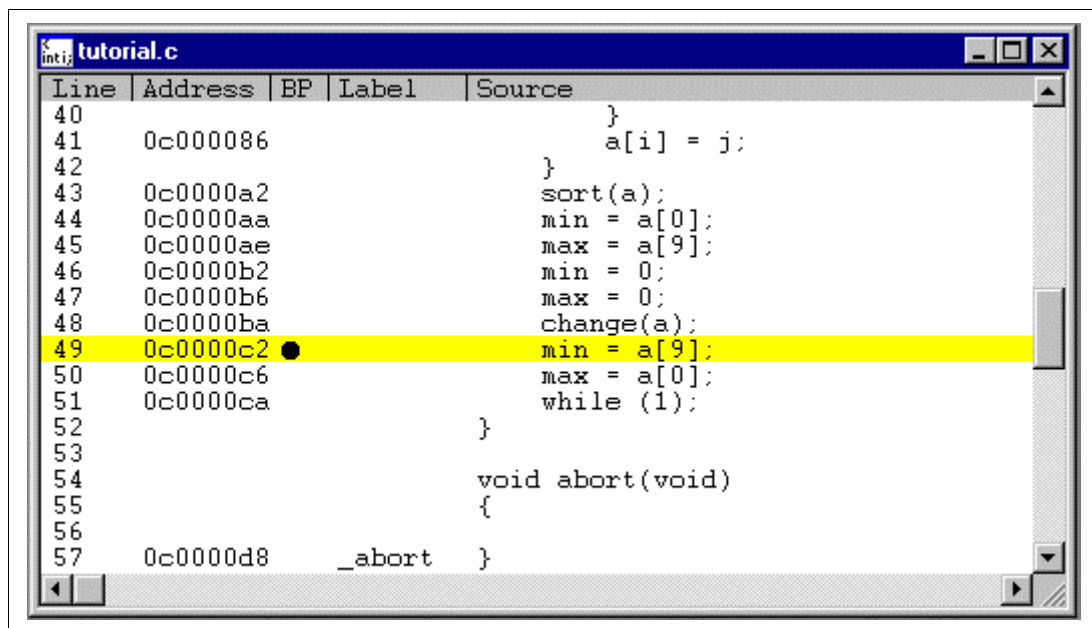
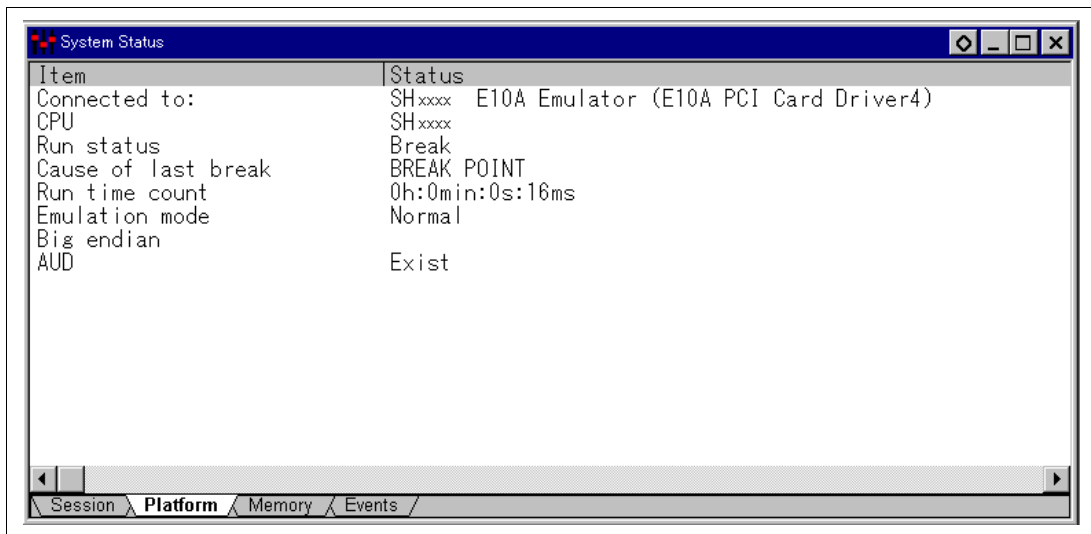


Figure 3.40 [Source] Window at Execution Stop (Software Break)

The [System Status] window displays the following contents.



The screenshot shows a window titled "System Status" with a blue header bar. Inside, there is a table with two columns: "Item" and "Status". The table contains the following data:

Item	Status
Connected to:	SHxxx E10A Emulator (E10A PCI Card Driver4)
CPU	SHxxx
Run status	Break
Cause of last break	BREAK POINT
Run time count	0h:0min:0s:16ms
Emulation mode	Normal
Big endian	
AUD	Exist

At the bottom of the window, there is a tabbed interface with four tabs: "Session", "Platform", "Memory", and "Events". The "Platform" tab is currently selected and highlighted.

Figure 3.41 Displayed Contents of the [System Status] Window (Software Break)

Note: The items that can be displayed in this window differ according to the product. For the items that can be displayed, refer to the online help.

3.18 Hardware Break Function

A method is given below in which the address bus condition and the read cycles for the bus status condition are set under Break Condition 1 as hardware break conditions.

- Select [Breakpoint Window] from the [View] menu. The [Breakpoints] window is displayed.
- Click the [Breakpoints] window with the right mouse button and select [Delete All] from the pop-up menu to cancel all breakpoints that have been set.
- Click the [Breakpoints] window with the right mouse button and select [Add] from the pop-up menu.

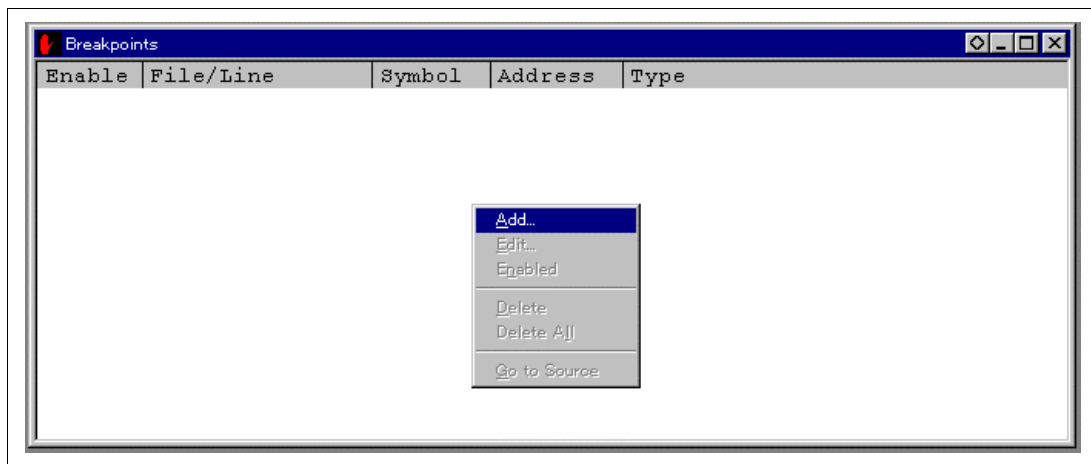


Figure 3.42 [Breakpoints] Window (Before Hardware Break Condition Setting)

The [Break] dialog box is displayed. To set hardware break conditions, select [Condition] in the [Break] dialog box to display the [Condition] page.

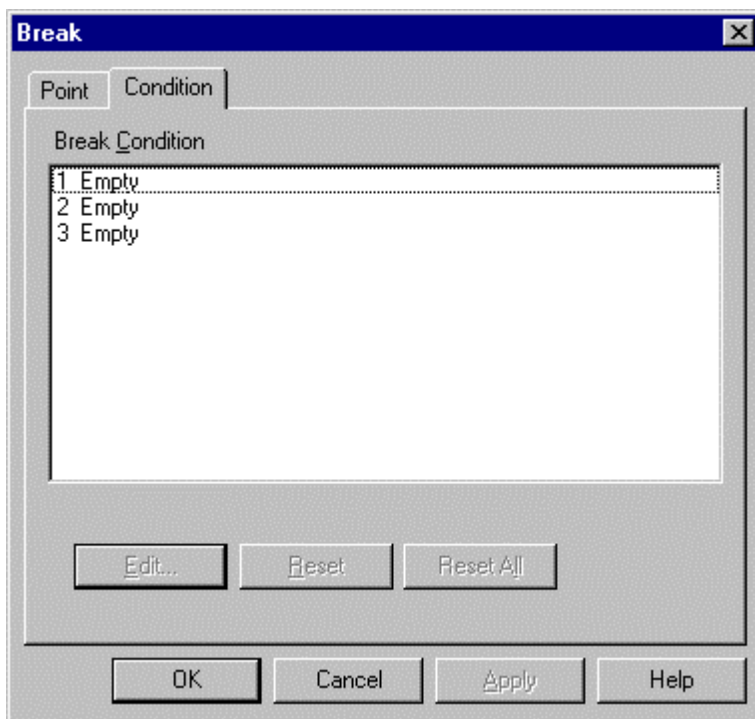


Figure 3.43 [Condition] Page ([Break] Dialog Box)

Up to three breakpoints can be set independently for the Break Condition hardware break condition. In this example, set the hardware break condition for Break Condition 1.

Note: The number of hardware break conditions differs according to the product. For the number that can be specified for each product, refer to the online help.

- Highlight the first point in the [Break Condition] list box.
- Click the [Edit...] button. The [Break Condition 1] dialog box is displayed.

- Clear the [Don't Care] check box in the [Address] page.
- Select the [Address] radio button and enter **H'0c0000b2** as the value in the [Address] edit box.

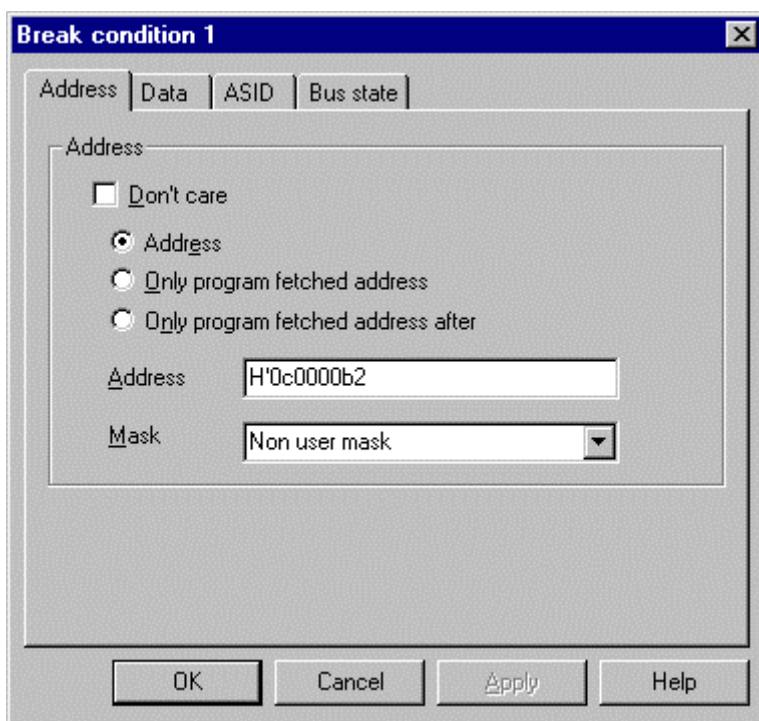


Figure 3.44 [Address] Page ([Break Condition 1] Dialog Box)

Note: The items that can be set in this window differ according to the product. For the settings for each product, refer to the online help.

- Select [Bus State] to display the [Bus State] page.
- Select the [Read] radio button in the [Read/Write] group box.

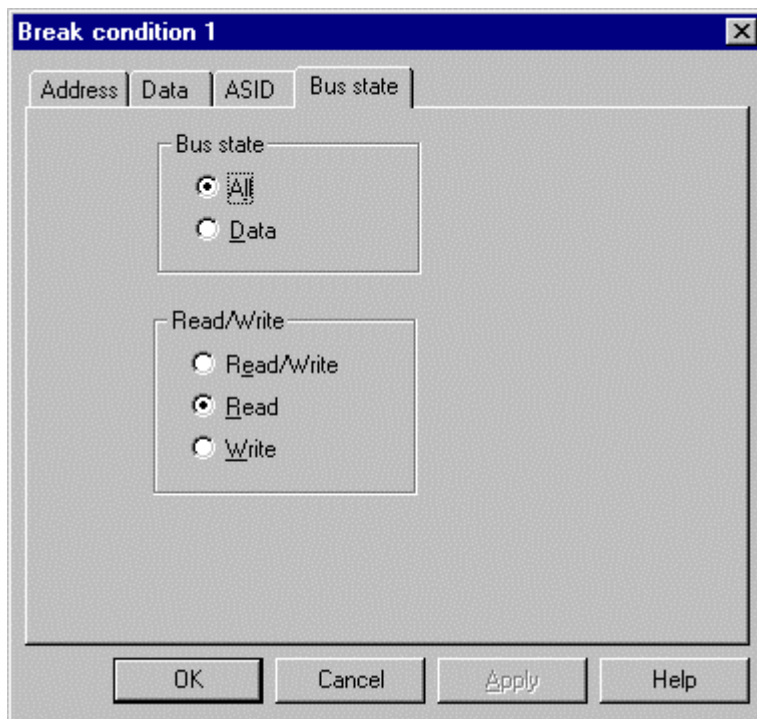


Figure 3.45 [Bus State] Page ([Break Condition 1] Dialog Box)

Note: The items that can be set in this window differ according to the product. For the settings for each product, refer to the online help.

- Click the [OK] button.
- The [Break] dialog box is displayed, and the first point display in the [Break Condition] list box changes from Empty to Enable.

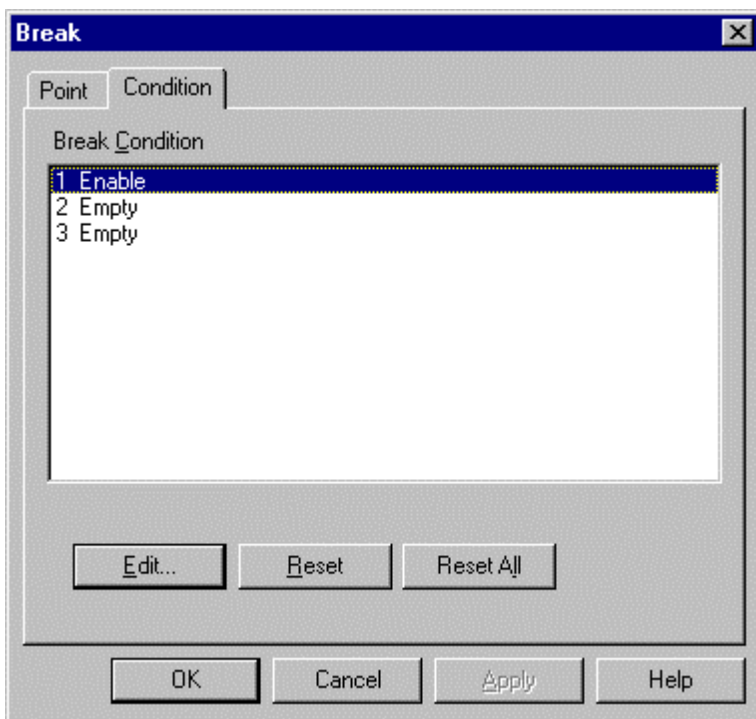


Figure 3.46 [Break] Dialog Box (After Hardware Break Condition Setting)

Note: The number of hardware break conditions differs according to the product. For the number that can be specified for each product, refer to the online help.

- Click the [OK] button.

The newly set hardware breakpoint is displayed in the [Breakpoints] window. With this setting, Break Condition 1 is displayed in [Type] in the [Breakpoints] window.

This completes the setting of the Break Condition 1 hardware break condition. When the program is executed, a break will occur when address H'0c0000b2 is accessed in a read cycle.

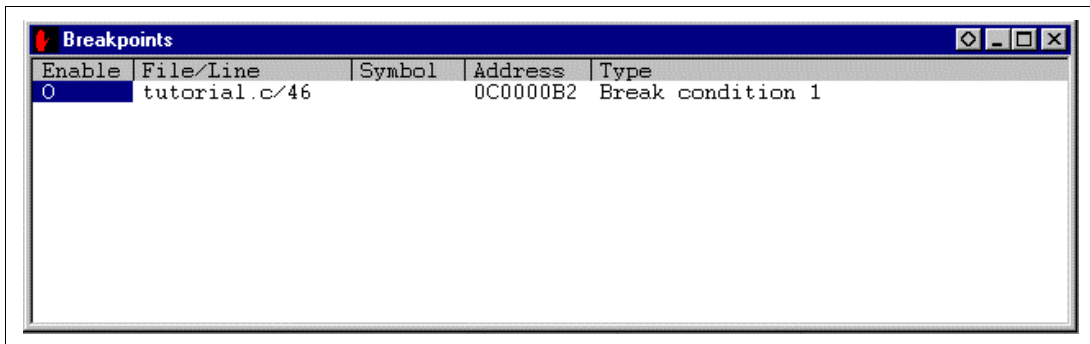


Figure 3.47 [Breakpoints] Window ([Break Condition 1] Setting)

- Close the [Breakpoints] window.
- Set the program counter and stack pointer values (PC = H'0c00006c and R15 = H'0c000c00) that have been set in section 3.9, Setting Registers, in the [Registers] window. Click the [Go] button.

The program runs then stops at the condition specified under Break Condition 1.

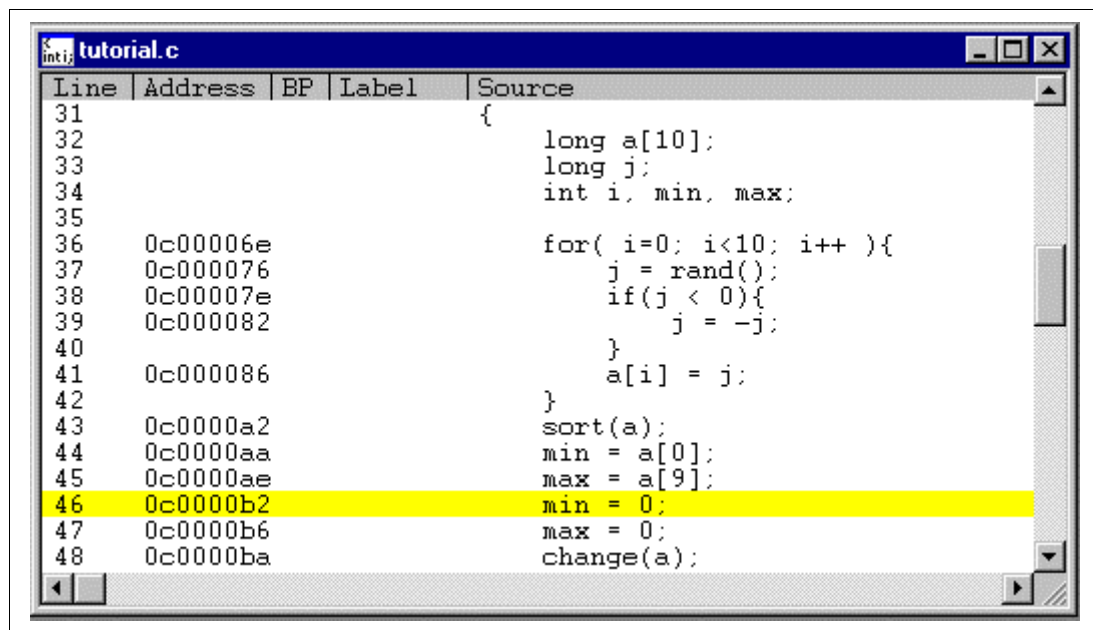


Figure 3.48 [Source] Window at Execution Stop (Break Condition 1)

The [System Status] window displays the following contents.

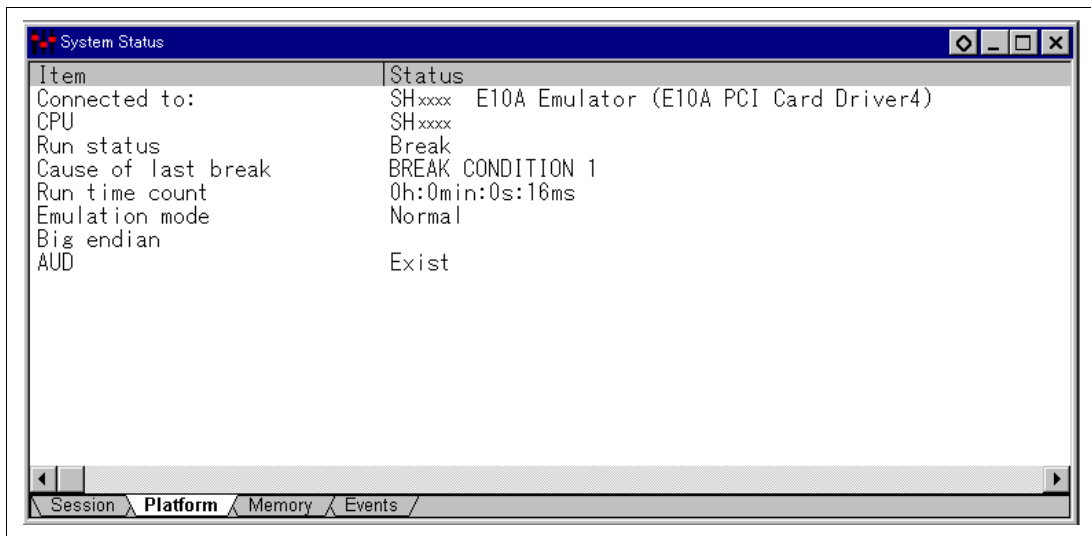


Figure 3.49 Displayed Contents of the [System Status] Window (Break Condition 1)

Note: The items that can be displayed in this window differ according to the product. For the items that can be displayed, refer to the online help.

3.18.1 Setting the Sequential Break Condition

The emulator has sequential break functions. When the hardware break conditions listed in table 3.3 are satisfied, program execution is halted. This mode is called sequential break.

Table 3.3 Sequential Break Conditions

Break Condition	Description
Sequential break condition 2-1	Program is halted when Break Condition 2 and Break Condition 1 are satisfied in that order.

Sequential break condition 2-1 is described below as an example.

Before executing the program, change setting in the [Configuration] dialog box. Otherwise, the sequential break does not function.

- Select [Configure Platform...] from the [Setup] menu. The [Configuration] dialog box is displayed.
- Select Sequential break condition 2-1 from the [Emulation mode] combo box.

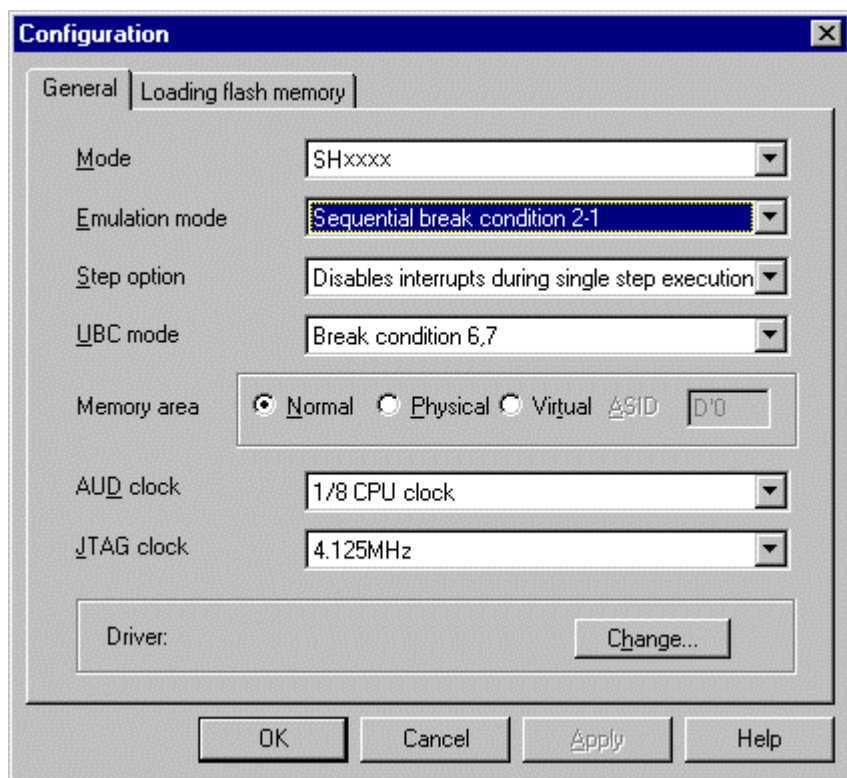


Figure 3.50 [Configuration] Dialog Box (Sequential Break Setting)

Note: The items that can be set in this window differ according to the product. For the settings for each product, refer to the online help.

Click the [OK] button and close the [Configuration] dialog box.

Set hardware break conditions as follows:

Break condition 1: When address H'0c0000c6 is accessed in a read cycle, a break condition is satisfied.

Break condition 2: When address H'0c0000b2 is accessed in a read cycle, a break condition is satisfied.

Follow the setting method described in the previous section.

- When Break Condition 1,2 setting has been completed, the state of the [Breakpoints] window is as follows.

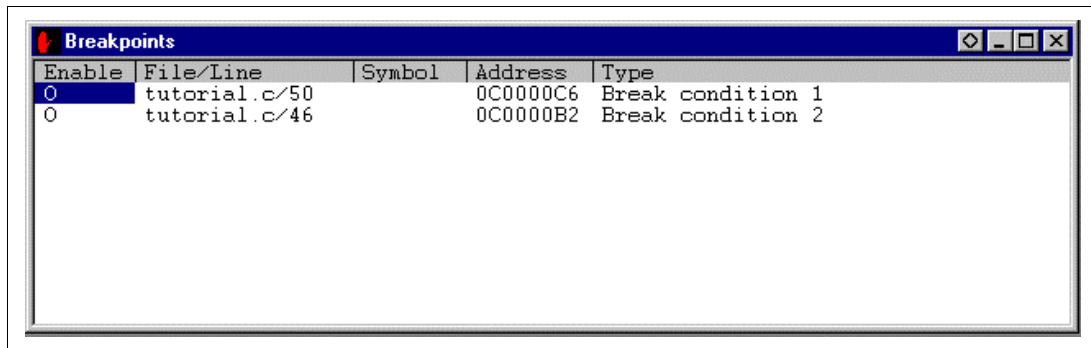


Figure 3.51 [Breakpoints] Window (After Sequential Break Condition Setting)

- Close the [Breakpoints] window.
- Set the program counter and stack pointer values (PC = H'0c00006c and R15 = H'0c000c00) that have been set in section 3.9, Setting Registers, in the [Registers] window. Click the [Go] button.

The program runs then stops at the condition specified under Break Condition 1.

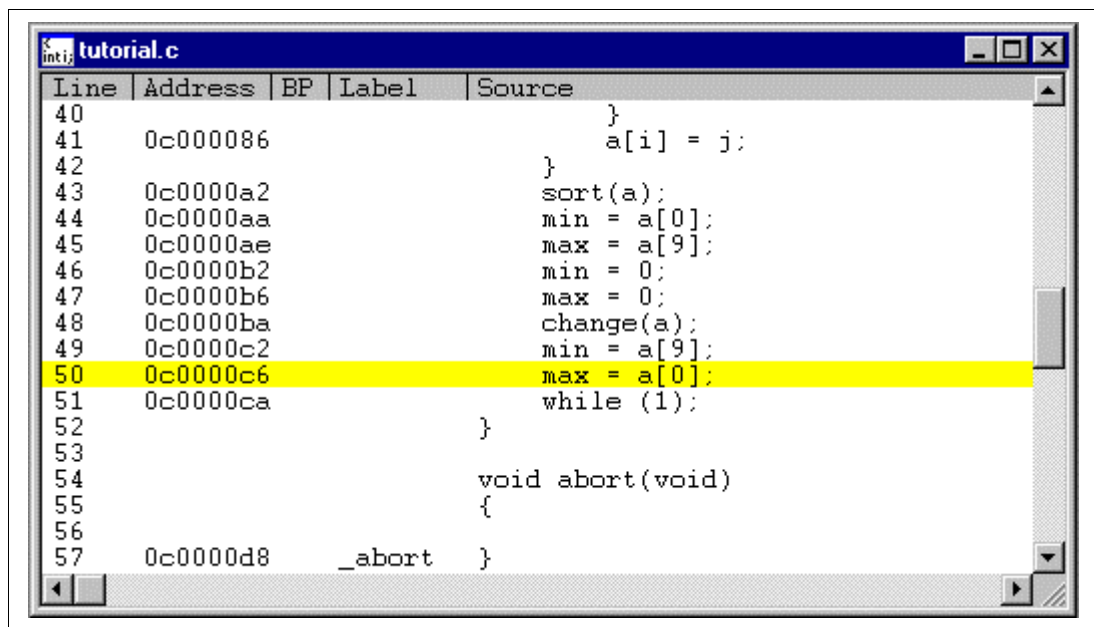


Figure 3.52 [Source] Window at Execution Stop (Sequential Break)

The [System Status] window displays the following contents.

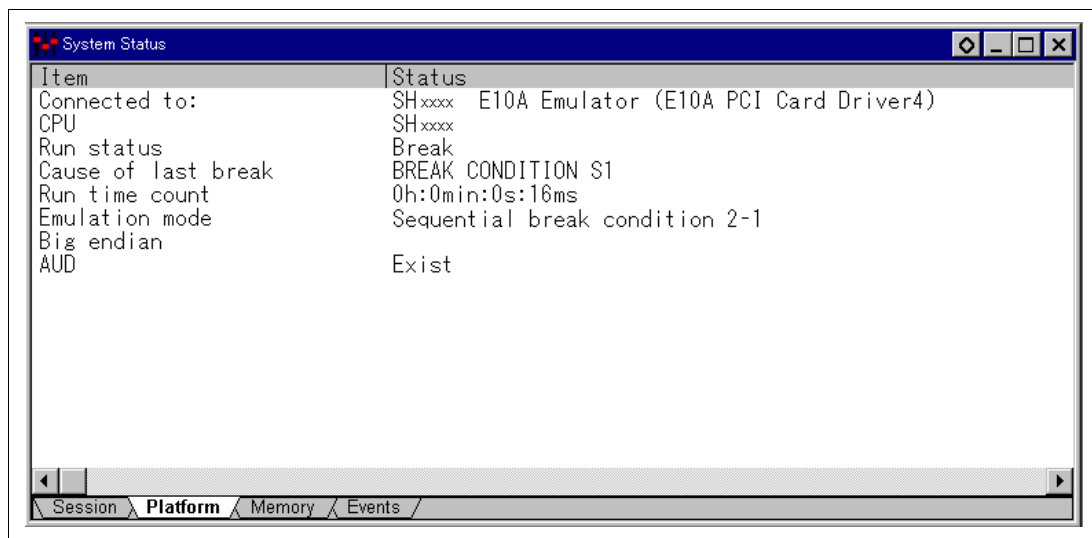


Figure 3.53 Displayed Contents of the [System Status] Window (Sequential Break)

Note: The items that can be displayed in this window differ according to the product. For the items that can be displayed, refer to the online help.

3.19 Trace Functions

The E10A emulator has two branch-instruction trace functions.

(1) Internal Trace Function

The branch source and branch destination addresses, mnemonics, operands, and source lines are displayed. Since this function uses the trace buffer built into the MCU, a realtime trace can be acquired.

- Notes:
1. The number of branch instructions that can be acquired by a trace differs according to the product. For the number that can be specified for each product, refer to the online help.
 2. The internal trace function is not supported for all products. For the specifications of each product, refer to the section related to the trace functions in section 6, SHxxxx E10A Emulator Specifications, or to the online help.
 3. The internal trace function is not extended for all products. For the specifications of each product, refer to the section related to the trace functions in section 6, SHxxxx E10A Emulator Specifications, or to the online help.

(2) AUD Trace Function

This is the large-capacity trace function that is enabled when the AUD pin is connected to the emulator. This function displays the branch source and branch destination addresses, mnemonics, operands, and source lines.

When the branch source and branch destination instructions are one branch, the number of branch instructions acquired by a trace is a maximum of 4,096 in the PCMCIA-type emulator and a maximum of 16,384 in the PCI-type emulator.

Table 3.4 shows the AUD trace function.

- Notes:
1. The AUD trace function is not supported for all products. For the specifications of each product, refer to the section related to the trace functions in section 6, SHxxxx E10A Emulator Specifications, or to the online help.
 2. The AUD trace function is not extended for all products. For the specifications of each product or the number of acquired branches, refer to the section related to the trace functions in section 6, SHxxxx E10A Emulator Specifications, or to the online help.

Table 3.4 AUD Trace Functions

Type	Mode	Description
Acquisition mode when branches continuously occur	Realtime trace	When the next branch occurs while the trace information is being output, the output is stopped and the next trace information is output. The user program can be executed in realtime, but some trace information will not be output.
	Non realtime trace	When the next branch occurs while the trace information is being output, the CPU stops operations until the information is output. The user program is not executed in realtime.
Acquisition mode when the trace buffer of the emulator becomes full	Trace continue	This function always overwrites the oldest trace information to acquire the latest trace information.
	Trace stop	The trace information is not acquired. The user program is continuously executed.

3.19.1 Internal Trace Function

The branch source and branch destination information for the latest several branch instructions are displayed.

The following is a procedure to set the internal trace function (this function is not needed to be set in the emulator that does not support the AUD trace function):

1. Select [Trace] from the [View] menu.
2. Click the [Trace] window with the right mouse button and select [Acquisition] from the pop-up menu to display the [Trace Acquisition] window.
3. Select the [Internal trace] radio button in the [Trace type] group box.

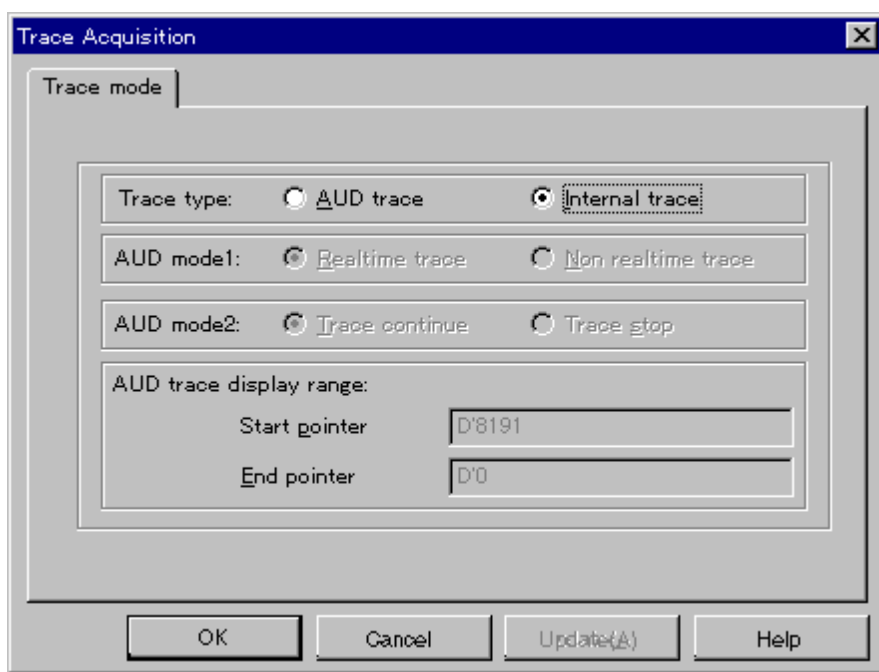


Figure 3.54 [Trace mode] Window

Note: The items that can be set in this window differ according to the product. For the settings for each product, refer to the online help.

Run the program as shown in the example of section 3.17.1, Software Break Function. The trace results are displayed in the [Trace] window after the program execution is completed.

Trace - 16 records (no filter)						
No.	IP	TYPE	ADDR	DATA	MNEMONIC	Source
-000015	-D'000007	BRANCH	0C0001F4	*****	BF @H'C0001D0:8	
-000014		DESTIN	0C0001D0	*****	MOV.L @R15,R1	a[i] = tmp[9 -
-000013	-D'000006	BRANCH	0C0001F4	*****	BF @H'C0001D0:8	
-000012		DESTIN	0C0001D0	*****	MOV.L @R15,R1	a[i] = tmp[9 -
-000011	-D'000005	BRANCH	0C0001F4	*****	BF @H'C0001D0:8	
-000010		DESTIN	0C0001D0	*****	MOV.L @R15,R1	a[i] = tmp[9 -
-000009	-D'000004	BRANCH	0C0001F4	*****	BF @H'C0001D0:8	
-000008		DESTIN	0C0001D0	*****	MOV.L @R15,R1	a[i] = tmp[9 -
-000007	-D'000003	BRANCH	0C0001F4	*****	BF @H'C0001D0:8	
-000006		DESTIN	0C0001D0	*****	MOV.L @R15,R1	a[i] = tmp[9 -
-000005	-D'000002	BRANCH	0C0001F4	*****	BF @H'C0001D0:8	
-000004		DESTIN	0C0001D0	*****	MOV.L @R15,R1	a[i] = tmp[9 -
-000003	-D'000001	BRANCH	0C0001F4	*****	BF @H'C0001D0:8	
-000002		DESTIN	0C0001D0	*****	MOV.L @R15,R1	a[i] = tmp[9 -
-000001	-D'000000	BRANCH	0C0001F8	*****	RTS	
+000000		DESTIN	0C0000C2	*****	MOV.L @(H'34:4,R15),R2	min = a[9];

Figure 3.55 [Trace] Window

- If necessary, adjust the column width by dragging the header bar immediately below the title bar.

Note: The number of branch instructions that can be acquired by a trace differs according to the product. For the number that can be specified for each product, refer to the online help.

3.19.2 AUD Trace Function

This function is operational when the AUD pin of the MCU is connected to the emulator.

The following is the procedure for setting the AUD trace function (this function does not need to be set in an emulator that does not support the internal trace function):

1. Select [Trace] from the [View] menu.
2. Click the [Trace] window with the right mouse button and select [Acquisition] from the pop-up menu to display the [Trace Acquisition] window.
3. Select the [AUD trace] radio button in the [Trace type] group box.

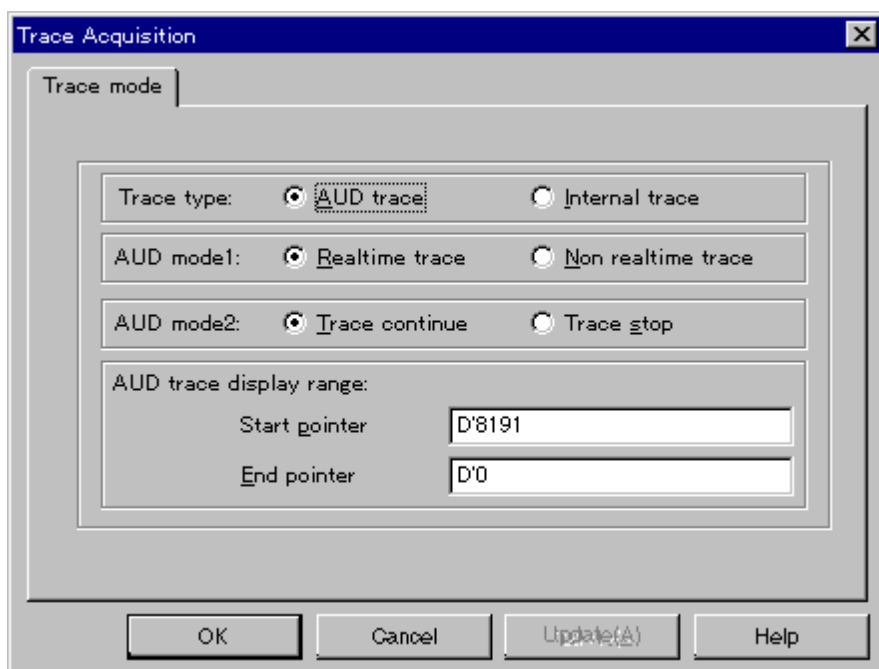


Figure 3.56 [Trace mode] Window

Note: For a description of each option, refer to table 3.4.

Note: The items that can be set in this window differ according to the product. For the settings for each product, refer to the online help.

The trace results are displayed in the [Trace] window after the program execution is completed. The display specifications in the [Trace] window are the same as the internal trace function.

The following is an example of the display in the SH7751 E10A emulator.

Trace - 1409 records (no filter)							
No.	IP	TYPE	ADDR	DATA	MNEMONIC	OPERAND	Source
-000038	-D' 000034	MEMORY	0C000BC4	0C000BD8			
-000037	-D' 000033	MEMORY	0C000B98	00000006			
-000036	-D' 000032	MEMORY	0C000BA8	00003BE6			
-000035	-D' 000031	MEMORY	0C000BF0	00003BE6			
-000034	-D' 000030	MEMORY	0C000B98	00000006			
-000033	-D' 000029	MEMORY	0C000B98	00000007			
-000032	-D' 000028	MEMORY	0C000B98	00000007			
-000031	-D' 000027	BRANCH	0C000210	*****	BF	@H'C0001EC:8	
-000030		DESTINATION	0C0001EC	*****	MOV.L	@R15,R1	a[i] = tmp[9]
-000029	-D' 000026	MEMORY	0C000B98	00000007			
-000028	-D' 000025	MEMORY	0C000BC4	0C000BD8			
-000027	-D' 000024	MEMORY	0C000B98	00000007			
-000026	-D' 000023	MEMORY	0C000BA4	00002A14			
-000025	-D' 000022	MEMORY	0C000BF4	00002A14			
-000024	-D' 000021	MEMORY	0C000B98	00000007			
-000023	-D' 000020	MEMORY	0C000B98	00000008			
-000022	-D' 000019	MEMORY	0C000B98	00000008			
-000021	-D' 000018	BRANCH	0C000210	*****	BF	@H'C0001EC:8	
-000020		DESTINATION	0C0001EC	*****	MOV.L	@R15,R1	a[i] = tmp[9]
-000019	-D' 000017	MEMORY	0C000B98	00000008			
-000018	-D' 000016	MEMORY	0C000BC4	0C000BD8			
-000017	-D' 000015	MEMORY	0C000B98	00000008			
-000016	-D' 000014	MEMORY	0C000BA0	000006CC			
-000015	-D' 000013	MEMORY	0C000BF8	000006CC			
-000014	-D' 000012	MEMORY	0C000B98	00000008			
-000013	-D' 000011	MEMORY	0C000B98	00000009			
-000012	-D' 000010	MEMORY	0C000B98	00000009			
-000011	-D' 000009	BRANCH	0C000210	*****	BF	@H'C0001EC:8	
-000010		DESTINATION	0C0001EC	*****	MOV.L	@R15,R1	a[i] = tmp[9]
-000009	-D' 000008	MEMORY	0C000B98	00000009			
-000008	-D' 000007	MEMORY	0C000BC4	0C000BD8			
-000007	-D' 000006	MEMORY	0C000B98	00000009			
-000006	-D' 000005	MEMORY	0C000B9C	00000063			
-000005	-D' 000004	MEMORY	0C000BFC	00000063			
-000004	-D' 000003	MEMORY	0C000B98	00000009			
-000003	-D' 000002	MEMORY	0C000B98	0000000A			
-000002	-D' 000001	MEMORY	0C000B98	0000000A			
-000001	-D' 000000	BRANCH	0C000214	*****	RTS		
+000000		DESTINATION	0C0000DA	*****	MOV.L	@(H'34:4,R15),R2	min = a[9];

Figure 3.57 [Trace] Window in the SH7751 E10A Emulator

3.19.3 VP_MAP Translation

The MCU, which has an MMU, translates internal addresses (virtual addresses) to actual memory addresses (physical addresses). Address translation is performed according to the address translation table (translation look-aside buffer: TLB) in the MCU. The MMU operates during command input wait state as well as during user program execution. When a command for memory access is executed while the MMU address translation function is enabled, the address translated by the MMU is accessed. If the specified address is not within the TLB, a TLB miss occurs, and the TLB must be updated by the user program.

The emulator has address translation functions according to the VP_MAP tables. The VP_MAP tables are the address translation tables for the emulator created with the VP_MAP_SET command.

The following shows an example of how to use the VP_MAP tables.

Example:

1. Create VP_MAP tables for translating virtual addresses H'10000 to H'10FFF to physical addresses H'4000000 to H'4000FFF and virtual addresses H'11000 to H'11FFF to physical addresses H'0 to H'FFF.

```
>vs 10000 10FFF 4000000 (RET)
>vs 11000 11FFF 0 (RET)
>vd (RET)
<VADDR_TOP>   <VADDR_END>   <PADDR_TOP>
00010000      00010FFF   04000000
00011000      00011FFF   00000000
DISABLE
```

2. Then, enable the VP_MAP tables. (When the tables are disabled, addresses are not translated.)

```
>ve ;enable (RET)
>vd (RET)
<VADDR_TOP>   <VADDR_END>   <PADDR_TOP>
00010000      00010FFF   04000000
00011000      00011FFF   00000000
ENABLE
```

Here, virtual addresses correspond to physical addresses as shown in figure 3.58.

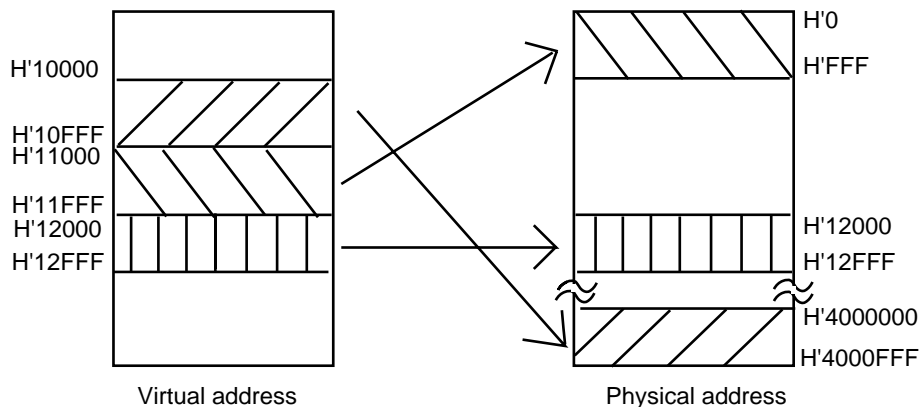


Figure 3.58 Address Translation according to VP_MAP Tables

How to translate addresses depends on the settings of the radio buttons of the memory area group in the [Configuration] dialog box. The following shows how to translate addresses in each setting state.

- When the Normal radio button is selected:
The VP_MAP table has a priority over the TLB. When the VP_MAP table is enabled and the specified address is within the VP_MAP table settings, the emulator translates the address according to the VP_MAP table. If the specified address is outside the VP_MAP table settings even when the VP_MAP table is enabled, or when the VP_MAP table is disabled, the emulator translates the address according to the MMU state.
- When the Virtual radio button is selected:
The address is translated according to the TLB. If the specified address is outside the TLB table settings, a TLB error will occur.
- When the Physical radio button is selected:
The address is not translated.

Table 3.5 Address Translation Tables

	VP_MAP	MMU			
Radio Button*	Enabled/ Disabled	Within/ Outside the range	Enabled/ Disabled	Within/Outside the TLB Range	Table Used for Translation
Normal	Enabled	Within the Range	Enabled	Within the Range	Translated according to the VP_MAP table
				Outside the range	Translated according to the VP_MAP table
			Disabled	Within/outside the range	Translated according to the VP_MAP table
		Outside the Range	Enabled	Within the Range	Translated according to the TLB table
				Outside the range	TLB error
			Disabled	Within/outside the range	Not translated
	Disabled	Within/ outside the range	Enabled	Within the Range	Translated according to the TLB table
				Outside the range	TLB error
			Disabled	Within/outside the range	Not translated
Virtual	Enabled/ disabled	Within/ outside the range	Enabled	Within the Range	Translated according to the TLB table
				Outside the range	TLB error
			Disabled	Within the Range	Translated according to the TLB table
				Outside the range	TLB error
Physical	Enabled/ disabled	Within/ outside the range	Enabled/ disabled	Within/outside the range	Not translated

Note: Specified by the [Memory area] group box in the [Configuration] dialog box.

3.20 Stack Trace Function

The emulator uses the stack's information to display the name of the calling function for a function at which the program counter is currently pointing.

- Notes:
1. This function can be used only when the load module that has the Dwarf2-type debugging information is loaded.
 2. For details on the stack trace function, refer to the online help.
- Double-click the [BP] column in the `sort` function and set a software breakpoint.

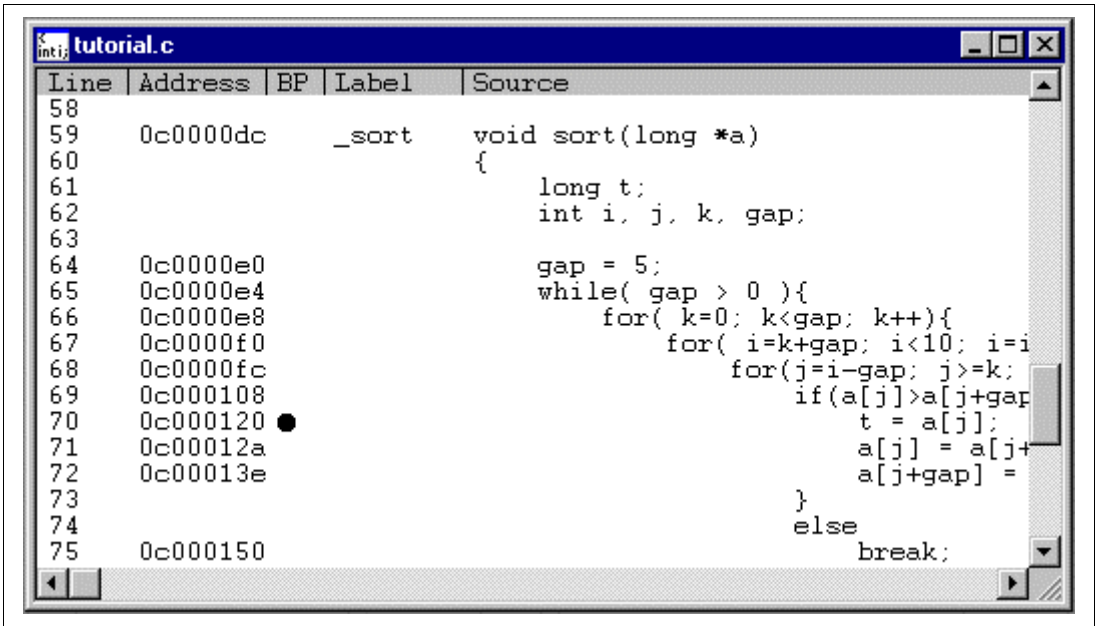


Figure 3.59 [Source] Window (Software Breakpoint Setting)

- Set the same program counter and stack pointer values (PC = H'0c00006c and R15 = H'0c000c00) as were set in section 3.9, Setting Registers (again, use the [Registers] window). Click the [Go] button.
- After the break in execution, select [Stack Trace] from the [View] menu to open the [Stack Trace] window.

Stack Trace		
Kind	Name	Value
F	sort(long*)	{ 0x0c000120 }
F	main()	{ 0x0c0000aa }
F	main()	{ 0x0c0000aa }
F	main()	{ 0x0c0000aa }
F	main()	{ 0x0c0000aa }
F	main()	{ 0x0c0000aa }
F	main()	{ 0x0c0000aa }
F	main()	{ 0x0c0000aa }
F	main()	{ 0x0c0000aa }
F	main()	{ 0x0c0000aa }

Figure 3.60 [Stack Trace] Window

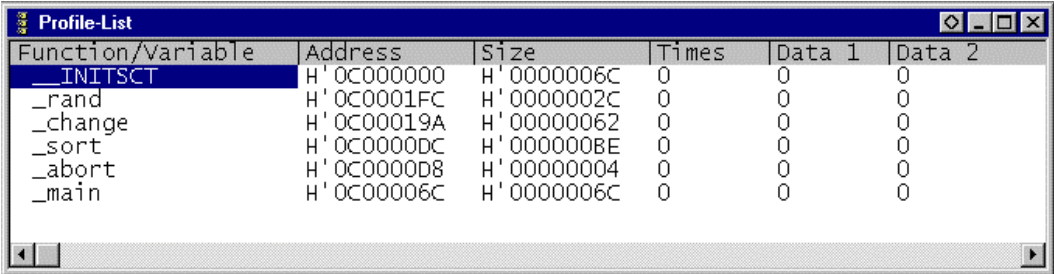
Figure 3.60 shows that the position of the program counter is currently at the selected line of the `sort()` function, and that the `sort()` function is called from the `main()` function.

- Notes:
1. If the function is not deeply nested (lower than 10), the `main()` function will be displayed multiple times.
 2. For details on this function, refer to the online help. The online help is opened when the [F1] key is pressed in the [Stack Trace] window.

3.21 Profiling Function

The profile function can measure performance for each function.

- Notes:
1. Realtime operation is not possible while this function is in operation, since internal breaks are generated during program execution. Measuring the profile itself affects the measurements. For details, refer to section 6, SHxxxx Emulator Specifications.
 2. When this function is to be used, click the [Load stack information file (SNI file)] check box in the [Load Program] dialog box to load the stack information file.
 3. Performance profile measurement is not supported for all products. On those products for which it is supported, its characteristics differ according to the product. For specifications for each product, refer to the section related to the trace functions in section 6, SHxxxx E10A Emulator Specifications, or to the online help file.
 4. For details, refer to section 13 in the Hitachi Debugging Interface User's Manual.
- Select [Profile-List] from the [View] menu to open the [Profile-List] window. A different set of data to be measured can be set for each function.



The screenshot shows a window titled "Profile-List" with a table containing the following data:

Function/Variable	Address	Size	Times	Data 1	Data 2
__INIT\$CT	H'0C000000	H'0000006C	0	0	0
_rand	H'0C0001FC	H'0000002C	0	0	0
_change	H'0C00019A	H'00000062	0	0	0
_sort	H'0C0000DC	H'000000BE	0	0	0
_abort	H'0C0000D8	H'00000004	0	0	0
_main	H'0C00006C	H'0000006C	0	0	0

Figure 3.61 [Profile-List] Window

- The profile function is now enabled. Place the mouse cursor on an entry in the [Profile-List] window, click the right-hand mouse button, then select [Enable Profiler] from the pop-up menu.

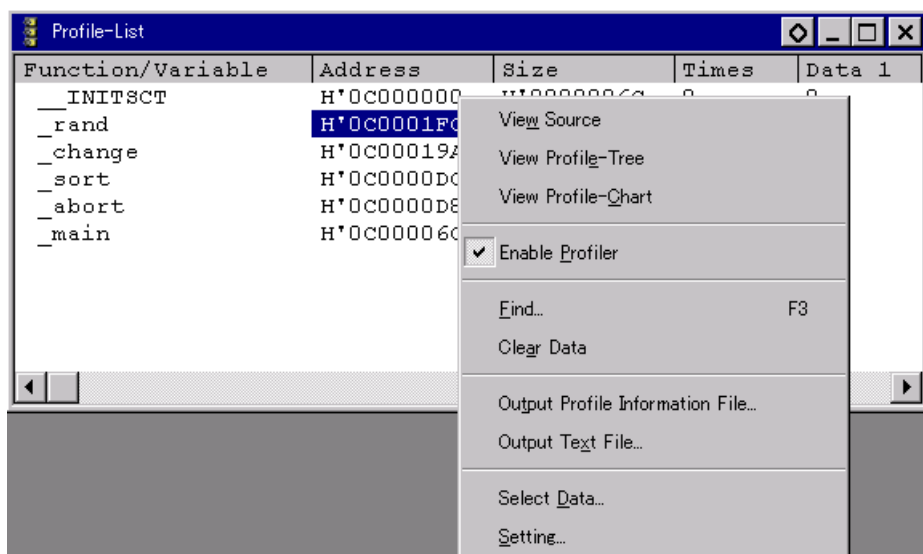


Figure 3.62 Selection of [Enable Profiler]

- Data to be measured for the selected function is now set. Select [Select Data] from the pop-up menu by clicking with the right mouse button. The [Select Data] dialog box is displayed.

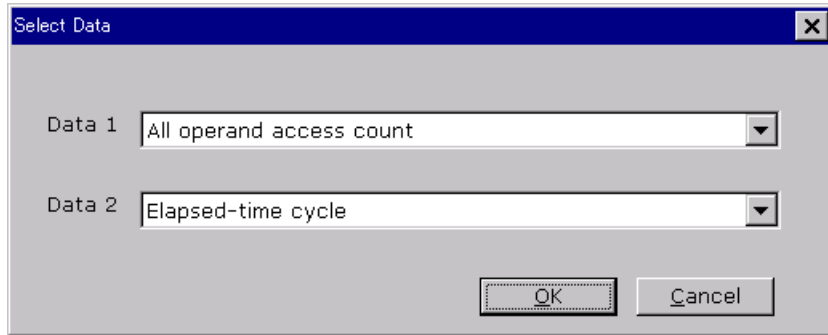


Figure 3.63 [Select Data] Dialog Box

- Use the [Select Data] dialog box to select the data to be measured. [All operand access count] is selected for Data1 as a first item to be measured. [Elapsed-time cycle] is selected for Data2 as a second item to be measured.
- After the data has been selected, press the [OK] button.
- Double-click the [BP] column for the `while` statement of the main function to set a software breakpoint.

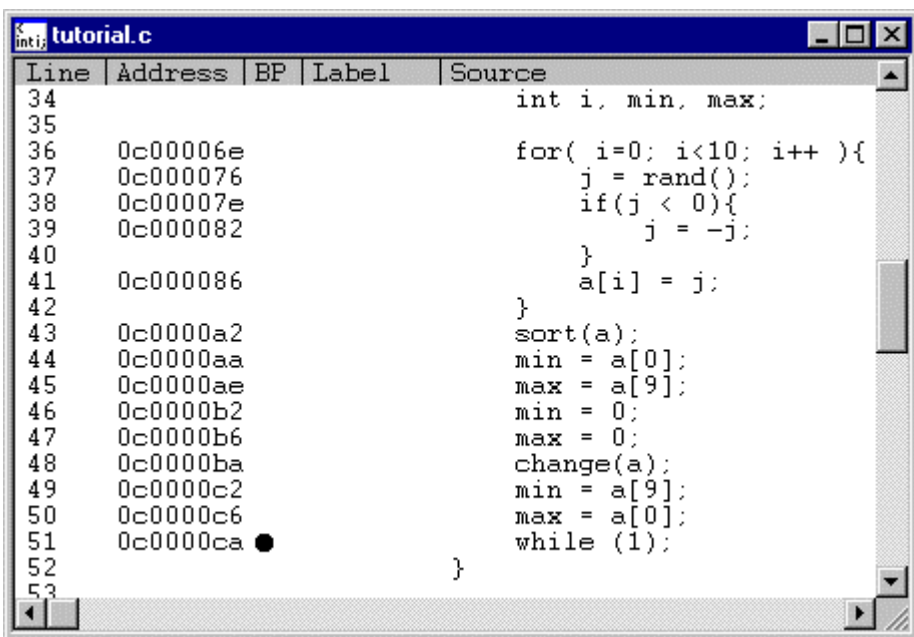


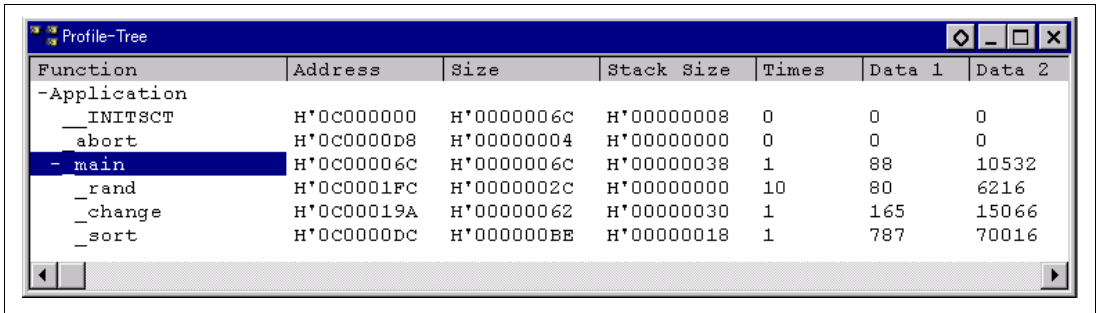
Figure 3.64 [Source] Window (Software Break Setting)

- Set the same program counter and stack pointer values (PC = H'0c00006c and R15 = H'0c000c00) as were set in section 3.9, Setting Registers (again, use the [Registers] window). Click the [Go] button.
- After the break in execution, the results of the measurements are displayed in the [Profile-List] window.

Profile-List					
Function/Variable	Address	Size	Times	Data 1	Data 2
__INIT\$CT	H'0c000000	H'00000006c	0	0	0
_rand	H'0c0001fc	H'00000002c	10	80	6216
_change	H'0c00019a	H'000000062	1	165	15066
sort	H'0c0000dc	H'0000000be	1	787	70016
_abort	H'0c0000d8	H'000000004	0	0	0
_main	H'0c00006c	H'00000006c	1	88	10532

Figure 3.65 [Profile-List] Window

- Figures 3.66 and 3.67 show the [Profile-Tree] and [Profile-Chart] windows, respectively.



The Profile-Tree window displays a table with the following columns: Function, Address, Size, Stack Size, Times, Data 1, and Data 2. The data is as follows:

Function	Address	Size	Stack Size	Times	Data 1	Data 2
-Application						
__INIT8CT	H'0C000000	H'0000006C	H'00000008	0	0	0
abort	H'0C0000D8	H'00000004	H'00000000	0	0	0
- main	H'0C00006C	H'0000006C	H'00000038	1	88	10532
_rand	H'0C0001FC	H'0000002C	H'00000000	10	80	6216
_change	H'0C00019A	H'00000062	H'00000030	1	165	15066
_sort	H'0C0000DC	H'000000BE	H'00000018	1	787	70016

Figure 3.66 [Profile-Tree] Window

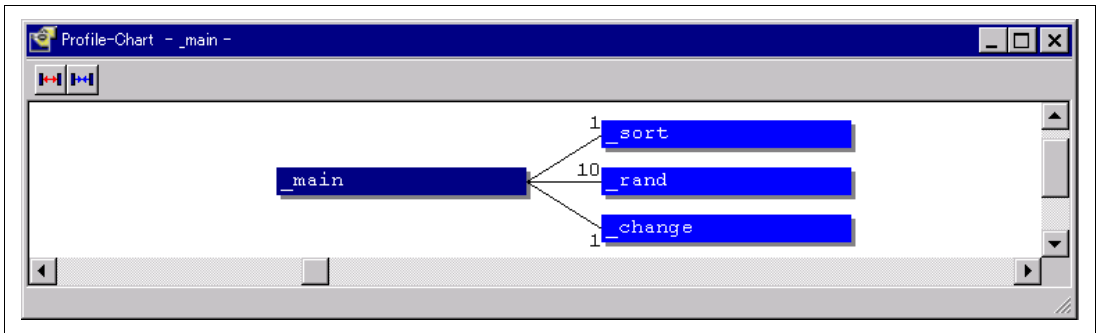


Figure 3.67 [Profile-Chart] Window

3.22 Download Function to the Flash Memory Area

The E10A emulator enables downloading to the flash memory area. This function requires a program for writing the flash memory (hereinafter referred to as a write module), a program for erasing the flash memory (hereinafter referred to as an erase module), and the RAM area for downloading and executing these modules.

Note: The write/erase module must be prepared by users.

— Interface with write/erase module and E10A emulator firmware

The write/erase module is branched from the E10A emulator firmware. To branch from the E10A emulator firmware to the write/erase module or to return from the write/erase module to the E10A emulator firmware, the following conditions must be observed:

- Describe all the write/erase modules with the assembly language.
- Guarantee all the general/control register values before and after calling the write/erase module.
- Return the write/erase module to the calling source after processing.

The module interface must be as follows to pass correctly the information that is required for flash memory accessing.

Table 3.6 Module Interface

Module Name	Argument	Return Value
Write module	R4(L): Write address	R0(L): End code Normal end = 0, Abnormal end = other than 0, Verify error = BT
	R7(L): Verify option 0 = no verify, 1 = verify	
	R5(L): Access size 0x4220 = byte, 0x5720 = word, 0x4C20 = longword	
	R6(L): Write data	
Erase module	R4(L): Access size 0x4220 = byte, 0x5720 = word, 0x4C20 = longword	None

Note: The (L) means the longword size.

Note: Write module:

The write data for the access size is set to the R6 register. When the access size is word or byte, 0 is set to the upper bit of the R6 register.

— Flash memory download method

It is required to perform necessary settings on the [Loading flash memory] page in the [Configuration] window for downloading to the flash memory.

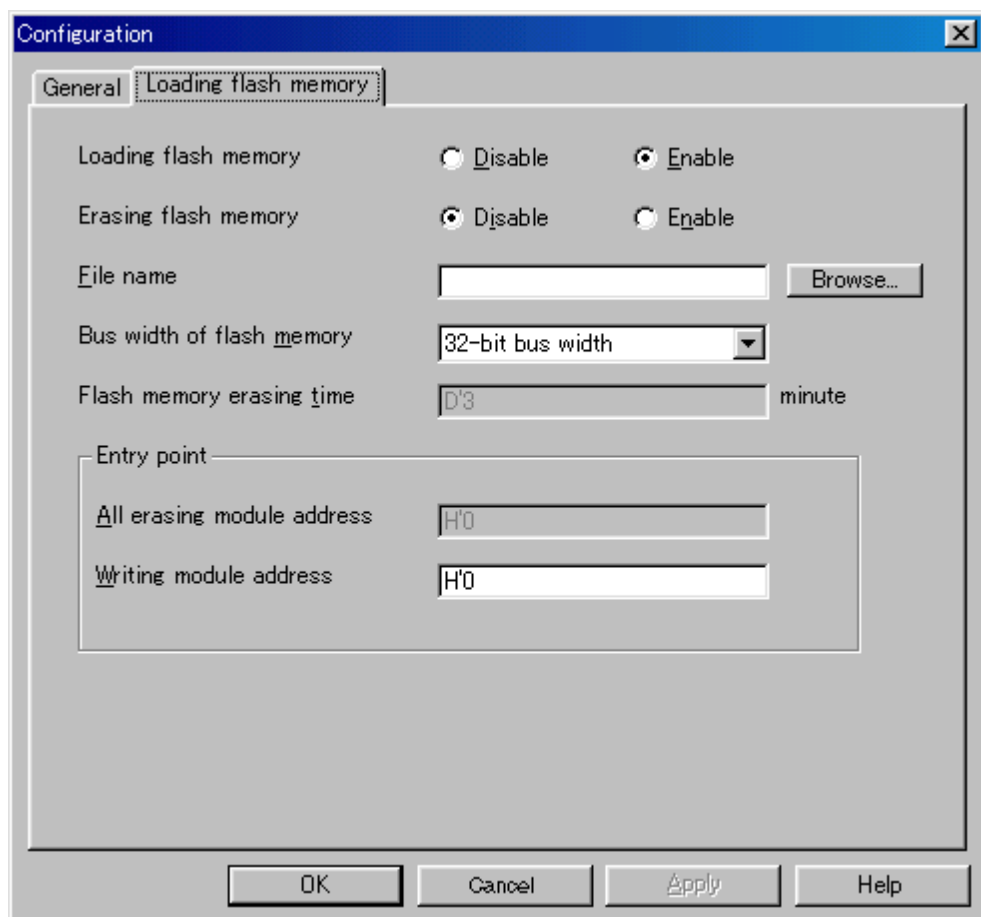


Figure 3.68 [Loading flash memory] Page

Table 3.7 shows the options for the [Loading flash memory] page.

Table 3.7 [Loading flash memory] Page Options

Option	Description
[Loading flash memory] radio button	Sets Enable for flash memory downloading. At Enable, when [File load] is selected from the [File] menu for downloading, the write module is always called. Enable: Download to the flash memory Disable: Not download to the flash memory
[Erasing flash memory] radio button	Sets Enable for erasing before the flash memory is written. At Enable, the erase module is called before calling the write module. Enable: Erase the flash memory Disable: Not erase the flash memory
[File name] edit box	Sets the write/erase module name. The file that has been set is loaded to the RAM area before loading to the flash memory. A maximum of 128 characters can be input for the file name.
[Bus width of flash memory] list box	Sets the bus width of the flash memory.
[Flash memory erasing time] edit box*	Sets the TIMEOUT value at flash memory erasing. Increase the value if erasing requires much time; the default time is three minutes. The radix for the input value is decimal. It becomes hexadecimal by adding H'.
[Entry point] group box	Sets the calling destination address of the write/erase module. [All erasing module address] edit box: Inputs the calling destination address of the erase module. [Writing module address] edit box: Inputs the calling destination address of the write module.

Note: Although the values that can be set are D'0 to D'65535, the TIMEOUT hours may be extended according to the set value. Therefore, it is recommended to input the minimum value. The value to be input must only be positive integer.

— Notes on using the flash memory download function

The following are notes on downloading to the flash memory.

- When the flash memory download is enabled, downloading other than to the flash memory area is disabled.
- Downloading is only enabled to the flash memory area. Perform memory write or software break only to the RAM area.
- When the flash memory erase is enabled, the [Stop] button cannot stop erasing.
- The area for the write/erase module must be MMU-disabled space.
- Only the S-type-formatted file is enabled for the write/erase module.

— Examples of downloading to the flash memory

The following shows examples of downloading to the flash memory manufactured by Intel Corporation (type number: G28F640J5-150) that has been mounted on Hitachi's SH7751 CPU board (type number: HS7751STC01H). A sample is provided in the \Fmtool folder in the installation destination folder. Create the program for user specification according to this sample. The SH7751 E10A emulator must be used when the SH7751 CPU board is used.

Table 3.8 Example of Board Specifications

Item	Contents	
SDRAM address	H'0C000000 to H'0FFFFFFF	
Flash memory address	H'01000000 to H'01FFFFFF	
Bus width of flash memory	32 bits	
Operating environments	CPU internal frequency	167 MHz
	Bus frequency	55.7 MHz
	CPU internal module frequency	27.83 MHz
	Endian	Big endian

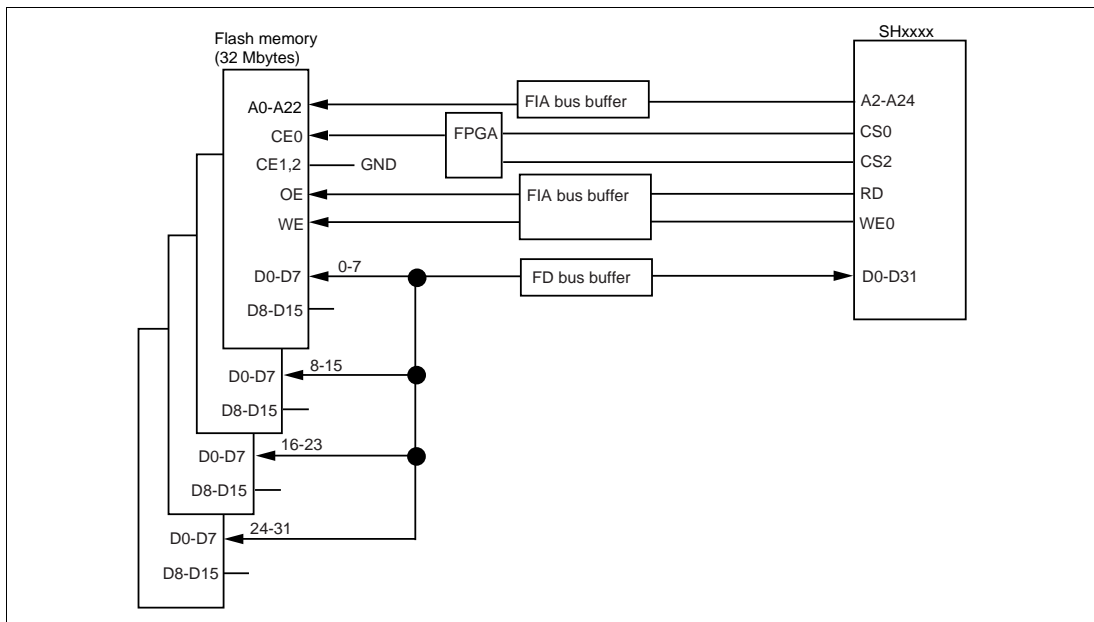
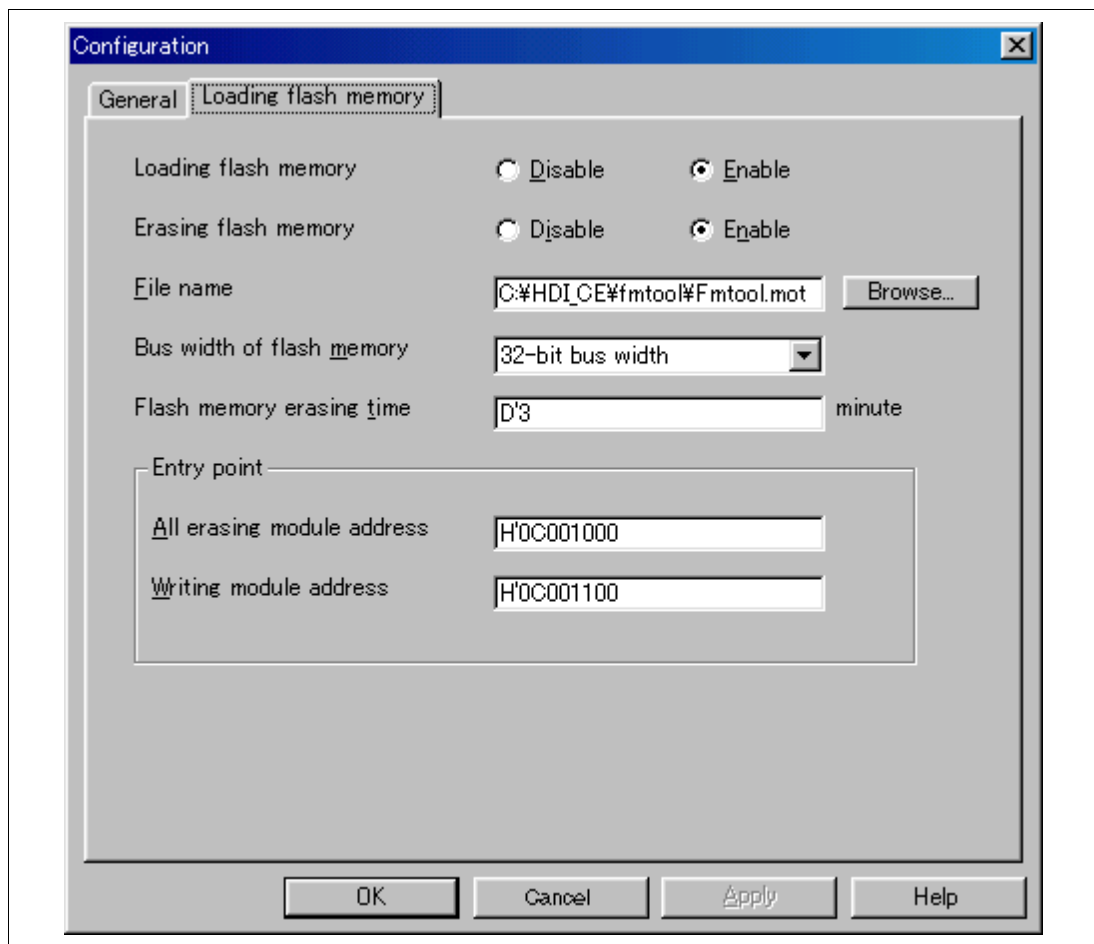


Figure 3.69 Flash Memory Wiring

Table 3.9 Sample Program Specifications

Item	Contents
RAM area to be used	H'0C001000 to H'0C0015BF
Write module start address	H'0C001100
Erase module start address	H'0C001000

- (i) Since the SDRAM is used, the bus controller is set.
- (ii) Options on the [Loading flash memory] page in the [Configuration] window are set as follows:

**Figure 3.70 [Loading flash memory] Page**

- Notes:
1. When the data has already been written in the flash memory, be sure to select [Enable] for [Erasing flash memory]. If [Disable] is selected, a verify error occurs.
 2. When [Erasing flash memory] is selected, it takes about one minute.
- (iii) [Load Program...] is selected from the [File] menu for downloading to the flash memory area.

3.23 What Next?

This tutorial has described the major features of the emulator and the use of the HDI.

Sophisticated debugging can be carried out by using the emulation functions that the emulator offers. This provides for effective investigation of hardware and software problems by accurately isolating and identifying the conditions under which such problems arise.

Further details on the use of the HDI can be found in the separately issued Hitachi Debugging Interface User's Manual.

Section 4 Descriptions of Windows

4.1 HDI Windows

HDI window menu bars and the corresponding pull-down menus are listed in table 4.1. Where a description of a menu is included in the Hitachi Debugging Interface User's Manual or in this manual, a O mark or the relevant section number is shown. Related commands in the E10A Emulator User's Manual are also shown.

Table 4.1 HDI Window Menus and Related Manual Entries

Menu Bar	Pull-Down Menu	Hitachi Debugging Interface User's Manual	This Manual
File menu	New Session...	O	—
	Load Session...	O	—
	Save Session	O	2.6
	Save Session As...	O	—
	Load Program...	O	3.7.1
	Initialize	O	—
	Exit	O	—
Edit Menu	Cut	O	—
	Copy	O	—
	Paste	O	—
	Find...	O	—
	Evaluate...	O	—

Table 4.1 HDI Window Menus and Related Manual Entries (cont)

Menu Bar	Pull-Down Menu	Hitachi Debugging Interface User's Manual	This Manual
View Menu	Breakpoints	O	3.11, 3.17.1, 4.2.4, 6.5.5
	Command Line	O	—
	Disassembly...	O	—
	I/O Area	O	—
	Labels	O	—
	Locals	O	3.16
	Memory...	O	3.12
	Performance Analysis	O	—
	Profile-List	O	3.21
	Profile-tree	O	3.21
	Registers	O	3.9
	Source...	O	3.7.2
	Stack Trace	X	3.20
	Status	O	3.10, 3.17.1, 4.2.9
	Trace	O	4.2.7, 6.5.3, 6.5.7
	Watch	O	3.13
Run Menu	Reset CPU	O	—
	Go	O	3.10
	Reset Go	O	—
	Go to Cursor	O	—
	Set PC To Cursor	O	—
	Run...	O	—
	Step In	O	3.14.1
	Step Over	O	3.14.3
	Step Out	O	3.14.2
	Step...	O	—
	Halt	O	—

Table 4.1 HDI Window Menus and Related Manual Entries (cont)

Menu Bar	Pull-Down Menu	Hitachi Debugging Interface User's Manual	This Manual
Memory Menu	Refresh	O	—
	Load	O	—
	Save	O	—
	Verify	O	—
	Test	O	—
	Fill	O	—
	Copy	O	—
	Compare	O	—
Setup Menu	Status bar	O	—
	Options	O	—
	Radix	O	—
	Customise	O	—
	Configure Platform...	O	3.5, 4.2
Window Menu	Cascade	O	—
	Tile	O	—
	Arrange Icons	O	—
	Close All	O	—
Help Menu	Index	O	—
	Using Help	O	—
	Search for Help on	O	—
	About HDI	O	—

4.2 Descriptions of Each Window

This section describes each window. Figures in this section are used as examples. Each E10A emulator type has explanatory notes. Read section 6, SHxxxx E10A Emulator Specifications.

4.2.1 [Configuration] Dialog Box

Function:

This dialog box sets the emulation conditions of the emulator.

Window:

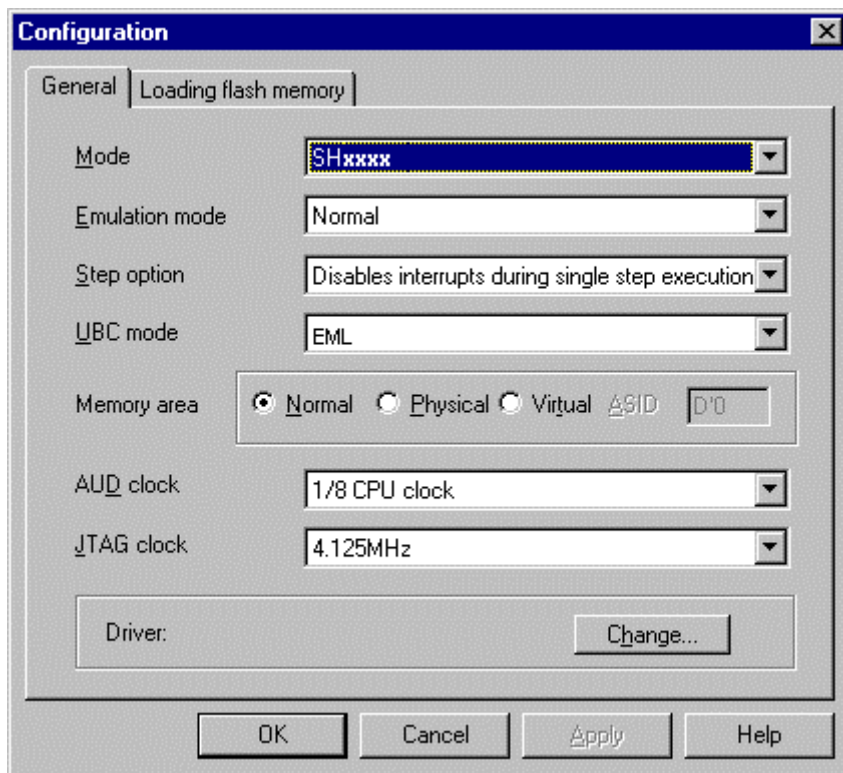


Figure 4.1 [Configuration] Dialog Box

Note: The items that can be set in this window differ according to the product. For the settings for each product, refer to the online help.

Description:

The [Configuration] dialog box consists of the [General] page listed in table 4.2.

Table 4.2 [Configuration] Dialog Box Page

Page Name	Description
[General]	Sets and displays the emulation mode conditions.
[Loading flash memory]	Sets the download function for the flash memory.

Clicking the [OK] button sets the emulation conditions. If the [Cancel] button is clicked, this dialog box is closed without setting the emulation conditions.

(1) [General] Page ([Configuration] Dialog Box)

Function:

This page sets the emulator operation conditions, displays the device name, sets the emulation mode, UBC mode, and memory area (only for a product that supports a device with the MMU function), sets and displays the AUD clock (AUDCK) and JTAG clock (TCK), and selects the driver.

Window:

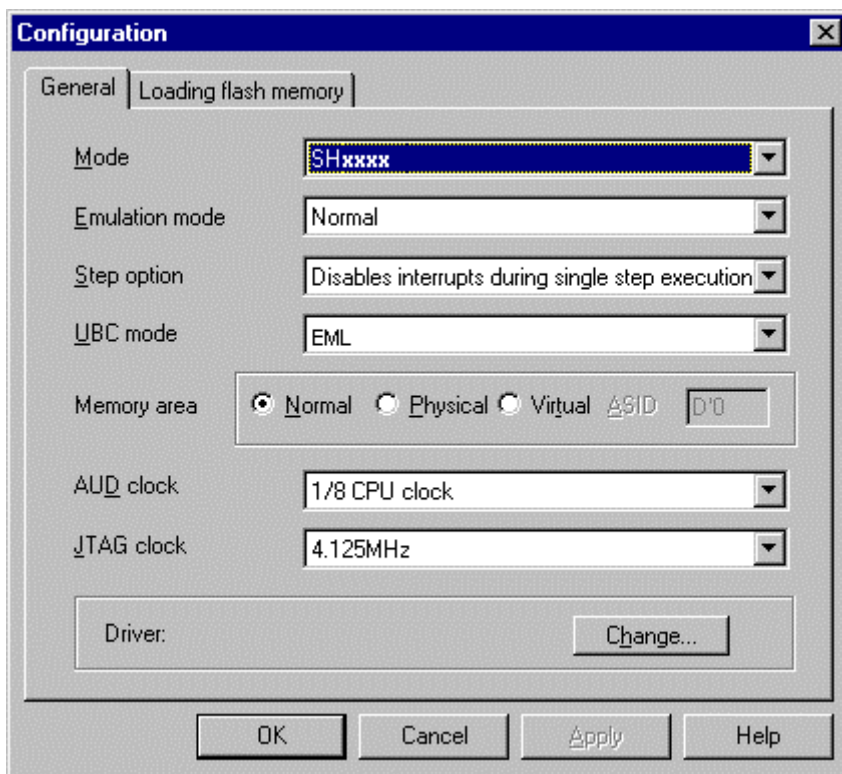


Figure 4.2 [General] Page ([Configuration] Dialog Box)

Note: The items and displayed contents that can be set in this window differ according to the product. For the settings for each product, refer to the online help.

Description:

Table 4.3 [General] Page Options

Option	Description
[Mode] combo box	Displays the device name.
[Emulation mode] combo box	Selects the execution mode. Select Normal to perform normal emulation. Select No Break to disable breakpoint settings. Select Sequential break Condition 2-1, etc. to use the sequential break function ¹ . (For Sequential break Condition 2-1, execution stops when conditions are satisfied in the order of Break Condition 2 and Break Condition 1.)
[Step option] combo box	Enables or disables interrupts during step execution. Disables interrupts during single step execution: Interrupts during step execution are masked. Enables interrupts during single step execution: Interrupts during step execution are released.
[UBC mode] combo box	EML: The UBC is used as a Break Condition by the emulator. USER: The UBC is released for users. In this case, the [Break Condition] page becomes non-active.
[Memory area] group box	Sets the address setting mode to the memory area. The default is Normal. When the VP_MAP is enabled and the address is within the table range, address translation is done according to the VP_MAP table. For other cases, address translation is done according to the MMU state. Select Physical when setting with a physical address. Select Virtual when address translation is done by the TLB table.
[AUD clock] combo box	Selects the AUD clock ² .
[JTAG clock] combo box	Sets the JTAG frequency ³ .
[Driver] group box	Displays the driver currently selected.
[Change...] button	Displays the [E10A Driver Details] dialog box. Use when a driver currently connected is changed.

- Notes:
1. When using the sequential break function, set the corresponding hardware break conditions.
 2. The range of frequencies that the AUD operates under is different according to the devices used. For details, refer to section 6.5.4, Notes on Using the JTAG Clock (TCK) and AUD Clock (AUDCK).
 3. The range of frequencies that the JTAG operates at is different according to the devices used. For details, refer to section 6.5.4, Notes on Using the JTAG Clock (TCK) and AUD Clock (AUDCK).

When a driver is to be changed with the [Change..] button, the following message is displayed.

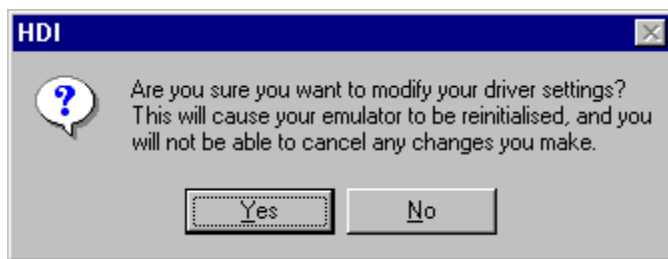


Figure 4.3 Warning Message Box

When the [Yes] button is clicked, the [E10A Driver Details] dialog box is displayed. When the [No] button is clicked, the display returns to the [Configuration] dialog box.

Related Command:

GO_OPTION command

(2) [E10A Driver Details] Dialog Box Function

Function:

When the [Change] button in the [Driver] group box is clicked on the [General] page in the [Configuration] dialog box, the [E10A Driver Details] dialog box is displayed.

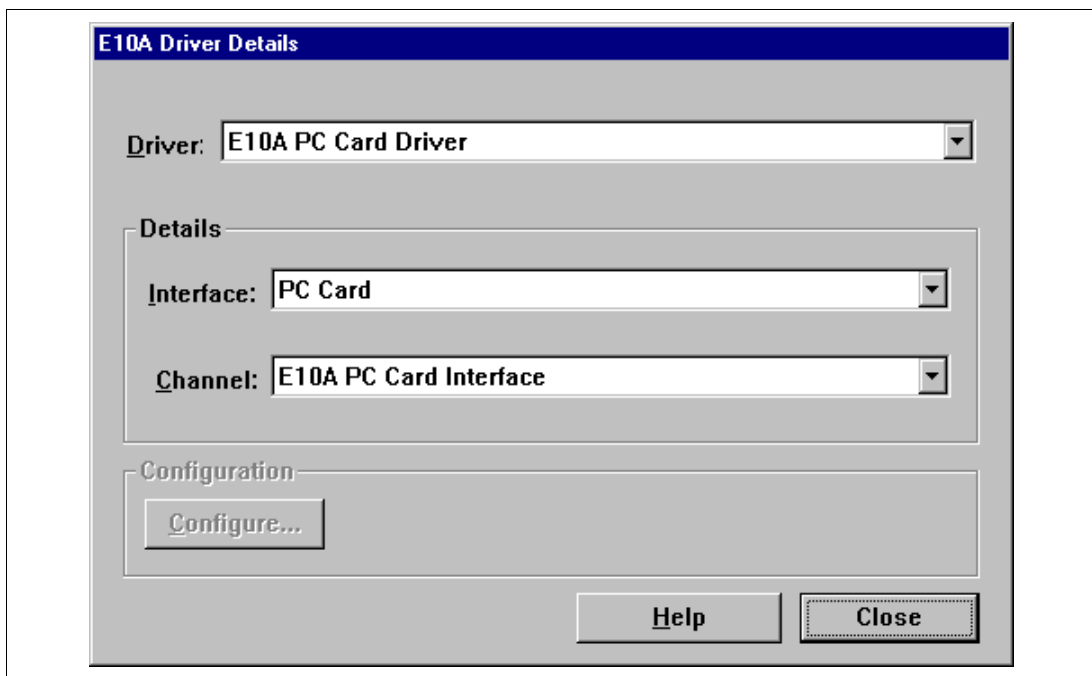


Figure 4.4 [E10A Driver Details] Dialog Box

Description:

Table 4.4 Options for the [E10A Driver Details] Dialog Box

Option	Description
[Driver] combo box	Selects the driver to connect the HDI with the emulator. Selects [E10A PC Card Driver] to use the PCMCIA card emulator. Selects [E10A PCI Card Driver] to use the PCI card emulator. For details, refer to section 6.5.1, Emulator Driver Selection.
[Interface] combo box	Displays the interface name of the card emulator to be connected. Selects [PC Card] to use the PCMCIA card emulator. Selects [PCI] to use the PCI card emulator. (If the driver is not installed, the [PC Card] or [PCI] is not displayed.)
[Channel] combo box	Displays the interface to which the board is connected.

(3) [Loading flash memory] Page ([Configuration] Dialog Box)

Function:

Downloading to the flash memory is set on the [Loading flash memory] page.

Window:

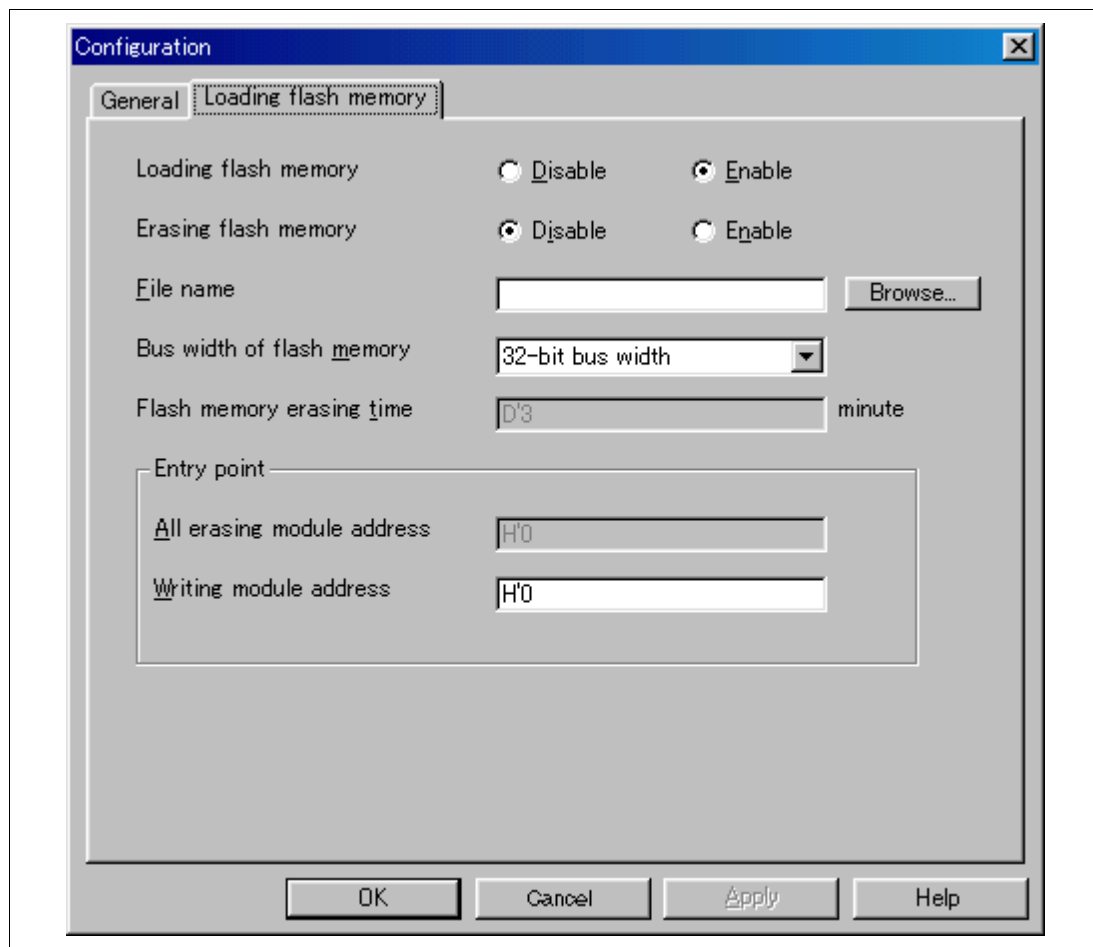


Figure 4.5 [Loading flash memory] Page ([Configuration] Dialog Box)

Description:

Table 4.5 [Loading flash memory] Page Options

Option	Description
[Loading flash memory] radio button	Sets Enable for flash memory downloading. At Enable, when [File load] is selected from the [File] menu for downloading, the write module is always called. Enable: Download to the flash memory Disable: Not download to the flash memory
[Erasing flash memory] radio button	Sets Enable for erasing before the flash memory is written. At Enable, the erase module is called before calling the write module. Enable: Erase the flash memory Disable: Not erase the flash memory
[File name] edit box	Sets the write/erase module name. The file that has been set is loaded to the RAM area before loading to the flash memory. A maximum of 128 characters can be input for the file name.
[Bus width of flash memory] list box	Sets the bus width of the flash memory.
[Flash memory erasing time] edit box*	Sets the TIMEOUT value at flash memory erasing. Increase the value if erasing requires much time; the default time is three minutes. The radix for the input value is decimal. It becomes hexadecimal by adding H'.
[Entry point] group box	Sets the calling destination address of the write/erase module. [All erasing module address] edit box: Inputs the calling destination address of the erase module. [Writing module address] edit box: Inputs the calling destination address of the write module.

Note: Although the values that can be set are D'0 to D'65535, the TIMEOUT hours may be extended according to the set value. Therefore, it is recommended to input the minimum value. The value to be input must only be positive integer.

— Notes on using the flash memory download function

The following are notes on downloading to the flash memory.

- When the flash memory download is enabled, downloading other than to the flash memory area is disabled.
- Downloading is only enabled to the flash memory area. Perform memory write or software break only to the RAM area.
- When the flash memory erase is enabled, the [Stop] button cannot stop erasing.
- The entry area for the user module must be MMU-disabled space.

4.2.2 [Breakpoints] Window

Function:

This window lists all break conditions that have been set.

Window:

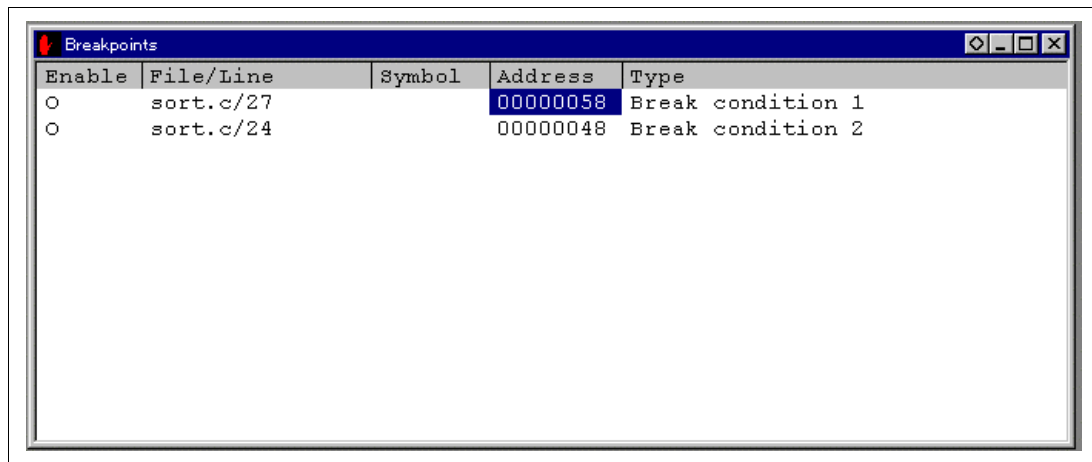


Figure 4.6 [Breakpoints] Window

Description:

The [Breakpoints] window displays breakpoint setting information. The items listed in the following tables are displayed.

Table 4.6 [Breakpoints] Window Display Items

Item	Description
[Enable]	Displays whether the break condition is enabled or disabled. BREAKPOINT: ● Break Condition: ○ (If the address is the same as the one that has been set to the BREAKPOINT, the mark is ●.)
[File/Line]	Displays the file name and line number where the breakpoint is set.
[Symbol]	Displays the symbol corresponding to the breakpoint address. If no symbol has been defined for the address, nothing is displayed.
[Address]	Displays the address where the breakpoint is set.
[Type]	Displays the break condition type as follows: Break Point: Software breakpoint (Virtual or physical address is determined according to the MMU state at setting.) Break Point Virtual Space ASID = D'xxx: Software breakpoint (Virtual address. ASID value is displayed in decimal.) Break Point Physical Space: Software breakpoint (Physical address.) Break Condition 1 to Break Condition 3: Hardware break condition

Note: Only "Break Point" is displayed in the [Type] item when the device does not support the MMU.

The pop-up menu, which is opened by clicking the right mouse button, can be used to set, change, and clear breakpoints, and to enable or disable break conditions. The pop-up menu functions are described in the following table.

Table 4.7 [Breakpoints] Window Pop-up Menu Operation

Menu Name	Description
[Add]	Sets break conditions. Clicking this button will display the [Break] dialog box, enabling break conditions to be set.
[Edit]	Changes break conditions. Select break conditions to be changed and click this button. The break condition setting dialog box will be displayed, enabling the break condition to be changed.
[Disable] ([Enable])	Enables or disables break conditions. Select break conditions to be enabled or disabled and click this button.
[Delete]	Clears break conditions. Select break conditions to be cleared and click this button.
[Del All]	Clears all break conditions.
[Go to Source]	Jumps to the address which sets the break in the [Source] window.

4.2.3 [Break] Dialog Box

Function:

This dialog box displays the break condition settings.

Window:

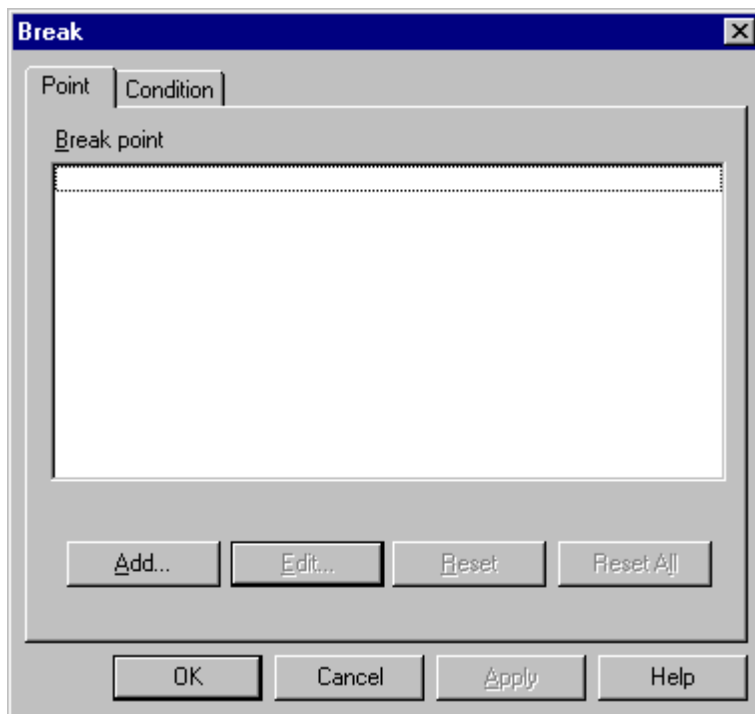


Figure 4.7 [Break] Dialog Box

Description:

The [Break] dialog box consists of the pages listed in table 4.8.

Table 4.8 [Break] Dialog Box Pages

Page Name	Description
[Point]	Displays software breakpoint settings.
[Condition]	Displays Break Condition settings.

The dialog boxes which set or modify break conditions can be displayed from the pages above.

Clicking the [OK] button (or [Close] button in some emulator products) will close this dialog box.

(1) [Point] Page ([Break] Dialog Box)

Function:

This page displays software breakpoint settings. In this page, software breakpoints can be set, changed, and cleared.

Window:

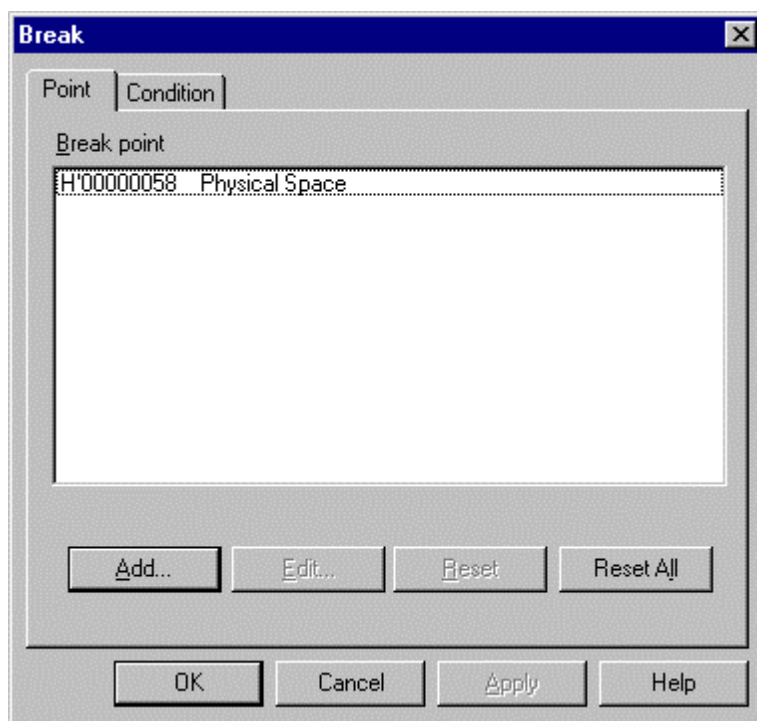


Figure 4.8 [Point] Page ([Break] Dialog Box)

Description:

Table 4.9 [Point] Page Options

Option	Description
[Break point] list box	<p>Lists the software breakpoints currently being set. The display contents are <breakpoint address> and <address space>. <address space> is displayed as follows:</p> <ul style="list-style-type: none">• Physical Space• Virtual Space ASID = D'xxx (xxx is the ASID value displayed in decimal form.)
[Add...] button	Sets software breakpoints. Clicking this button displays the [Break Point] dialog box.
[Edit...] button	Changes the software breakpoint selected in the [Break point] list box. Clicking this button displays the [Break Point] dialog box.
[Reset] button	Clears the software breakpoint selected in the [Break Point] list box.
[Reset All] button	Clears all software breakpoints displayed in the [Break Point] list box.

Related Commands:

BREAKPOINT command

BREAKPOINT_CLEAR command

BREAKPOINT_ENABLE command

BREAKPOINT_DISPLAY command

(2) [Condition] Page ([Break] Dialog Box)

Function:

This page displays the Break Condition settings. These conditions can also be set or cleared in this page.

Window:

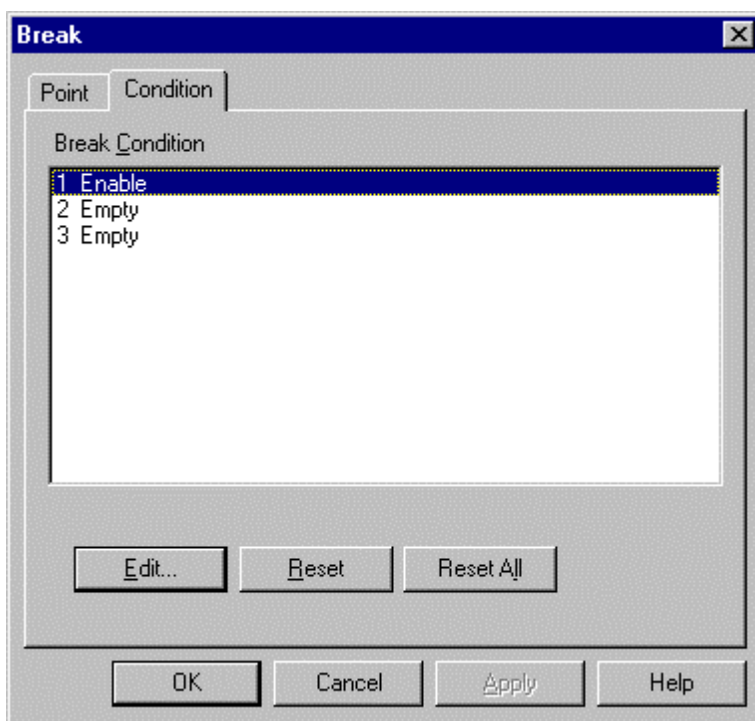


Figure 4.9 [Condition] Page ([Break] Dialog Box)

Note: The number of hardware break conditions differs according to the product. For the number that can be specified for each product, refer to the online help.

Description:

Table 4.10 [Condition] Page Options

Option	Description
[Break Condition] list box	Displays the Break Condition settings. The display at system initiation is as follows: When conditions are set, Enable is displayed. When no conditions are set, Empty is displayed. 1 Empty (setting of Break Condition 1) 2 Empty (setting of Break Condition 2) :
[Edit...] button	Changes the Break Condition settings selected in the [Break Condition] list box. Clicking this button displays the [Break Condition] dialog boxes.
[Reset] button	Clears the Break Condition settings selected in the [Break Condition] list box.
[Reset All] button	Clears all Break Condition settings in the [Break Condition] list box.

Related Commands:

BREAKCONDITION_CLEAR command
BREAKCONDITION_DISPLAY command
BREAKCONDITION_ENABLE command
BREAKCONDITION_SET command

4.2.4 [Break Point] Dialog Box

Function:

This dialog box sets software breakpoints.

Window:

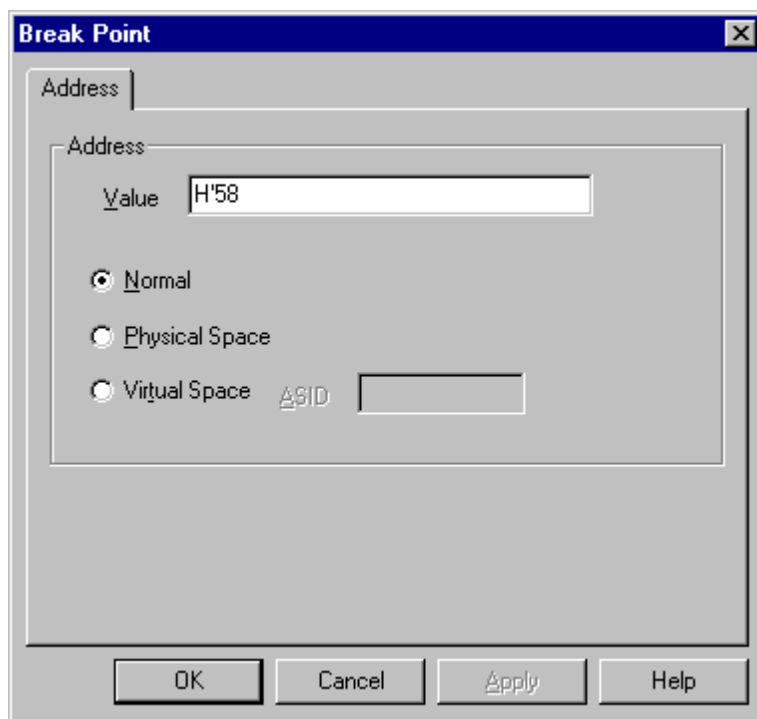


Figure 4.10 [Break Point] Dialog Box

Note: The items that can be set in this window differ according to the product. For the settings for each product, refer to the online help.

Description:

The [Break Point] dialog box consists only of the [Address] page. This dialog box sets address conditions and address areas. The [Address] page options are as follows:

Table 4.11 [Address] Page Options

Option	Description
[Value] edit box	Sets a breakpoint address with a number or a symbol.
[Normal] radio button	Does not set an address area.*
[Physical Space] radio button	Shows that the break condition is the physical area.*
[Virtual Space] radio button	Shows that the break condition is the virtual area.*
[ASID] edit box	Sets an ASID value (0 to 255) when the breakpoint address is in the virtual area. Nothing is set as default.*

Note: These options are not supported in a device in which the MMU is not built-in.

Clicking the [OK] button enables breakpoints to be set. If the [Cancel] button is clicked, this dialog box is closed without setting breakpoints.

Related Commands:

BREAKPOINT command

BREAKPOINT_CLEAR command

BREAKPOINT_DISPLAY command

BREAKPOINT_SET command

4.2.5 [Break Condition] Dialog Box

Function:

This dialog box sets hardware break conditions.

Window:

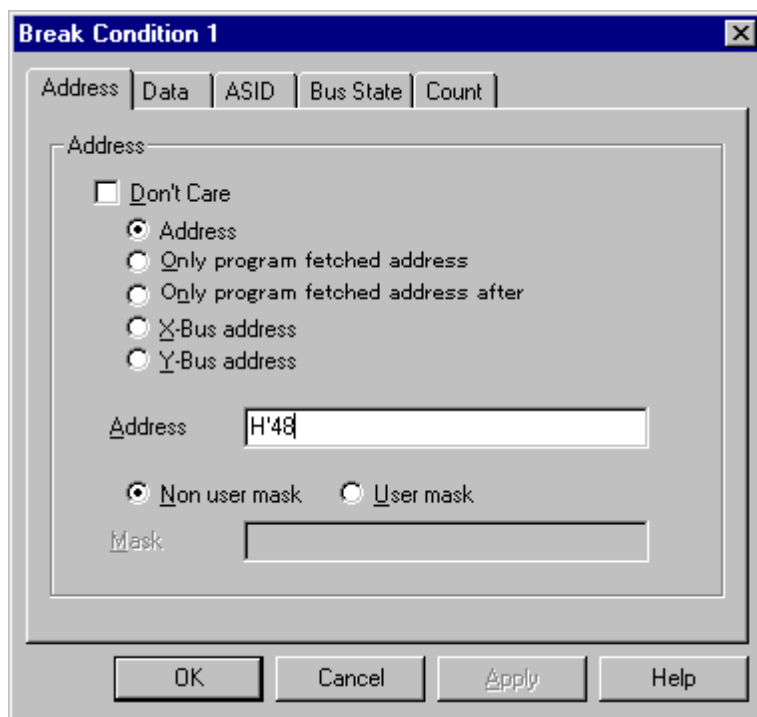


Figure 4.11 [Break Condition] Dialog Box

Note: The items that can be set in this window differ according to the product. For the settings for each product, refer to the online help.

Description:

The [Break Condition] dialog box consists of multiple pages. A condition to halt the program can be set in each page.

Contents to be set by each page are described in section 4.2.6, [Break Condition] Dialog Box Pages.

Clicking the [OK] button sets the hardware break conditions. If the [Cancel] button is clicked, the dialog box is closed without setting the hardware break conditions.

Related Commands:

BREAKCONDITION_CLEAR command

BREAKCONDITION_DISPLAY command

BREAKCONDITION_ENABLE command

BREAKCONDITION_SET command

4.2.6 [Break Condition] Dialog Box Pages

Function:

The [Break Condition] dialog box pages allow a number of hardware break conditions to be set. Some functions may not be supported by some types of emulators. The setting conditions may differ from the dialog box name in table 4.12. For details, refer to section 6.5.2, Break Condition Functions.

Table 4.12 Setting Conditions in [Break Condition] Dialog Boxes

Dialog Box	Type				
	Address Bus Condition	Data Bus Condition	Bus State and Read/Write Conditions	Count Condition	LDTLB Instruction Break and Internal I/O Access Break Conditions
[Break Condition 1] dialog box	O	O	O	O	X
[Break Condition 2] dialog box	O	X	O	X	X
[Break Condition 3] dialog box	X	X	X	X	O

Note: O: Can be set by checking the radio button in the dialog box.

X: Cannot be set in the dialog box.

Table 4.13 shows all the [Break Condition] dialog box pages.

Table 4.13 [Break Condition] Dialog Box Pages

Page Name	Function
[Address]	Sets the address conditions of Break Condition 1 and Break Condition 2. (Address condition is not displayed in the [Break Condition 3] dialog box page.)
[Data]	Sets the data conditions of Break Condition 1. (Data condition is not displayed in the [Break Condition 2] and [Break Condition 3] dialog box pages.)
[ASID]	Sets the ASID conditions of Break Condition 1 and Break Condition 2. (ASID condition is not displayed in the [Break Condition 3] dialog box page.)
[Bus State]	Sets the bus state conditions and read/write cycle conditions of Break Condition 1 and Break Condition 2. (Bus state condition is not displayed in the [Break Condition 3] dialog box page.)
[Count]	Sets the satisfaction count conditions of Break Condition 1. (Count condition is not displayed in the [Break Condition 2] and [Break Condition 3] dialog box pages.)
[General]	Sets the conditions of Break Condition 3. (Data condition is not displayed in the [Break Condition 1] and [Break Condition 2] dialog box pages.)

Note: This function differs according to the product. For the specifications of each product, refer to section 6.5.2, Break Condition Functions, or to the online help.

(1) [Address] Page ([Break Condition] Dialog Box)

Function:

This page sets the address bus conditions.

Window:

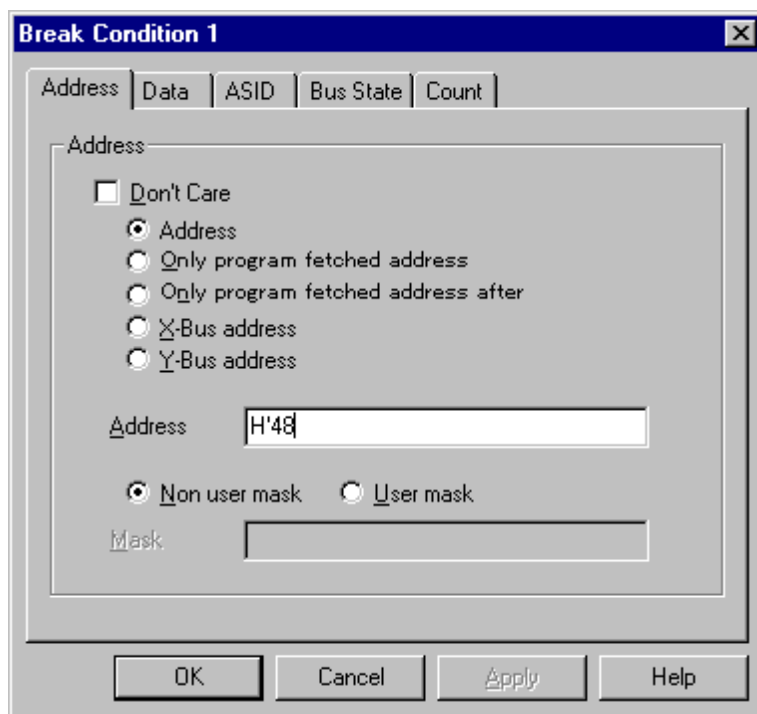


Figure 4.12 [Address] Page ([Break Condition 1] Dialog Box)

Note: The items that can be set in this window differ according to the product. For the settings for each product, refer to the online help.

Description:

Table 4.14 [Address] Page Options

Option	Description
[Don't Care] check box	Does not set address conditions.
[Address] radio button	Sets use of the normal address bus as break conditions.
[Only program fetched address] radio button	Sets a break before prefetched address execution as break conditions.
[Only program fetched address after] radio button	Sets a break after prefetched address execution as break conditions.
[X-bus address] radio button	Sets the X-bus address as a break condition. Can be set only with Break Condition 1.
[Y-bus address] radio button	Sets the Y-bus address as a break condition. Can be set only with Break Condition 1.
[Address] edit box	Sets the address bus value with a number or a symbol.
[Non user mask] radio button	Sets no mask conditions.
[User mask] radio button	Sets mask conditions.
[Mask] edit box	Sets the mask bits if [User mask] is selected. For masked bits, the break condition is satisfied regardless of the address values.

Note: This page is displayed when the conditions of Break Condition 1 and Break Condition 2 are set.

A page name to be displayed and the contents of an option that can be set will change depending on the radio button selected.

Table 4.15 Address Options

Option	Description
[Address] radio button, [X-Bus address] radio button, and [Y-Bus address] radio button	All pages can be selected and masks can be set.
[Only program fetched address] radio button	The [Address] and [ASID] pages can be set; however, no mask can be set.
[Only program fetched address after] radio button	The [Address] and [ASID] pages can be set.

Note: This function differs according to the product. For the specifications of each product, refer to section 6.5.2, Break Condition Functions, or to the online help.

(2) [Data] Page ([Break Condition] Dialog Box)

Function:

This page sets the data bus conditions.

Window:

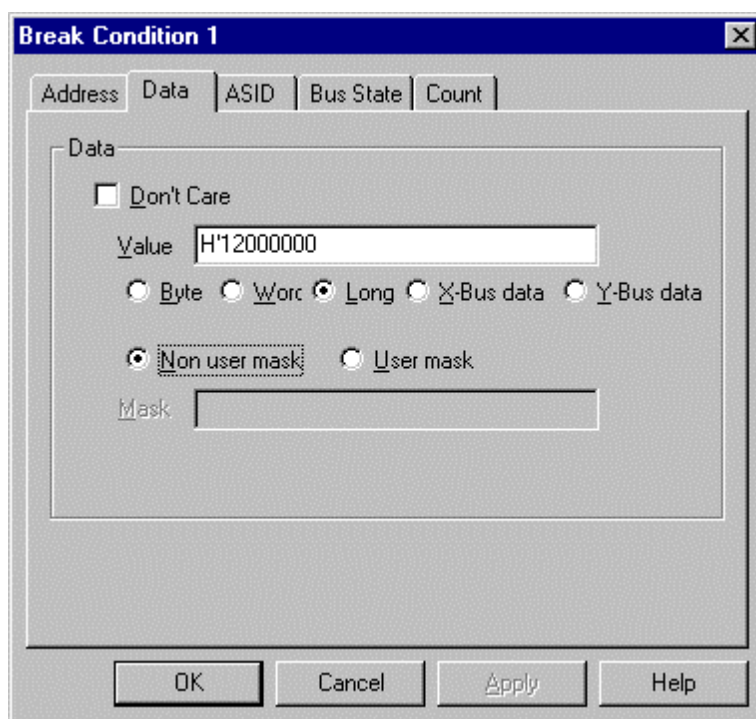


Figure 4.13 [Data] Page ([Break Condition 1] Dialog Box)

Note: The items that can be set in this window differ according to the product. For the settings for each product, refer to the online help.

Description:**Table 4.16 [Data] Page Options**

Option	Description
[Don't Care] check box	Does not set data conditions.
[Value] edit box	Sets the data bus value with a number.
[Byte] radio button	Sets byte data access cycles.
[Word] radio button	Sets word data access cycles.
[Long] radio button	Sets longword data access cycles.
[X-bus data] radio button	Sets X-bus data access cycles.
[Y-bus data] radio button	Sets Y-bus data access cycles.
[Non user mask] radio button	Does not set mask conditions.
[User mask] radio button	Sets mask conditions.
[Mask] edit box	Sets the mask bits when [User mask] is selected. Mark a bit to be masked with *. For masked bits, the break conditions will be satisfied regardless of the data values.

Note: This page is displayed when the conditions of Break Condition 1 are set.

(3) [ASID] Page ([Break Condition] Dialog Box)

Function:

This page sets the ASID conditions.

Window:

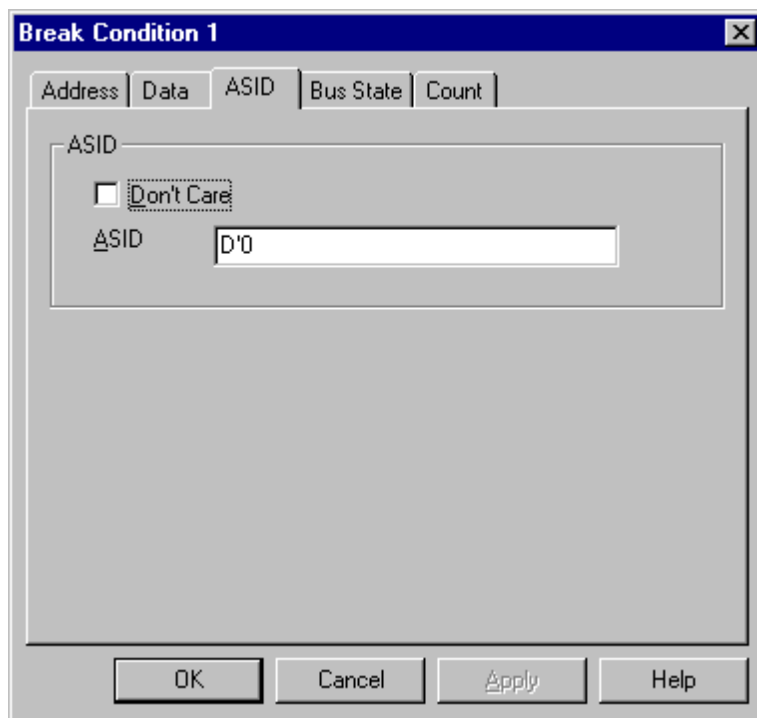


Figure 4.14 [ASID] Page ([Break Condition] Dialog Box)

Description:

Table 4.17 [ASID] Page Options

Option	Description
[Don't Care] check box	Does not set ASID conditions.
[ASID] edit box	Sets the ASID condition value. The default is 0.

Note: This page is displayed when the conditions of Break Condition 1 and Break Condition 2 are set.

Note: These options are not supported in a device in which the MMU is not built-in.

(4) [Bus State] Page ([Break Condition] Dialog Box)

Function:

This page sets bus state conditions and read/write cycle conditions.

Window:

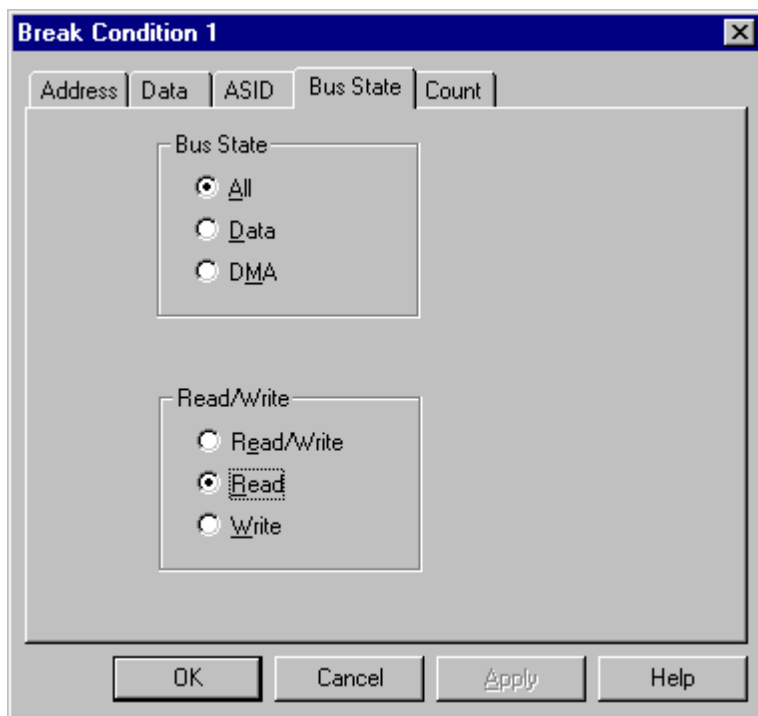


Figure 4.15 [Bus State] Page ([Break Condition] Dialog Box)

Note: The items that can be set in this window differ according to the product. For the settings for each product, refer to the online help.

Description:**Table 4.18 [Bus State] Page Options**

Group Box	Option	Description
[Bus State] group box	[All] radio button	Sets the bus state conditions as break conditions.
	[Data] radio button	Sets the execution cycle as break conditions.
	[DMA] radio button	Sets DMA cycles as a break condition.
[Read/Write] group box	[Read/Write] radio button	Sets the read/write cycle conditions as break conditions.
	[Read] radio button	Sets read cycles as break conditions.
	[Write] radio button	Sets write cycles as break conditions.

Note: This page is displayed when the conditions of Break Condition 1 and Break Condition 2 are set.

(5) [Count] Page ([Break Condition] Dialog Box)

Function:

This page sets the conditions for Break Condition 1.

Window:

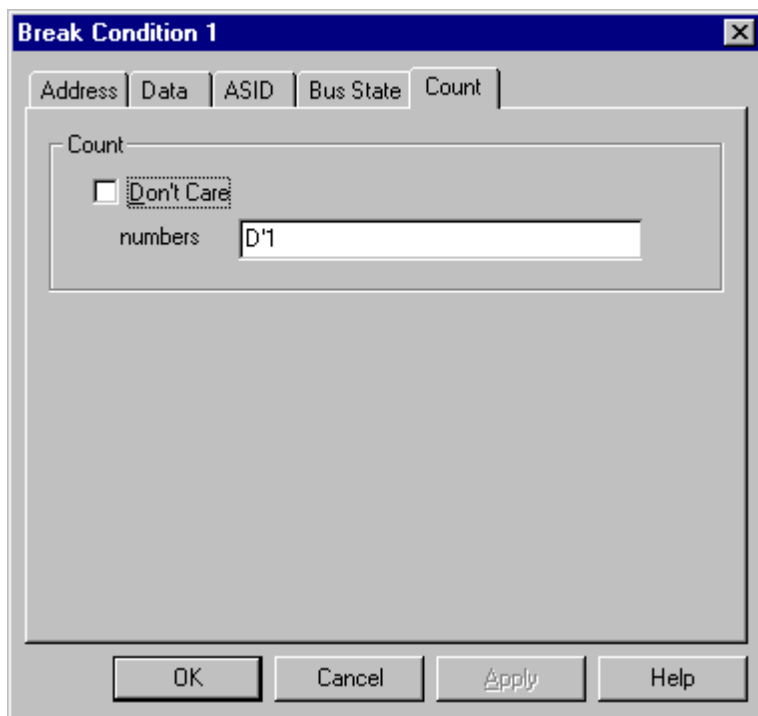


Figure 4.16 [Count] Page ([Break Condition] Dialog Box)

Table 4.19 [Count] Page Options

Option	Description
[Don't Care] check box	Sets no satisfaction count conditions.
Input area	Sets the satisfaction count as a break condition. The maximum count is 4,095. Breaks when the conditions set by the [Break Condition] dialog box for the specified times are satisfied. The default is D'1.

Note: Some products are not supported by this function. For the specifications of each product, refer to the online help.

(6) [General] Page ([Break Condition] Dialog Box)

Function:

This page sets the conditions for Break Condition 3.

Window:

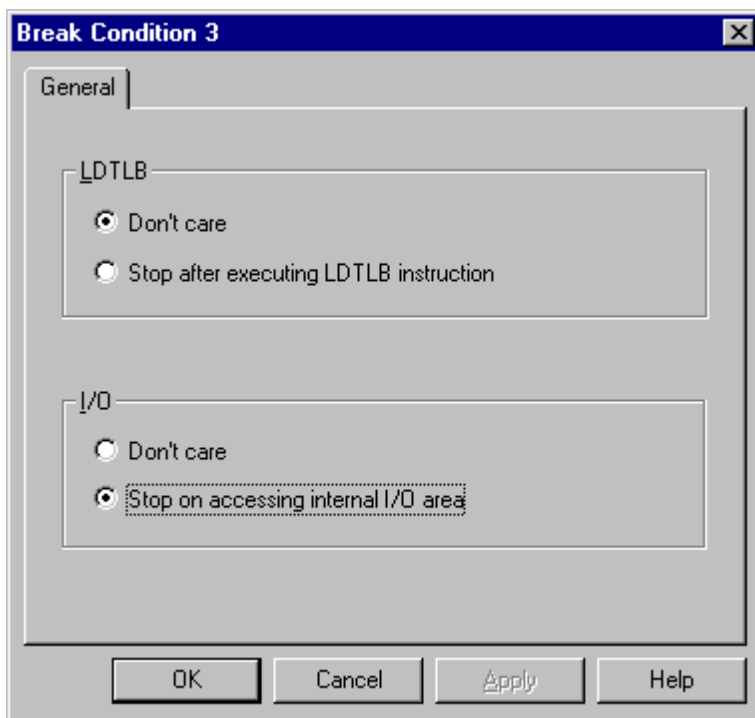


Figure 4.17 [General] Page ([Break Condition] Dialog Box)

Description:

Table 4.20 [General] Page Options

Group Box	Option	Description
[LDTLB] group box	[Don't Care] radio button	Does not set break conditions when the LDTLB instruction is executed.
	[Stop after executing LDTLB instruction] radio button	Sets the LDTLB instruction execution as break conditions.
	[DMA] radio button	Sets the DMA cycle as break conditions.
[I/O] group box	[Don't Care] radio button	Does not set break conditions when the internal I/O area is accessed.
	[Stop on accessing internal I/O area] radio button	Sets the internal I/O area access as break conditions.
	[Write] radio button	Sets only write cycle as break conditions.

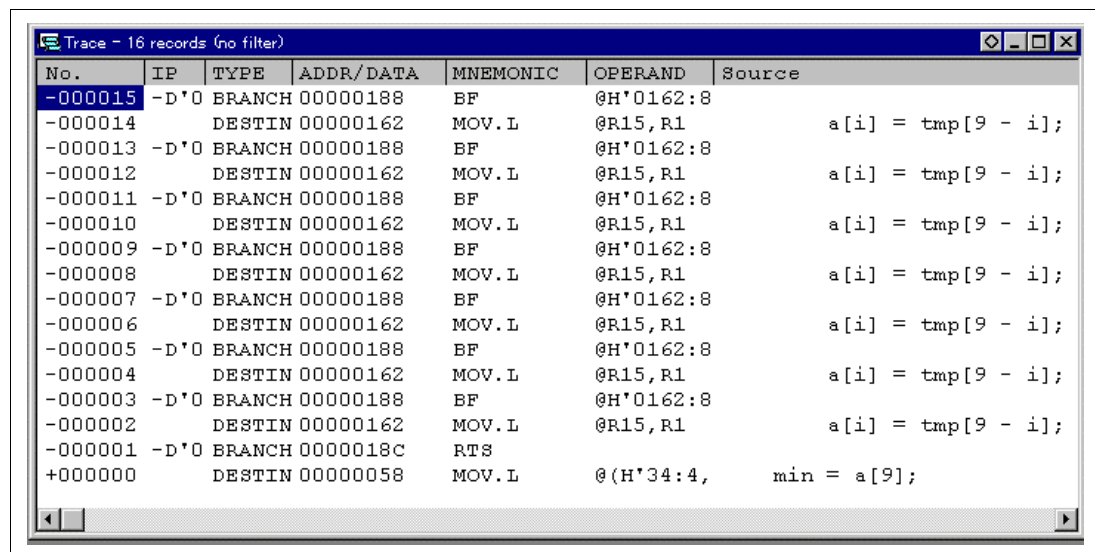
Note: Some products are not supported by this function. For the specifications of each product, refer to the online help.

4.2.7 [Trace] Window

Function:

This window displays the trace buffer contents.

Window:



No.	IP	TYPE	ADDR/DATA	MNEMONIC	OPERAND	Source
-000015	-D'0	BRANCH	00000188	BF	@H'0162:8	
-000014		DESTIN	00000162	MOV.L	@R15,R1	a[i] = tmp[9 - i];
-000013	-D'0	BRANCH	00000188	BF	@H'0162:8	
-000012		DESTIN	00000162	MOV.L	@R15,R1	a[i] = tmp[9 - i];
-000011	-D'0	BRANCH	00000188	BF	@H'0162:8	
-000010		DESTIN	00000162	MOV.L	@R15,R1	a[i] = tmp[9 - i];
-000009	-D'0	BRANCH	00000188	BF	@H'0162:8	
-000008		DESTIN	00000162	MOV.L	@R15,R1	a[i] = tmp[9 - i];
-000007	-D'0	BRANCH	00000188	BF	@H'0162:8	
-000006		DESTIN	00000162	MOV.L	@R15,R1	a[i] = tmp[9 - i];
-000005	-D'0	BRANCH	00000188	BF	@H'0162:8	
-000004		DESTIN	00000162	MOV.L	@R15,R1	a[i] = tmp[9 - i];
-000003	-D'0	BRANCH	00000188	BF	@H'0162:8	
-000002		DESTIN	00000162	MOV.L	@R15,R1	a[i] = tmp[9 - i];
-000001	-D'0	BRANCH	0000018C	RTS		
+000000		DESTIN	00000058	MOV.L	@(H'34:4,	min = a[9];

Figure 4.18 [Trace] Window

Note: The types of information and the number of branch instructions differ according to the product. For the settings for each product, refer to the online help.

Description:

This window displays the trace buffer contents. The items listed in table 4.21 are displayed.

Table 4.21 [Trace] Window Display Items

Item	Description
[No.]	Displays the number in ascending order as the trace stop point is 0 (signed decimal).
[IP]	Displays the instruction pointer (signed decimal).
[TYPE]	For the branch instruction trace, displays the information type, that is, branch source or branch destination. BRANCH: Branch source DESTINATION: Branch destination
[ADDR/DATA]	For the branch instruction trace, displays the branch source or branch destination address.
[MNEMONIC]	Displays the execution instruction mnemonic.
[OPERAND]	Displays the execution instruction operand.
[Source]	Displays the C-source line of the address that the trace has been acquired.

The pop-up menu, opened by clicking the right mouse button, can be used to set, change, and clear trace conditions. For details, refer to the Hitachi Debugging Interface User's Manual.

Notes:

1. In some cases, the emulator address may be acquired by trace. In such a case, the following message will be displayed. Ignore this address because it is not a user program address.

*** EML ***

2. The [Halt] menu in the pop-up menu is active only when the [Trace] window is open during user program execution. When the internal trace is used, realtime emulation cannot be performed by using the [Halt] menu.

Related Command:

TRACE_DISPLAY command

4.2.8 [Trace Acquisition] Dialog Box

Function:

This dialog box sets trace acquisition conditions. When the [Acquisition] menu is selected from the pop-up menu, which is displayed by clicking the right mouse button in the [Trace] window, the [Trace Acquisition] dialog box is displayed.

Table 4.22 [Trace Acquisition] Dialog Box Page Options

Page Name	Description
[Trace Mode]	Sets the conditions of trace mode.

(1) [Trace Mode] Page ([Trace Acquisition] Dialog Box)

Function:

This page sets the conditions for trace mode.

Window:

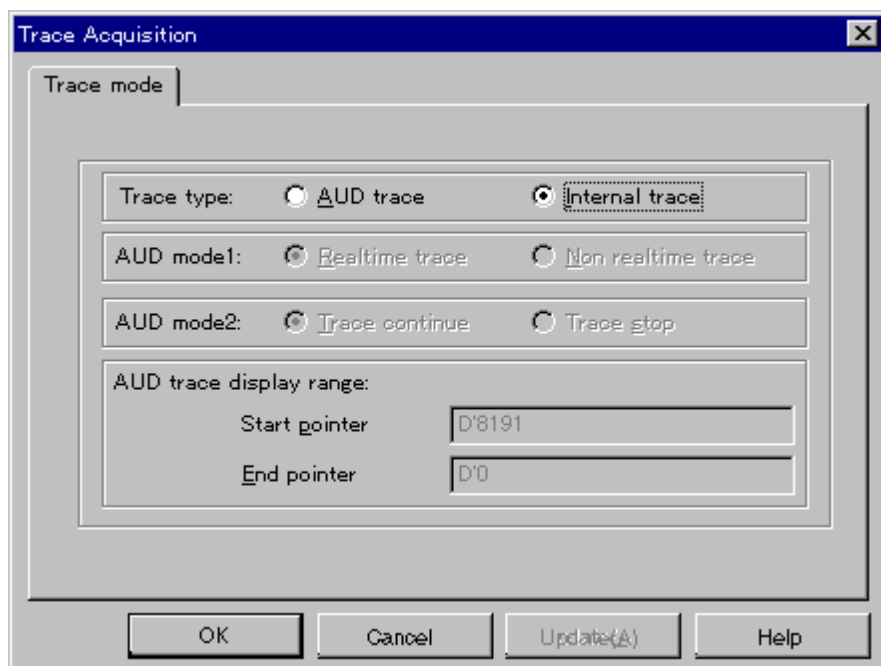


Figure 4.19 [Trace mode] Page ([Trace Acquisition] Dialog Box)

Note: This function differs according to the product. For the specifications of each product, refer to the section related to the trace functions in section 6, SHxxxx E10A Emulator Specifications, or to the online help.

Description:

Table 4.23 [Trace mode] Page Options

Option	Description
[AUD trace] radio button	Uses AUD trace functions. By default, this box is not checked.
[Internal trace] radio button	Uses the internal trace functions. By default, this box is checked.
[Realtime trace] radio button	When the next branch occurs while the trace information is being output, the information is stopped and the next trace information is output. The user program can be executed in realtime, but some trace information will not be output. By default, this box is checked.
[Non realtime trace] radio button	When the next branch occurs while the trace information is being output, the CPU stops operations until the information is output. The user program is not executed in realtime. By default, this box is not checked.
[Trace continue] radio button	When the trace buffer becomes full, this function always overwrites the oldest trace information to acquire the latest trace information.
[Trace stop] radio button	When the trace buffer becomes full, the trace information is not acquired.
[AUD trace display range] group box	Inputs the start or end pointer value in the trace display range as numerical values. By default, the start pointer is -D'8191 and the end pointer is -D'0000. In the PCMCIA card emulator, -D'8191 to D'0 can be set to the trace pointer. In the PCI card emulator, -D'32767 to D'0 can be set.

Related Command:

AUD_MODE command

4.2.9 [System Status] Window

Function:

This window lists information, such as conditions that have been set to the emulator and execution results.

Window:

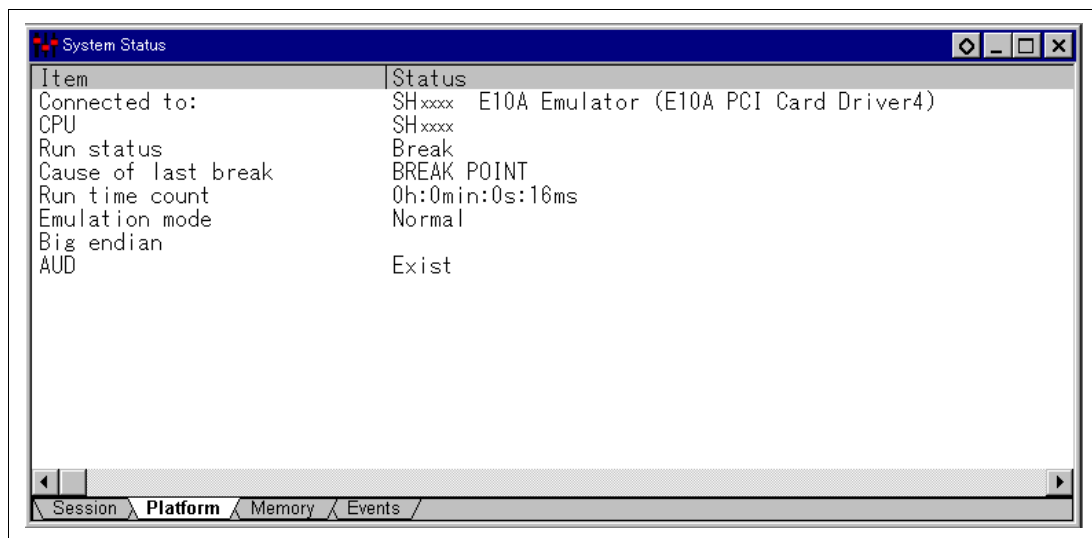


Figure 4.20 [System Status] Window

Note: The items that can be displayed in this window differ according to the product. For the items that can be displayed, refer to the online help.

The items listed in the following table are displayed in the [System Status] window.

Table 4.24 [System Status] Window Display Items

Page	Item	Description
[Session]	Target System	Always displays Connected.
	Session Name	Displays the session file name.
	Program Name	Displays the load module file name.
[Platform]	Connected To	Displays the name of the connected emulator and the selected driver name.
	CPU	Displays the target device name.
	Run status	Displays the execution status: RUNNING: Being executed Break: Stopped
	Cause of last break	Displays the cause of the emulator stopping at break. In this example, the cause of the stop is BREAK POINT.
	Run time count	Displays the program execution time. The display format is h: hours, min: minutes, s: seconds, and ms: milliseconds. In this example, 0h:0min:0s:16ms is displayed.
	Emulator mode	Displays the emulator operating mode (setting information for [Emulation Mode] of the [Configuration] dialog box).
	Big Endian/Little Endian	Displays the endian state (Big Endian or Little Endian). In this example, Big Endian is displayed.
	AUD	Displays whether the AUD function can be used. This item is displayed by the emulator with the AUD function.
[Memory]	Loaded Memory Areas	Displays the loaded area of the load module.
[Events]	Resources	Displays the usage states of BREAKPOINT and Break Condition.

Section 5 Command-line Functions

5.1 Table and Symbol Description

This section describes the format used in section 5.2, Command Descriptions. The descriptions of some commands are given over two or more pages.

5.1.1 Format

The input format for each command is as follows. Characters shown in bold-italics are to be input.

[] : Parameters enclosed by [] can be omitted.

< > : Contents shown in < > are set.

< >=: The parameter to the left of the "=" sign is input in the format shown to the right.

| : This represents a non-exclusive selection.

|| : This represents an exclusive selection.

The command parameter details are described in the parameter table.

5.1.2 Parameter Input

Numerical Parameters:

A binary, octal, decimal, or hexadecimal value, a symbol, or a formula can be input. A symbol can contain up to 32 characters. Terms in a formula are separated with operators (such as + or –).

Keyword Parameters:

One of the bold characters given in the description column of the table can be input. If a character string not shown in the description is input, an error will occur.

Character-String Parameters:

Character-string parameters are used to input mask data or a file name. In the mask data, set a radix (H': hexadecimal or B': binary) at the top of a character string and set * at the digit to be masked.

5.1.3 Examples

These are actual input examples. For commands whose execution results in a specific display output, an example of the display is given.

5.1.4 Related Items

Related E10A HDI commands (abbreviations) and dialog boxes are shown. (Refer to section 4, Descriptions of Windows.)

5.2 Command Descriptions

The command list of the E10A emulator is shown below.

Table 5.1 E10A HDI Commands

No.	Command	Abb.	Function
1	AUD_CLOCK	AUCL	Sets the AUD clock (AUDCK).
2	AUD_MODE	AUM	Sets AUD trace conditions.
3	AUD_TRACE	AUT	Displays trace information.
4	BREAKCONDITION_ CLEAR	BCC	Clears hardware breakpoints that have been set.
5	BREAKCONDITION_ DISPLAY	BCD	Displays hardware breakpoints that have been set.
6	BREAKCONDITION_ ENABLE	BCE	Enables or disables hardware breakpoints that have been set.
7	BREAKCONDITION_ SET	BCS	Sets hardware breakpoints.
8	BREAKPOINT	BP	Sets software breakpoints.
9	BREAKPOINT_ CLEAR	BC	Clears software breakpoints that have been set.
10	BREAKPOINT_ DISPLAY	BD	Displays software breakpoints that have been set.
11	BREAKPOINT_ ENABLE	BE	Enables or disables software breakpoints that have been set.
12	DEVICE_ TYPE	DE	Displays device type currently selected.
13	GO_ OPTION	GP	Displays or sets the emulation mode during user program execution.
14	JTAG_ CLOCK	JCK	Displays or sets a JTAG clock (TCK) frequency.
15	MEMORYAREA_ SET	MAS	Displays or sets memory area at command input, such as load, verify, save, memory display, or memory change.

Table 5.1 E10A HDI Commands (cont)

No.	Command	Abb.	Function
16	REFRESH	RF	Updates the HDI memory information to the latest contents.
17	RESTART	RST	Restarts the emulator.
18	STATUS	STS	Displays emulator state information.
19	STEP_INTERRUPT	SI	Displays or sets the enable or disable status of interrupts during step execution.
20	TRACE_DISPLAY	TD	Displays acquired trace buffer information.
21	UBC_MODE	UM	Displays or sets UBC use states.
22	VPMAP_CLEAR	VC	Clears the emulator address translation (VP_MAP) table which has been set.
23	VPMAP_DISPLAY	VD	Displays the emulator address translation (VP_MAP) table.
24	VPMAP_ENABLE	VE	Enables or disables the emulator address translation (VP_MAP) table.
25	VPMAP_SET	VS	Sets emulator address translation (VP_MAP) table.

Note: Support for these commands varies with the product. For the specifications of each product, refer to the online help.

5.2.1 AUD_CLOCK:AUCL

Description:

Sets or displays the AUD clock (AUDCK) values that have been set.

Format:

```
aucl [<option>]
```

<option> = <aud_clock>

Table 5.2 AUD_CLOCK Command Parameter

Parameter	Type	Description
<aud_clock>	Numerical value	Sets values from 1 to 7. 1: 5 MHz (PCI), 7.5 MHz (PCMCIA) 2: 10 MHz (PCI), 15 MHz (PCMCIA) 3: 20 MHz (PCI), 30 MHz (PCMCIA) 4: 30 MHz (PCI), 60 MHz (PCMCIA) 5: 40 MHz (PCI) 6: 50 MHz (PCI) 7: 60 MHz (PCI)

- Notes:
1. When <option> is omitted, the AUD clock (AUDCK) values that have been set are displayed.
 2. The range of frequencies that the AUD operates under differs according to the devices used. For details, refer to section 6.5.4, Notes on Using the JTAG Clock (TCK) and AUD Clock (AUDCK).
 3. The AUD clock (AUDCK) value, which can be set with this command, may differ according to emulator products. For details, refer to section 6.5.4, Notes on Using the JTAG Clock (TCK) and AUD Clock (AUDCK).

Note: The parameters for this command differ according to the product. For the specifications of each product, refer to the online help.

Examples:

To set AUD clock (AUDCK) to 15 MHz:

```
>AUD_CLOCK 2 (RET)  
AUD_CLOCK = 15MHz
```

The AUD clock (AUDCK) is displayed:

```
>AUD_CLOCK (RET)  
AUD_CLOCK = 15MHz
```

Related Item:

[Configuration] dialog box

5.2.2 AUD_MODE:AUM

Description:

Sets or displays AUD trace acquisition conditions.

Format:

aum [<option1>] [<option2>]

<option1> = mode<mode>

<option2> = full<full>

Table 5.3 AUD_MODE Command Parameter

Parameter	Type	Description
<mode>	Keyword	Selects the trace mode. N : Internal trace F : Non realtime trace R : Realtime trace
<full>	Keyword	Continues or stops emulation when the trace memory is full. C : Always overwrites the oldest information to acquire the latest information. S : When the trace buffer memory is full, information acquisition stops.

Note: When <option1> and <option2> are omitted, the current setting conditions are displayed.

Note: The parameters for this command differ according to the product. For the specifications of each product, refer to the online help.

Examples:

To select realtime trace mode and set continue option:

```
>aum mode R full c (RET)
```

To display settings:

```
>aum (RET)  
mode = Realtime trace, continue
```

To use internal trace mode:

```
>aum (RET)  
mode = Internal trace
```

Related Item:

[Trace Acquisition] dialog box

5.2.3 AUD_TRACE:AUT

Description:

Displays the trace information.

Format:

```
aut [<option1>] [<option2>]
```

- <option1> = start<start_pointer>
- <option2> = end<end_pointer>

Table 5.4 AUD_TRACE Command Parameter

Parameter	Type	Description
<start_pointer>	Numerical value (-n)	Start pointer value for trace display.
<end_pointer>	Numerical value (-m)	End pointer value for trace display.

- Notes:
1. In the PCMCIA card emulator, -D’8191 to D’0 can be set to the trace pointer. In the PCI card emulator, -D’32767 to D’0 can be set.
 2. When the internal trace is selected, the AUT command displays the information that has been acquired by using the AUD function.

Example:

To display trace information according to the information acquired during user program execution:

>AUD_TRACE (RET)				
IP	TYPE	ADDR	MNEMONIC	OPERAND
-D’xxxxxx	BRANCH	*****10		
	DESTINATION	01000020	MOV.L	R1, @R1
(a)	(b)	(c)	(d)	(e)

- (a) Instruction pointer (signed decimal)
- (b) Types of branch source or branch destination
 - BRANCH: Branch source
 - DESTINATION: Branch destination
- (c) Address of instruction word
- (d) Instruction mnemonic
- (e) Instruction operand

Related Item:

[Trace] dialog box

5.2.4 BREAKCONDITION_CLEAR: BCC

Description:

Clears hardware breakpoints that have been set.

Format:

`bcc [<channel>]`

`<channel> = channel <channel_number>`

Table 5.5 BREAKCONDITION_CLEAR Command Parameter

Parameter	Type	Description
<code><channel number></code>	Numerical value	Hardware break channel number

Note: When `<channel>` is omitted, all hardware breakpoints that have been set are cleared.

Examples:

To clear all hardware breakpoints:

```
>bcc (RET)
```

To clear a hardware breakpoint set at channel 2:

```
>bcc channel 2 (RET)
```

Related Items:

BCD, BCE, and BCS commands

[Breakpoints] window

[Break] and [Break Condition] dialog boxes

5.2.5 BREAKCONDITION_DISPLAY: BCD

Description:

Displays hardware breakpoints that have been set. The display contents include a hardware breakpoint channel number, enable or disable of the setting, and setting conditions.

Format:

bcd [<channel>]

<channel> = channel <channel_number>

Table 5.6 BREAKCONDITION_DISPLAY Command Parameter

Parameter	Type	Description
<channel_number>	Numerical value	Hardware breakpoint channel number

Note: When <channel> is omitted, all hardware breakpoints that have been set are displayed.

Examples:

To display all hardware breakpoint settings:

>*bcd (RET)*

Break Condition 1:Enable data 20 long

Break Condition 2:Disable address 126

Break Condition 3:Disable LDTLB break

To display the hardware breakpoint set at channel 1:

>*bcd channel 1 (RET)*

Break Condition 1:Enable data 20 long

Note: The items displayed with this command differ according to the product. For the display specifications of each product, refer to the online help.

Related Items:

BCC, BCE, and BCS commands

[Breakpoints] window

[Break] and [Break Condition] dialog boxes

5.2.6 BREAKCONDITION_ENABLE: BCE

Description:

Enables or disables hardware breakpoints that have been set.

Format:

```
bce [<channel>] <mode>
```

<channel> = channel <channel_number>

Table 5.7 BREAKCONDITION_ENABLE Command Parameters

Parameter	Type	Description
<channel_number>	Numerical value	Hardware break channel number
<mode>	Keyword	Enables or disables hardware break settings. Set either of the following keywords: enable : Enables hardware break settings. disable : Disables hardware break settings.

Note: When <channel> is omitted, all hardware breakpoints that have been set are enabled or disabled.

Examples:

To enable all hardware breakpoints:

```
>bce enable (RET)
```

To disable the hardware breakpoints set at channel 1:

```
>bce channel 1 disable (RET)
```

Related Items:

BCC, BCD, and BCS commands

[Breakpoints] window

[Break] and [Break Condition] dialog boxes

5.2.7 BREAKCONDITION_SET: BCS

Description:

Sets hardware breakpoints.

Note: The function will differ according to the devices used. For functions of each emulator product, refer to section 6.5.2, Break Condition Functions.

Format:

```
bcs <channel> <option> [<option> ... ]
```

<channel>	=	channel <channel_number>
<option>	=	[<addropt> <dataopt> <asidopt> <r/wopt> <accessopt>] [<countopt>] [<ldtlbopt> <iiopt>]
<addropt>	=	address <address> [<addrcycle>] address mask <maskdata> <addrcycle>
<dataopt>	=	data <data> <datawidth> data mask <maskdata> <datawidth>
<asidopt>	=	asid <asid>
<r/wopt>	=	direction <r/w>
<accessopt>	=	access <access>
<countopt>	=	count <count>
<ldtlbopt>	=	ldtlb <ldtlb>
<iiopt>	=	io <io>

Table 5.8 BREAKCONDITION_SET Command Parameters

Parameter	Type	Description
<channel_number>	Numerical value	<p>Hardware break condition channel number</p> <p>Specifiable options change depending on the channel number. For details, refer to section 6.5.2, Break Condition Functions.</p> <p>1: <addropt>, <dataopt>, <asidopt>, <r/wopt>, and <accessopt> can be set.</p> <p>2: <addropt>, <asidopt>, <r/wopt>, and <accessopt> can be set.</p> <p>3: <ldtlbopt> and <ioopt> can be set.</p>
<address>	Numerical value	Virtual address as an address bus value
<addrcycle>	Keyword	<p>Address bus access conditions for program fetch cycles</p> <p>Set either of the following keywords:</p> <p>pc: Breaks before the address set by the <address> parameter is executed. When this keyword is set, only the <addropt> and <asidopt> cannot be set as conditions. In addition, when pc is set, the <maskdata> parameter cannot be set.</p> <p>pcafter: Breaks after the address set by the <address> parameter is executed. When this keyword is set, only the <addropt> and <asidopt> cannot be set as conditions. When pcafter is not set, the address bus during data access cycles and program fetch cycles is targeted as the access condition.</p> <p>x: X-Bus address bus access</p> <p>y: Y-Bus address bus access</p>
<maskdata>	Character string	<p>Mask specification for desired bits in the data</p> <p>Set a radix (H' for hexadecimal or B' for binary) at the top of a character string and set * in the bit to the masked. Conditions are satisfied regardless of the values of masked bits.</p>
<data>	Numerical value	Data bus value

Table 5.8 BREAKCONDITION_SET Command Parameters (cont)

Parameter	Type	Description
<datawidth>	Keyword	Data bus access conditions Set one of the following keywords: byte : byte access word : word access long : longword access x : X-Bus data bus access y : Y-Bus data bus access
<asid>	Numerical value	ASID value from 0 to H'FF.
<r/w>	Keyword	Bus cycle read/write conditions Set either of the following keywords: read : read cycles write : write cycles
<access>	Keyword	Bus cycle access type dat : execution cycles
<count>	Numerical value	Set satisfaction count from 1 to H'FFFF
<ldtlb>	Keyword	Set LDTLB instruction execution as a break condition break : Breaks when the LDTLB instruction is executed.
<io>	Keyword	Set internal I/O access condition as a break condition. break : Breaks when the internal I/O area is accessed.

Note: The parameters for this command differ according to the product. For the specifications of each product, refer to the online help.

Examples:

To set the following conditions for channel 1 hardware breakpoint:

<addropt> item: An address bus value of H'1000000,
<dataopt> item: D0 bit of the byte access data is 0,
<r/wopt> item: write cycle.

>bcs channel 1 address H'1000000 data mask B'**0 byte
direction write (RET)***

To set the following conditions for channel 2 hardware breakpoint:

<addropt> item: Sets an address bus value of H'1000000 during the
program fetch cycles, and breaks before execution,
<asidopt> item: The ASID value is H'0.

>bcs channel 2 address H'1000000 pc asid H'0 (RET)

To set the following conditions for channel 1 hardware breakpoint:

<addropt> item: Sets an address bus value of H'1000000 during the program fetch cycles
with a mask set to the lower 10 bits, and breaks after execution,
<asidopt> item: H'10 to the ASID value.

>bcs channel 1 address H'1000000 pcafter m1 asid H'10 (RET)

To set the following conditions for channel 2 hardware breakpoint:

<accessopt> item: Execution cycles,
<r/wopt> item: Read cycles.

>bcs channel 2 access dat direction read (RET)

To set the following conditions for channel 3 hardware breakpoint:

<ldtlbopt> item: Breaks during LDTLB instruction execution,
<iiopt> item: Breaks when the internal I/O area is accessed.

>bcs channel 3 ldtlb break io (RET)

Related Items:

BCC, BCD, BCE, and TM commands

[Breakpoints] window

[Break] and [Break Condition] dialog boxes

5.2.8 BREAKPOINT: BP

Description:

Sets software breakpoints.

Note: The function will differ according to the devices used.

Format:

```
bp <address> [<address_space> [<asidopt>]]
```

<address_space> = space <space>

<asidopt> = asid <asid>

Table 5.9 BREAKPOINT Command Parameters

Parameter	Type	Description
<address>	Numerical value	Breakpoint address When an odd address is set, the address is rounded down to an even address.
<space>	Keyword	Breakpoint address area Set either of the following keywords: physical : physical address virtual : virtual address
<asid>	Numerical value	ASID value of a breakpoint when virtual is set to the <space> parameter.

Note: When virtual is set and the <asidopt> item is omitted in the <address_space> item, a breakpoint is set to a virtual address corresponding to the ASID value at command input.

Note: The parameters for this command differ according to the product. For the specifications of each product, refer to the online help.

Examples:

To set a software breakpoint at physical address H'10002C8:

```
>bp H'10002C8 space physical (RET)
```

To set a software breakpoint at logical address H'1000000, whose ASID value is H'10:

```
>bp H'1000000 space virtual asid H'10 (RET)
```

Related Items:

BC, BD, BE, VC, VD, VE, and VS commands

[Breakpoints] window

[Break] dialog box

5.2.9 BREAKPOINT_CLEAR: BC

Description:

Clears software breakpoints that have been set.

Format:

```
bc [<address> [<address_space> [<asidopt>]]]
```

<address_space> = space <space>

<asidopt> = asid <asid>

Table 5.10 BREAKPOINT_CLEAR Command Parameters

Parameter	Type	Description
<address>	Numerical value	Breakpoint address
<space>	Keyword	Address area of a breakpoint Set either of the following keywords: physical : physical address virtual : virtual address
<asid>	Numerical value	ASID value of a breakpoint when virtual is set to the <space> parameter.

Notes: 1. When no parameters are set, all software breakpoints are cleared.
2. When <address_space> and <asidopt> are not set, all software breakpoints that match the specified address are cleared.

Note: The parameters for this command differ according to the product. For the specifications of each product, refer to the online help.

Examples:

To clear all breakpoints:

```
>bc (RET)
```

To clear all software breakpoints whose address value is H'1000000:

```
>bc H'1000000 (RET)
```

To clear a software breakpoint whose virtual address is H'1000000, according to the ASID value at command input:

```
>bc H'1000000 space virtual (RET)
```

To clear the software breakpoint at virtual address H'1000000, whose ASID value is H'10:

```
>bc H'1000000 space virtual asid H'10 (RET)
```

Related Items:

BP, BD, BE, VC, VD, VE, and VS commands

[Breakpoints] window

[Break] dialog box

5.2.10 BREAKPOINT_DISPLAY: BD

Description:

Displays software breakpoints that have been set.

Format:

bd

Table 5.11 BREAKPOINT_DISPLAY Command Parameter

Parameter	Type	Description
None		

Example:

To display the software breakpoints that have been set:

>**bd** (*RET*)

```
H'00000110 physical enable
H'0000011c virtual asid H'0 disable
H'00000250 physical enable
```

Note: The items displayed with this command differ according to the product. For the display specifications of each product, refer to the online help.

Related Items:

BP, BC, and BE commands

[Breakpoints] window

[Break] dialog box

5.2.11 BREAKPOINT_ENABLE: BE

Description:

Enables or disables software breakpoints that have been set.

Format:

```
be <address> <address_space> <asidopt> <mode>

<address_space> = space <space>
<asidopt> = asid <asid>
```

Table 5.12 BREAKPOINT_ENABLE Command Parameters

Parameter	Type	Description
<address>	Numerical value	Breakpoint address
<space>	Keyword	Address area Set either of the following keywords: physical : physical address virtual : virtual address
<asid>	Numerical value	ASID value of a breakpoint when virtual is set to the <space> parameter.
<mode>	Keyword	Enables or disables breakpoints. Set either of the following keywords: enable : Enables breakpoints. disable : Disables breakpoints.

Examples:

To enable a software breakpoint at physical address H'1002:

```
>be H'1002 space physical enable (RET)
```

To enable a software breakpoint at logical address H'1000000, whose ASID value is H'10:

```
>be H'1000000 space virtual asid H'10 enable (RET)
```

- Notes:
1. The parameters for this command differ according to the product. For the specifications of each product, refer to the online help.
 2. The items displayed with this command differ according to the product. For the display specifications of each product, refer to the online help.

Related Items:

BC, BD, BP, VC, VD, VE, and VS commands

[Breakpoints] window

[Break] dialog box

5.2.12 DEVICE_TYPE; DE

Description:

Displays the currently selected device.

Format:

de

Table 5.13 DEVICE_TYPE Command Parameter

Parameter	Type	Description
None		

Example:

To display the currently selected device:

>de (RET)

Current device = SHxxxx

5.2.13 GO_OPTION: GP

Description:

Displays or sets the emulation mode.

Format:

Displays emulation mode.

gp

Sets emulation mode.

gp <eml_opt>

<eml_opt> = eml_mode <eml_mode>

Table 5.14 GO_OPTION Command Parameter

Parameter	Type	Description
<eml_mode>	Keyword	<p>Specifies the emulation mode.</p> <p>normal: Normal execution</p> <p>sequence1: Stops the user program only when the conditions are satisfied in the order of hardware breakpoints 2 to 1. Hardware breakpoints 1 and 2 must be set.</p> <p>no_break: Makes software breakpoints and hardware breakpoints temporarily invalid and executes the user program.</p>

- Notes:
1. The sequential break function differs according to emulator products. For details, refer to online help.
 2. The parameters for this command differ according to the product. For the specifications of each product, refer to the online help.

Examples:

To display the currently set emulation mode for user program execution:

```
>gp (RET)
```

```
Emulator execution mode = Sequential break Condition 2-1
```

To set the normal emulation mode for user program execution:

```
>gp eml_mode normal (RET)
```

Note: The items displayed with this command differ according to the product. For the display specifications of each product, refer to the online help.

Related Items:

BCS and BS commands,

[Breakpoints] window,

[Break], [Break Condition], and [Configuration] dialog boxes

5.2.14 JTAG_CLOCK: JCK

Description:

Displays or sets the JTAG clock (TCK) frequency.

Format:

Displays the JTAG clock (TCK) frequency.

```
jck
```

Sets the JTAG clock (TCK) frequency.

```
jck <jck_opt>
```

Table 5.15 JTAG_CLOCK Command Parameter

Parameter	Type	Description
<jck_opt>	Numerical value	Sets one of the JTAG clock (TCK) frequency. (PCMCIA used: 3.75 MHz, 7.5 MHz, or 15 MHz) 3: 3.75 MHz 7: 7.5 MHz 15: 15 MHz (PCI used: 4.125 MHz, 8.25 MHz, or 16.5 MHz) 4: 4.125 MHz 8: 8.25 MHz 16: 16.5 MHz

Note: The range of frequencies that the Hitachi-UDI operates at differs according to the devices used. For details, refer to section 6.5.4, Notes on Using the JTAG Clock (TCK) and AUD Clock (AUDCK).

Examples:

(when PCMCIA used):

To set the JTAG clock (TCK) frequency:

```
> jck 15 (RET)
```

```
JTAG Clock    15MHz
```

To display the JTAG clock (TCK) frequency:

```
> jck (RET)
```

```
JTAG Clock    15MHz
```

(when PCI used):

To set the JTAG clock (TCK) frequency:

```
> jck 16 (RET)
```

```
JTAG Clock    16.5MHz
```

To display the JTAG clock (TCK) frequency:

```
> jck (RET)
```

```
JTAG Clock    16.5MHz
```

5.2.15 MEMORYAREA_SET: MAS

Description:

Displays and sets memory area at command input, such as load, verify, save, memory display, or memory change.

Format:

Displays memory area.

mas

Sets memory area.

mas <memory_area> [**<asidopt>**]
<asidopt> = **asid** <asid>

Table 5.16 MEMORYAREA_SET Command Parameters

Parameter	Type	Description
<memory_area>	Keyword	Sets memory area. normal: Does not set memory area. physical: Sets physical address area. virtual: Sets virtual address area.
<asid>	Numerical value	Sets an ASID value from 0 to H'FF when virtual is set to the <memory_area> parameter.

Notes: 1. When virtual is set and <asid> is omitted in <memory_area>, a virtual address corresponding to the ASID value at command input is accessed.
2. When a memory is accessed, the contents in the instruction cache are disabled after this command is executed.

Examples:

To display a memory area for command input, such as load, verify, save, memory display, and memory change:

```
>mas (RET)  
memoryarea_set virtual asid H'10
```

To set a memory area for command input, such as load, verify, save, memory display, and memory change, to a physical address area:

```
>mas physical (RET)
```

To set a memory area for command input, such as load, verify, save, memory display, and memory change, to a virtual address area whose ASID value is H'10:

```
>mas virtual asid H'10 (RET)
```

5.2.16 REFRESH: RF

Description:

Updates the HDI memory information.

Format:

rf

Table 5.17 REFRESH Command Parameter

Parameter	Type	Description
None		

Example:

To update the HDI memory information:

>rf (RET)

5.2.17 **RESTART: RST**

Description:

Restarts the emulator. The settings of breakpoints or trace acquisition conditions are not reset here.

Format:

`rst`

Table 5.18 RESTART Command Parameter

Parameter	Type	Description
None		

Example:

To restart the emulator:

```
>rst (RET)
```

5.2.18 STATUS: STS

Description:

Displays status information of the emulator.

Format:

sts

Table 5.19 STATUS Command Parameter

Parameter	Type	Description
None		

Example:

To display status information of the emulator:

```
>sts (RET)
Emulator Status
Connected to:      SHxxxx E10A Emulator (E10A PC Card Driver)
CPU               SHxxxx
Run status        Break
Cause of last break BREAK POINT
Run time count    0h:0min:0s:10ms
Emulator mode     Normal
Big endian
AUD              Exist
```

Note: The items displayed with this command differ according to the product. For the display specifications of each product, refer to the online help.

5.2.19 STEP_INTERRUPT: SI

Description:

Sets or displays the enable or disable status of interrupts during step execution. If enabled, interrupts occur and stop at the top address in the interrupt routine.

Format:

Displays the enable or disable status of interrupts during step execution.

si

Sets the enable or disable status of interrupts during step execution.

si <mode>

Table 5.20 STEP_INTERRUPT Command Parameter

Parameter	Type	Description
<mode>	Keyword	Enables or disables interrupts during step execution. Set either of the following: enable : Enables interrupts. disable : Disables interrupts.

Example:

To enable interrupts during step execution:

```
si enable (RET)
```

To display interrupt status during step execution:

```
>si (RET)  
Emulator step interrupt mode = ENABLE
```

5.2.20 TRACE_DISPLAY: TD

Description:

Displays the acquired trace information. The information to be acquired is the branch source and branch destination addresses when a branch is made during the user program execution.

Format:

td

Table 5.21 TRACE_DISPLAY Command Parameter

Parameter	Type	Description
None		

Notes:

1. In some cases, the emulator address may be acquired. In such a case, the following message will be displayed at the place where the mnemonic or operand is displayed. Ignore this address because it is not a user program address.
*** EML ***
2. If a TLB error occurs while acquired trace information is displayed, the following error message will be displayed:

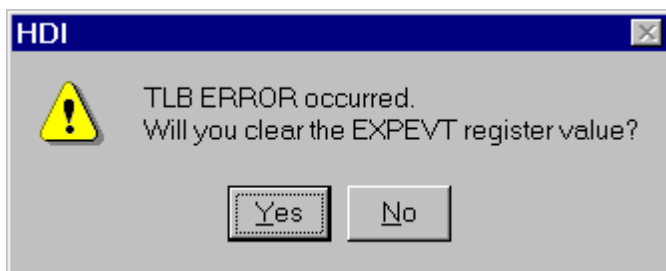


Figure 5.1 TLB Error Message Dialog

Example:

To display trace information according to information acquired during user program execution:

>td (RET)				
IP	TYPE	ADDR	MNEMONIC	OPERAND
-D'xxxxxx	BRANCH	01000010	JSR	@R0
	DESTINATION	01000020	MOV.L	R1, @R1
(a)	(b)	(c)	(d)	(e)

- (a) Instruction pointer (signed decimal)
- (b) Types of branch source or branch destination
 BRANCH: Branch source
 DESTINATION: Branch destination
- (c) Address of instruction word
- (d) Instruction mnemonic
- (e) Instruction operand

Related Items:

TM command

[Trace] window

[Trace Acquisition] dialog box

5.2.21 UBC_MODE:UM

Description:

Sets or displays the current UBC state.

Format:

um [<ubc_mode>]

Table 5.22 UBC_MODE Command Parameter

Parameter	Type	Description
<ubc_mode>	Keyword	Selects the UBC mode. EML: Uses the UBC as Break Condition by the emulator. USER: Releases the UBC to the user. (Break Condition cannot be used.)

Note: When <option> is omitted, the current setting conditions are displayed.

Note: The parameters for this command differ according to the product. For the specifications of each product, refer to the online help.

Examples:

To release the UBC to the user:

```
>UBC_mode user (RET)
UBC_mode = USER
```

To display the current UBC state:

```
>UBC_mode (RET)
UBC_mode = EML
```

Note: The items displayed with this command differ according to the product. For the display specifications of each product, refer to the online help.

Related Item:

[Configuration] dialog box

5.2.22 VPMAP_CLEAR: VC

Description:

Clears the address translation (VP_MAP) table that is set in the emulator.

Format:

vc [<address>]

Table 5.23 VPMAP_CLEAR Command Parameter

Parameter	Type	Description
<address>	Numerical value	Sets the virtual start address of the VP_MAP table range to be cleared.

Note: All contents in the VP_MAP table are cleared if <address> is omitted.

Examples:

To clear all the contents in the VP_MAP table:

```
>vc (RET)
```

To clear the contents in the VP_MAP table range starting from virtual address H'4000:

```
>vc H'4000 (RET)
```

Related Items:

VD, VE, and VS commands

Note: This command is not supported in a device in which the MMU is not built-in.

5.2.23 VPMAP_DISPLAY: VD

Description:

Displays the address translation (VP_MAP) table set in the emulator.

Format:

vd

Table 5.24 VPMAP_DISPLAY Command Parameter

Parameter	Type	Description
None		

Example:

To display the VP_MAP table:

```
>vd (RET)
<VADDR_TOP>          <VADDR_END>          <PADDR_TOP>
01000000             0100ffff             02000000
01010000             0101ffff             03000000
ENABLE
```

<VADDR_TOP>, <VADDR_END>, and <PADDR_TOP> represent the virtual start address, the virtual end address, and the physical start address, respectively. ENABLE or DISABLE in the last line indicates that the VP_MAP table is valid or invalid.

Related Items:

VC, VE, and VS commands

Note: This command is not supported in a device in which the MMU is not built-in.

5.2.24 VPMAP_ENABLE: VE

Description:

Enables or disables the setting of the address translation (VP_MAP) table in the emulator.

Format:

ve <enable>

Table 5.25 VPMAP_ENABLE Command Parameter

Parameter	Type	Description
<enable>	Keyword	Enables or disables the setting of the VP_MAP table. enable : Enables the setting of the VP_MAP table. disable : Disables the setting of the VP_MAP table.

Note: The setting of the VP_MAP table is disabled at the emulator initiation.

Example:

To enable the setting of the VP_MAP table:

```
>ve enable (RET)
```

Related Items:

VC, VD, and VS commands

Note: This command is not supported in a device in which the MMU is not built-in.

5.2.25 VPMAP_SET: VS

Description:

Sets the address translation (VP_MAP) table in the emulator.

Format:

```
vs <lsaddress> <leaddress> <paddress>
```

Table 5.26 VPMAP_SET Command Parameters

Parameter	Type	Description
<lsaddress>	Numerical value	Specifies the virtual start address to be set in the VP_MAP table in the page size units supported by the MMU. Setting a physical fixed area or an internal I/O area as a virtual address will result in an error.
<leaddress>	Numerical value	Specifies the virtual end address to be set in the VP_MAP table in the page size units supported by the MMU. Setting a physical fixed area or an internal I/O area as a virtual address will result in an error.
<paddress>	Numerical value	Specifies the physical start address to be set in the VP_MAP table.

Note: The virtual address range to be newly set cannot overlap a virtual address that has already been set. Clear the previous set range when making a new setting.

Example:

To set the virtual address range H'4000 to H'4FFF to be translated into the physical address range H'400000 to H'400FFF:

```
>vs H'4000 H'4fff H'400000 (RET)
```

Related Items:

VC, VD, and VE commands

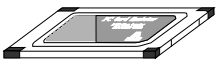
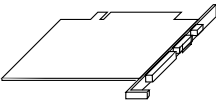

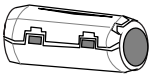
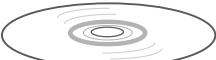
Note: This command is not supported in a device in which the MMU is not built-in.

Section 6 SH7294 E10A Emulator Specifications

6.1 Components of the Emulator

The SH7294 E10A emulator supports the SH7294. Table 6.1 lists the components of the emulator.

Table 6.1 Components of the Emulator (HS7294KCM01H, HS7294KCM02H, HS7294KCI01H, or HS7294KCI02H)

Classification	Component	Appearance	Quantity	Remarks
Hard-ware	Card emulator		1	HS7294KCM01H (PCMCIA: 14-pin type): Depth: 85.6 mm, Width: 54.0 mm, Height: 5.0 mm, Mass: 27.0 g
		(PCMCIA)		HS7294KCM02H (PCMCIA: 36-pin type): Depth: 85.6 mm, Width: 54.0 mm, Height: 5.0 mm, Mass: 28.0 g
		or		HS7294KCI01H (PCI: 14-pin type): Depth: 122.0 mm, Width: 96.0 mm, Mass: 80.0 g
		 (PCI)		HS7294KCI02H (PCI: 36-pin type): Depth: 122.0 mm, Width: 96.0 mm, Mass: 90.0 g
	User system interface cable		1	HS7294KCM01H (PCMCIA: 14-pin type): Length: 80 cm, Mass: 45.0 g
				HS7294KCM02H (PCMCIA: 36-pin type): Length: 30 cm, Mass: 55.0 g
				HS7294KCI01H (PCI: 14-pin type): Length: 150 cm, Mass: 86.0 g
				HS7294KCI02H (PCI: 36-pin type): Length: 80 cm, Mass: 69.0 g
	Ferrite core (connected with the user interface cable)		1	Countermeasure for EMI* (only for HS7294KCM02H and HS7294KCI02H)
Soft-ware	SH7294 E10A emulator setup program, SH7294 E10A Emulator User's Manual, and Hitachi Debugging Interface User's Manual		1	HS7294KCM01SR, HS7294KCM01HJ, HS7294KCM01HE, HS6400DIIW5SJ, and HS6400DIIW5SE (provided on a CD-R)

Note: The EMI is an abbreviation of the Electrical Magnetic Interference.

For EMI countermeasure, use the ferrite core by connecting the user interface cable. When the user interface cable is connected with the emulator or user system, connect the ferrite core in the user system as shown in figure 6.1.

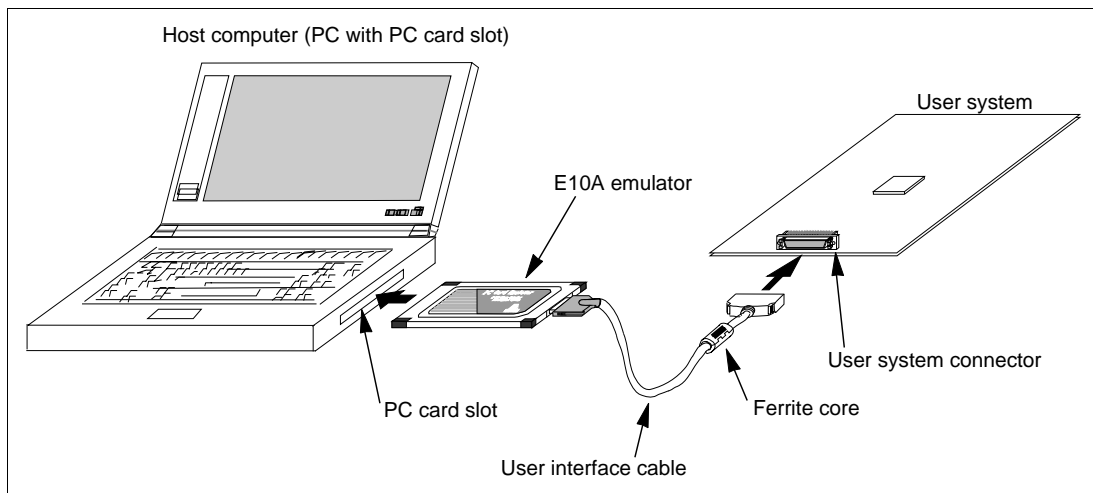


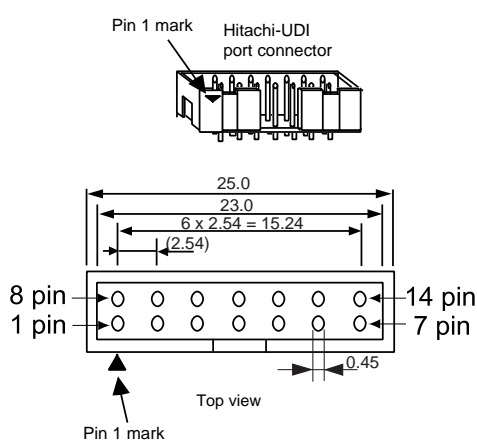
Figure 6.1 Connecting Ferrite Core

6.2 Pin Arrangement of the Hitachi-UDI Port Connector

Figure 6.2 shows the pin arrangement of the Hitachi-UDI port connector (14 pins).

CAUTION

Note that the pin number assignment of the Hitachi-UDI differs from that of the connector manufacturer.



Pin No.	Signal	Input/ Output* 1	SH7294 Pin No.	Note
1	TCK	Input	C2	
2*2	/TRST	Input	E2	
3	TDO	Output	E1	
4*2	/ASEBRKAK	Output	G4	
5	TMS	Input	F4	
6	TDI	Input	D2	
7*2	/RESETP	Output	H15	
11*2	/CA	Output	H18	
8 to 10 and 12 to 13	GND	—	—	
14*3	GND	Output	—	

Notes: 1. Input to or output from the user system.

2. The slash (/) means that the signal is active-low.

3. The emulator monitors the GND signal of the user system and detects whether or not the user system is connected.

Figure 6.2 Pin Arrangement of the Hitachi-UDI Port Connector (14 Pins)

- Notes:**
- 1. Handling of the TCK, TMS, TDI, /TRST, /ASEBRKAK, /ASEMD0, and /CA pins depends on the use conditions of the Hitachi-UDI as follows:**
 - (a) When the user system is used by connecting the emulator, the /ASEMD0 pin must be grounded. The /CA pin must be pulled up by a resistance of several kilo-ohms. The TCK, TMS, TDI, /TRST, and /ASEBRKAK pins must be pulled up by a resistance of several kilo-ohms to protect the devices.**
 - (b) When the emulator is used and the user system is independently used, the /ASEMD0 pin should be switched on the board as the method of handling this pin depends on whether the emulator is used or not. The /CA pin must be pulled up by a resistance of several kilo-ohms. The TCK, TMS, TDI, and /ASEBRKAK pins must be pulled up by a resistance of several kilo-ohms to protect the devices. The /TRST pin must be pulled down by a resistance of several kilo-ohms and controlled individually.**
 - (c) When the user system is independently used without using the emulator and Hitachi-UDI, the /ASEMD0 and /CA pins must be pulled up by a resistance of several kilo-ohms. It is recommended to pull up the /ASEBRKAK pins by a resistance of several kilo-ohms. It is recommended to pull up the TCK, TMS, and TDI pins by a resistance of several kilo-ohms.
For the /TRST pin, input the same signal as for the /RESETP pin or pull it down by a resistance of several kilo-ohms.**
 - 2. The /RESET signal in the user side is input to the /RESETP pin of the SH7294. Connect this pin to the Hitachi-UDI port connector as the output from the user system.**
 - 3. When the U standby function is not used, do not connect the /CA pin to the Hitachi-UDI port connector.**
 - 4. To use the emulator, pull up the /CA pin by a resistance of several kilo-ohms, whether the U standby function is used or not.**
 - 5. The /TRST pin must be low for a certain period when the emulator is turned on whether the H-UDI is used or not. Input the same signal as for the /RESETP pin to the /TRST pin, or pull it down by a resistance of several kilo-ohms. After the /RESETP pin is reset, set HIZB9 to 0 in the HIZCRB register to cut out the current in the /TRST pin.**

Figure 6.3 shows the pin arrangement of the Hitachi-UDI port connector (36 pins).

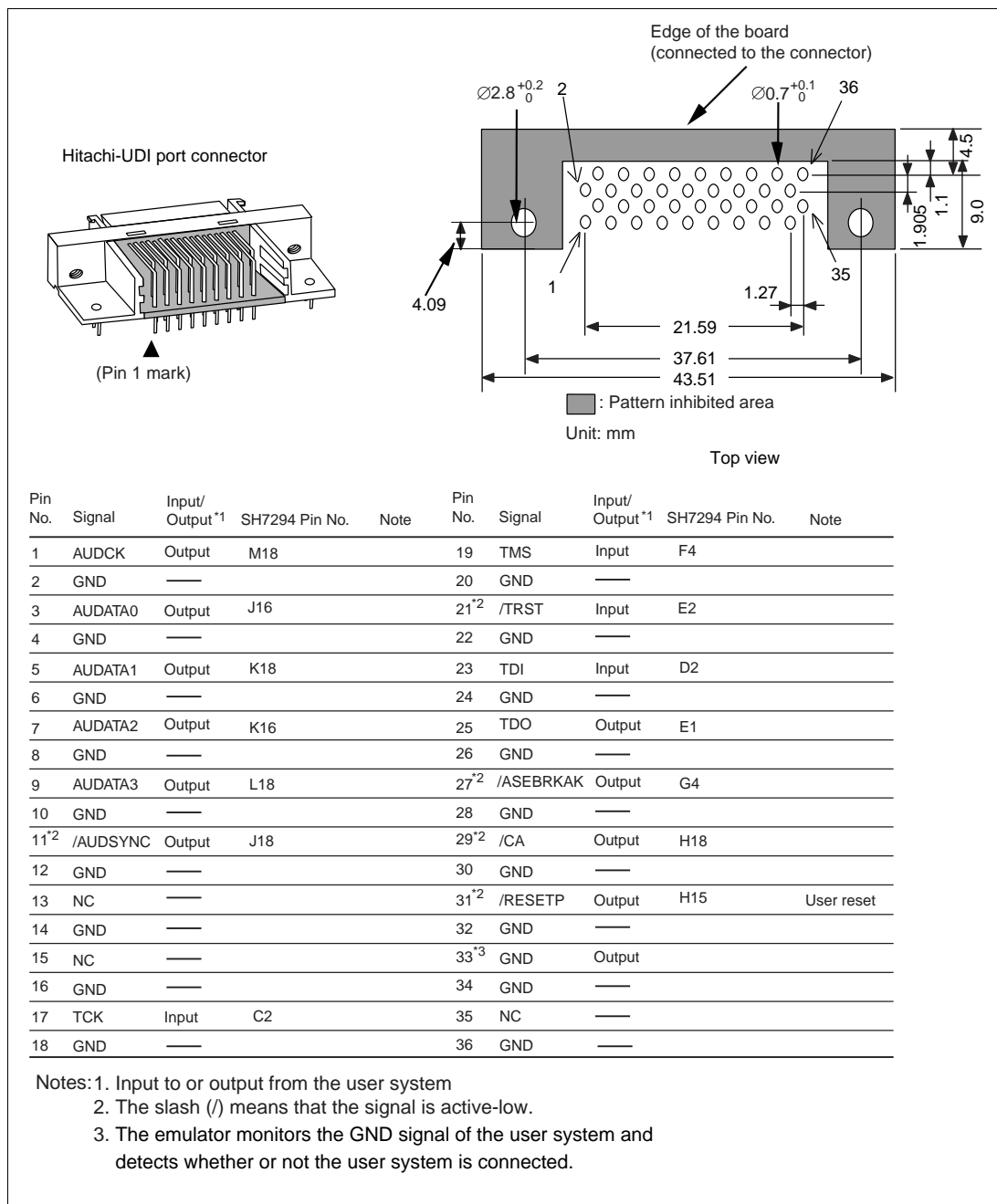


Figure 6.3 Pin Arrangement of the Hitachi-UDI Port Connector (36 Pins)

- Notes:**
- 1. Handling of the TCK, TMS, TDI, /TRST, /ASEBRKAK, /ASEMD0, and /CA pins depends on the use conditions of the Hitachi-UDI as follows:**
 - (a) When the user system is used by connecting the emulator, the /ASEMD0 pin must be grounded. The /CA pin must be pulled up by a resistance of several kilo-ohms. The TCK, TMS, TDI, /TRST, and /ASEBRKAK pins must be pulled up by a resistance of several kilo-ohms to protect the devices.**
 - (b) When the emulator is used and the user system is independently used, the /ASEMD0 pin should be switched on the board as the method of handling this pin depends on whether the emulator is used or not. The /CA pin must be pulled up by a resistance of several kilo-ohms. The TCK, TMS, TDI, and /ASEBRKAK pins must be pulled up by a resistance of several kilo-ohms to protect the devices. The /TRST pin must be pulled down by a resistance of several kilo-ohms and controlled individually.**
 - (c) When the user system is independently used without using the emulator and Hitachi-UDI, the /ASEMD0 and /CA pins must be pulled up by a resistance of several kilo-ohms. It is recommended to pull up the /ASEBRKAK pins by a resistance of several kilo-ohms. It is recommended to pull up the TCK, TMS, and TDI pins by a resistance of several kilo-ohms.
For the /TRST pin, input the same signal as for the /RESETP pin or pull it down by a resistance of several kilo-ohms.**
 - 2. The /RESET signal in the user side is input to the /RESETP pin of the SH7294. Connect this pin to the Hitachi-UDI port connector as the output from the user system.**
 - 3. When the U standby function is not used, do not connect the /CA pin to the Hitachi-UDI port connector.**
 - 4. To use the emulator, pull up the /CA pin by a resistance of several kilo-ohms, whether the U standby function is used or not.**
 - 5. The /TRST pin must be low for a certain period when the emulator is turned on whether the H-UDI is used or not. Input the same signal as for the /RESETP pin to the /TRST pin, or pull it down by a resistance of several kilo-ohms. After the /RESETP pin is reset, set HIZB9 to 0 in the HIZCRB register to cut out the current in the /TRST pin.**

6.3 User System Interface Circuit

The emulator is connected to the user system via the user interface cable. Figure 6.4 shows the user system interface circuit of the emulator (HS7294KCM01H).

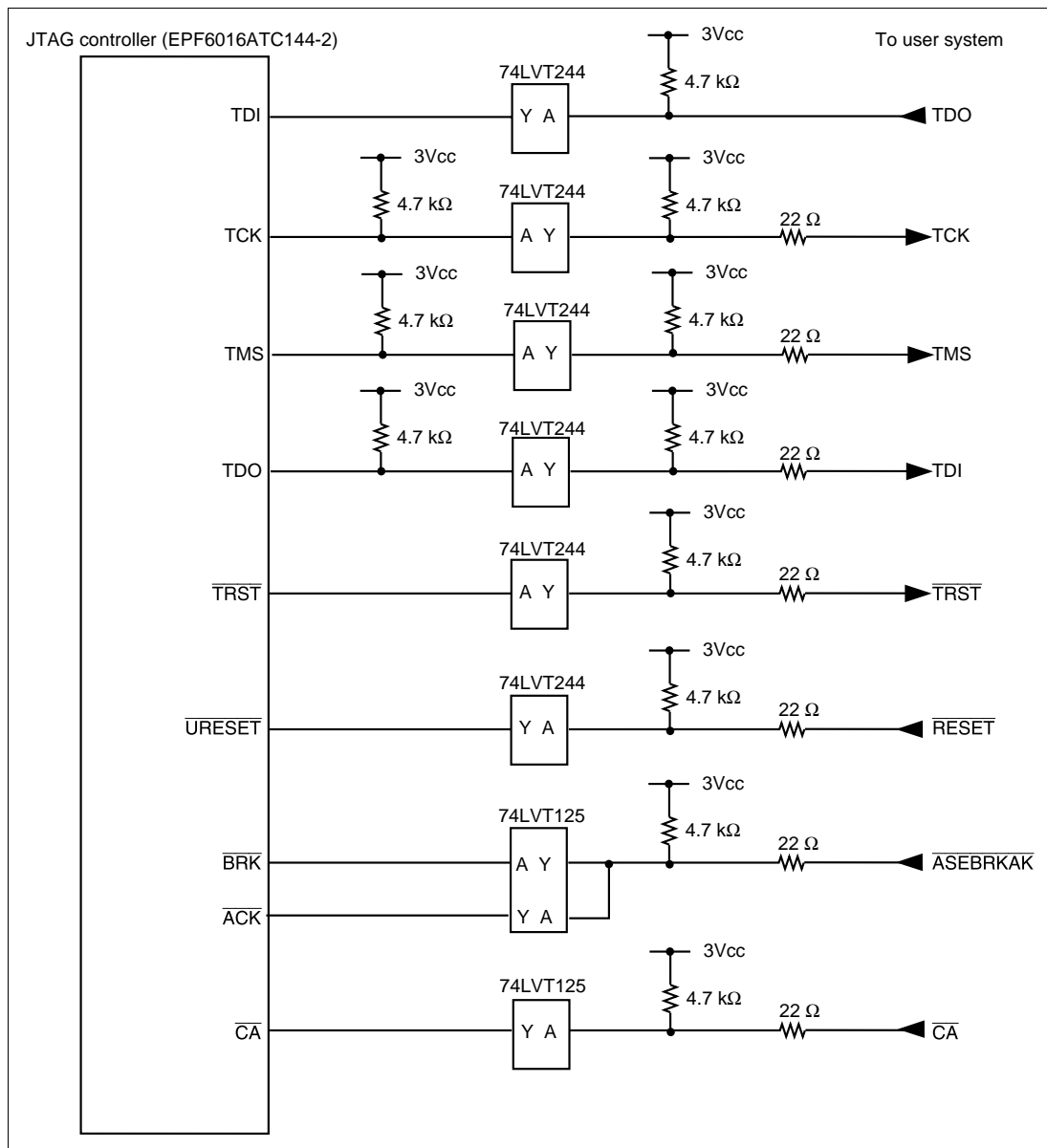


Figure 6.4 User System Interface Circuit (HS7294KCM01H)
(Model Name: HS0005KCM07H)

The user system interface circuits of the emulator (HS7294KCM02H) are shown. Figures 6.5 and 6.6 show the circuits of the Hitachi-UDI pin and AUD pin, respectively.

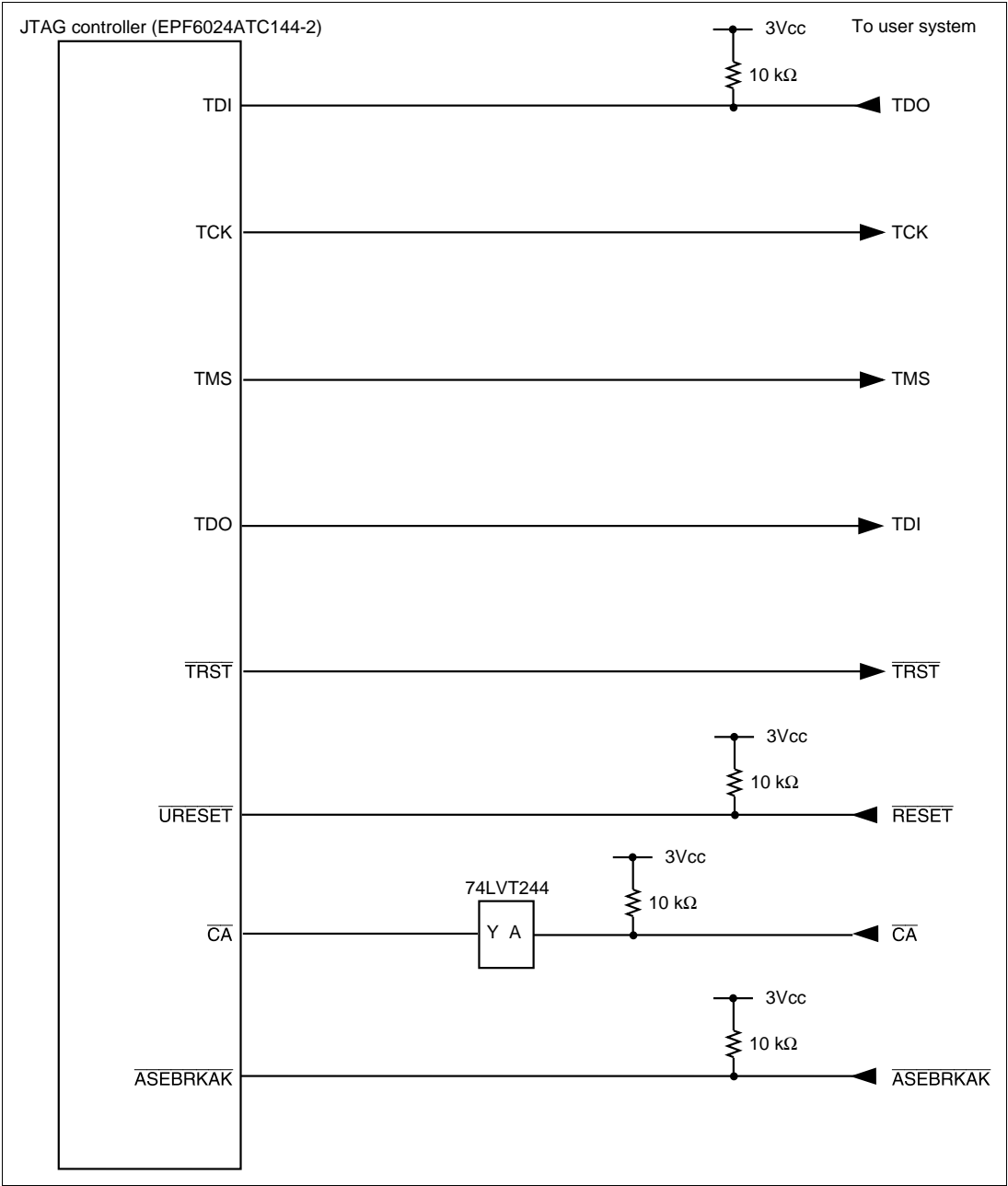
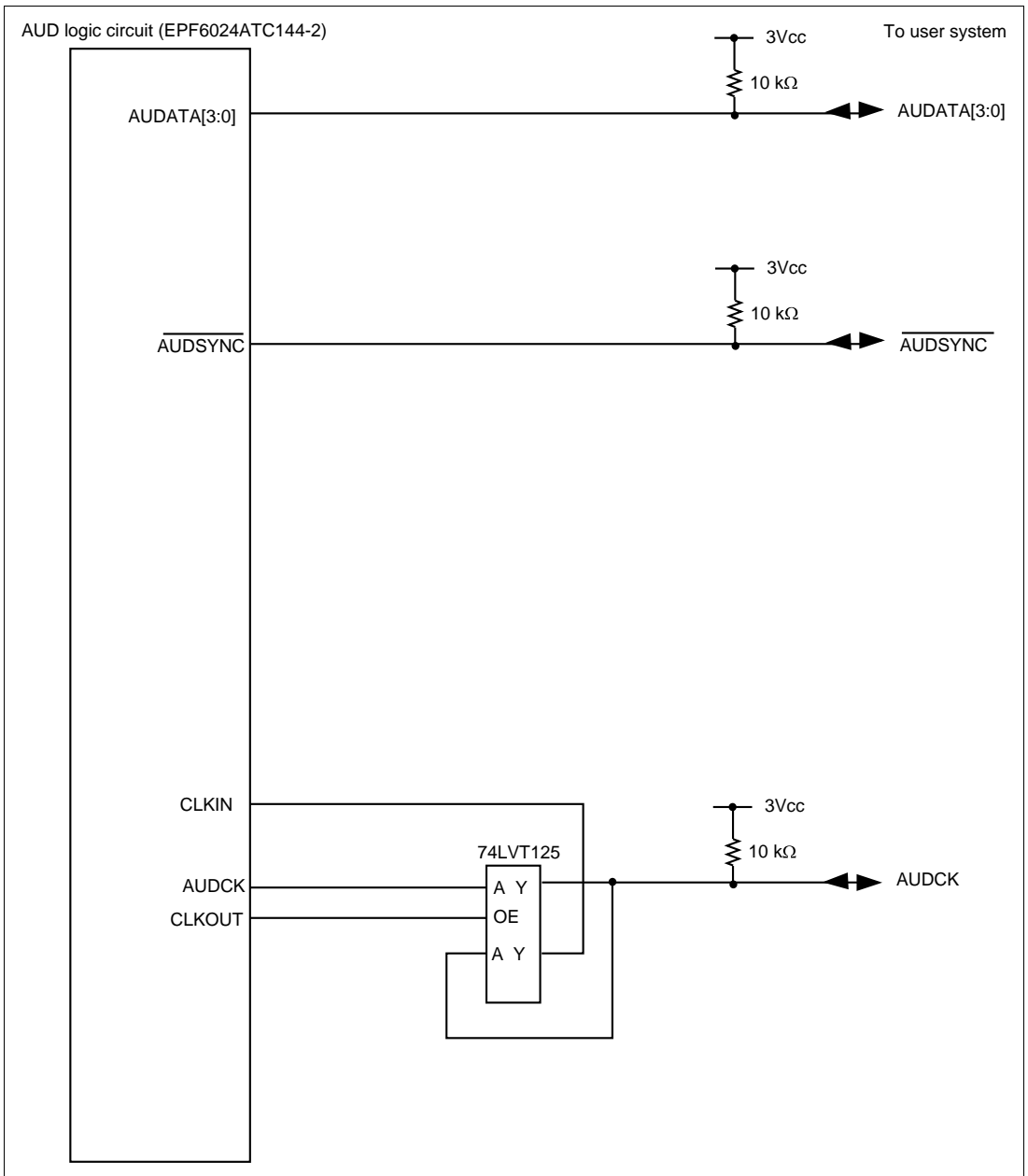


Figure 6.5 User System Interface Circuit of the Hitachi-UDI Pin (HS7294KCM02H)
(Model Name: HS0005KCM08H)



**Figure 6.6 User System Interface Circuit of the AUD Pin (HS7294KCM02H)
(Model Name: HS0005KCM08H)**

Figure 6.7 shows the user system interface circuit of the emulator (HS7294KCI01H).

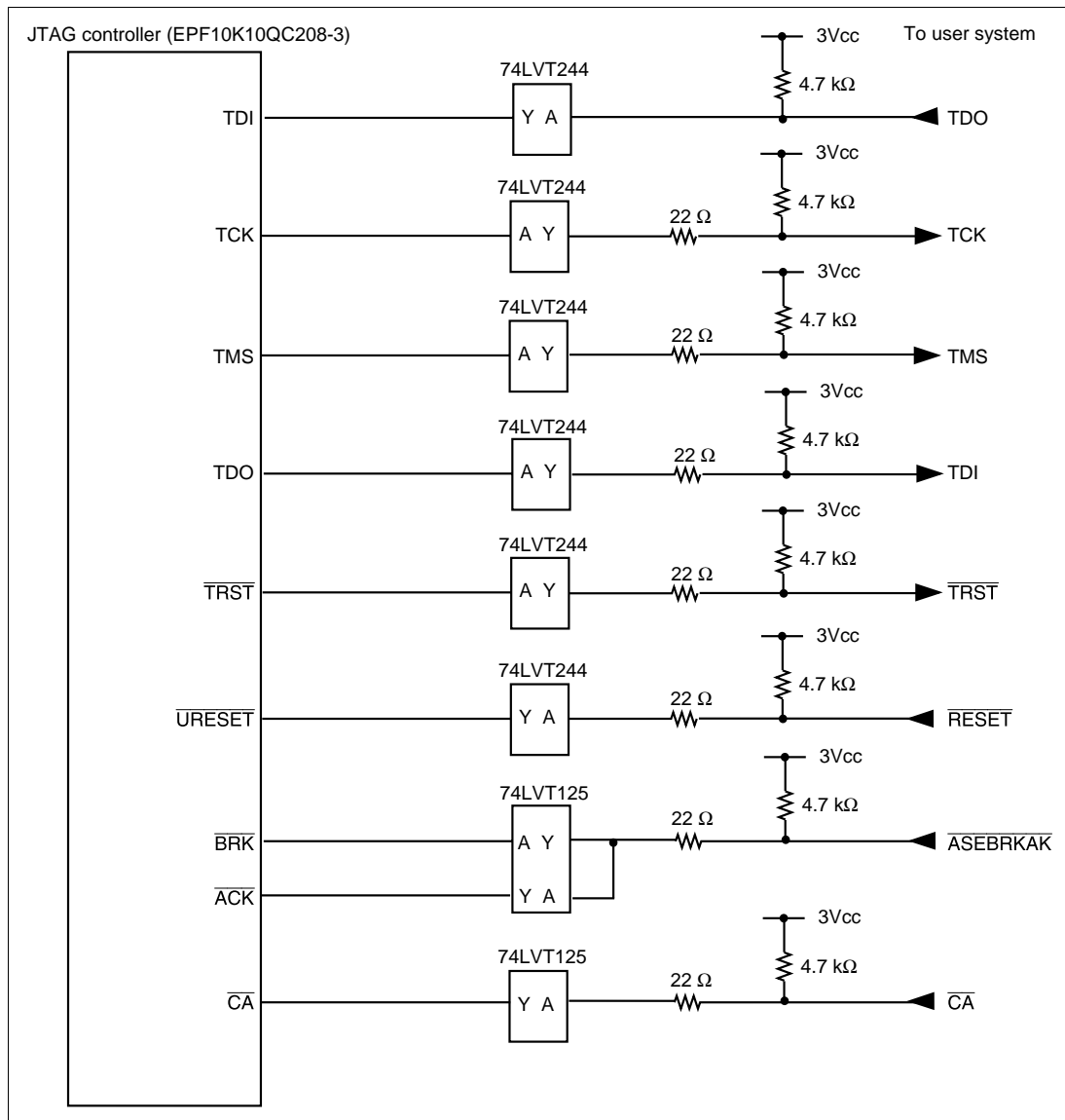


Figure 6.7 User System Interface Circuit (HS7294KCI01H)
(Model Name: HS0005KCI07H)

The user system interface circuits of the emulator (HS7294KCI02H) are shown. Figures 6.8 and 6.9 show the circuits of the Hitachi-UDI pin and AUD pin, respectively.

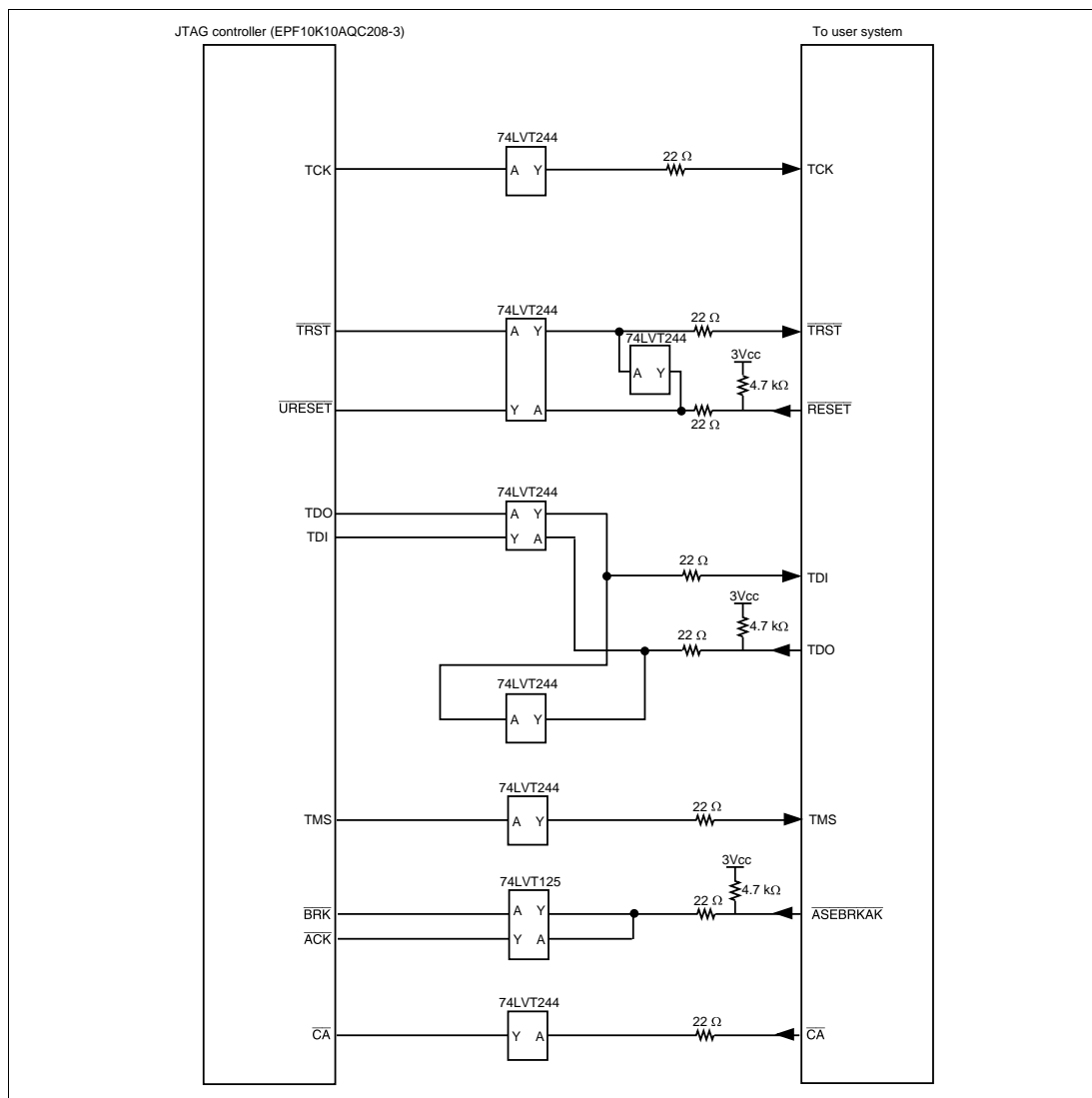
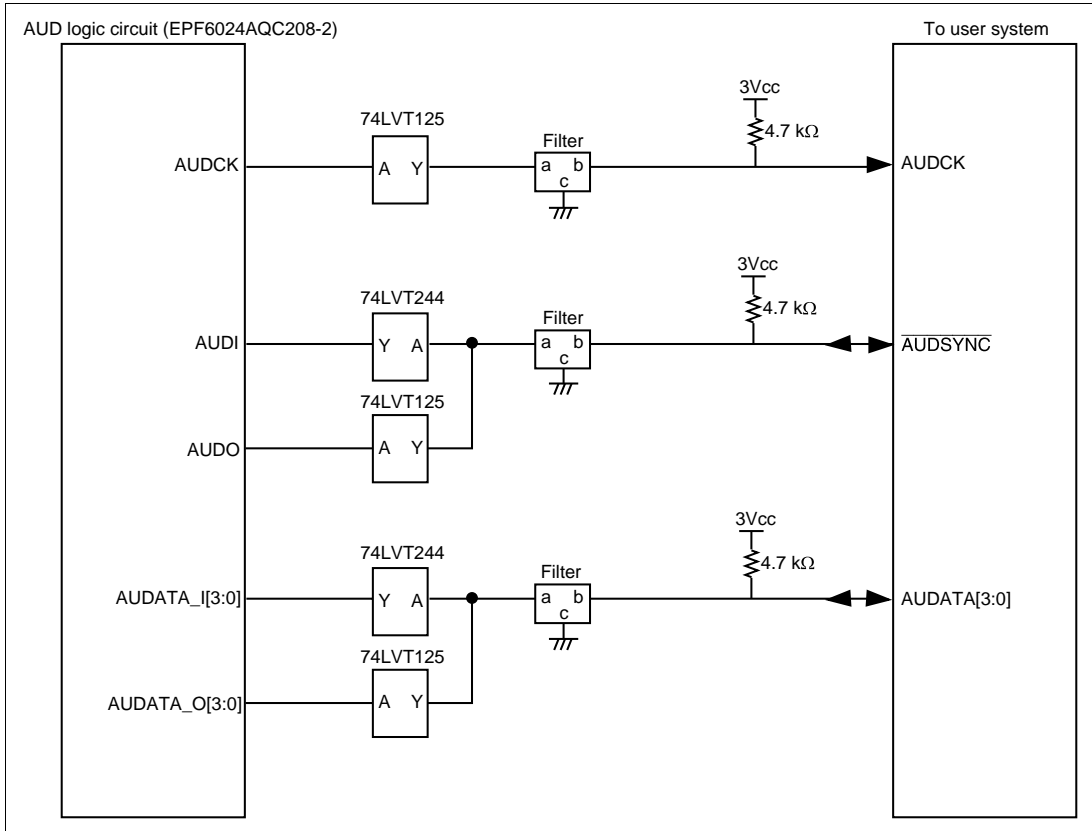


Figure 6.8 User System Interface Circuit of the Hitachi-UDI Pin (HS7294KCI02H)
(Model Name: HS0005KCI08H)



**Figure 6.9 User System Interface Circuit of the AUD Pin (HS7294KCI02H)
(Model Name: HS0005KCI08H)**

6.4 Differences between the SH7294 and the Emulator

- When the emulator system is initiated, it initializes the general registers and part of the control registers as shown in table 6.2. The initial values of the actual SH7294 registers are undefined.

Table 6.2 Register Initial Values at Emulator Power-On

Register	Emulator at Power-on
R0 to R14	H'00000000
R15 (SP)	H'A0000000
R0_BANK to R7_BANK	H'00000000
PC	H'A0000000
SR	H'700000F0
GBR	H'00000000
VBR	H'00000000
MACH	H'00000000
MACL	H'00000000
PR	H'00000000
SPC	H'00000000
SSR	H'000000F0
RS	H'00000000
RE	H'00000000
MOD	H'00000000
A0G, A1G	H'00000000
A0, A1	H'00000000
X0, X1	H'00000000
Y0, Y1	H'00000000
M0, M1	H'00000000
DSR	H'00000000

- The emulator uses the Hitachi-UDI; do not access the Hitachi-UDI.
- Low-Power States (Sleep, Software Standby, Module Standby, and U Standby)

For low-power consumption, the SH7294 has sleep, software standby, module standby, and U standby states.

The sleep, software standby, and module standby states are switched using the SLEEP instruction. When the emulator is used, only the sleep state can be cleared with either the normal clearing

function or with the [Stop] button, and a break will occur. For the U standby state, refer to section 6.5.10, Notes on U Standby State.

Note: The memory must not be accessed or modified in sleep state.

- Reset Signals

The SH7294 reset signals are only valid during emulation started with clicking the GO or STEP-type button. If these signals are input from the user system in command input wait state, they are not sent to the SH7294.

Note: Do not break the user program when the /RESETP and /WAIT signals are being low. A TIMEOUT error will occur. If the /WAIT signal is fixed to low during break, a TIMEOUT error will occur at memory access.

- Direct Memory Access Controller (DMAC)

The DMAC operates even when the emulator is used. When a data transfer request is generated, the DMAC executes DMA transfer.

- Memory Access during User Program Execution

When a memory is accessed from the memory window, etc. during user program execution, the user program is resumed after it has stopped in the E10A emulator to access the memory. Therefore, realtime emulation cannot be performed.

The stopping time of the user program is as follows:

Environment:

Host computer: 650 MHz (Pentium® III)

SH7294: 60 MHz (CPU clock)

JTAG clock: 3.75 MHz

When a one-byte memory is read from the command-line window, the stopping time will be about 20 ms.

- Memory Access during User Program Break

The emulator can download the program for the flash memory area. Other memory write operations are enabled for the RAM area. Therefore, an operation such as memory write or BREAKPOINT should be set only for the RAM area.

- Cache Operation during User Program Break

When cache is enabled, the emulator accesses the memory by the following methods:

- At memory write: Writes through the cache, then writes to the memory.
- At memory read: Does not change the cache write mode that has been set.

Therefore, when memory read or write is performed during user program break, the cache state will be changed.

- Port G

The AUD and Hitachi-UDI pins are multiplexed as shown in table 6.3.

Table 6.3 Multiplexed Functions

Port	Function 1	Function 2
G	PTG5 input/output (port) ^{*1}	/ASEBRKAK (Hitachi-UDI)
G	PTG4 input/output (port) ^{*2}	/AUDSYNC (AUD)
G	PTG3 input/output (port) ^{*2}	AUDATA3 (AUD)
G	PTG2 input/output (port) ^{*2}	AUDATA2 (AUD)
G	PTG1 input/output (port) ^{*2}	AUDATA1 (AUD)
G	PTG0 input/output (port) ^{*2}	AUDATA0 (AUD)

Notes: 1. PTG5 cannot be used when the E10A emulator is used.
 2. Function 1 can be used when the AUD pins of the device are not connected to the SH7294 E10A emulator. Note that the SH7294 E10A setting should be made so that function 2 would be forcibly used during activation. If function 1 is to be used, the setting should be made by the pin function controller.

- UBC

When [User] is specified in the [UBC mode] list box in the [Configuration] dialog box, the UBC can be used in the user program.

Do not use the UBC in the user program as it is used by the E10A emulator when [EML] is specified in the [UBC mode] list box in the [Configuration] dialog box.

- MFI Boot Mode

When the MFI boot mode is used, be sure to allocate the boot program from the top of MFRAM.

- Using RWDT

At power-on reset, the operation of RWDT is enabled. When RWDT is not used, be sure to disable the operation of RWDT at the top of the user-reset program.

- Memory Access during Break

In the enabled MMU, when a memory is accessed and a TLB error occurs during break, it can be selected whether the TLB exception is controlled or the program jumps to the user exception handler in [TLB Mode] in the [Configuration] dialog box. When [TLB miss exception is enable] is selected, a Communication Timeout error will occur if the TLB exception handler does not operate correctly. When [TLB miss exception is disable] is selected, the program does not jump to the TLB exception handler even if a TLB exception occurs. Therefore, if the TLB exception handler does not operate correctly, a Communication Timeout error will not occur but the memory contents may not be correctly displayed.

- Loading Sessions

Information in [JTAG clock] of the [Configuration] dialog box cannot be recovered by loading sessions. Thus the TCK value will be as follows:

When HS7294KCI01H or HS7294KCI02H is used: TCK = 1.031 MHz

When HS7294KCM01H or HS7294KCM02H is used: TCK = 0.937 MHz

6.5 Specific Functions for the SH7294 E10A Emulator

The SH7294 E10A emulator supports the following useful function:

- Performance measurement function
Measures several performances, such as the cache-miss count.

The SH7294 E10A emulator does not support the following function:

- Profiler function

6.5.1 Emulator Driver Selection

Table 6.4 shows drivers which are selected in the [E10A Driver Details] dialog box.

Table 6.4 Type Number and Driver

Type Number	Driver
HS7294KCM01H	E10A PC Card Driver 7
HS7294KCM02H	E10A PC Card Driver 8
HS7294KCI01H	E10A PCI Card Driver 7
HS7294KCI02H	E10A PCI Card Driver 8

6.5.2 Break Condition Functions

In addition to BREAKPOINT functions, the emulator has Break Condition functions. In the HDI, three types of conditions can be set under Break Condition 1, 2, 3. Table 6.5 lists these conditions of Break Condition.

Table 6.5 Types of Break Conditions

Break Condition Type	Description
Address bus condition (Address)	Breaks when the SH7294 address bus value or the program counter value matches the specified value.
Data bus condition (Data)	Breaks when the SH7294 data bus value matches the specified value. Byte, word, or longword can be specified as the access data size.
X-Bus or Y-Bus condition (Address and data)	Breaks when the X-Bus or Y-Bus address bus or data bus matches the specified value.
Bus state condition (Bus State)	<p>There are two bus state condition settings:</p> <p>Read/Write condition: Breaks when the SH7294 RD or RDWR signal level matches the specified condition.</p> <p>Bus state condition: Breaks when the operating state in an SH7294 bus cycle matches the specified condition.</p> <p>Types of buses that can be specified are listed below.</p> <ul style="list-style-type: none">• L-bus (CPU-ALL): Indicates an instruction fetch and data access, including a hit to the cache memory.• L-bus (CPU-Data): Indicates a data access by the CPU, including a hit to the cache memory.• I-bus (CPU.DMA): Indicates a CPU cycle when the cache memory is not hit, and a data access by the DMA.
Internal I/O break condition	Breaks when the SH7294 accesses the internal I/O.
LDTLB instruction break condition	Breaks when the SH7294 executes the LDTLB instruction.
Count	Breaks when the conditions set are satisfied the specified number of times.

Note: When U-RAM or X/Y-RAM is accessed from the P0 space, the I-bus must be selected, and when accessed from the P2 space, the L-bus must be selected. When cache fill cycle is acquired, the I-bus must be selected.

Table 6.6 lists the combinations of conditions that can be set under Break Condition 1, 2, 3.

Table 6.6 Dialog Boxes for Setting Break Conditions

Dialog Box	Type					
	Address Bus Condition (Address)	Data Bus Condition (Data)	Bus State Condition (Bus Status)	Count Condition (Count)	Internal I/O Break	LDTLB Instruction Break
[Break Condition 1] dialog box	O	O	O	O	X	X
[Break Condition 2] dialog box	O	X	O	X	X	X
[Break Condition 3] dialog box	X	X	X	X	O	O

Note: O: Can be set in the dialog box.
X: Cannot be set in the dialog box.

6.5.3 Trace Functions

The SH7294 E10A emulator supports the trace functions listed in table 6.7.

Table 6.7 Trace Functions

Function	Internal Trace	AUD Trace
Branch trace	Supported (eight branches)	Supported
Range memory access trace	Not supported	Supported
Software trace	Not supported	Supported

Table 6.8 shows the type numbers that the AUD function can be used.

Table 6.8 Type Number and AUD Function

Type Number	AUD Function
HS7294KCM01H, HS7294KCI01H	Not supported
HS7294KCM02H, HS7294KCI02H	Supported

AUD Trace Functions: This function is operational when the AUD pin of the device is connected to the emulator. Table 6.9 shows the AUD trace acquisition mode that can be set in each trace function.

Table 6.9 AUD Trace Acquisition Mode

Type	Mode	Description
Continuous trace occurs	Realtime trace	When the next branch occurs while the trace information is being output, the trace information being output is output but the next trace information is not output. The user program can be executed in realtime, but some trace information may be lost.
	Non realtime trace	When the next branch occurs while the trace information is being output, the CPU stops operations until the information is output. The user program is not executed in realtime.
Trace buffer full	Trace continue	This function overwrites the oldest trace information to store the latest trace information.
	Trace stop	After the trace buffer becomes full, the trace information is no longer acquired. (The user program is continuously executed.)

To set the AUD trace acquisition mode, click the [Trace] window with the right mouse button and select [Acquisition] from the pop-up menu to display the [Trace Acquisition] dialog box. The AUD trace acquisition mode can be set in the [AUD mode1] or [AUD mode2] group box in the [Trace mode] page of the [Trace acquisition] dialog box.

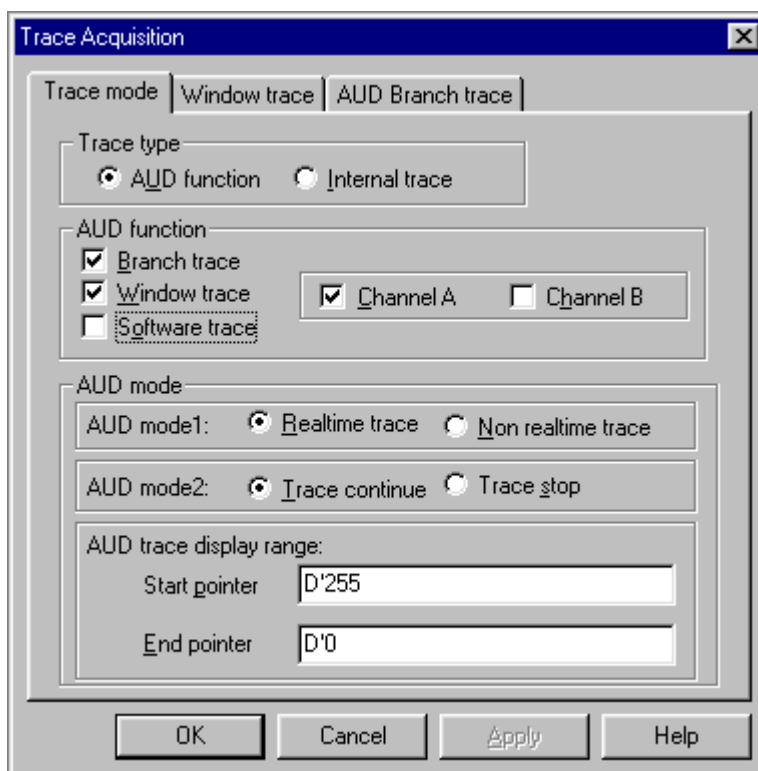


Figure 6.10 [Trace mode] Page

When the AUD trace function is used, select the [AUD function] radio button in the [Trace type] group box of the [Trace mode] page.

(a) Branch Trace Function

The branch source and destination addresses and their source lines are displayed.

Branch trace can be acquired by selecting the [Branch trace] check box in the [AUD function] group box of the [Trace mode] page.

The branch type can be selected in the [AUD Branch trace] page.

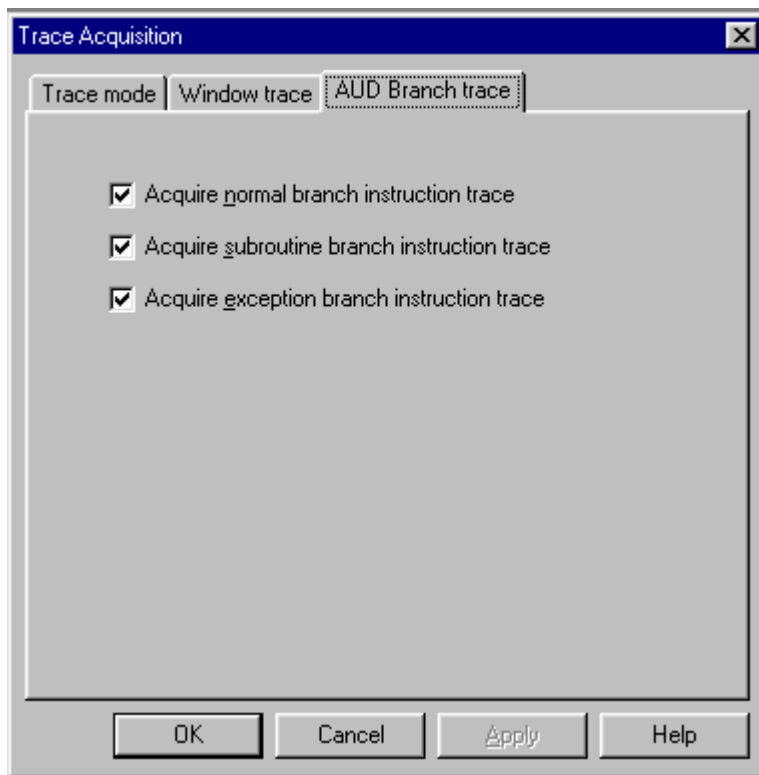


Figure 6.11 [AUD Branch trace] Page

(b) Window Trace Function

Memory access in the specified range can be acquired by trace.

Two memory ranges can be specified for channels A and B. The read, write, or read/write cycle can be selected as the bus cycle for trace acquisition.

[Setting Method]

- (i) Select the [Channel A] and [Channel B] check boxes in the [AUD function] group box of the [Trace mode] page. Each channel will become valid.
- (ii) Open the [Window trace] page and specify the bus cycle and memory range that are to be set for each channel.

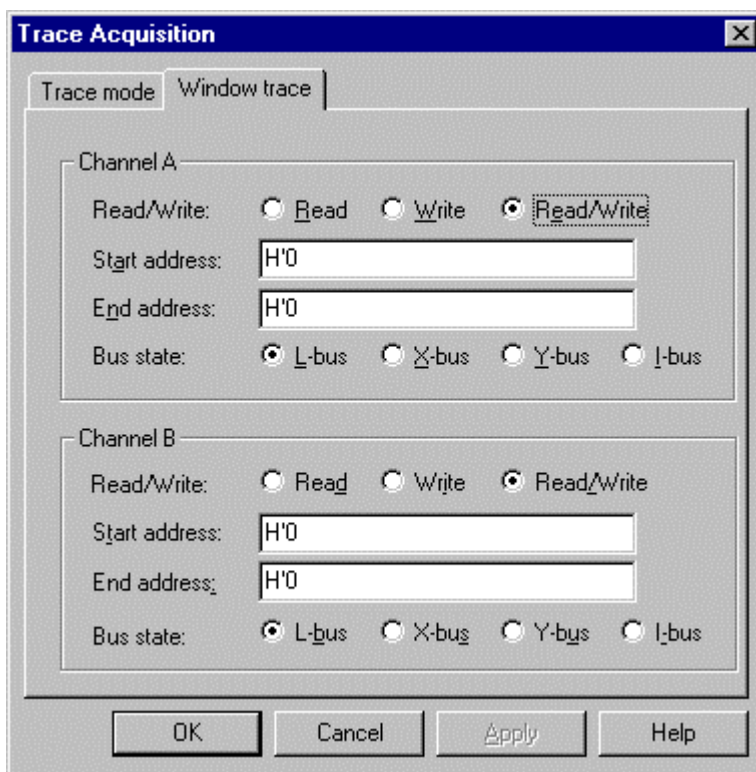


Figure 6.12 [Window trace] Page

Notes: 1. When the [L-bus] or [I-bus] radio button is selected, the following bus cycles will be traced.

L-bus: A bus cycle generated by the CPU is acquired. A bus cycle is also acquired when the cache has been hit.

I-bus: A bus cycle generated by the CPU or DMA is acquired. A bus cycle is not acquired when the cache has been hit. The address information acquired by the I-bus is 28 bits and the upper 4 bits are displayed as '*'. The source cannot be displayed in the [Trace] window.

When U-RAM or X/Y-RAM is accessed from the P0 space, the I-bus must be selected, and when accessed from the P2 space, the L-bus must be selected. When a cache fill cycle is acquired, I-bus must be selected.

2. Address setting when X/Y-bus is selected

To trace both the X/Y-bus when the X/Y-bus is accessed at the same time, the X-bus condition must be set in channel A, and the Y-bus condition must be set in channel B.

(c) Software Trace Function

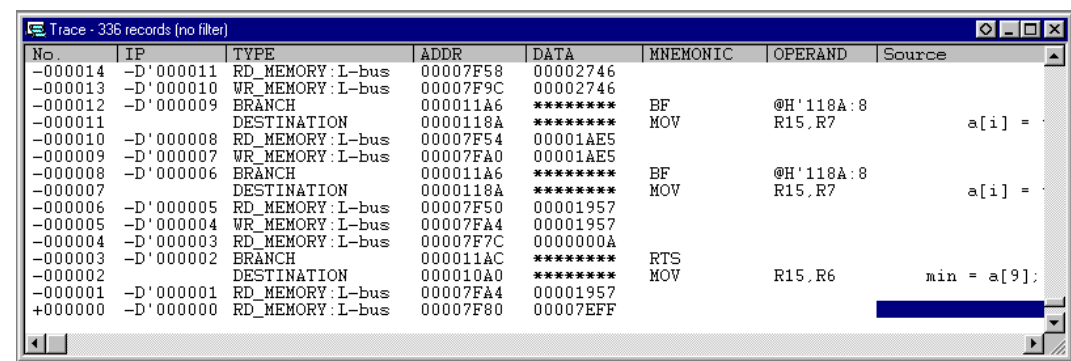
Note: This function can be supported with SHC compiler V7.0 and later.

When a specific instruction is executed, the PC value at execution and the contents of one general register are acquired by trace. Describe the Trace(x) function (x is a variable name) to be compiled and linked beforehand. For details, refer to the SHC manual.

When the load module is loaded on the emulator and a valid software trace function is executed, the PC value that has executed the Trace(x) function, the general register value for x, and the source lines are displayed.

To activate the software trace function, select the [Software trace] check box in the [AUD function] group box of the [Trace mode] page.

AUD Trace Display: Figure 6.13 shows an example of the AUD trace display.



No.	IP	TYPE	ADDR	DATA	MNEMONIC	OPERAND	Source
-000014	-D'000011	RD_MEMORY:L-bus	00007F58	00002746			
-000013	-D'000010	WR_MEMORY:L-bus	00007F9C	00002746			
-000012	-D'000009	BRANCH	000011A6	*****	BF	@H'118A:8	
-000011		DESTINATION	0000118A	*****	MOV	R15,R7	a[i] =
-000010	-D'000008	RD_MEMORY:L-bus	00007F54	00001AE5			
-000009	-D'000007	WR_MEMORY:L-bus	00007FA0	00001AE5			
-000008	-D'000006	BRANCH	000011A6	*****	BF	@H'118A:8	
-000007		DESTINATION	0000118A	*****	MOV	R15,R7	a[i] =
-000006	-D'000005	RD_MEMORY:L-bus	00007F50	00001957			
-000005	-D'000004	WR_MEMORY:L-bus	00007FA4	00001957			
-000004	-D'000003	RD_MEMORY:L-bus	00007F7C	0000000A			
-000003	-D'000002	BRANCH	000011AC	*****	RTS		
-000002		DESTINATION	000010A0	*****	MOV	R15,R6	min = a[9];
-000001	-D'000001	RD_MEMORY:L-bus	00007FA4	00001957			
+000000	-D'000000	RD_MEMORY:L-bus	00007F80	00007EFF			

Figure 6.13 [Trace] Window

Double-clicking the source line jumps the cursor to the corresponding section on the [Source] window.

The TYPE, ADDR, and DATA columns have different meanings according to the selected AUD trace types.

When the TYPE column displays 'LOST', it means that the trace information being output in the realtime trace mode has been suspended. When that column displays 'CPU-WAIT', it means that a wait has been generated in the CPU to output a trace in the non-realtime trace mode.

Table 6.10 [Trace] Window Display Contents

Trace Type	TYPE Column	ADDR Column	DATA Column
Branch trace	BRANCH	Branch source address	No display
	DESTINATION	Branch destination address	No display
Window trace	RD_MEMORY, WR_MEMORY	Memory access address	Memory access data
Software trace	S_TRACE	Address at which the Trace (x) function is executed	Register value
Information	LOST	None	None
	CPU-WAIT	None	None

- Notes:
1. When the trace display is performed during user program execution, the mnemonics, operands, or source is not displayed.
 2. The AUD trace function outputs the differences between newly output branch source addresses and previously output branch source addresses. The window trace function outputs the differences between newly output addresses and previously output addresses. If the previous branch source address is the same as the upper 16 bits, the lower 16 bits are output. If it matches the upper 24 bits, the lower 8 bits are output. If it matches the upper 28 bits, the lower 4 bits are output. The emulator regenerates the 32-bit address from these differences and displays it in the [Trace] window. If the emulator cannot display the 32-bit address, it displays the difference from the previously displayed 32-bit address.
 3. If the 32-bit address cannot be displayed, the source line is not displayed.
 4. In the SH7294 E10A emulator, when multiple loops are performed to reduce the number of AUD trace displays, only the IP counts up.
 5. In the SH7294 E10A emulator, the maximum number of trace display pointers is as follows:
When HS7294KCM02H is used: D'8191 to -0
When HS7294KCI02H is used: D'32767 to -0
However, the maximum number of trace display pointers differs according to the AUD trace information to be output. Therefore, the above pointers cannot be always acquired.
 6. The AUD trace acquisition is not available when [User] is selected in the [UBC mode] list box of the [Configuration] dialog box. In this case, close the [Trace] window.
 7. Do not use the AUD full-trace mode for the VIO function.

Internal Trace Function: This function is activated by selecting the [Internal trace] radio button in the [Trace type] group box of the [Trace mode] page. This function traces and displays the branch instructions. The branch source address and branch destination address for the eight latest branch instructions are displayed. See figure 6.10, [Trace mode] Page.

Notes: 1. If an interrupt is generated at the program execution start or end, including a step execution, the emulator address may be acquired. In such a case, the following message will be displayed. Ignore this address because it is not a user program address.

*** EML ***

2. If a completion-type exception occurs during exception branch acquisition, the next address to the address in which an exception occurs is acquired.
3. Trace information cannot be acquired for the following branch instructions:
 - The BF and BT instructions whose displacement value is 0
 - Branch to H'A0000000 by reset
4. The internal trace acquisition is not available when [User] is selected in the [UBC mode] list box of the [Configuration] dialog box. In this case, close the [Trace] window.

6.5.4 Notes on Using the JTAG Clock (TCK) and AUD Clock (AUDCK)

1. Set the JTAG clock (TCK) frequency to less than the frequency of the SH7294 peripheral module clock (CKP).
2. Set the AUD clock (AUDCK) frequency to 50 MHz or below for PCMCIA and PCI cards.

6.5.5 Notes on Setting the [Breakpoint] Dialog Box

1. When an odd address is set, the next lowest even address is used.
2. A BREAKPOINT is accomplished by replacing instructions of the specified address. Accordingly, it can be set only to the internal RAM area. However, a BREAKPOINT cannot be set to the following addresses:
 - An area other than CS0 to CS6 and the internal RAM
 - An instruction in which Break Condition 2 is satisfied
 - A slot instruction of a delayed branch instruction
 - An area that can be only read by MMU
3. During step execution, a BREAKPOINT is disabled.
4. Conditions set at Break Condition 2 are disabled when an instruction to which a BREAKPOINT has been set is executed. Do not set a BREAKPOINT to an instruction in which Break Condition 2 is satisfied.
5. When execution resumes from the address where a BREAKPOINT is specified, single-step execution is performed at the address before execution resumes. Therefore, realtime operation cannot be performed.

6. When a BREAKPOINT is set to the slot instruction of a delayed branch instruction, the PC value becomes an illegal value. Accordingly, do not set a BREAKPOINT to the slot instruction of a delayed branch instruction.
7. When a BREAKPOINT is set to the cacheable area, the cache block containing the BREAKPOINT address is filled immediately before and after user program execution.
8. Note on DSP repeat loop:
A BREAKPOINT is equal to a branch instruction. In some DSP repeat loops, branch instructions cannot be set. For these cases, do not set BREAKPOINTS. Refer to the hardware manual for details.
9. When the [Normal] option is selected in the [Memory area] group box in the [General] page of the [Configuration] dialog box, a BREAKPOINT is set to a physical address or a virtual address according to the SH7294 MMU status during command input when the VPMAP_SET command setting is disabled. The ASID value of the SH7294 PTEH register during command input is used. When VPMAP_SET command setting is enabled, a BREAKPOINT is set to a physical address into which address translation is made according to the VP_MAP table. However, for addresses out of the range of the VP_MAP table, the address to which a BREAKPOINT is set depends on the SH7294 MMU status during command input. Even when the VP_MAP table is modified after BREAKPOINT setting, the address translated when the BREAKPOINT is set valid.
10. When the [Physical] option is selected in the [Memory area] group box in the [General] page of the [Configuration] dialog box, a BREAKPOINT is set to a physical address. A BREAKPOINT is set after disabling the SH7294 MMU upon program execution. After setting, the MMU is returned to the original state. When a break occurs at the corresponding virtual address, the cause of termination displayed in the status bar and the [System Status] window is ILLEGAL INSTRUCTION, not BREAKPOINT.
11. When the [Virtual] option is selected in the [Memory area] group box in the [General] page of the [Configuration] dialog box, a BREAKPOINT is set to a virtual address. A BREAKPOINT is set after enabling the SH7294 MMU upon program execution. After setting, the MMU is returned to the original state. When an ASID value is specified, the BREAKPOINT is set to the virtual address corresponding to the ASID value. The emulator sets the BREAKPOINT after rewriting the ASID value to the specified value, and returns the ASID value to its original value after setting. When no ASID value is specified, the BREAKPOINT is set to a virtual address corresponding to the ASID value at command input.
12. An address (physical address) to which a BREAKPOINT is set is determined when the BREAKPOINT is set. Accordingly, even if the VP_MAP table is modified after BREAKPOINT setting, the BREAKPOINT address remains unchanged. When a BREAKPOINT is satisfied with the modified address in the VP_MAP table, the cause of termination displayed in the status bar and the [System Status] window is ILLEGAL INSTRUCTION, not BREAKPOINT.
13. If an address of a BREAKPOINT cannot be correctly set in the ROM or flash memory area, a mark ● will be displayed in the [BP] area of the address on the [Source] or [Disassembly] window by refreshing the [Memory] window, etc. after Go execution. However, no break will

occur at this address. When the program halts with the break condition, the mark ● disappears.

6.5.6 Notes on Setting the [Break Condition] Dialog Box and the BREAKCONDITION_SET Command

1. When [Go to cursor], [Step In], [Step Over], or [Step Out] is selected, the settings of Break Condition 2 are disabled.
2. Break Condition 2 is disabled when an instruction to which a BREAKPOINT has been set is executed. Accordingly, do not set a BREAKPOINT to an instruction which satisfies Break Condition 2.
3. When a Break Condition is satisfied, emulation may stop after two or more instructions have been executed.
4. If a PC break address condition is set to the slot instruction after a delayed branch instruction, user program execution cannot be terminated before the slot instruction execution; execution stops before the branch destination instruction.
5. Break Condition 1,2 is used as the measurement range in the performance measurement function when (P) is added and displayed as Enable(P) on the [Condition] page. This applies when the Break Condition is displayed with the BREAKCONDITION_DISPLAY command in the command-line function. In this case, a break does not occur when Break Condition 1,2 is satisfied.

6.5.7 Notes on Setting the [Trace] Window

When a completion-type exception occurs during exception branch acquisition, the next address to the address in which an exception occurs is acquired.

6.5.8 Notes on Setting the UBC_MODE Command

In the [Configuration] dialog box, if [User] is set while the [UBC mode] list box has been set, the STEP-type commands that use Break Condition 2 for implementation cannot be used. If these commands are used, the message box shown in figure 6.14 is displayed.

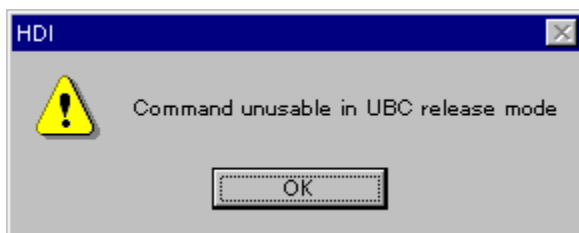


Figure 6.14 [HDI] Message Box

6.5.9 Performance Measurement Function

The SH7294 E10A emulator supports the performance measurement function.

1. Setting the performance measurement conditions

To set the performance measurement conditions, use the [Performance 1] dialog box and the PERFORMANCE_SET command. When the right mouse button is clicked on the [Performance Analysis] window, the popup menu is displayed and the [Performance 1] dialog box is displayed by selecting [Setting].

Note: For the command line syntax, refer to the online help.

— Specifying the measurement start/end conditions

The measurement start/end conditions are specified in the [Mode] drop-down list box in the [Performance 1] dialog box. Three conditions can be set as shown in table 6.11.

Table 6.11 Conditions Specified in [Mode]

Item	Description
Normal break	Measurement is started by executing a program and ended when a break condition is satisfied.
Break Condition 1 -> 2	Measurement is started from the satisfaction of the condition set in Break Condition 1 and ended with the satisfaction of the condition set in Break Condition 2.
Break Condition 2 -> 1	Measurement is started from the satisfaction of the condition set in Break Condition 2 and ended with the satisfaction of the condition set in Break Condition 1.

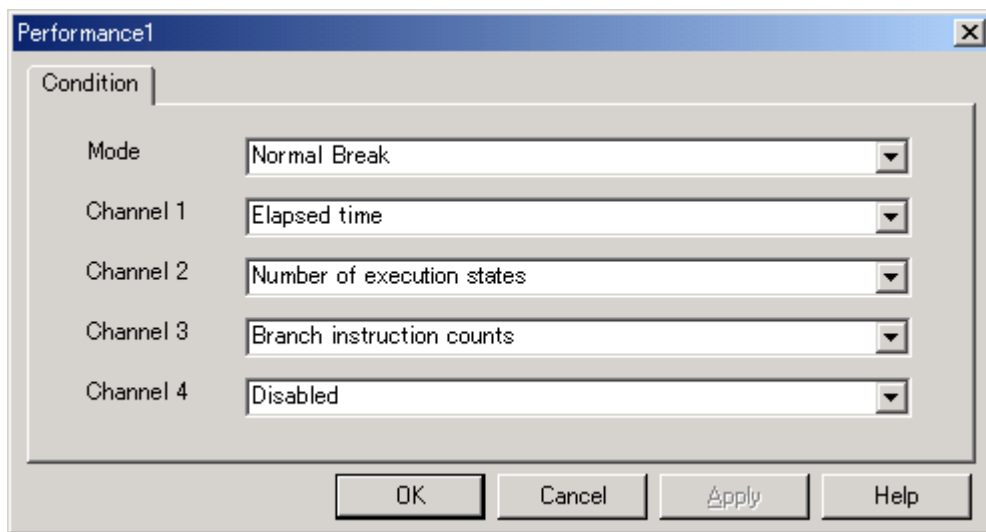


Figure 6.15 [Performance 1] Dialog Box

— Measurement range

One of the following ranges can be specified. This depends on the item selected for [Mode] in the [Performance 1] dialog box.

1. From the start to the end of the user program execution (When Normal Break is selected for [Mode])
2. From the satisfaction of the condition set in Break Condition 1 to the satisfaction of the condition set in Break Condition 2 (When Break condition 1->2 is selected for [Mode])
3. From the satisfaction of the condition set in Break Condition 2 to the satisfaction of the condition set in Break Condition 1 (When Break condition 2->1 is selected for [Mode])

(In the second and third ranges, (P) is added and displayed as Enable(P) on the [Condition] page in the [Break] dialog box.)

For measurement errors,

- The measured value includes errors.
- Error will occur before or after a break.

- Notes:
1. When the second and third ranges are specified, execute the user program after the measurement start condition is set to Break Condition 1 (or Break Condition 2) and the measurement end condition to Break Condition 2 (or Break Condition 1).
 2. Step execution is not possible when Break condition 1->2 or Break condition 2->1 is selected for the PERFORMANCE_SET command or in [Mode] of the [Performance 1] dialog box.
 3. When Break condition 1->2 or Break condition 2->1 is selected in [Mode] of the [Performance 1] dialog box, specify one or more items for measurement. When there is no item, the error message “Measurement item does not have specification. Please set up a measurement item.” will be displayed. When no item is specified for the PERFORMANCE_SET command, the settings of Break condition 1->2 or Break condition 2->1 will be an error.

— Measurement item

Items are measured with [Channel 1 to 4] in the [Performance 1] dialog box. Maximum four conditions can be specified at the same time. Table 6.12 shows the measurement items (Options in table 6.12 are parameters for <mode> of the PERFORMANCE_SET command. They are displayed for CONDITION in the [Performance Analysis] window).

Table 6.12 Measurement Item

Selected Name	Option
Disabled	None
Elapsed time	AC
Number of execution states	VS
Branch instruction counts	BT
Number of execution instructions	I
Instruction/data conflict cycle	MAC
Other conflict cycles than instruction/data	OC
Exception/interrupt counts	EA
Data-TLB miss cycle	MTS
Instruction-TLB miss cycle	ITS
Interrupt counts	INT
Number of BL=1 instructions	BL1
Number of MD=1 instructions	MD1
Instruction cache-miss counts	IC
Data cache-miss counts	DC
Instruction fetch stall	IF
Data access stall	DA
Instruction cache-miss stall	ICS
Data cache-miss stall	DCS
Cacheable access stall	CS
X/Y-RAM access stall	XYs
URAM access stall	US
Instruction/data access stall cycle	MA
Other access cycles than instruction/data	NMA
Non-cacheable area access cycle	NCC
Non-cacheable area instruction access cycle	NCI
Non-cacheable area data access cycle	NCD

Table 6.12 Measurement Item (cont)

Selected Name	Option
Cacheable area access cycle	CC
Cacheable area instruction access cycle	CIC
Cacheable area data access cycle	CDC
Access counts other than instruction/data	NAM
Non-cacheable area access counts	NCN
Non-cacheable area instruction access counts	NCIN
Non-cacheable area data access counts	NCDN
Cacheable area access counts	CN
Cacheable area instruction access counts	CIN
Cacheable area data access counts	CDN

Each measurement condition is also counted when conditions in table 6.13 are generated.

Table 6.13 Performance Measurement Conditions to be Counted

Measurement Condition	Notes
No caching due to the settings of TLB cacheable bit	Counted for accessing the cacheable area.
Cache-on counting	Accessing the non-cacheable area is counted less than the actual number of cycles and counts. Accessing the cacheable, X/Y RAM, and U RAM areas is counted more than the actual number of cycles and counts.
Branch count	The counter value is incremented by 2. This means that two cycles are valid for one branch.

- Notes:
1. In the non realtime trace mode of the AUD trace, normal counting cannot be performed because the generation state of the stall or the execution cycle is changed.
 2. Since the clock source of the counter is the CPU clock, counting also stops when the clock halts in the sleep mode.

2. Displaying the measured result

The measured result is displayed in the [Performance Analysis] window or the PERFORMANCE_ANALYSIS command with hexadecimal (32 bits).

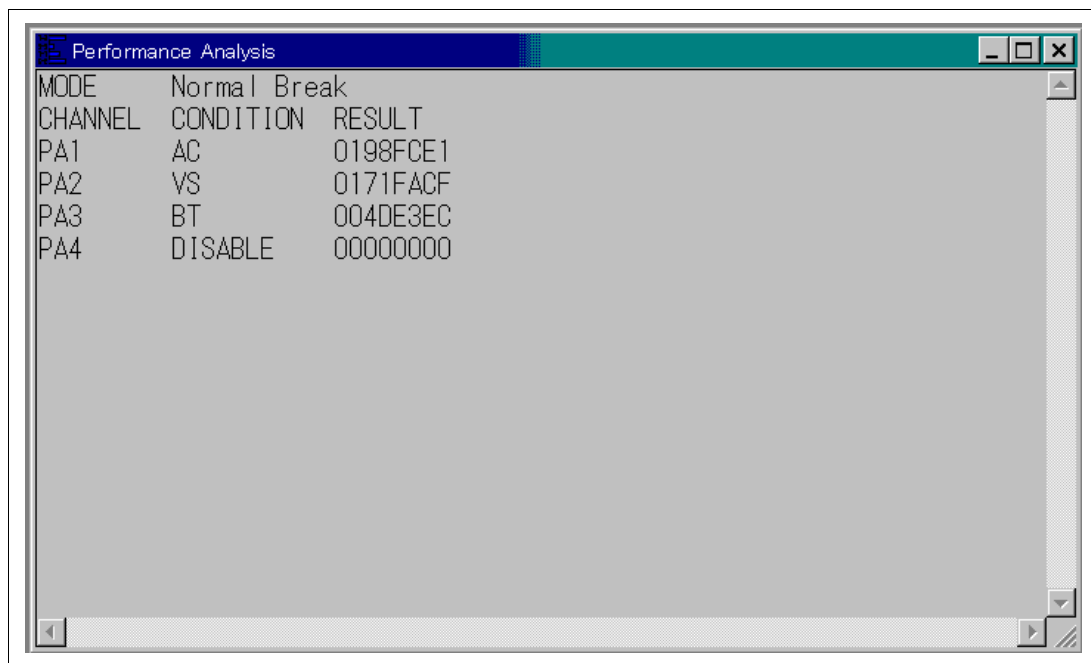


Figure 6.16 [Performance Analysis] Window

Note: If a performance counter overflows as a result of measurement, “*****” will be displayed.

3. Initializing the measured result

To initialize the measured result, select [Initialize] from the popup menu in the [Performance Analysis] window or specify INIT with the PERFORMANCE_ANALYSIS command.

6.5.10 Notes on U Standby State

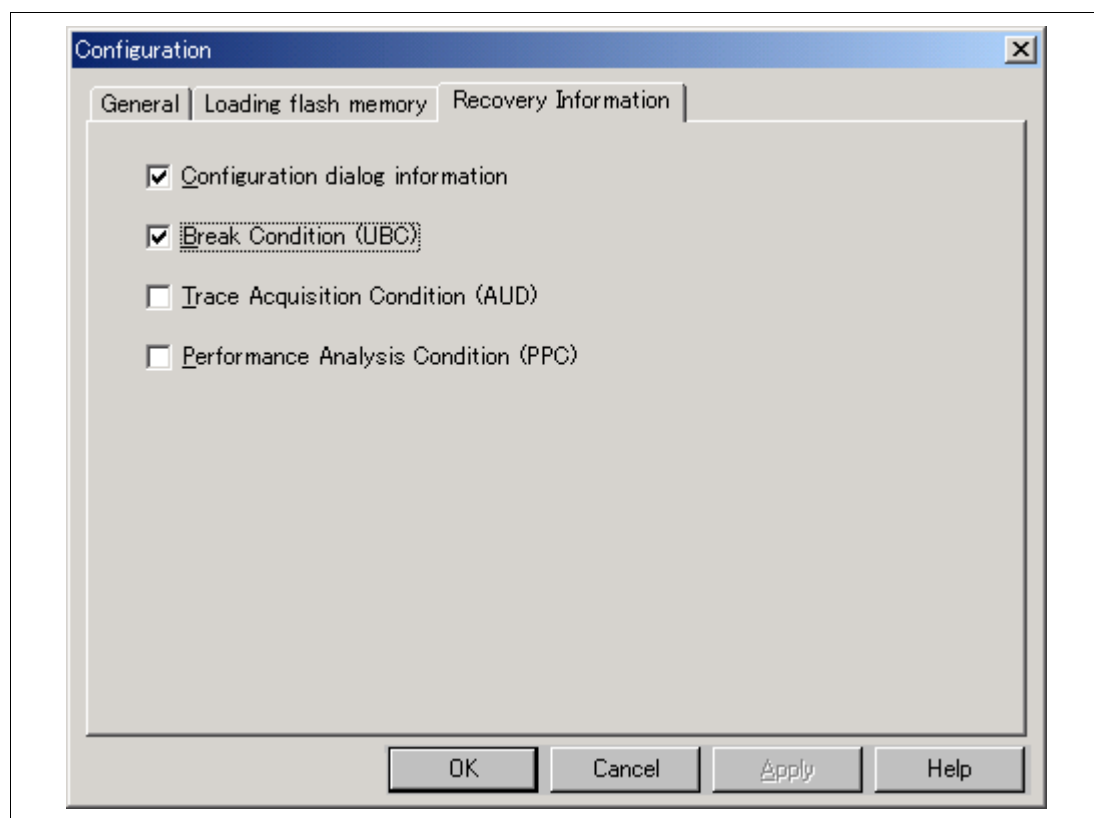
Controlling Vcc (main) in the U standby state clears settings for peripheral modules in the SH7294 which the emulator uses for debugging. Thus the appropriate function must be selected in the [Recovery Information] page of the [Configuration] dialog box so that debugging can be continued after returning to the U standby state.

Note: When using the U standby state, be sure to connect the /CA pin to the emulator. For details, refer to section 6.3, User System Interface Circuit.

Table 6.14 lists the items to be selected in the [Recovery Information] page of the [Configuration] dialog box.

Table 6.14 Selected Items in the [Recovery Information] Page

Selected Item	Description
Configuration dialog information	Resets the information on the [General] page in the [Configuration] dialog box.
Break Condition (UBC)	Resets the conditions of Break Condition.
Trace Acquisition Condition (AUD)	Resets the AUD trace conditions.
Performance Analysis Condition (PPC)	Resets the performance conditions.

**Figure 6.17 [Recovery Information] Page**

The following shows the procedures recovered from the U standby state.

1. In the software standby mode, when the /CA pin is asserted, the emulator enters the U standby state and the message box shown in figure 6.18 is displayed. To cancel the U standby state, be sure to end the message box before asserting /RESETP from the user system.
2. Turn Vcc (main) off.

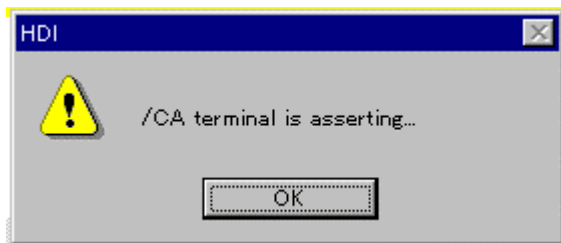


Figure 6.18 Message Box for Entering the U Standby State

3. Assert /RESETP and turn Vcc (main) on.
4. After the power has been stable, negate the /CA pin.
5. Negate /RESETP, and the U standby state is cancelled.
6. Before the program execution is restarted from the reset vector, the E10A debugging function, which has been set in the [Recovery Information] page in the [Configuration] dialog box, is recovered. During this period, the HDI cannot operate.

Notes: 1. For entering or canceling the U standby state, refer to the section of low power mode in the SH7294 Series Hardware Manual.

2. When Vcc (main) is turned on in the U standby state, set the TCK value to less than the number of peripheral module clocks and select [Configuration dialog information] in the [Recovery Information] page. If this is not selected, the TCK value will be as follows:

When HS7294KCI01H or HS7294KCI02H is used: TCK = 1.031 MHz

When HS7294KCM01H or HS7294KCM02H is used: TCK = 0.937 MHz

3. After the /CA pin has been negated, if /RESETP is not input for about 30 seconds, a timeout error will occur.
4. Software breakpoints that have recovered from the U standby state remain to be set on the memory. However, the contents displayed in the [Breakpoints] window may be different from the actual points when the contents of memory is initialized by the control of Vcc (main). In this case, clear all the breakpoints and set them again.
5. In functions for which conditions were not recovered by a selection in [Recovery information], the settings made will not be the same as the contents displayed in a dialog box or command after the recovery from the U standby state until when the user program is halted.

6. If [User] is set in the [UBC mode] list box in the [Configuration] dialog box, Break Condition (UBC) and Trace Acquisition Condition (AUD) cannot be selected.
7. A noise filter is provided in the emulator to prevent malfunction due to noises of the /CA pin. It takes about 70 ns from assertion (negation) of /CA to reflection to the emulator. Therefore, set 70 ns or more periods during assertion to negation of /CA.
8. When the U standby state has been cancelled, before the program execution is restarted from the reset vector, the E10A debugging function, which has been set in the [Recovery Information] page in the [Configuration] dialog box, is recovered. During this period, the HDI cannot be operated.
When the debugging functions except for [Configuration dialog information] are not recovered in the [Recovery Information] page, the following waiting time will be generated before the user program is reexecuted:
 Frequency: 833 MHz (Pentium® III)
 OS: Windows® 2000
 Operating state of SH7294: Operating mode 3, 20-MHz input clock, 1.87-MHz TCK value
 Waiting time: Approximately 24 ms

6.5.11 Downloading to the Flash Memory

Examples of connecting with and downloading to the flash memory (DA28F640J5-150) manufactured by Intel Corporation are described below. Note that a sample is provided in the \Fmtool folder within the folder in which the SH7294 E10A is installed. File configuration of the sample program is as follows:

Table 6.15 File Configuration of the Sample Program

Item	File name
Sample program for downloading to the flash memory (load module file, S-type format, with big endian)	\Fmtool\Fmtool.mot
Sample program for downloading to the flash memory (source file)	\Fmtool\Fmtool.asm

Specifications of the sample program are as follows:

Table 6.16 Specifications of the Sample Program

Item	Contents
RAM area to be used	H'0C001000 to H'0C001FFF
Writing module start address	H'0C001100
Erasing module start address	H'0C001000

Operating conditions of the sample program are listed below.

Table 6.17 Operating Conditions

Item		Contents
SDRAM addresses		H'0C000000 to H'0C07FFFF
Bus width of SDRAM addresses		16 bits
Flash memory addresses		H'00000000 to H'0007FFFF
Bus width of flash memory addresses		16 bits
Operating environment	CPU operating mode	3
	Input clock	19.8 MHz
	Value in the frequency change register (FRQCR)	H'1003
	Endian	Big

Figure 6.19 shows a connection of the flash memory using the sample program.

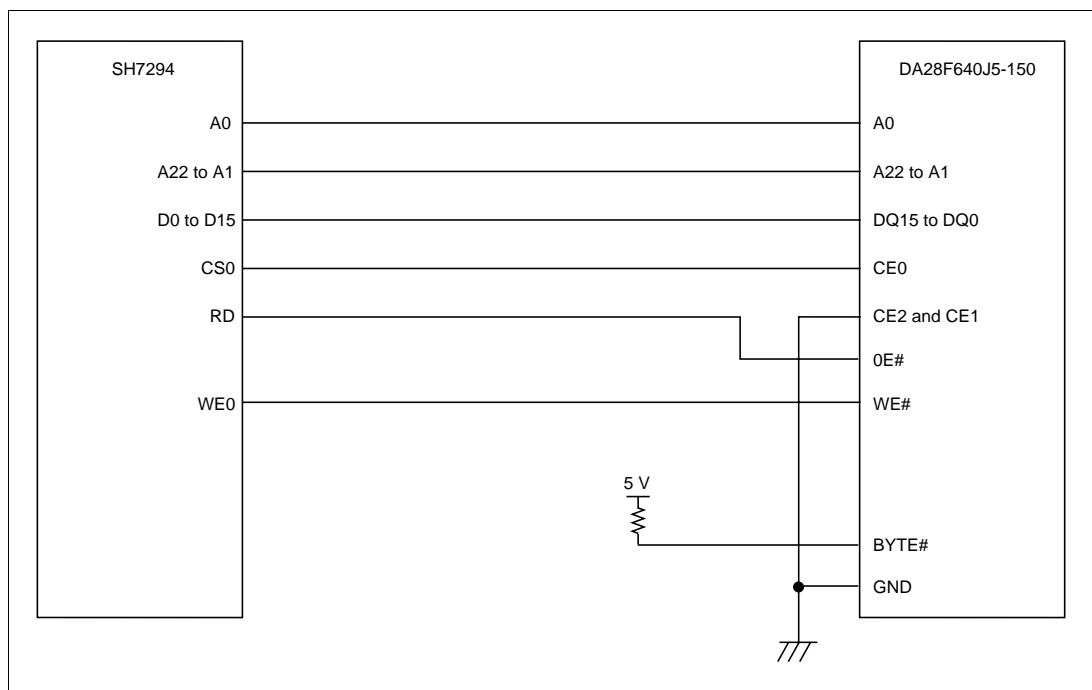


Figure 6.19 Example of Connecting the Flash Memory

6.5.12 Notes on HDI

1. Moving Source File Position after Creating Load Module

When the source file is moved after creating the load module, the [Open] dialog box may be displayed to specify the source file during the debugging of the created load module. Select the corresponding source file and click the [Open] button.

2. Source-level Execution

— Source file

Do not display source files that do not correspond to the load module in the program window. For a file having the same name as the source file that corresponds to the load module, addresses are displayed in the program window but operation in the window may not work properly.

— Step

Even standard C libraries are executed. To return to a higher-level function, enter Step Out. In a for statement or a while statement, executing a single step does not move execution to the next line. To move to the next line, execute two steps.

3. Operation During Accessing Files

Do not perform other operations during saving in the [Load Program], [Verify Memory], [Save Memory], or [Trace] window because this will not allow correct saving to be performed.

4. Source Window at Program Change

When a program being displayed in the source window is changed and the source file and load module are reloaded, close and reopen the source window once. If the window is not closed and reopened, the display may be incorrect.

5. Watch

— Local variables at optimization

Depending on the generated object code, local variables in a C source file that is compiled with the optimization option enabled will not be displayed correctly. Check the generated object code by displaying the [Disassembly] window.

If the allocation area of the specified local variable does not exist, displays as follows.

Example: The variable name is asc.

asc = ? - target error 2010 (xxxx)

— Variable name specification

When a name other than a variable name, such as a symbol name or function name, is specified, no data is displayed.

Example: The function name is main.

 main =

— Array display

When array elements exceed 1000, elements from after 1000 will not be displayed.

6. Memory Load Function

When [Load...] is selected from the [Memory] menu, the Memory Load function can be used although it takes time to download. It is recommended that the File Load function ([Load Program...] selected from the [File] menu) is used to load the S-type file.

Note: The File Load function deletes the debugging information of the previously loaded program. When other load modules are loaded after the program to be debugged has been loaded, use the following sequence: When the program to be debugged is linked, save the debugging information in another file. Load the debugging information file after all the load modules have been loaded.

7. Line Assembly

— Input radix

Regardless of the Radix setting, the default for line assembly input is decimal. Specify H' or 0x as the radix for a hexadecimal input.

8. Command Line Interface

— Batch file

To display the message “Not currently available” while executing a batch file, enter the sleep command. Adjust the sleep time length which differs according to the operating environment.

Example: To display “Not currently available” during memory_fill
 execution:

 sleep d'3000

 memory_fill 0 ffff 0

— Overwrite file

In Command Line Interface, a file having the same name as the output file is overwritten without asking the user.

— File specification by commands

The current directory may be altered by file specifications in commands. Absolute paths are recommended to be used to specify the files in a command file so that the current directory alteration is not affected.

Example: `FILE_LOAD C:\\HEW\\HDI5\\E10A\\7294\\TUTORIAL\\TUTORIAL
\\DEBUG\\TUTORIAL.ABS`

9. About Hitachi Debugging Interface User's Manual

This version of HDI does not support section 10, Selecting Functions, written in Hitachi Debugging Interface User's Manual.

10. Initiating HDI

When the emulator is initiated by using another card emulator after it has been initiated by using the PCI card emulator, delete the [TARGET] line from the C:\\windows\\HDI.INI file.

11. Usage with Another Version of HDI

— Automatic load of session files

Since the emulator cannot use another version of HDI, re-install this HDI whenever another version has been previously installed.

If another version has been used, initiate this HDI with “Run” as follows without using the session files.

```
<Directory path name in which HDI is installed>\\hdi /n (RET)
```

/n initiates the HDI without loading the recently used session files.

If there is another session file in the different debug platform, the following error message is displayed:

```
invalid target system: <recently used debug platform name>
```

— Uninstallation of another version of HDI

If another version of HDI is uninstalled after this HDI has been installed, the stack trace function will not be used. In this case, reinstall this HDI.

12. [Select Function] Dialog Box

This HDI does not support BREAKPOINT setting in the [Select Function] dialog box (described in section 10, Selecting Functions, in the Hitachi Debugging Interface User's Manual).

13. Memory Save During User Program Execution

Do not execute memory save or verifying during user program execution.

14. Load of Motorola S-type Files

This HDI does not support Motorola S-type files with only the CR code (H'0D) at the end of each record. Load Motorola S-type files with the CR and LF codes (H'0D0A) at the end of each record.

15. [Memory] Window

If the following memory contents are displayed, they will be incorrect.

Word access from address $2n + 1$

Longword access from address $4n + 1$, $4n + 2$, or $4n + 3$

16. Scrolling Window During User Program Execution

Do not scroll the [Memory] and [Disassembly] windows by dragging the scroll box during user program execution. This generates many memory reads causing the user program to stop execution until the memory reads have been completed.

17. [I/O Registers] window

— Display and modification

- Do not change values of the User Break Controller because it is used by the emulator.
- For each watchdog timer and RCLK watchdog timer register, there are two registers to be separately used for write and read operations.

Table 6.18 Watchdog Timer Register

Register Name	Usage	Register
WTCSR(W)	Write	Watchdog timer control/status register
WTCNT(W)	Write	Watchdog timer counter
WTCSR(R)	Read	Watchdog timer control/status register
WTCNT(R)	Read	Watchdog timer counter
RWTCSR(W)	Write	RCLK watchdog timer control/status register
RWTCNT(W)	Write	RCLK watchdog timer counter
RWTCSR(R)	Read	RCLK watchdog timer control/status register
RWTCNT(R)	Read	RCLK watchdog timer counter

- The watchdog timer operates only when the user program is executed. Do not change the value of the frequency change register in the [I/O Registers] window or [Memory] window.

- The internal I/O registers can be accessed from the [I/O registers] window. However, note the following when accessing the SDMR register of the bus-state controller. Before accessing the SDMR register, specify addresses to be accessed in the I/O-register definition file (SH7294.IO) and then activate the HDI. For details on I/O-register definition file, refer to the Hitachi Debugging Interface User's Manual. Note that, however, the E10A emulator does not support the bit-field function described in the Hitachi Debugging Interface User's Manual.
- Verify
In the [I/O Registers] window, the verify function of the input value is disabled.

18. Note on [Registers] Window Operation During Program Execution

Although a dialog box is displayed in which the register contents can be changed by double-clicking the [Registers] window, do not change the register contents during program execution.

19. Note on Radix in the [Register] Dialog Box

The default input radix in the [Register] dialog box is hexadecimal irrespective of the Radix display. When a radix other than a hexadecimal is input, specify the prefix code such as B'.

20. BREAKPOINT

— Session file

When the contents of the BREAKPOINT address set in the session file is H'0, the BREAKPOINT will not be set. If the address set as the BREAKPOINT is wrong, the error message is not output. The BREAKPOINT is registered as DISABLE in the [Breakpoints] window.

— Breakpoint cancellation

When the contents of the BREAKPOINT address is modified during user program execution, the following message is displayed when the user program stops.

BREAKPOINT IS DELETED A=xxxxxxx

If the above message is displayed, cancel all BREAKPOINT settings with the [Delete All] or [Disable] button in the [Breakpoints] window.

— [Run program] dialog box

If a disabled BREAKPOINT address is specified as a stop address in the [Run Program] dialog box, the disabled BREAKPOINT will become enabled after the user program has stopped.

— [Breakpoints] window

During user program execution, it is impossible to jump from the breakpoint to the source or address line on the [Source] or [Disassembly] window by using [Go to Source] in the popup menu displayed on the [Breakpoints] window.

21. Number of BREAKPOINT and [Stop At] Settings in the [Run...] Menu

The maximum number of BREAKPOINTS and [Stop At] settings allowed in the [Run...] menu is 255. Therefore, when 255 BREAKPOINTS are set, specification by [Stop At] in the [Run...] menu becomes invalid. Use the BREAKPOINTS and [Stop At] in the [Run...] menu with 255 or less total settings.

22. Note on RUN-TIME Display

The execution time of the user program displayed in the [Status] window may not be accurate since the timer in the host computer is used.

23. Note on Displaying COMMUNICATION TIMEOUT ERROR

If COMMUNICATION TIMEOUT ERROR is displayed, the emulator cannot communicate with the chip. Select [Initialize] from the [File] menu to initialize the emulator.

24. Note on Downloading Program

In the [Load Program] dialog box, which is opened when [Load Program...] is selected, the verify function is invalid. After downloading the program, perform verify in the [Verify S-Record File with Memory] dialog box, which is opened when [Verify] is selected from the [Memory] menu.

25. Support of Double Float Format

In the following memory operations, the double float format is not supported:

- [Fill Memory] dialog box
- [Search Memory] dialog box
- MEMORY_FILL command

The [Format] specification in the [Copy Memory] dialog box is ignored. Memory is copied in a byte unit.

- Double float display at little endian operation

26. Note on Continuous Step Execution

When the step is continuously executed by selecting [Step...] from the [Run] menu, do not use the BREAKPOINT because this will cause the HDI to abnormally operate.

27. Note on Using the [Run program] Dialog Box

When [Run...] is selected from the [Run] menu to specify the stop address, there is the following note:

- When the breakpoint that has been set as Disable is specified as the stop address, note that the breakpoint becomes Enable when the user program stops.

28. Memory Test Function

This product does not support the memory test function, which is used by selecting [test] from the [memory] menu.

29. Illegal Instructions

If illegal instructions are executed by STEP-type commands, the emulator cannot go to the next program counter.

