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CUSTOMER NOTIFICATION

ZUD-CD-06-0109 (1/4)

June 1, 2006

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CP (K), O

QB-78K0FX2

(Control Code: A, B)

Operating Precautions

Be sure to read this document before using the product.

1.	Product Version	. 2
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3.	Details of Bugs and Added Specifications	. 3
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Notes on Using QB-78K0FX2

This document describes restrictions applicable only to the emulator and restrictions that are planned for correction in the emulator.

Refer to the following documents for the restrictions in the target device.

- User's manual of target device
- Restrictions notification document for target device

Also refer to the user's manual of the emulator for cautions on using the emulator.

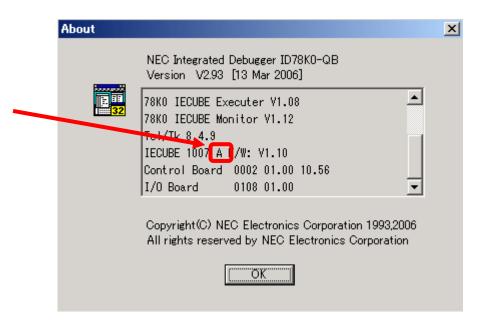
1. Product Version

Control Code ^{Note}	Remark
А	_
В	_

Note The "control code" is the second digit from the left in the 10-digit serial number.

If the product has been upgraded, the control code can be checked by selecting [About] from the [Help] menu when the ID78K0-QB is running.

"X" in version information "IECUBE **** X F/W: V*.**" is the control code.



2. Product History

	D 101 /A11:: 1 0 :5 :5		Control Code	
No.	Bugs and Changes/Additions to Specifications	А	В	
1	Restriction on power-on-clear (POC) function	Permanent restriction		
2	Internal ROM area is overwritten during program execution	×	V	

×: Applicable, √: Not applicable or already corrected

3. Details of Bugs and Added Specifications

No. 1 Restriction on power-on-clear (POC) function

[Description]

• Device:

The POC detection voltage differs between the QB-78K0FX2 and the device.

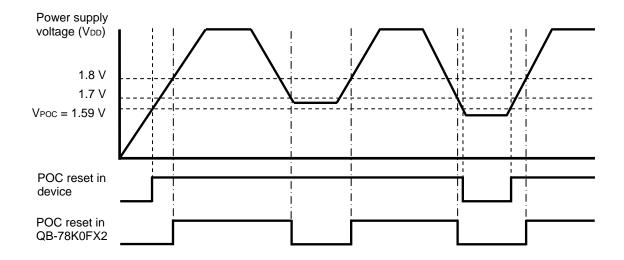
released when the power supply voltage (VDD) exceeds the detection voltage (VPOC = 1.59 V ±0.15 V). The power supply voltage (VDD) and detection voltage (VPOC = 1.59 V ± 0.15 V) are

An internal reset signal is generated at power application, and the reset state is

compared, an internal reset signal is generated when VDD drops lower than VPOC (VDD < VPOC), and the reset state is released when VDD becomes VPOC or higher (VDD \geq VPOC).

QB-78K0FX2: An internal reset signal is generated at power application, and the reset state is released when the power supply voltage (VDD) exceeds 1.80 V.

> An internal reset signal is generated when VDD drops lower than 1.70 V (VDD < 1.70 V), and the reset state is released when VDD becomes 1.80 V or higher (VDD ≥ 1.80 V).



[Workaround]

There is no workaround. Regard this item as a permanent restriction.

No. 2 Internal ROM area is overwritten during program execution

[Description]

Data in the internal ROM area may be overwritten if the Source window or Assemble window is open during program execution. As a result, an unexpected fail-safe break (such as Write Protect Break or Non Map Break) may occur.

[Workaround]

There is no workaround.

This bug has been corrected in products with control code B or later.

4. Cautions

No. 1 Caution on target system voltage during break

Do not decrease the target system voltage during a break. If a reset by LVI or POC occurs during a break, the debugger operation may become illegal or a communication error may occur.

No. 2 General cautions on handling this product

- a. Circumstances not covered by product guarantee
- If the product was disassembled, altered, or repaired by the customer
- If it was dropped, broken, or given another strong shock
- Use at overvoltage, use outside guaranteed temperature range, storing outside guaranteed temperature range
- If power was turned on while the AC adapter, interface cable, or target system connection was in an unsatisfactory state
- If the AC adapter cable, interface cable, emulation probe, or the like was bent or pulled excessively
- If an AC adapter other than the one supplied with the product is used
- If the product got wet
- If the product and target system were connected while a potential difference existed between the GND of the product and the GND of the target system
- If a connector or cable was removed while the power was being supplied to the product
- If an excessive load was placed on a connector or socket

b. Safety precautions

- If used for a long time, the product may become hot (50°C to 60°C). Be careful of low temperature burns and other dangers due to the product becoming hot.
- Be careful of electrical shock. There is a danger of electrical shock if the product is used as described above in a. Circumstances not covered by product guarantee.

5. Revision History

Document Number	Issued on	Description
ZBG-BG-05-0015	July 27, 2005	Newly created.
ZUD-CD-06-0109	June 1, 2006	Addition of bug (No. 2)