# ISL59605-Catx-EVZ/ISL59960-Catx-EVZ Evaluation Board 

## Description

The ISL59605-Catx-EVZ/ISL59960-Catx-EVZ is a standalone board designed for evaluation of the ISL5960x (MegaQ ${ }^{\text {TM }}$ ) and ISL59960 ( $960 \mathrm{MegaQ}^{\text {TM }}$ ) parts without digital programming. It also provides the reference schematic and PCB layout of a typical MegaQ ${ }^{\text {TM }} / 960$ MegaQ $^{\text {TM }}$ Cat x application. This reference schematic is easy to implement and is only needed at the receive side of the transmission path.

## Power Connection and PCB Layout

The power connector is at the top left of the evaluation board. The board requires a +12V DC power supply capable of supplying at least 100 mA .

The board uses the ISL8500 DC-DC buck converter to generate the +5 V supply for the chip. MegaQ ${ }^{\text {TM }} / 960$ MegaQ $^{\text {TM }}$ employs up to 70 dB of high frequency gain to equalize long lengths of cable; therefore the ground for the ISL8500 is isolated from the ground of the MegaQ ${ }^{\text {TM }} / 960$ MegaQ $^{\text {TM }}$ except at the bypass capacitor (C7). This is done to prevent the switching noise from coupling into the MegaQ ${ }^{\text {TM }} / 960$ MegaQ $^{\text {TM }}$ ground and corrupting the output signal when utilizing high equalization.

The PCB was laid out so that the feedback path from the +5 V output back to pins 1 and 2 on the ISL8500 does not cross any of the high current switching components (L1 and D1). For more details on the power supply layout, see Figures 3-6.

## Input Connection

Unshielded twisted pair (UTP) signals arrive via the RJ-45 Cat 5 connector on the left. The input signal can be on any arbitrary combination of pins, though it would typically be one of the standard Cat $x$ pairs of 1 and 2,3 and 6, 4 and 5, or 7 and 8. The $3 \times 8$ jumper block to the right of the $\mathrm{RJ}-45$ jack is used to assign which Cat $x$ pin goes to the positive
MegaQ ${ }^{\text {TM }} / 960$ MegaQ $^{\text {TM }}$ input (in Figure 1, it's pin 8), and which goes to the negative input (pin 7).

## Output Connection

Standard $75 \Omega$ composite video is available at the "VIDEO OUT" BNC connector on the right.

## Switch Settings

The DIP switch (SW1) is used to put MegaQ ${ }^{\text {TM }} / 960$ MegaQ $^{\text {TM }}$ in its different operational modes (recommended settings are underlined):

- Continuous/Lock until Reset: When set to "Continuous", MegaQ ${ }^{\text {TM }}$ will continuously update the EQ settings. When set to "Lock until Reset", the FREEZE input is connected to the LOCKED output so that the device will stop updating once the first optimum solution is reached. "Lock until Reset" is the recommended mode of operation. See the datasheet for more detailed information on the locking modes.
- COLOR-AUTO/COLOR-MAN: Determines whether the COLOR pin is floating ("Auto") or set by the "COLOR-O/COLOR-1" switch ("Man"). When set to "Auto", MegaQ ${ }^{\text {TM }} / 960$ MegaQ $^{\text {TM }}$ will automatically determine if the incoming signal is color or monochrome. If set to "Man" (manual), the polarity of the COLOR is determined by the "COLOR-O/COLOR-1" switch.


FIGURE 1. TOP VIEW OF EVALUATION BOARD


FIGURE 2. BOTTOM VIEW OF EVALUATION BOARD

- INV-AUTO/INV-MAN: Determines whether the INVERT pin is floating ("Auto") or set by the "INV-0/INV-1" switch ("Man"). When set to "Auto", MegaQ™ $960 \mathrm{MegaQ}^{\text {™ }}$ will automatically determine the polarity of the incoming signal. If set to "Man" (manual), the polarity of the INVERT pin is determined by the "INV-0/INV-1" switch.
- FREEZE-0/FREEZE-1: Slide the switch left to the "0" position to put the part in "Continuous Update" mode. The "1" position freezes the equalizer in its current state. This switch is ignored when the "Continuous/Lock until Reset" switch is set to "Lock until Reset". See the datasheet for more detailed information on the locking modes.
- EQ DIS-0/EQ DIS-1: This can be used to disable the EQ so that an external source can drive a signal over the cable. This switch should be set to the " 0 " position for normal operation.
- COLOR-0/COLOR-1: Slide the switch left to the "0" position to force monochrome (sync tip) equalization mode. Slide the switch right to the " 1 " position to force color-burst equalization mode. This switch is ignored when the "COLOR-AUTO/COLOR-MAN" switch is set to the "Auto" position.
- INV-0/INV-1: Slide the switch left to the "0" position for nominal input signal polarity (positive signal on IN+ and negative signal on IN-). Slide the switch right to "1" position for inverted polarity (positive signal on IN- and negative signal on IN+). This switch is ignored when the "INV-AUTO/INV-MAN" switch is set to the "Auto" position.


## LEDs

There are 5 LEDs at the bottom right of the board that indicate the current state of the EQ_DIS, COLOR, INVERT, LOCKED, and FREEZE signals. When any LED is illuminated then the corresponding signal is a logic high. When the LEDs are illuminated, they will draw additional supply current from the 5V supply.

## Related Documents

- ISL5960x Datasheet (isl59601-02-03-04-05.pdf)
- ISL59960 Datasheet (isl59960.pdf)
- ISL59605-SPI-EVALZ/ISL59960-SPI-EVALZ Evaluation Board (with Serial Interface) Operation (Rev 5.0) (AN1775)
- ISL59603-Coax-EVZ/ISL59960-Coax-EVZ Evaluation Board (AN1776)


## Troubleshooting

1. Make sure all the switches are in the correct position for the intended operation.
2. Verify that there is +12 V at the " 12 V " test point and +5 V at the " 5 V " test point. Check that the "EQ DIS" switch is set to " 0 ".
3. Probe the $I N+$ and $I N$ - pins for a video signal. You should see a signal on both inputs (with IN- being the inverse of the signal on IN+). If you do not see the expected signals, inspect your cables, video sources, etc. Verify that the jumpers in the jumper block are set correctly. If you are not certain which pair of Cat $x$ wires is carrying the signal, you can probe the middle row of 8 pins in the jumper block, where you should see the signal on 2 of them.
4. If you see the correct input signal on IN+ and IN- but are still not seeing a valid output signal, set the "INV-AUTO/INV-MAN" switch to the "Auto" position.
5. Probe the VIDEO OUT BNC connector with a scope to see if there is a video signal. If you do not see a composite video signal at the output, and there is a signal present at the input, then the device may be damaged, in which case, please contact your vendor.

ISL59605-Catx-EVZ/ISL59960-Catx-EVZ Evaluation Board Schematic


## ISL59605-Catx-EVZ/ISL59960-Catx-EVZ Board Layout



FIGURE 3. PCB TOP LAYER WITH SILKSCREEN


FIGURE 4. PCB BOTTOM LAYER WITH SILKSCREEN

Application Note 1780
ISL59605-Catx-EVZ/ISL59960-Catx-EVZ Board Layout (Continued)


FIGURE 6. PCB BOTTOM LAYER

## Application Note 1780

Bill of Materials

| QUANTITY | COMPONENT NAME | VALUE | FOOTPRINT |
| :---: | :---: | :---: | :---: |
| 1 | J1 | +12VIN | Power Conn |
| 2 | Z1, $\mathrm{z2}$ | CPDFR5V0 | 1005 |
| 1 | R17 | 0.0 | 0603 |
| 1 | C20 | 0.01 $\mu \mathrm{F}$ | 0603 |
| 5 | C14, C15, C16, C17, C19 | $0.1 \mu \mathrm{~F}$ | 0603 |
| 1 | C18 | $0.47 \mu \mathrm{~F}$ | 0603 |
| 4 | C1, C2, C12, C13 | 1.0رF | 0603 |
| 1 | R11 | 10 | 0603 |
| 5 | R12, R13, R14, R15, R16 | 100k | 0603 |
| 2 | C10, C11 | 100nF | 0603 |
| 1 | C4 | 1500pF | 0603 |
| 6 | R3, R18, R19, R20, R21, R22 | 1k | 0603 |
| 1 | R10 | 20k | 0603 |
| 1 | c9 | 220pF | 0603 |
| 1 | C5 | 22pF | 0603 |
| 1 | R8 | 23.2k | 0603 |
| 1 | R7 | 2k | 0603 |
| 1 | R6 | 3.16k | 0603 |
| 2 | R5, R23 | 300 | 0603 |
| 1 | C8 | 330pF | 0603 |
| 2 | R1, R2 | 49.9 | 0603 |
| 1 | R9 | 69.8k | 0603 |
| 1 | R4 | 75.0 | 0603 |
| 2 | C6, C7 | $4.7 \mu \mathrm{~F}$ | 0805 |
| 1 | C3 | 47nF | 0805 |
| 1 | L1 | $97 \mu \mathrm{H}$ | 1812 |
| 1 | D1 | BAT42-W-7 | SOD-123 |
| 1 | D2 | BLUE | LED-0805 |
| 1 | D3 | GREEN | LED-0805 |
| 1 | D6 | YELLOW | LED-0805 |
| 1 | D4 | ORANGE | LED-0805 |
| 1 | D5 | RED | LED-0805 |
| 1 | J2 | CAT5 IN | Modular-8S |
| 1 | J3 | VIDEO OUT | BNC-EDGE |
| 8 | H1, H2, H3, H4, H5, H6, H7, H8 | HEADER-3PIN | HEADER-3P-0.05 |
| 1 | U4 | ISL59600 | QFN20 4MM |
| 1 | U1 | ISL8500 | DFN12 4X3MM |
| 2 | U2, U3 | 74HCO4 | TSSOP-14 |
| 1 | SW1 | SWITCH-DIP16 | SWITCH-DIP8X2 |

[^0]For information regarding Intersil Corporation and its products, see www.intersil.com


[^0]:    Intersil Corporation reserves the right to make changes in circuit design, software and/or specifications at any time without notice. Accordingly, the reader is cautioned to verify that the Application Note or Technical Brief is current before proceeding.

