

RX65N Group

Renesas Starter Kit+ Smart Configurator Tutorial Manual For CS+

RENESAS 32-Bit MCU RX Family / RX600 Series

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General Precautions in the Handling of Microprocessing Unit and Microcontroller Unit Products

The following usage notes are applicable to all Microprocessing unit and Microcontroller unit products from Renesas. For detailed usage notes on the products covered by this document, refer to the relevant sections of the document as well as any technical updates that have been issued for the products.

1. Handling of Unused Pins

Handle unused pins in accordance with the directions given under Handling of Unused Pins in the manual

34 The input pins of CMOS products are generally in the high-impedance state. In operation with an unused pin in the open-circuit state, extra electromagnetic noise is induced in the vicinity of LSI, an associated shoot-through current flows internally, and malfunctions occur due to the false recognition of the pin state as an input signal become possible. Unused pins should be handled as described under Handling of Unused Pins in the manual.

2. Processing at Power-on

The state of the product is undefined at the moment when power is supplied.

- 3/4 The states of internal circuits in the LSI are indeterminate and the states of register settings and pins are undefined at the moment when power is supplied.
 In a finished product where the reset signal is applied to the external reset pin, the states of pins are not guaranteed from the moment when power is supplied until the reset process is completed. In a similar way, the states of pins in a product that is reset by an on-chip power-on reset function are not guaranteed from the moment when power is supplied until the power reaches the level at which resetting has been specified.
- 3. Prohibition of Access to Reserved Addresses

Access to reserved addresses is prohibited.

3/4 The reserved addresses are provided for the possible future expansion of functions. Do not access these addresses; the correct operation of LSI is not guaranteed if they are accessed.

4. Clock Signals

After applying a reset, only release the reset line after the operating clock signal has become stable. When switching the clock signal during program execution, wait until the target clock signal has stabilized.

When the clock signal is generated with an external resonator (or from an external oscillator) during a reset, ensure that the reset line is only released after full stabilization of the clock signal. Moreover, when switching to a clock signal produced with an external resonator (or by an external oscillator) while program execution is in progress, wait until the target clock signal is stable.

5. Differences between Products

Before changing from one product to another, i.e. to a product with a different part number, confirm that the change will not lead to problems.

34 The characteristics of Microprocessing unit or Microcontroller unit products in the same group but having a different part number may differ in terms of the internal memory capacity, layout pattern, and other factors, which can affect the ranges of electrical characteristics, such as characteristic values, operating margins, immunity to noise, and amount of radiated noise. When changing to a product with a different part number, implement a system-evaluation test for the given product.

Disclaimer

By using this Renesas Starter Kit (RSK+), the user accepts the following terms:

The RSK is not guaranteed to be error free, and the entire risk as to the results and performance of the RSK+ is assumed by the User. The RSK+ is provided by Renesas on an "as is" basis without warranty of any kind whether express or implied, including but not limited to the implied warranties of satisfactory quality, fitness for a particular purpose, title and non-infringement of intellectual property rights with regard to the RSK+. Renesas expressly disclaims all such warranties. Renesas or its affiliates shall in no event be liable for any loss of profit, loss of data, loss of contract, loss of business, damage to reputation or goodwill, any economic loss, any reprogramming or recall costs (whether the foregoing losses are direct or indirect) nor shall Renesas or its affiliates be liable for any other direct or indirect special, incidental or consequential damages arising out of or in relation to the use of this RSK+, even if Renesas or its affiliates have been advised of the possibility of such damages.

Precautions

The following precautions should be observed when operating any RSK+ product:

This Renesas Starter Kit+ is only intended for use in a laboratory environment under ambient temperature and humidity conditions. A safe separation distance should be used between this and any sensitive equipment. Its use outside the laboratory, classroom, study area or similar such area invalidates conformity with the protection requirements of the Electromagnetic Compatibility Directive and could lead to prosecution.

The product generates, uses, and can radiate radio frequency energy and may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment causes harmful interference to radio or television reception, which can be determined by turning the equipment off or on, you are encouraged to try to correct the interference by one or more of the following measures;

- · ensure attached cables do not lie across the equipment
- reorient the receiving antenna
- increase the distance between the equipment and the receiver
- connect the equipment into an outlet on a circuit different from that which the receiver is connected
- power down the equipment when not in use
- consult the dealer or an experienced radio/TV technician for help NOTE: It is recommended that wherever possible shielded interface cables are used.

The product is potentially susceptible to certain EMC phenomena. To mitigate against them it is recommended that the following measures be undertaken;

- The user is advised that mobile phones should not be used within 10m of the product when in use.
- The user is advised to take ESD precautions when handling the equipment.

The Renesas Starter Kit+ does not represent an ideal reference design for an end product and does not fulfil the regulatory standards for an end product.

How to Use This Manual

1. Purpose and Target Readers

This manual is designed to provide the user with an understanding of how to use Application Leading Tool (Smart Configurator) for RX together with the CS+ IDE to create a working project for the RSK+ platform. It is intended for users designing sample code on the RSK+ platform, using the many different incorporated peripheral devices.

The manual comprises of step-by-step instructions to generate code and import it into CS+, but does not intend to be a complete guide to software development on the RSK+ platform. Further details regarding operating the RX65N microcontroller may be found in the Hardware Manual and within the provided sample code.

Particular attention should be paid to the precautionary notes when using the manual. These notes occur within the body of the text, at the end of each section, and in the Usage Notes section.

In this manual, the display may differ slightly from screen shots. There is no problem in reading this manual.

The revision history summarizes the locations of revisions and additions. It does not list all revisions. Refer to the text of the manual for details.

The following documents apply to the RX65N Group. Make sure to refer to the latest versions of these documents. The newest versions of the documents listed may be obtained from the Renesas Electronics Web site.

Document Type	Description	Document Title	Document No.
User's Manual	Describes the technical details of the RSK+ hardware.	RSK+RX65N-2MB User's Manual	R20UT3888EG
Tutorial Manual	Provides a guide to setting up RSK+ environment, running sample code and debugging programs.	RSK+RX65N-2MB Tutorial Manual	R20UT3889EG
Quick Start Guide	Provides simple instructions to setup the RSK+ and run the first sample.	RSK+RX65N-2MB Quick Start Guide	R20UT3890EG
Smart Configurator Tutorial Manual	Provides a guide to code generation and importing into the CS+ IDE.	RSK+RX65N-2MB Smart Configurator Tutorial Manual	R20UT3891EG
Schematics	Full detail circuit schematics of the RSK+.	RSK+RX65N-2MB Schematics	R20UT3887EG
Hardware Manual	Provides technical details of the RX65N microcontroller.	RX65N Group, RX651 Group Hardware Manual	R01UH0590EJ

2. List of Abbreviations and Acronyms

Abbreviation	Full Form		
ADC	Analog-to-Digital Converter		
API	Application Programming Interface		
bps	bits per second		
CMT	Compare Match Timer		
СОМ	COMmunications port referring to PC serial port		
CPU	Central Processing Unit		
DVD	Digital Versatile Disc		
E1/E2 Lite	Renesas On-chip Debugging Emulator		
GUI	Graphical User Interface		
IDE	Integrated Development Environment		
IRQ	Interrupt Request		
LCD	Liquid Crystal Display		
LED	Light Emitting Diode		
LSB	Least Significant Bit		
LVD	Low Voltage Detect		
MCU	Micro-controller Unit		
MSB	Most Significant Bit		
PC	Personal Computer		
PLL	Phase-locked Loop		
Pmod™	This is a Digilent Pmod [™] Compatible connector. Pmod [™] is registered to <u>Digilent Inc.</u> Digilent-Pmod_Interface_Specification		
PSU	Power Supply Unit		
RAM	Random Access Memory		
ROM	Read Only Memory		
RSK+	Renesas Starter Kit+		
RTC	Real Time Clock		
SAU	Serial Array Unit		
SCI	Serial Communications Interface		
SPI	Serial Peripheral Interface		
TAU	Timer Array Unit		
TFT	Thin Film Transistor		
TPU	Timer Pulse Unit		
UART	Universal Asynchronous Receiver/Transmitter		
USB	Universal Serial Bus		
WDT	Watchdog Timer		

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RSK+RX65N-2MB

RENESAS STARTER KIT+

R20UT3891EG0100 Rev. 1.00 May 31, 2017

1. Overview

1.1 **Purpose**

This RSK+ is an evaluation tool for Renesas microcontrollers. This manual describes how to use the CS+ IDE Smart Configurator to create a working project for the RSK+ platform.

1.2 **Features**

This RSK+ provides an evaluation of the following features:

- Project Creation with CS+
- Code generation using the Smart Configurator.
- User circuitry such as switches, LEDs and a potentiometer

The RSK+ board contains all the circuitry required for microcontroller operation.

RSK+RX65N-2MB 2. Introduction

2. Introduction

This manual is designed to answer, in tutorial form, how to use the Smart Configurator for the RX family together with the CS+ IDE to create a working project for the RSK+ platform. The tutorials help explain the following:

- Project generation using the CS+
- Detailed use of the Smart Configurator for CS+
- Integration with custom code
- Building the project CS+

The project generator will create a tutorial project with three selectable build configurations:

- 'DefaultBuild' is a project with debug support and optimisation level set to two.
- 'Debug' is a project built with the debugger support included. Optimisation is set to zero.
- 'Release' is a project with optimised compile options (level two) and no 'Outputs debugging information'
 options not selected, producing code suitable for release in a product.

The tutorial examples in this manual assume that installation procedures described in the RSK+ Quick Start Guide have been completed. Please refer to the Quick Start Guide for details of preparing the configuration.

These tutorials are designed to show you how to use the RSK+ and are not intended as a comprehensive introduction to the CS+ debugger, compiler toolchains or the E2 emulator Lite. Please refer to the relevant user manuals for more indepth information.

3. Project Creation with CS+

3.1 Introduction

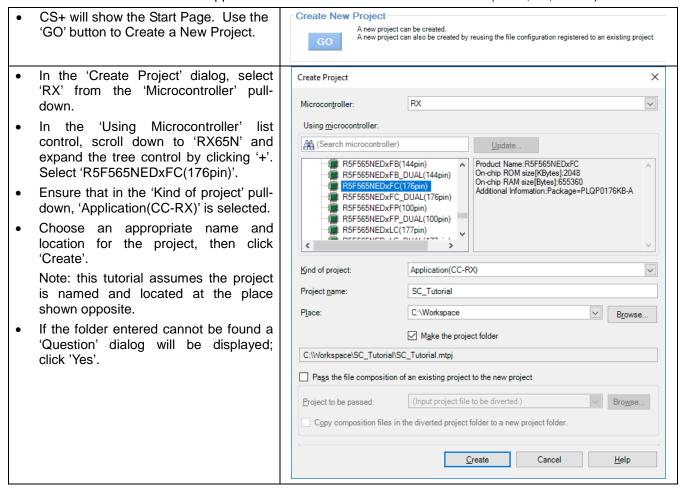
In this section, the user will be guided through the steps required to create a new C project for the RX65N MCU, ready to generate peripheral driver code using Smart Configurator. This project generation step is necessary to create the MCU-specific project and debug files.

3.2 Creating the Project

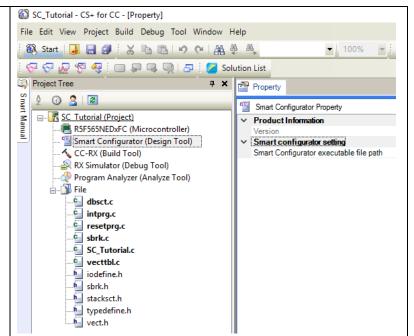
To use the program, start CS+:

Windows™ 7: Start Menu > All Programs > Renesas Electronics CS+ > CS+ for CC (RL78,RX,RH850)

Windows[™] 8.1 & 8: From Apps View , click 'CS+ for CC (RL78,RX,RH850)' icon Windows[™] 10: Start Menu > All Apps > Renesas Electronics CS+ > CS+ for CC (RL78,RX,RH850)



 CS+ will create the blank project with the standard project tree. A 'Smart Configurator' node may also be shown, if previously enabled.



4. Smart Configurator Using the CS+

4.1 Introduction

The Smart Configurator for the RX65N has been used to generate the sample code discussed in this document. Smart Configurator for CS+ is a tool for generating template 'C' source code and project settings for the RX65N. When using Smart Configurator, it supports user with a visual way of configuring the target device, clocks, software components, hardware resources and interrupts for the project. Thereby bypassing the need in most cases to refer to sections of the Hardware Manual.

By following the steps detailed in this tutorial, the user will generate a CS+ project called SC_Tutorial. A fully completed Tutorial project is contained on the DVD and may be imported into CS+ by following the steps in the Quick Start Guide. This tutorial is intended as a learning exercise for users who wish to use the Smart Configurator to generate their own custom projects for CS+.

Once the user has configured the project, the 'Generate Code' function is used to generate three code modules for each specific MCU feature selected. These code modules are name 'Config_xxx.h', 'Config_xxx.c', and 'Config_xxx_user.c', where 'xxx' is an acronym for the relevant MCU feature, for example 'S12AD'. Within these code modules, the user is then free to add custom code to meet their specific requirement. Custom code should be added, whenever possible, in between the following comment delimiters:

```
/* Start user code for adding. Do not edit comment generated here */
/* End user code. Do not edit comment generated here */
```

Smart Configurator will locate these comment delimiters, and preserve any custom code inside the delimiters on subsequent code generation operations. This is useful if, after adding custom code, the user needs to revisit Smart Configurator to change any MCU operating parameters.

The SC_Tutorial project uses interrupts for switch inputs, the ADC module, the Compare Match Timer (CMT), the Serial Communications Interface (SCI) and uses these modules to perform A/D conversion and display the results via the Virtual COM port to a terminal program and also on the LCD display on the RSK+.

Following a tour of the key user interface features of Smart Configurator and the reader is guided through each of the peripheral function configuration dialogs in §4.2. In §5, the reader is familiarised with the structure of the template code, as well as how to add their own code to the user code areas provided by the code generator.

4.2 Project Configuration using Smart Configurator - Overview page

In this section, a brief tour of Smart Configurator is presented. For further details of the Smart Configurator paradigm and reference, refer to the Smart Configurator Usage.

You can download the latest document from: https://www.renesas.com/smart-configurator.

Smart Configurator will start up by double clicking on "Smart Configurator (Design Tool)" on the project tree. The Smart Configurator initial view is displayed as illustrated in **Figure 4-1**.

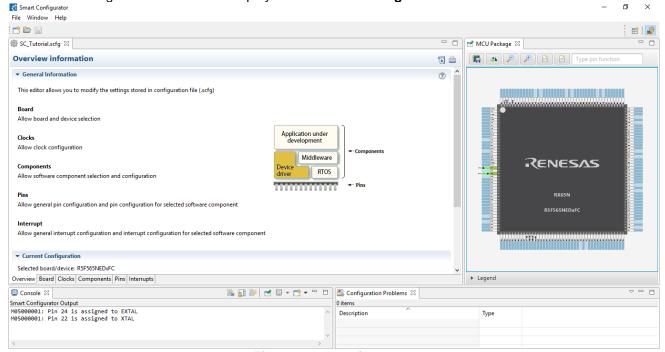


Figure 4-1 Overview page

Smart Configurator provides GUI features for configuration of MCU sub systems. Once the user has configured all required MCU sub systems and peripherals, the user can click the 'Generate Code' button, resulting in a fully configured CS+ project that builds and runs without error.

4.3 Clocks configuration page

Clocks configuration page configures clocks of the device selected. Clock source, frequency, PLL settings and clock divider settings can be configured for the output clocks. Clock configurations will be reflected to 'r_bsp_config.h' file in 'Smart Configurator\r_config' of project tree.

4.3.1 Clocks configuration

Figure 4-2 shows a screenshot of Smart Configurator with the Clocks tab. Click on the 'Clocks' tab. Configure the system clocks as shown in the figure. In this tutorial, we are using the on-board 24 MHz crystal resonator for our main clock oscillation source and the PLL circuit is in operation. The PLL output is used as the main system clock and the divisors should be set as shown in **Figure 4-2**.

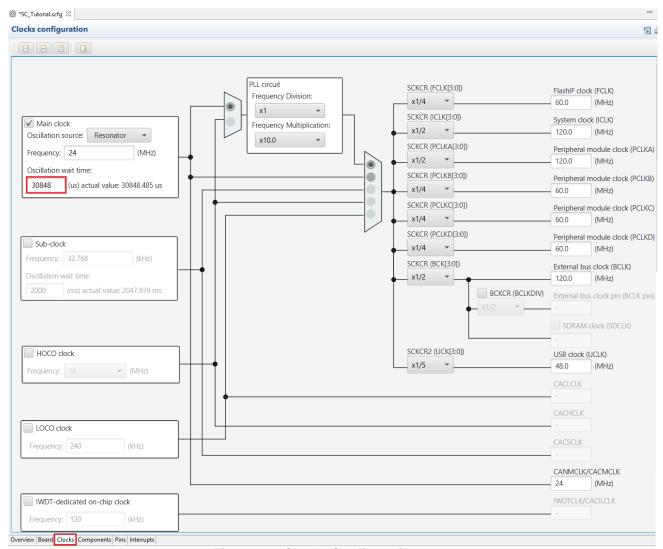


Figure 4-2 Clocks Configuration page

4.4 Component page

Drivers and middleware are handled as software components in Smart Configurator. Component page allows user to select and configure software components.

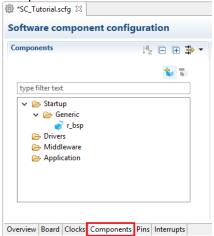


Figure 4-3 Component page

4.4.1 Add a software component into the project

Smart Configurator supports two types of software components: Code Generator and Firmware Integration Technology. In the following sub-sections, the reader is guided through the steps to configure the MCU for a simple project containing interrupts for switch inputs, timers, ADC and a SCI by component of Code Generator.

Click 'Add component' icon.

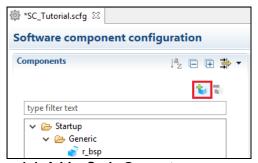


Figure 4-4 Add a Code Generator component (1)

In 'Software Component Selection' dialog -> Type, select 'Code Generator'.

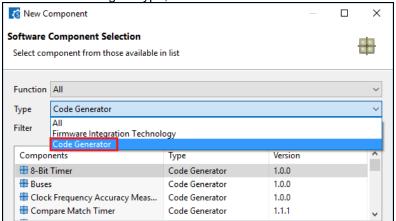


Figure 4-5 Add a Code Generator component (2)

4.4.2 Compare Match Timer

CMT0 will be used as an interval timer for generation of accurate delays. CMT1 and CMT2 will be used as timers in de-bouncing of switch interrupts.

Select 'Compare Match Timer' as shown in Figure 4-6 below then click 'Next'.

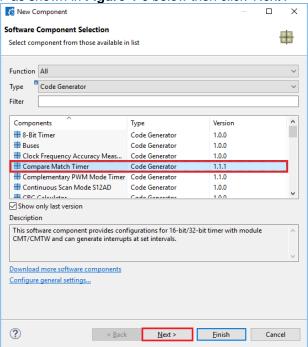


Figure 4-6 Select Compare Match Timer

In 'Add new configuration for selected component' dialog -> Resource, select 'CMT0' as shown in **Figure 4-7** below then click 'Finish'.

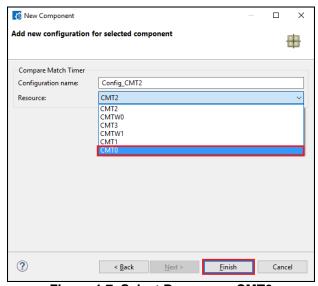


Figure 4-7 Select Resource - CMT0

In the 'Config_CMT0' configures CMT0 as shown in **Figure 4-8**. This timer is configured to generate a high priority interrupt every 1ms. We will use this interrupt later in the tutorial to provide an API for generating high accuracy delays required in our application.

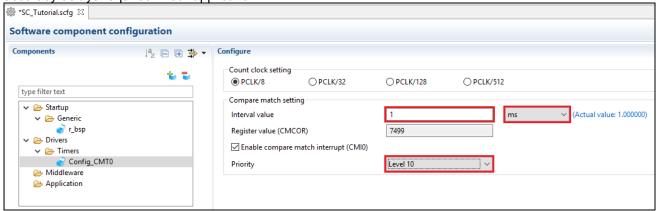


Figure 4-8 Config_CMT0 setting

Click 'Add component' icon. In 'Software Component Selection' dialog -> Type, select 'Code Generator'. Select 'Compare Match Timer' then click 'Next'. In 'Add new configuration for selected component' dialog -> Resource, select 'CMT1' as shown in **Figure 4-9** below then click 'Finish'.

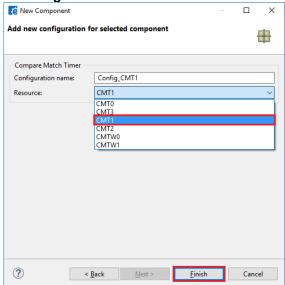


Figure 4-9 Select Resource - CMT1

Navigate to the 'Config_CMT1' and configure CMT1 as shown in **Figure 4-10**. This timer is configured to generate a high priority interrupt after 20ms. This timer is used as our short switch de-bounce timer later in this tutorial.

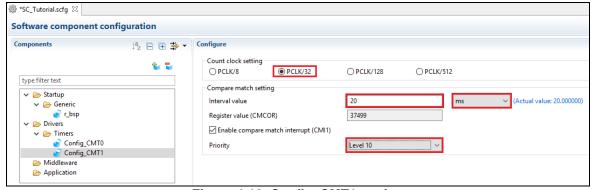


Figure 4-10 Config_CMT1 setting

Click 'Add component' icon. In 'Software Component Selection' dialog -> Type, select 'Code Generator'. Select 'Compare Match Timer' then click 'Next'. In 'Add new configuration for selected component' dialog -> Resource, select 'CMT2' as shown in **Figure 4-11** below then click 'Finish'.

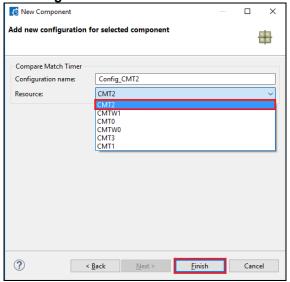


Figure 4-11 Select Resource - CMT2

Navigate to the 'Config_CMT2' and configure CMT2 as shown in **Figure 4-12**. This timer is configured to generate a high priority interrupt after 200ms. This timer is used as our long switch de-bounce timer later in this tutorial.

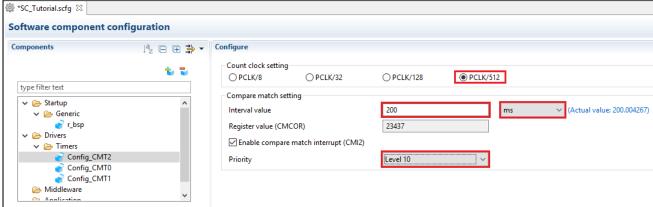


Figure 4-12 Config_CMT2 setting

4.4.3 Interrupt Controller

Referring to the RSK+ schematic, SW1 is connected to IRQ11(P03) and SW2 is connected to IRQ13 (P05). SW3 is connected IRQ15(P07) and the ADTRG0n. Tutorial used ADTRG0n and will be configured later in §4.4.7.

Click 'Add component' icon. In 'Software Component Selection' dialog -> Type, select 'Code Generator'. Select 'Interrupt Controller' as shown in **Figure 4-13** then click 'Next'.

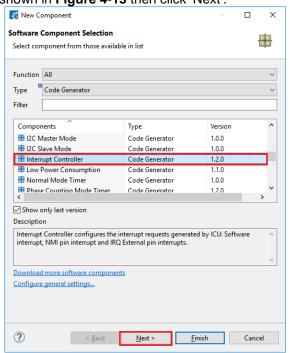


Figure 4-13 Select Interrupt Controller

In 'Add new configuration for selected component' dialog -> Resource, select 'ICU' as shown in **Figure 4-14** below then click 'Finish'.

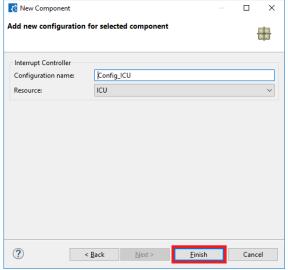


Figure 4-14 Select resource - ICU

Navigate to the 'Config_ICU', configure these two interrupts as falling edge triggered as shown in **Figure 4-15** below.

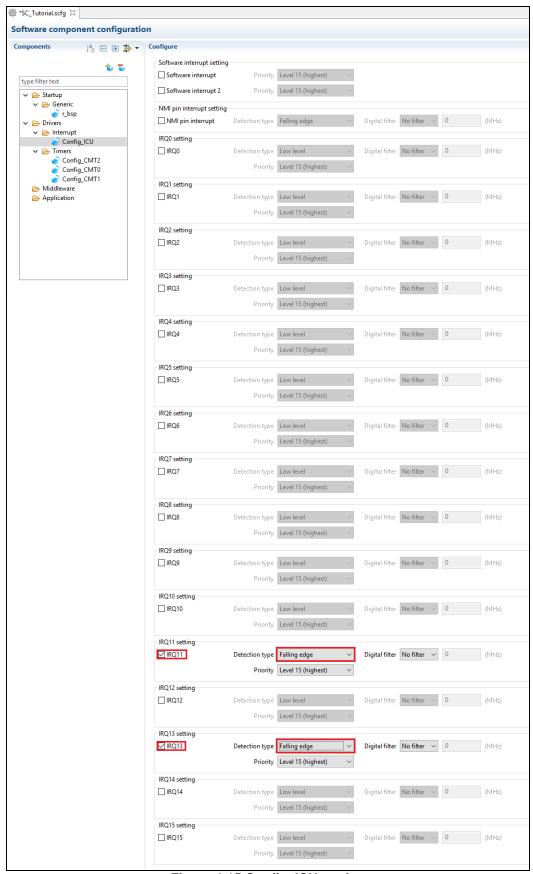


Figure 4-15 Config_ICU setting

4.4.4 Ports

Referring to the RSK+ schematic, LED0 is connected to P73, LED1 is connected to PG7, LED2 is connected to PG6 and LED3 is connected to PG5. PJ3 is used as one of the LCD control lines, together with PF5, PG3 and PG4.

Click 'Add component' icon. In 'Software Component Selection' dialog -> Type, select 'Code Generator'. Select 'Ports' as shown in **Figure 4-16** then click 'Next'.

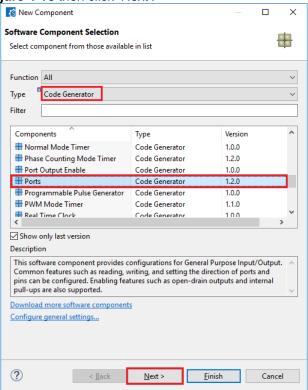


Figure 4-16 Select Ports

In 'Add new configuration for selected component' dialog -> Resource, select 'PORT' as shown in **Figure 4-17** below then click 'Finish'.

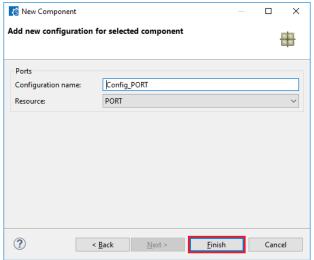


Figure 4-17 Select resource - PORT

'PORT7', 'PORTF', 'PORTG', 'PORTJ' tick box is checked as shown in Figure 4-18 below.

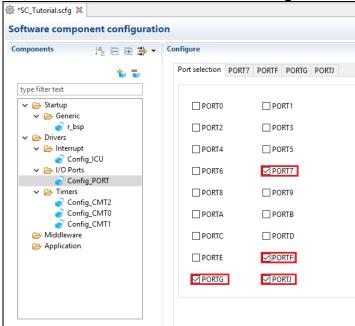


Figure 4-18 Select Port selection

Navigate to the 'Ports' configure these four I/O lines and LCD control lines as shown in **Figure 4-19**, **Figure 4-20**, **Figure 4-21** and **Figure 4-22** below. Ensure that the 'Output 1' tick box is checked, except PG3. Select 'PORT7' tab.

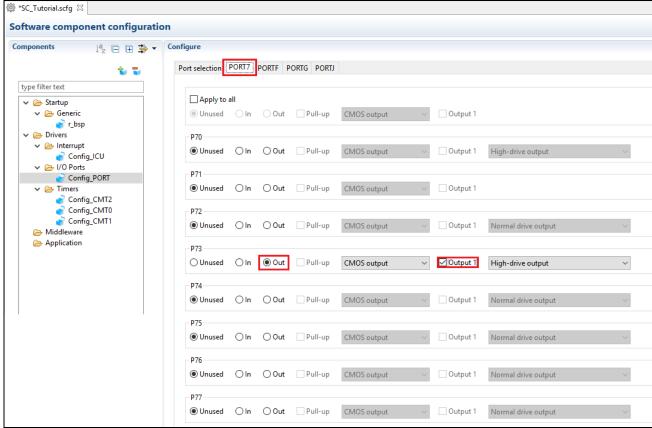


Figure 4-19 Select PORT7 tab

Select 'PORTF' tab.

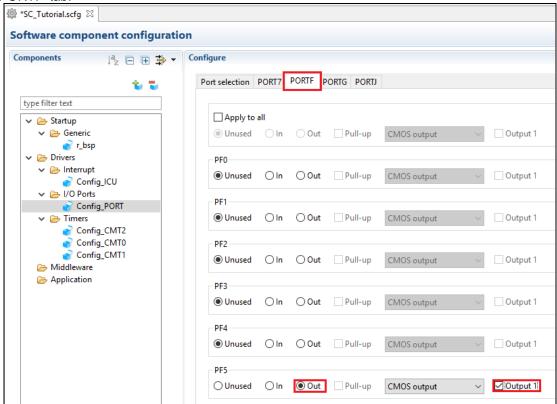


Figure 4-20 Select PORTF tab

Select 'PORTG' tab.

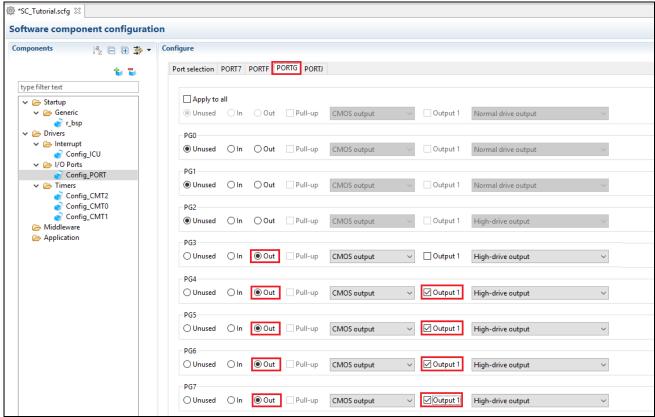


Figure 4-21 Select PORTG tab

Select 'PORTJ' tab.

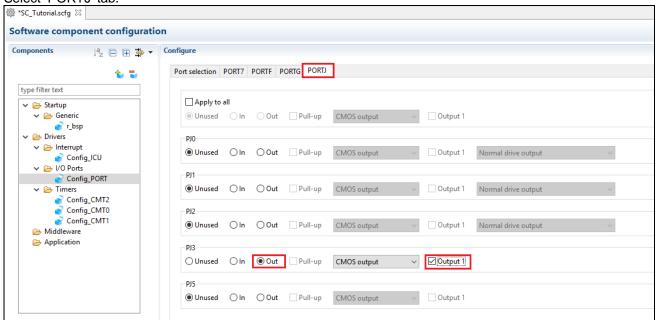


Figure 4-22 Select PORTJ tab

4.4.5 SCI/SCIF Asynchronous Mode

In the RSK+RX65N-2MB SCI8 is connected via a Renesas RL78/G1C to provide a USB virtual COM port as shown in the schematic.

Click 'Add component' icon. In 'Software Component Selection' dialog -> Type, select 'Code Generator'. Select 'SCI/SCIF Asynchronous Mode' as shown in **Figure 4-23** then click 'Next'.

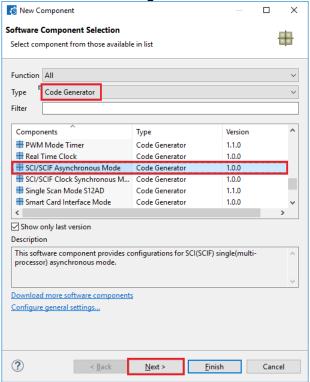


Figure 4-23 Select SCI/SCIF Asynchronous Mode

In 'Add new configuration for selected component' dialog -> Work mode, select 'Transmission/Reception' as shown in **Figure 4-24** below.

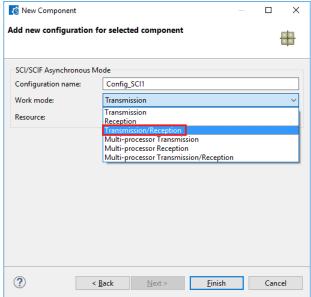


Figure 4-24 Select Work mode – Transmission/Reception

In 'Resource', select 'SCI8' as shown in Figure 4-25 below.

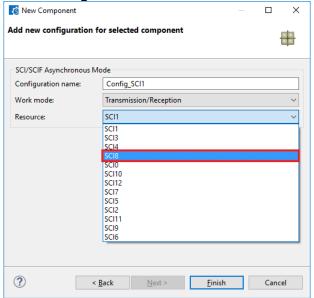


Figure 4-25 Select Resource - SCI8

Ensure that the 'Configuration name' is set to 'Config_SCI8' as shown in Figure 4-26 below then click 'Finish'

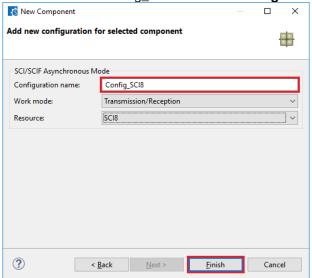


Figure 4-26 Ensure Configuration name - Config_SCI8

Configure SCI8 as shown in **Figure 4-27**. Ensure the 'Start bit edge detection' is set as 'Falling edge on RXD8 pin' and the 'Bit rate' is set to 19200 bps. All other settings remain at their defaults.

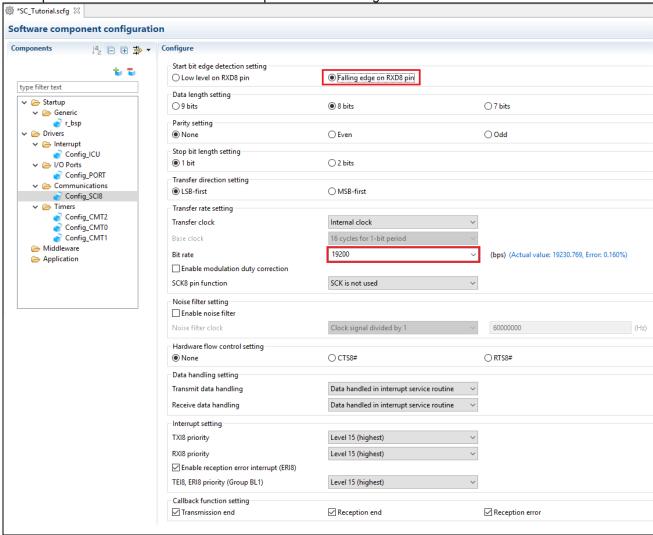


Figure 4-27 Config_SCI8 setting

4.4.6 SPI Clock Synchronous Mode

In the RSK+RX65N-2MB SCI6 is used as an SPI master for the Pmod LCD on the PMOD1 connector as shown in the schematic. Click 'Add component' icon. In 'Software Component Selection' dialog -> Type, select 'Code Generator'. Select 'SPI Clock Synchronous Mode' as shown in **Figure 4-28** then click 'Next'.

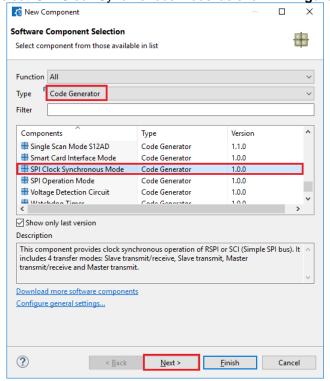


Figure 4-28 Select SPI Clock Synchronous Mode

Ensure Operation, is set to 'Master transmit only' as shown in Figure 4-29 below.

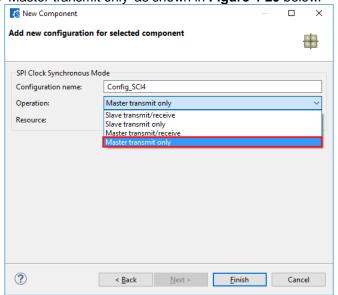


Figure 4-29 Select Operation - Master Transmit

In 'Resource', select 'SCI6' as shown in Figure 4-30 below.

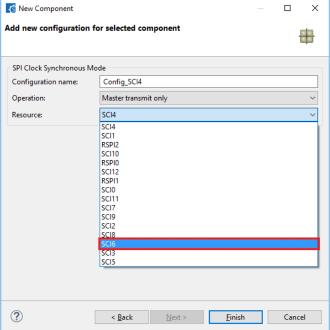


Figure 4-30 Select Resource - SCI6

Ensure that the 'Configuration name' is set to 'Config_SCI6' as shown in Figure 4-31 below then click 'Finish'

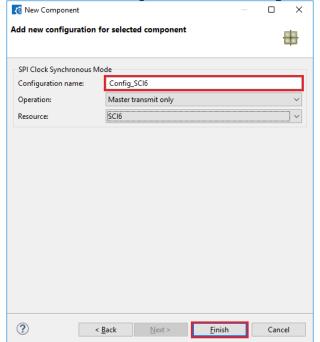


Figure 4-31 Ensure Configuration name - Config_SCI6

Configure SCI6 as shown in **Figure 4-32**. Ensure the 'Transfer direction' is set as 'MSB-first' and the 'Bit rate' is set to 15000 kbps. All other settings remain at their defaults.

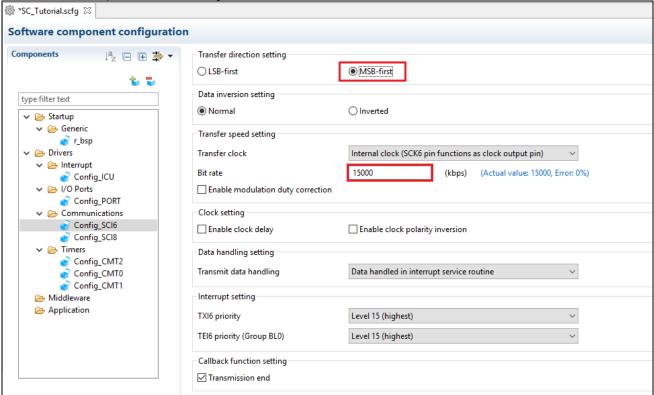


Figure 4-32 Config_SCI6 setting

4.4.7 Single Scan Mode S12AD

We will be using the S12AD on Single Scan Mode on the AN000 input, which is connected to the RV1 potentiometer output on the RSK. The conversion start trigger will be via the pin connected to SW3. Click

'Add component' icon. In 'Software Component Selection' dialog -> Type, select 'Code Generator'. Select

'Single Scan Mode S12AD' as shown in Figure 4-33 then click 'Next'.

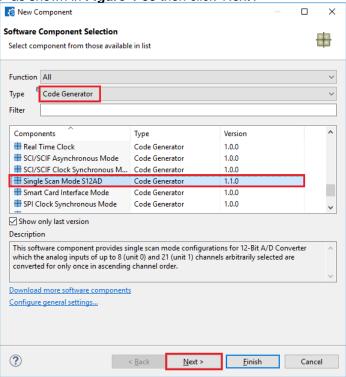


Figure 4-33 Select Single Scan Mode S12AD

In 'Add new configuration for selected component' dialog -> Resource, select 'S12AD0' as shown in **Figure 4-34** below then click 'Finish'.

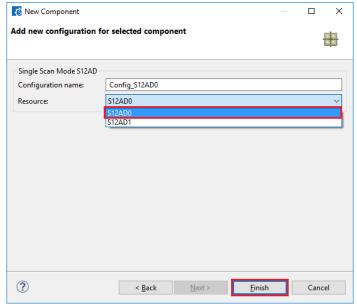


Figure 4-34 Select resource - S12AD0

Configure S12AD0 as shown in **Figure 4-35** and **Figure 4-36**. Ensure the 'Analog input channel' tick box for AN000 is checked and the 'Start trigger source' is set to 'A/D conversion start trigger pin'. All other settings remain at their defaults.

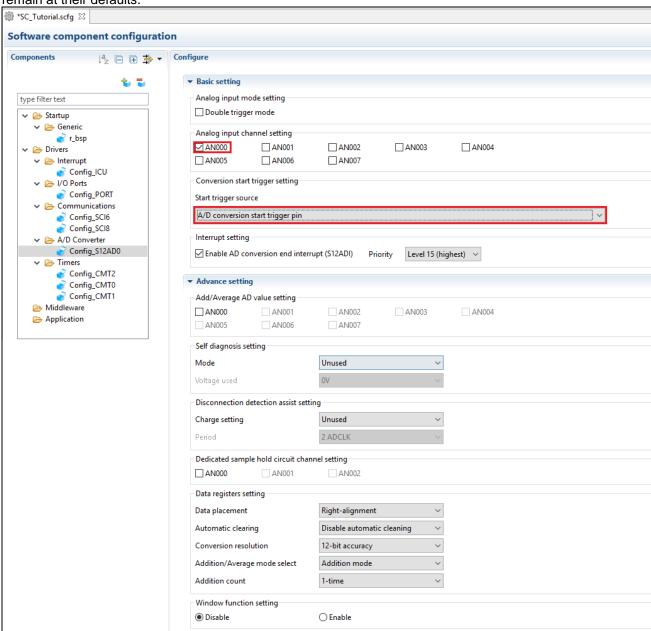


Figure 4-35 Config_S12AD0 setting (1)

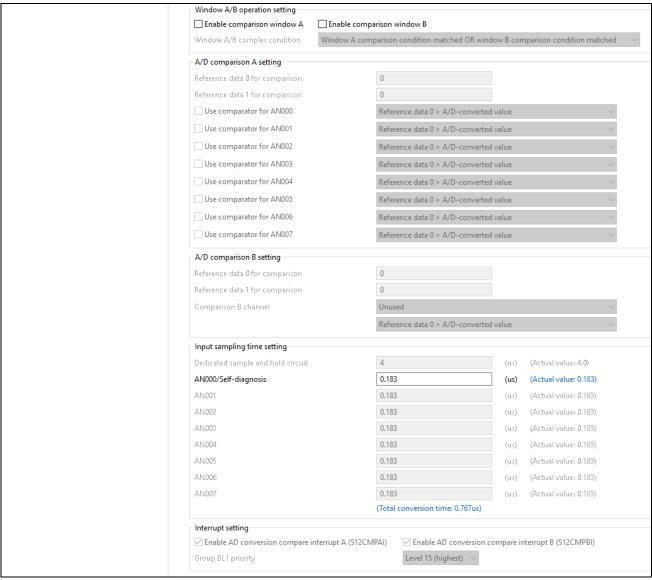


Figure 4-36 Config_S12AD0 setting (2)

4.5 Pins configuration page

Smart Configurator assigns pins to the software components that are added to the project. Assignment of the pins can be changed at Pins page.

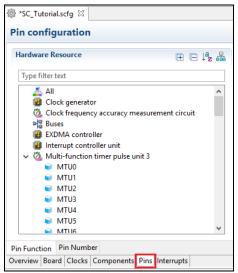


Figure 4-37 Pin configuration page

4.5.1 Change pin assignment of a software component

To change the pin assignment of a software component in Pin Function list. Click to change view to show by Software Components.



Figure 4-38 Change view to show by Software Components

Select the Config_ICU of software component. In the Pin Function list -> Assignment column, change the pin assignment IRQ13 to P05, IRQ11 to P03. Ensure the 'Enable' tick box of IRQ11 and IRQ13 are checked, as shown in **Figure 4-39**.

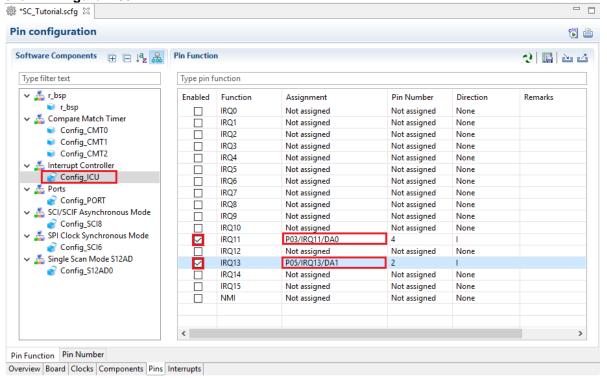


Figure 4-39 Configure pin assignment - Config_ICU

Select the Config_SCI8 of software component. In the Pin Function list -> Assignment column, Ensure the 'Enable' tick box of RXD8 and TXD8 are checked and Assignment column of RXD8 is PJ1 and TXD8 is PJ2 as shown in **Figure 4-40**.

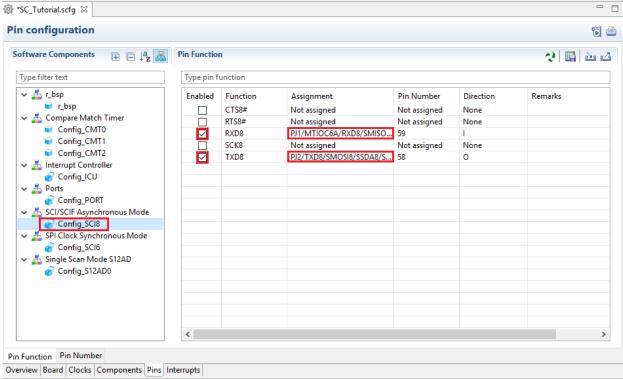


Figure 4-40 Configure pin assignment - Config SCI8

RENESAS

Select the Config_SCI6 of software component. In the Pin Function list -> Assignment column, Ensure the 'Enable' tick box of SCK6 and SMOSI6 are checked and Assignment column of SCK6 is P02, SMOSI6 is P00 as shown in **Figure 4-41**.

_ _ Pin configuration **6 Software Components** Pin Function ⊞ □ ↓ La 😍 🖫 🗠 🛎 Type filter text Type pin function 🗸 🚣 r_bsp Enabled Function Pin Number Direction Remarks Assignment SCK6 P02/TMCI1/SCK6/IRQ10/A 10 Compare Match Timer SMISO6 Not assigned Not assigned None Config_CMT0 SMOSI6 P00/TMRI0/TXD6 \checkmark IO Config_CMT1 SS6# Not assigned Not assigned None Config_CMT2 Config_ICU Config_PORT 🔗 Config_SCI8 SPI Clock Synchronous Mode Single Scan Mode S12AD Config_S12AD0 < Pin Function Pin Number Overview Board Clocks Components Pins Interrupts

Figure 4-41 Configure pin assignment - Config_SCI6

Select the Config_S12AD0 of software component. In the Pin Function list -> Assignment column, Ensure the 'Enable' tick box of AN000, AVCC0, AVSS0, ADTRG0#, VREFH0 and VREFL0 are checked and Assignment column of AN000 is P40, ADTRG0# is P07 as shown in **Figure 4-42**.

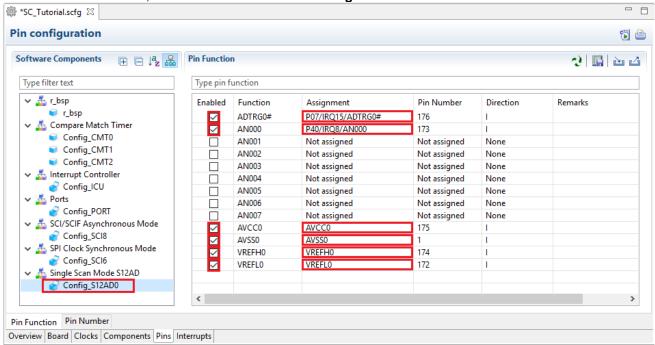


Figure 4-42 Configure pin assignment - Config_S12AD0

Peripheral function configuration is now complete. Save the project using the File -> Save, then click 'EGenerate Code' at location of Figure 4-43.



Figure 4-43 Generate Code Button

The Console pane should report 'Code generation is successful', as shown Figure 4-44 below.

```
🖳 🚮 🐶 | 🛃 🖫 🔻 🗂
■ Console S3
Smart Configurator Output
M04000001: File generated:src\smc_gen\general\r_cg_dmac.h
M04000001: File generated:src\smc_gen\general\r_cg_sci.h
M04000001: File generated:src\smc_gen\general\r_cg_rspi.h
M04000001: File generated:src\smc_gen\general\r_cg_ppg.h
M04000001: File generated:src\smc_gen\general\r_cg_tmr.h
M04000001: File generated:src\smc_gen\general\r_cg_lvd.h
M04000001: File generated:src\smc_gen\general\r_cg_dmac_user.c
M04000001: File generated:src\smc_gen\general\r_smc_entry.h
M04000001: File generated:src\smc_gen\general\r_cg_cmt.h
M04000001: File generated:src\smc_gen\general\r_cg_crc.h
M04000001: File generated:src\smc_gen\general\r_cg_doc.h
M04000001: File generated:src\smc_gen\general\r_cg_poe.h
M04000001: File generated:src\smc_gen\general\r_cg_riic.h
M04000001: File generated:src\smc_gen\general\r_cg_rtc.h
M04000001: File generated:src\smc_gen\general\r_cg_elc.h
M04000001: File generated:src\smc_gen\general\r_cg_bsc.h
M05000012: File generated:src\smc_gen\r_pincfg\Pin.h
M05000012: File generated:src\smc_gen\r_pincfg\Pin.c
M06000002: File generated:src\smc_gen\r_config\r_bsp_interrupt_config.h
M06000002: File generated:src\smc_gen\general\r_smc_interrupt.c
M06000002: File generated:src\smc_gen\general\r_smc_interrupt.h
M00000002: Code generation is successful
M03000004: File modified:src\smc_gen\r_config\r_bsp_config.h
```

Figure 4-44 Smart Configurator console

When code generation is executed, the startup files generated at the time of CS+ project creation are replaced with those generated by Smart Configurator. **Figure 4-45** the project tree after code generation. In the next chapter, user code is added to these files, and SC_Tutorial is completed by adding a new source file to the project.

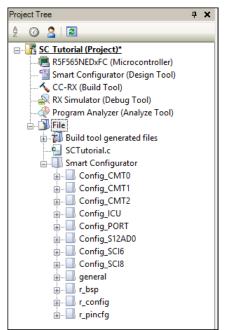
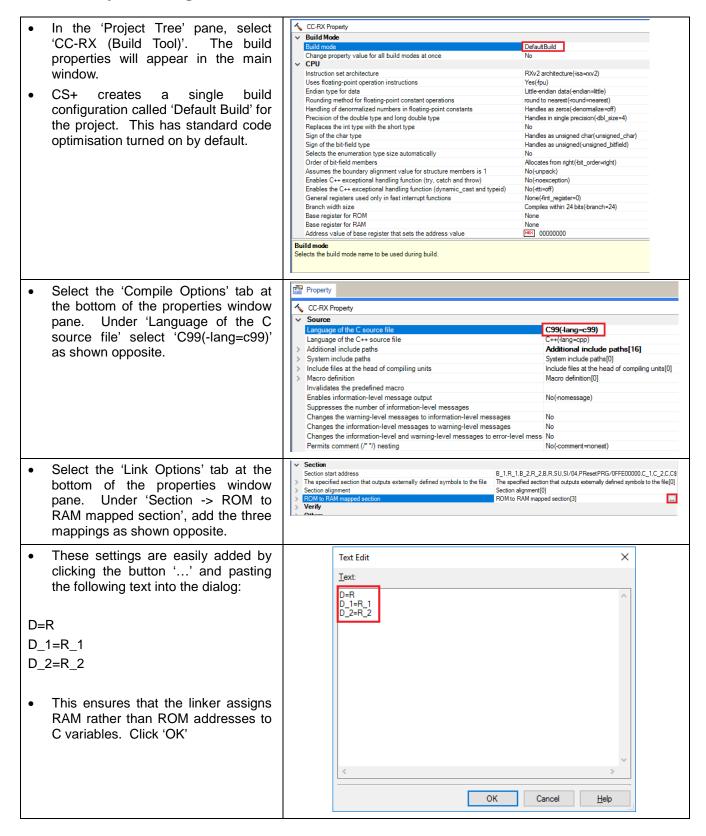


Figure 4-45 Smart Configurator folder structure

5. Completing the Tutorial Project

5.1 Project Settings



From the 'Build' menu, select 'Build SC_Tutorial - CS+ for CC - [Project Tree] Mode Settings...'. Click 'Duplicate' File Edit View Project Build Debug Tool Window Help and in the resulting 'Character String 🐧 Start 🔲 📙 谍 🕼 **Build Project** F7 Input' dialog, enter 'Debug' for the 😔 😔 🔬 🧐 🤻 Rebuild Project Shift+F7 name of the duplicate Build Mode. 6 Clean Project Project Tree Ton Rapid Build 2 3 2 2 Update Dependencies SC Tutorial (P

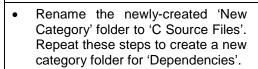
SC Tutorial (P) RSF565NEC Build SC_Tutorial Smart Con Rebuild SC_Tutorial ← CC-RX (Bu RX Simulat Clean SC_Tutorial 📣 Program A 📑 Update Dependencies of SC_Tutorial File Set Link Order of SC_Tutorial... Open the Optimization Performance Comparison Tool for SC_Tutorial... Stop Build Ctrl+F7 Build Mode Settings... Batch Build... **Build Option List Build Mode Settings** Selected build mode: Build mode list: **Duplicate** Close Property The new 'Debug' Build Mode will be added to the Build Mode list. Click CC-RX Property Build Mode 'Close'. Now, in the main CC-RX Default Build **Property** window. under Change property value for all build modes at once efault Build v CPU 'Common Options' tab, click on the Instruction set architecture line containing 'Build Mode', click the pull-down arrow and select 'Debug' from the pull-down'. In the 'Frequently Used Options (for Property group, Compile)' select CC-RX Property 'Optimization Level' option and select PIC/PID Output File Type and Path '0' from the pull-down. We have now Output file type Intermediate file output folder Execute Module(Load Module File) created a 'Debug' Build Mode with %BuildModeName% Frequently Used Options(for Compile) no code optimisation and will be Additional include paths Additional include paths[16] using the Build Mode to create and System include paths System include paths[0] Macro definition Macro definition[0] debug the project. Outputs debugging information Yes(-debug) 2(-optimize=2 Outputs additional information for inter-module optimization Optimization type Outputs a source list file 2(-optimize=2) Frequently Used Options(for Assemble) Max(-optimize=max)

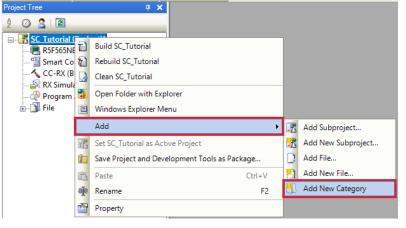
- All of the sample code projects contained in this RSK+ are configured with three Build Modes; 'DefaultBuild', 'Debug' and 'Release'. 'Release' is created in the same way as above; by duplicating 'Default Build'. 'Release' Build Mode leaves code optimisation turned on and removes debug information from the output file.
- To remove debug information from the 'Release' Build Mode, in the 'CC-RX Property' window, select the 'Common Options' tab at the bottom of the window pane. For the 'Outputs debugging information' option, select 'No(-nodebug).
- Reset the Build Mode back to 'Debug' using the 'Build Mode' pulldown control.
- From the menus, select 'File -> Save All' to save all project settings.

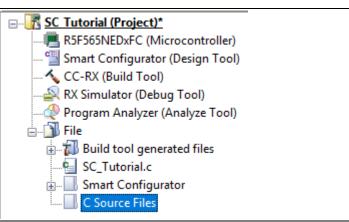
. (Build mode Change property value for all build modes at once CPU	Release No				
. (140				
	PIC/PID					
	Output File Type and Path					
	Frequently Used Options(for Compile)					
	Additional include paths	Additional include paths[16]				
	System include paths	System include paths[0]				
	Macro definition	Macro definition[0]				
(Outputs debugging information	Yes(-debug)				
(Optimization level	2(-optimize=2)				
(Outputs additional information for inter-module optimization	No				
(Optimization type	Optimizes with emphasis on code size(-size)				
(Outputs a source list file	No(-nolistfile)				
F	Frequently Used Options(for Assemble)					
	Frequently Used Options(for Link)					
	Using libraries	Using libraries[0]				
(Outputs debugging information	Yes (Outputs to the output file)(-DEBug)				
	Optimization type	Yes (Outputs to the output file)(-DEBug)				
	Section start address	Yes (Outputs to <output file="" name="">.dbg file)(-SDebu</output>				
/ F	Frequently Used Options(for Hex Output)	No(-NODEBug)				

5.2 Additional Folders

- Before new source files are added to the project, we will create two additional folders in the CS+ Project Tree.
- In the Project Tree pane, right-click the SC_Tutorial project and select 'Add -> Add New Category'.







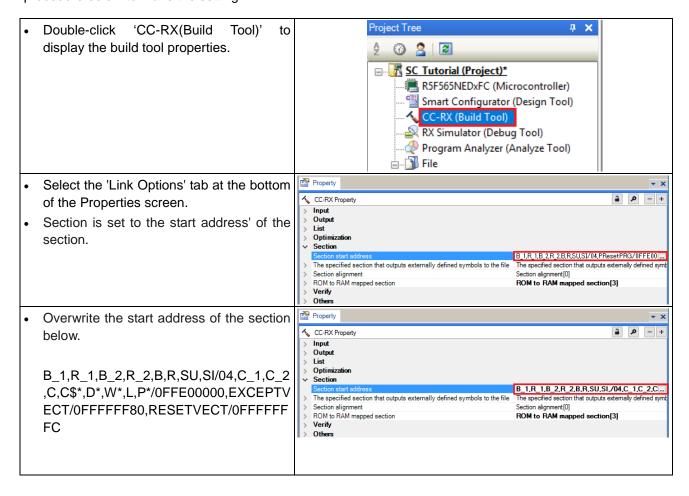
5.3 Precautions on using Smart Configurator

When executing the build using Smart Configurator, the warning message shown in **Figure 5-1** may be displayed.



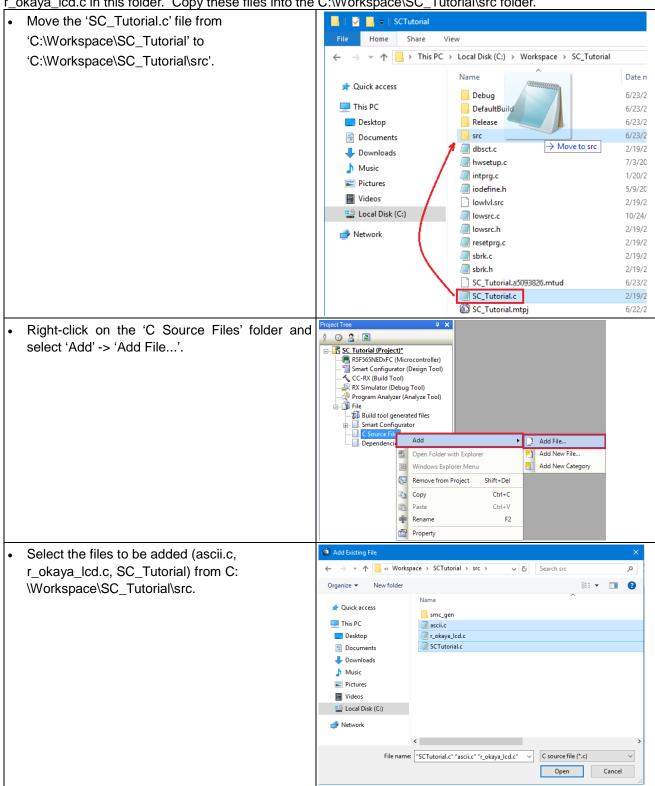
Figure 5-1: CS+ output window

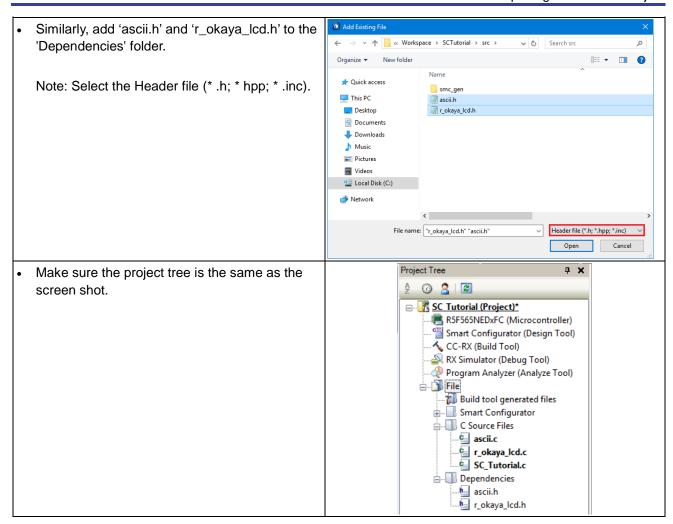
It is a warning sentence that is displayed because there are unused sections, and there is no problem in operation in SC_Tutorial created in this manual. If, do not want to display a warning message, follow the procedure below to make the setting.



5.4 LCD Code Integration

API functions for the Okaya LCD display are provided with the RSK+. Refer to the Tutorial project folder created according to the Quick Start Guide procedure. Locate the files ascii.h, r_okaya_lcd.h, ascii.c, and r_okaya_lcd.c in this folder. Copy these files into the C:\Workspace\SC_Tutorial\src folder.





Code must be inserted in to the user code area in many files in this project, in the areas delimited by comments as follows:

```
/^{\star} Start user code for <code>_xxxxx_</code>. Do not edit comment generated here ^{\star}/ /^{\star} End user code. Do not edit comment generated here ^{\star}/
```

Where _xxxx_ depends on the particular area of code, i.e. 'function' for insertion of user functions and prototypes, 'global' for insertion of user global variable declarations, or 'include' for insertion of pre-processor include directives. User code inserted inside these comment delimiters is protected from being overwritten by Smart Configurator, if the user needs to subsequently change any of the Smart Configurator-generated code.

In the CS+ Project Tree, expand the 'Smart Configurator/general' folder and open the file 'r_cg_userdefine.h' by double-clicking on it. Insert the following #defines in between the user code delimiter comments as shown below.

In the CS+ Project Tree, expand the 'C Source Files' folder and open the file 'SC_Tutorial.c' by double-clicking on it. Add header files above the 'main' function as shown below.

```
#include "r_smc_entry.h"
#include "r_okaya_lcd.h"
#include "r_cg_userdefine.h"
```

Scroll down to the 'main' function and insert the highlighted code as shown below into the beginning of the 'main' function:

5.4.1 SPI Code

The Okaya LCD display is driven by the SPI Master that was configured using Smart Configurator in §4.4.6. In the CS+ Project Tree, expand the 'Smart Configurator/Config_SCI6' and open the file 'Config_SCI6.h' by double-clicking on it. Insert the following code in the user code area at the end of the file:

```
/* Start user code for function. Do not edit comment generated here */
/* Exported functions used to transmit a number of bytes and wait for completion */
MD_STATUS R_SCI6_SPIMasterTransmit(uint8_t * const tx_buf, const uint16_t tx_num);
/* End user code. Do not edit comment generated here */
```

Now, open the Config_SCI6_user.c file and insert the following code in the user area for global:

```
/* Start user code for global. Do not edit comment generated here */
/* Flag used locally to detect transmission complete */
static volatile uint8_t sci6_txdone;
/* End user code. Do not edit comment generated here */
```

Insert the following code in the transmit end call-back function for SCI6:

```
static void r_Config_SCI6_callback_transmitend(void)
{
    /* Start user code for r_Config_SCI6_callback_transmitend. Do not edit comment generated here */
    sci6_txdone = TRUE;

    /* End user code. Do not edit comment generated here */
}
```

Now insert the following function in the user code area at the end of the file:

```
/* Start user code for adding. Do not edit comment generated here */
```

```
/***********************
* Function Name: R_SCI6_SPIMasterTransmit
 \hbox{\tt Description } : \hbox{\tt This function sends SPI6 data to slave device.}
 Arguments : tx_buf -
                  transfer buffer pointer
             tx_num -
                  buffer size
* Return Value : status -
                 MD_OK or MD_ARGERROR
MD_STATUS R_SCI6_SPIMasterTransmit (uint8_t * const tx_buf,
                                 const uint16_t tx_num)
   MD_STATUS status = MD_OK;
   /* Clear the flag before initiating a new transmission */
   sci6_txdone = FALSE;
   /* Send the data using the API */
   status = R_Config_SCI6_SPI_Master_Send(tx_buf, tx_num);
   /* Wait for the transmit end flag */
   while (FALSE == sci6_txdone)
       /* Wait */
   return (status);
* End of function R_SCI6_SPIMasterTransmit
     *************
```

This function uses the transmit end callback function to perform flow control on the SPI transmission to the LCD, and is used as the main API call in the LCD code module.

5.4.2 CMT Code

The LCD code needs to insert delays to meet the timing requirements of the display module. This is achieved using the dedicated timer which was configured using Smart Configurator in §4.4.2. In the CS+ Project Tree, expand the 'Smart Configurator\Config_CMT0\Config_CMT0.h' and insert the following code in the user area for function at the end of the file:

```
/* Start user code for function. Do not edit comment generated here */
void R_CMT_MsDelay(const uint16_t millisec);
/* End user code. Do not edit comment generated here */
```

Open the file 'Config_CMT0_user.c' and insert the following code in the user area for global at the beginning of the file:

```
/* Start user code for global. Do not edit comment generated here */
static volatile uint8_t one_ms_delay_complete = FALSE;
/* End user code. Do not edit comment generated here */
```

Scroll down to the r_Config_CMT0_cmi0_interrupt function and insert the following line in the user code area:

```
static void r_Config_CMT0_cmi0_interrupt(void)
{
    /* Start user code for r_Config_CMT0_cmi0_interrupt. Do not edit comment generated here */
    one_ms_delay_complete = TRUE;

    /* End user code. Do not edit comment generated here */
}
```

Then insert the following function in the user code area at the end of the file:

```
/st Start user code for adding. Do not edit comment generated here st/
```

```
* Function Name: R CMT MsDelay
* Description : Uses CMTO to wait for a specified number of milliseconds
* Arguments : uint16_t millisecs, number of milliseconds to wait
* Return Value : None
           void R_CMT_MsDelay (const uint16_t millisec)
  uint16 t ms count = 0;
  do
     R_Config_CMT0_Start();
     while (FALSE == one_ms_delay_complete)
        /* Wait */
     R_Config_CMT0_Stop();
     one_ms_delay_complete = FALSE;
     ms_count++;
  } while (ms_count < millisec);</pre>
End of function R_CMT_MsDelay
***********************
```

Select 'Build Project' from the 'Build' menu, or press F7. CS+ will build the project with no errors.

The project may now be run using the debugger as described in §6. The program will display 'RSK+RX65N-2MB Tutorial Press Any Switch' on 3 lines in the LCD display.

5.5 Switch Code Integration

API functions for user switch control are provided with the RSK+. Refer to the Tutorial project folder created according to the Quick Start Guide procedure. Locate the files rskrx65n2mbdef.h, r_rsk_switch.h and r_rsk_switch.c in this folder. Copy these files into the C:\Workspace\SC_Tutorial\src folder. Add these three files into the project in the same way as the LCD files.

The switch code uses interrupt code in the files Config_ICU.c, Config_ICU_user.c and Config_ICU.h and timer code in the files Config_ICU.c, Config_ICU_user.c, Config_CMT1.h, Config_CMT1.c, Config_CMT1_user.c, Config_CMT2.h, Config_CMT2.c, and Config_CMT2_user.c, as described in §4.4.2 and §4.4.3. It is necessary to provide additional user code in these files to implement the switch press/release detection and de-bouncing required by the API functions in r_rsk_switch.c.

5.5.1 Interrupt Code

In the CS+ Project Tree, expand the 'Smart Configurator/Config_ICU' folder and open the file 'Config_ICU.h' by double-clicking on it. Insert the following code in the user code area at the end of the file:

```
/* Start user code for function. Do not edit comment generated here */
/* Function prototypes for detecting and setting the edge trigger of ICU_IRQ */
uint8_t R_ICU_IRQIsFallingEdge(const uint8_t irq_no);
void R_ICU_IRQSetFallingEdge(const uint8_t irq_no, const uint8_t set_f_edge);
void R_ICU_IRQSetRisingEdge(const uint8_t irq_no, const uint8_t set_r_edge);
/* End user code. Do not edit comment generated here */
```

Now, open the Config_ICU.c file and insert the following code in the user code area at the end of the file:

/* Start user code for adding. Do not edit comment generated here */

```
* Function Name: R_ICU_IRQSetFallingEdge
 Description : This function sets/clears the falling edge trigger for the
               specified ICU_IRQ.
            : uint8_t irq_no
               uint8_t set_f_edge, 1 if setting falling edge triggered, 0 if
               clearing
* Return Value : None
void R_ICU_IRQSetFallingEdge (const uint8_t irq_no, const uint8_t set_f_edge)
   if (1 == set_f_edge)
       ICU.IRQCR[irq_no].BYTE |= _04_ICU_IRQ_EDGE_FALLING;
   else
       ICU.IRQCR[irq_no].BYTE &= (uint8_t) ~_04_ICU_IRQ_EDGE_FALLING;
* End of function R_ICU_IRQSetFallingEdge
* Function Name: R_ICU_IRQSetRisingEdge
* Description : This function sets/clear the rising edge trigger for the
               specified ICU_IRQ.
 Arguments
            : uint8_t irq_no
               uint8_t set_r_edge, 1 if setting rising edge triggered, 0 if
                clearing
* Return Value : None
void R_ICU_IRQSetRisingEdge (const uint8_t irq_no, const uint8_t set_r_edge)
   if (1 == set_r_edge)
   {
       ICU.IRQCR[irq_no].BYTE |= _08_ICU_IRQ_EDGE_RISING;
   else
   {
       ICU.IRQCR[irq_no].BYTE &= (uint8_t) ~_08_ICU_IRQ_EDGE_RISING;
        ******************
* End of function R ICU IROSetRisingEdge
```

/* End user code. Do not edit comment generated here */

Open the Config_ICU_user.c file and insert the following code in the user code area for include near the top of the file:

```
/* Start user code for include. Do not edit comment generated here */
/* Defines switch callback functions required by interrupt handlers */
#include "r_rsk_switch.h"
/* End user code. Do not edit comment generated here */
```

In the same file insert the following code in the user code area inside the function r_Config_ICU_irq11_interrupt:

```
/* Start user code for r_Config_ICU_irq11_interrupt. Do not edit comment generated here */
/* Switch 1 callback handler */
R_SWITCH_IsrCallback1();
/* End user code. Do not edit comment generated here */
```

In the same file insert the following code in the user code area inside the function r_Config_ICU_irq13_interrupt:

```
/* Start user code for r_Config_ICU_irq13_interrupt. Do not edit comment generated here */
/* Switch 2 callback handler */
R_SWITCH_IsrCallback2();
/* End user code. Do not edit comment generated here */
```

5.5.2 De-bounce Timer Code

In the Project Tree, expand the 'Smart Configurator\Config_CMT1' folder and open the 'Config_CMT1_user.c' file and insert the following code in the user code area for include near the top of the file:

```
/* Start user code for include. Do not edit comment generated here */
/* Defines switch callback functions required by interrupt handlers */
#include "r_rsk_switch.h"
/* End user code. Do not edit comment generated here */
```

In the 'Config_CMT1_user.c' file insert the following code in the user code area inside the function r_Config_CMT1_cmi1_interrupt:

```
/* Start user code for r_Config_CMT1_cmi1_interrupt. Do not edit comment generated here */
/* Stop this timer - we start it again in the de-bounce routines */
R_Config_CMT1_Stop();

/* Call the de-bounce call back routine */
R_SWITCH_DebounceIsrCallback();

/* End user code. Do not edit comment generated here */
```

In the Project Tree, expand the 'Smart Configurator\Config_CMT2' folder and open the 'Config_CMT2_user.c' file and insert the following code in the user code area for include near the top of the file:

```
/* Start user code for include. Do not edit comment generated here */
/* Defines switch callback functions required by interrupt handlers */
#include "r_rsk_switch.h"
/* End user code. Do not edit comment generated here */
```

Open the 'Config_CMT2_user.c' file and insert the following code in the user code area inside the function r_Config_CMT2_cmi2_interrupt:

```
/* Start user code for r_Config_CMT2_cmi2_interrupt. Do not edit comment generated here */
/* Stop this timer - we start it again in the de-bounce routines */
R_Config_CMT2_Stop();

/* Call the de-bounce call back routine */
R_SWITCH_DebounceIsrCallback();
/* End user code. Do not edit comment generated here */
```

5.5.3 Main Switch and ADC Code

In this part of the tutorial we add the code to act on the switch presses to activate A/D conversions and display the result on the LCD. In §4.4.7 we configured the ADC to be triggered from the ADTRG0# pin, SW3. In this code, we also perform software triggered A/D conversion from the user switches SW1 and SW2, by reconfiguring the ADC trigger source on-the-fly once an SW1 or SW2 press is detected.

In the CS+ Project Tree, expand the 'Smart Configurator\general' folder and open the file 'r_cg_userdefine.h' by double-clicking on it. Insert the following code the user code area, resulting in the code shown below

In the Project Tree, expand the 'C Source Files' folder and Open the file 'SC_Tutorial.c' and add the highlighted code, resulting in the code shown below:

```
#include "r_smc_entry.h"
#include "r_okaya_lcd.h"
#include "r_cg_userdefine.h"
#include "Config_S12AD0.h"
#include "r_rsk_switch.h"

/* Variable for flagging user requested ADC conversion */
volatile uint8_t g_adc_trigger = FALSE;

/* Prototype declaration for cb_switch_press */
static void cb_switch_press (void);

/* Prototype declaration for get_adc */
static uint16_t get_adc(void);

/* Prototype declaration for lcd_display_adc */
static void lcd_display_adc (const uint16_t adc_result);
```

Next add the highlighted code below in the main function and the code inside the while loop, resulting in the code shown below:

```
void main(void)
    /* Initialize the switch module */
    R_SWITCH_Init();
    /* Set the call back function when SW1 or SW2 is pressed */
    R_SWITCH_SetPressCallback(cb_switch_press);
    /* Initialize the debug LCD */
    R_LCD_Init();
    /* Displays the application name on the debug LCD */
   R_LCD_Display(0, (uint8_t *)" RSK+RX65N-2MB "
R_LCD_Display(1, (uint8_t *)" Tutorial ");
    R_LCD_Display(2, (uint8_t *) " Press Any Switch ");
    /* Start the A/D converter */
    R_Config_S12AD0_Start();
    while (1U)
        uint16_t adc_result;
        /* Wait for user requested A/D conversion flag to be set (SW1 or SW2) */
        if (TRUE == g_adc_trigger)
              * Call the function to perform an A/D conversion */
            adc_result = get_adc();
            /* Display the result on the LCD */
            lcd_display_adc(adc_result);
            /* Reset the flag *.
            g_adc_trigger = FALSE;
        /* SW3 is directly wired into the ADTRG0n pin so will
           cause the interrupt to fire */
        else if (TRUE == g_adc_complete)
            /* Get the result of the A/D conversion */
            R_Config_S12AD0_Get_ValueResult(ADCHANNEL0, &adc_result);
            /* Display the result on the LCD */
            lcd_display_adc(adc_result);
            /* Reset the flag */
            g_adc_complete = FALSE;
        else
            /* do nothing */
        }
    }
```

Then add the definition for the switch call-back, get_adc and lcd_display_adc functions adding at the below of the main function, as shown below:

```
/* set the flag indicating a user requested A/D conversion is required */
      g_adc_trigger = TRUE;
      /* Clear flag */
      g_switch_flag = 0x0;
* End of function cb_switch_press
     *******************
* Function Name : get_adc
* Description : Reads the ADC result, converts it to a string and displays
              it on the LCD panel.
         none
* Argument
* Return value : uint16_t adc value
static uint16_t get_adc (void)
   /* A variable to retrieve the adc result */
   uint16_t adc_result;
   /* Stop the A/D converter being triggered from the pin ADTRGOn */
   R_Config_S12AD0_Stop();
   /* Start a conversion */
   R_S12AD0_SWTriggerStart();
   /* Wait for the A/D conversion to complete */
   while (FALSE == g_adc_complete)
   {
      /* Wait */
   /* Stop conversion */
   R_S12AD0_SWTriggerStop();
   /* Clear ADC flag */
   g_adc_complete = FALSE;
   R_Config_S12AD0_Get_ValueResult(ADCHANNEL0, &adc_result);
   /* Set AD conversion start trigger source back to ADTRG0n pin */
   R_Config_S12AD0_Start();
   return (adc result);
* End of function get_adc
                  ********************
/**********************
* Function Name : lcd_display_adc
^{\star} Description \,: Converts adc result to a string and displays
             it on the LCD panel.
           : uint16_t adc result
* Argument
* Return value : none
                  ******************
static void lcd_display_adc (const uint16_t adc_result)
   /* Declare a temporary variable */
   uint8_t a;
   /* Declare temporary character string */
        lcd_buffer[11] = " ADC: XXXH";
   char
   \slash \, Convert ADC result into a character string, and store in the local.
     Casting to ensure use of correct data type. */
   a = (uint8_t)((adc_result & 0x0F00) >> 8);
   lcd_buffer[6] = (char)((a < 0x0A) ? (a + 0x30) : (a + 0x37));
   a = (uint8_t)((adc_result & 0x00F0) >> 4);
```

In the Project Tree, expand the 'Smart Configurator\Config_S12AD0' folder and open the file 'Config_S12AD0.h' by double-clicking on it. Insert the following code in the user code area for function, resulting in the code shown below:

```
/* Start user code for function. Do not edit comment generated here */
/* Flag indicates when A/D conversion is complete */
extern volatile uint8_t g_adc_complete;

/* Functions for starting and stopping software triggered A/D conversion */
void R_S12AD0_SWTriggerStart(void);
void R_S12AD0_SWTriggerStop(void);

/* End user code. Do not edit comment generated here */
```

Open the file 'Config_S12AD0.c' by double-clicking on it. Insert the following code in the user code area for adding at the end of the file, resulting in the code shown below:

```
/* Start user code for adding. Do not edit comment generated here */
Function Name: R_S12AD0_SWTriggerStart
* Description : This function starts the ADO converter.
* Arguments : None
* Return Value : None
*******************
void R_S12AD0_SWTriggerStart(void)
  IR(PERIB, INTB186) = OU;
  IEN(PERIB, INTB186) = 1U;
  S12AD.ADCSR.BIT.ADST = 1U;
/*******************************
/******************************
Function Name: R_S12AD0_SWTriggerStop
Description : This function stops the ADO converter.
Arguments : None
Return Value : None
*******************************
void R_S12AD0_SWTriggerStop(void)
  S12AD.ADCSR.BIT.ADST = 0U;
  IEN(PERIB, INTB186) = OU;
  IR(PERIB, INTB186) = OU;
}
/***********************************
/* End user code. Do not edit comment generated here */
```

Open the file Config_S12AD0_user.c and insert the following code in the user code area for global, resulting in the code shown below:

```
/* Start user code for global. Do not edit comment generated here */
/* Flag indicates when A/D conversion is complete */
volatile uint8_t g_adc_complete;
/* End user code. Do not edit comment generated here */
```

Insert the following code in the user code area of the r Config S12AD0 interrupt function, resulting in the code shown below:

```
static void r_Config_S12AD0_interrupt(void)
    /* Start user code for r_Confiq_S12AD0_interrupt. Do not edit comment generated here */
   g_adc_complete = TRUE;
    /* End user code. Do not edit comment generated here */
```

Select 'Build Project' from the 'Build' menu, or press F7. CS+ will build the project with no errors.

The project may now be run using the debugger as described in §6. When any switch is pressed, the program will perform an A/D conversion of the voltage level on the ADPOT line and display the result on the LCD panel. Return to this point in the SC_Tutorial to add the UART user code.

5.6 Debug Code Integration

API functions for trace debugging via the RSK serial port are provided with the RSK+. Refer to the Tutorial project folder created according to the Quick Start Guide procedure. Locate the files r_rsk_debug.h and r_rsk_debug.c in this folder. Copy these files into the C:\Workspace\SC_Tutorial\src folder. Add these two files into the project in the same way as the LCD files.

In the r_rsk_debug.h file, ensure the following macro definition is included:

```
/* Macro for definition of serial debug transmit function - user edits this */#define SERIAL_DEBUG_WRITE (R_SCI8_AsyncTransmit)
```

This macro is referenced in the r_rsk_debug.c file and allows easy re-direction of debug output if a different debug interface is used.

5.7 UART Code Integration

5.7.1 SCI Code

In the CS+ Project Tree, expand the 'Smart Configurator\Config_SCI8' folder and open the file 'Config_SCI8.h' by double-clicking on it. Insert the following code in the user code area at the end of the file:

```
/* Start user code for function. Do not edit comment generated here */
/* Exported functions used to transmit a number of bytes and wait for completion */
MD_STATUS R_SCI8_AsyncTransmit(uint8_t * const tx_buf, const uint16_t tx_num);

/* Character is used to receive key presses from PC terminal */
extern uint8_t g_rx_char;

/* End user code. Do not edit comment generated here */
```

Open the file 'Config_SCI8_user.c'. Insert the following code in the user area for global near the beginning of the file:

```
/* Start user code for global. Do not edit comment generated here */
/* Global used to receive a character from the PC terminal */
uint8_t g_rx_char;

/* Flag used locally to detect transmission complete */
static volatile uint8_t sci8_txdone;

/* End user code. Do not edit comment generated here */
```

In the same file, insert the following code in the user code area inside the r_Config_SCI8_callback_transmitend function:

```
static void r_Config_SCI8_callback_transmitend (void)
{
    /* Start user code for r_Config_SCI8_callback_transmitend. Do not edit comment generated here */
    sci8_txdone = TRUE;

    /* End user code. Do not edit comment generated here */
}
```

In the same file, insert the following code in the user code area inside the r_Config_SCI8_callback_receiveend function:

```
static void r_Config_SCI8_callback_receiveend(void)
{
    /* Start user code for r_Config_SCI8_callback_receiveend. Do not edit comment generated here */
    /* Check the contents of g_rx_char */
    if (('c' == g_rx_char) || ('C' == g_rx_char))
    {
        g_adc_trigger = TRUE;
    }

    /* Set up SCI8 receive buffer and callback function again */
    R_Config_SCI8_Serial_Receive((uint8_t *)&g_rx_char, 1);

    /* End user code. Do not edit comment generated here */
}
```

At the end of the file, in the user code area for adding, add the following function definition:

```
* Function Name: R_SCI8_AsyncTransmit
^{st} Description \,: This function sends SCI8 data and waits for the transmit end flag.
* Arguments : tx_buf -
                 transfer buffer pointer
             tx_num -
                 buffer size
* Return Value : status -
                MD_OK or MD_ARGERROR
                         MD_STATUS R_SCI8_AsyncTransmit(uint8_t * const tx_buf, const uint16_t tx_num)
   MD_STATUS status = MD_OK;
   /* Clear the flag before initiating a new transmission */
   sci8_txdone = FALSE;
   /* Send the data using the API */
   status = R_Config_SCI8_Serial_Send(tx_buf, tx_num);
   /* Wait for the transmit end flag */
   while (FALSE == sci8_txdone)
   {
      /* Wait */
   return (status);
* End of function R_SCI8_AsyncTransmit
```

5.7.2 Main UART code

In the Project Tree, expand the 'C Source Files' folder and open the file 'SC_Tutorial.c'. Add the following declaration to above the 'main' function:

```
#include "r_smc_entry.h"
#include "r_okaya_lcd.h"
#include "r_cg_userdefine.h"
#include "Config_S12AD0.h"
#include "r_rsk_switch.h"
#include "r_rsk_debug.h"
#include "Config_SCI8.h"
/* Variable for flagging user requested ADC conversion */
volatile uint8_t g_adc_trigger = FALSE;
/* Prototype declaration for cb switch press */
static void cb_switch_press (void);
/* Prototype declaration for get_adc */
static uint16_t get_adc(void);
/* Prototype declaration for lcd_display_adc */
static void lcd_display_adc (const uint16_t adc_result);
/* Prototype declaration for uart_display_adc */
static void uart_display_adc(const uint8_t adc_count, const uint16_t adc_result);
/* Variable to store the A/D conversion count for user display */
static uint8_t adc_count = 0;
```

Add the following highlighted code to the user code area in the main function:

```
void main(void)
{
    /* Initialize the switch module */
   R_SWITCH_Init();
    /* Set the call back function when SW1 or SW2 is pressed */
   R_SWITCH_SetPressCallback(cb_switch_press);
    /* Initialize the debug LCD */
   R_LCD_Init();
    /* Displays the application name on the debug LCD */
   R_LCD_Display(0, (uint8_t *)" RSK+RX65N-2MB ");
   R_LCD_Display(1, (uint8_t *)" Tutorial ");
   R_LCD_Display(2, (uint8_t *)" Press Any Switch ");
    /* Start the A/D converter */
   R_Config_S12AD0_Start();
    /* Set up SCI8 receive buffer and callback function */
   R_Config_SCI8_Serial_Receive((uint8_t *)&g_rx_char, 1);
    /* Enable SCI8 operations */
   R_Config_SCI8_Start();
   while (1U)
    {
        uint16_t adc_result;
        /* Wait for user requested A/D conversion flag to be set (SW1 or SW2) */
        if (TRUE == g_adc_trigger)
        {
            /* Call the function to perform an A/D conversion */
            adc_result = get_adc();
            /* Display the result on the LCD */
            lcd_display_adc(adc_result);
            /* Increment the adc_count */
            if (16 == (++adc_count))
                adc_count = 0;
            /* Send the result to the UART */
```

```
uart_display_adc(adc_count, adc_result);
            /* Reset the flag */
            g_adc_trigger = FALSE;
        /* SW3 is directly wired into the ADTRG0n pin so will
           cause the interrupt to fire */
        else if (TRUE == g_adc_complete)
            /* Get the result of the A/D conversion */
            R_Config_S12AD0_Get_ValueResult(ADCHANNEL0, &adc_result);
            /* Display the result on the LCD */
            lcd_display_adc(adc_result);
            /* Increment the adc_count */
            if (16 == (++adc_count))
                adc_count = 0;
            /* Send the result to the UART */
            uart_display_adc(adc_count, adc_result);
            /* Reset the flag */
            g_adc_complete = FALSE;
        else
            /* do nothing */
        }
    }
}
```

Then, add the following function definition in the end of the file:

```
Function Name : uart_display_adc
* Description : Converts adc result to a string and sends it to the UART1.
            : uint8_t : adc_count
               uint16_t: adc result
* Return value : none
      *************************
static void uart_display_adc (const uint8_t adc_count, const uint16_t adc_result)
   /* Declare a temporary variable */
   char a;
   /* Declare temporary character string */
   static char uart_buffer[] = "ADC xH Value: xxxH\r\n";
   \slash \, Convert ADC result into a character string, and store in the local.
     Casting to ensure use of correct data type. */
   a = (char)(adc\_count & 0x000F);
   uart_buffer[4] = (char)((a < 0x0A) ? (a + 0x30) : (a + 0x37));
   a = (char)((adc_result & 0x0F00) >> 8);
   uart\_buffer[14] = (char)((a < 0x0A) ? (a + 0x30) : (a + 0x37));
   a = (char)((adc_result & 0x00F0) >> 4);
   uart_buffer[15] = (char)((a < 0x0A) ? (a + 0x30) : (a + 0x37));
   a = (char)(adc result & 0x000F);
   uart\_buffer[16] = (char)((a < 0x0A) ? (a + 0x30) : (a + 0x37));
   /* Send the string to the UART */
   R_DEBUG_Print(uart_buffer);
* End of function uart_display_adc
***********************
```

Select 'Build Project' from the 'Build' menu, or press F7. CS+ will build the project with no errors.

RENESAS

The project may now be run using the debugger as described in §6. Connect the RSK G1CUSB0 port to a USB port on a PC. If this is the first time the RSK has been connected to the PC then a device driver will be installed automatically. Open Device Manager, the virtual COM port will now appear under 'Port (COM & LPT)' as 'RSK USB Serial Port (COMx)', where x is a number.

Open a terminal program, such as HyperTerminal, on the PC with the same settings as for SCI8 (see §4.4.5). When any switch is pressed, or when 'c' is sent via the COM port, the program will perform an A/D conversion of the voltage level on the ADPOT line and display the result on the LCD panel and send the result to the PC terminal program via the SCI8. Return to this point in the SC_Tutorial to add the LED user code.

5.8 LED Code Integration

In the Project Tree, expand the 'C Source Files' folder and open the file 'SC_Tutorial.c'. Add the following declaration to the above the 'main' function:

```
#include "r_smc_entry.h"
#include "r_okaya_lcd.h"
#include "r_cg_userdefine.h"
#include "Config_S12AD0.h"
#include "r_rsk_switch.h"
#include "r_rsk_debug.h"
#include "Config_SCI8.h"
#include "rskrx65n2mbdef.h"
/* Variable for flagging user requested ADC conversion */
volatile uint8_t g_adc_trigger = FALSE;
/* Prototype declaration for cb_switch_press */
static void cb_switch_press (void);
/* Prototype declaration for get_adc */
static uint16_t get_adc(void);
/* Prototype declaration for lcd_display_adc */
static void lcd_display_adc (const uint16_t adc_result);
/* Prototype declaration for uart_display_adc */
static void uart_display_adc(const uint8_t adc_count, const uint16_t adc_result);
/* Variable to store the A/D conversion count for user display */
static uint8_t adc_count = 0;
/* Prototype declaration for led_display_count */
static void led_display_count(const uint8_t count);
```

Add the following highlighted code to the user code area in the main function:

```
void main(void)
{
    /* Initialize the switch module */
    R_SWITCH_Init();

    /* Set the call back function when SW1 or SW2 is pressed */
    R_SWITCH_SetPressCallback(cb_switch_press);

    /* Initialize the debug LCD */
    R_LCD_Init();

    /* Displays the application name on the debug LCD */
    R_LCD_Display(0, (uint8_t *)" RSK+RX65N-2MB ");
    R_LCD_Display(1, (uint8_t *)" Tutorial ");
    R_LCD_Display(2, (uint8_t *)" Press Any Switch ");

    /* Start the A/D converter */
    R_Config_S12AD0_Start();
```

```
/* Set up SCI8 receive buffer and callback function */
R_Config_SCI8_Serial_Receive((uint8_t *)&g_rx_char, 1);
/* Enable SCI8 operations */
R_Config_SCI8_Start();
while (1U)
    uint16_t adc_result;
    /* Wait for user requested A/D conversion flag to be set (SW1 or SW2) */
    if (TRUE == g_adc_trigger)
    {
        /* Call the function to perform an A/D conversion */
        adc_result = get_adc();
        /\,^\star Display the result on the LCD ^\star/\,
        lcd_display_adc(adc_result);
        /* Increment the adc_count and display using the LEDs */
        if (16 == (++adc_count))
            adc_count = 0;
        led_display_count(adc_count);
        /* Send the result to the UART */
        uart_display_adc(adc_count, adc_result);
        /* Reset the flag */
        g_adc_trigger = FALSE;
    /* SW3 is directly wired into the ADTRGOn pin so will
       cause the interrupt to fire */
    else if (TRUE == g_adc_complete)
        /* Get the result of the A/D conversion */
        R_Config_S12AD0_Get_ValueResult(ADCHANNEL0, &adc_result);
        /* Display the result on the LCD */
        lcd_display_adc(adc_result);
        /* Increment the adc_count and display using the LEDs */
        if (16 == (++adc_count))
            adc_count = 0;
        led_display_count(adc_count);
        /* Send the result to the UART */
        uart_display_adc(adc_count, adc_result);
        /* Reset the flag */
        g_adc_complete = FALSE;
    else
        /* do nothing */
}
```

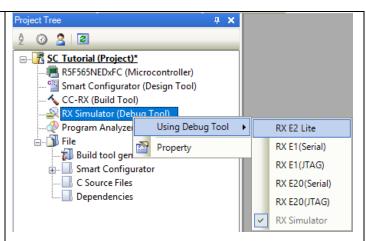
Then, add the following function definition in the user code area at the end of the file:

Select 'Build Project' from the 'Build' menu, or press F7. CS+ will build the project with no errors.

The project may now be run using the debugger as described in §6. The code will perform the same but now the LEDs will display the adc_count in binary form.

6. Debugging the Project

 In the 'Project Tree' pane, right-click the 'RX Simulator (Debug Tool)'. Select 'Using Debug Tool -> RX E2 Lite'.



- Double-click 'RX E2 Lite (Debug Tool)' to display the debugger tool properties. Under 'Clock', change the main clock frequency to 24 MHz, Communications method 'JTAG' and operating frequency to 120MHz.'
- All other settings can remain at their defaults.
- Property RX E2 Lite Property ✓ Internal ROWRAM Size of internal ROM[KBytes] 2048 Size of internal RAM[KBytes] 648 Size of DataFlash memory[KBytes] 32 Clock Main clock source **EXTAL** Main clock frequency[MHz] 24.0000 Operating frequency[MHz] 120.0000 Allow changing of the clock source on writing internal flash memory No Connection with Emulator Emulator serial No Connection with Target Board Power target from the emulator.(MAX 200mA) JTAG Communications method JTAG clock[MHz] 6.00
- Connect the E2 Lite to the PC and the RSK+ E1/E2 Lite connector. Connect the Pmod LCD to the PMOD1 connector.
- Connect the center positive +5V PSU to the PWR connector on the RSK+ and apply power.
- From the 'Debug' menu select 'Download' to start the debug session and download code to the target.



7. Running the Smart Configurator Tutorial

7.1 Running the Tutorial

Once the program has been downloaded onto the RSK device, the program can be executed. Click the 'Go' button or press F5 to begin the program from the current program counter position. It is recommended that you run through the program once first, and then continue to the Tutorial manual to review the code.

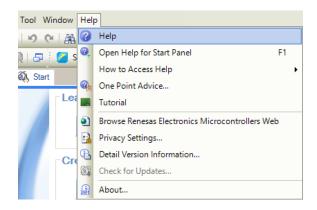


RSK+RX65N-2NB 8. Additional Information

8. Additional Information

Technical Support

For details on how to use CS+, refer to the help file by opening CS+, then selecting Help > Help Contents from the menu bar.



For information about the RX65N group microcontroller refer to the RX65N Group Hardware Manual.

For information about the RX assembly language, refer to the RX Family Software Manual.

Technical Contact Details

Please refer to the contact details listed in section 8 of the "Quick Start Guide".

General information on Renesas microcontrollers can be found on the Renesas website at: https://www.renesas.com/

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