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April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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Customer Notification

QB-78K0FX1TM

In-Circuit Emulator

Operating Precautions

Target Devices **78K0/Fx1+ Series**

Global Document No. U18092EE3V0IF00 (3rd edition)
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QB-78K0FX1

(A) Table of Operating Precautions

No.	Outline	Control Code ^{Note}	QB-78K0FX1		
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3: Not applicable

7: applicable

Note:

- The "control code" is the second digit from the left in the 10-digit serial number in the warranty supplied with the product you purchased (if it has not been upgraded). If the product has been upgraded, a label indicating the new version is attached to the product and the x in V-UP LEVEL x on this label indicates the control code.

(B) Description of Operating Precautions

No. 1	Ring oscillator operation during HALT (Technical Limitation)
	<u>Details</u> As long as the operation clock for the watchdog timer is not stopped, the watchdog timer does not stop operation when the HALT instruction is executed. This happens even if the mask option "Ring-OSC can be stopped by software" is set. As a result the reset signal is generated.
No. 2	Caution on option byte function (Direction of Use)
	<u>Details</u> When using a microcontroller with on chip flash memory and option byte function, the function set by the option byte at address 0080H cannot be emulated. To set the option byte function pls. use the Mask Option dialog box of the debugger instead of the option byte.
No. 3	Caution on target power during break (Direction of Use)
	<u>Details</u> Don't turn off the target power supply during break.
No. 4	aFCAN Emulation with Peripheral Break function on (Technical Limitation)
	<u>Details</u> The Peripheral Break function cannot be used, if the aFCAN is used. The values of the aFCAN special function registers and the message buffer RAM maybe undefined. <u>Workaround</u> Switch off the Peripheral Break function in the configuration dialog box of the debugger.
No. 5	aFCAN Receive/Transmit history function (Technical Limitation)
	<u>Details</u> The RGPT and TGPT registers are not updated/incremented when they are read by the CPU. 1. History List pointers cannot be used RGPT/TGPT: Always the same value will be read 2. History List empty flags cannot be used RHPM/THPM: Set after RESET, cleared on CAN activity, but not reset after reading by the CPU <u>Workaround</u> Scan all buffers by software (checking DN or TRQ appropriately) instead to use RGPT/TGPT/RHPM/THPM. There is no workaround for tracing the reception/transmission sequence.

No. 6	LVI Reset Control Flag Register RESF (Technical Limitation)
	<p><u>Details</u> The RESF register indicates the reset source after reset. If program execution is stopped, due to a breakpoint and during this break a LVI reset occurs, the RESF register will be cleared after restart of the program execution. Therefore the reset source cannot be determined.</p>
No. 7	Program execution due to simultaneous occurrence of a software break and an interrupt request (Technical Limitation)
	<p><u>Details</u> The following behaviour occurs, when a software break and an interrupt request occur simultaneously:</p> <ul style="list-style-type: none"> • The software break occurs before start of the interrupt service routine execution. • After this software break the PC points to a 1-byte lower address of the interrupt service routine. When the user program execution continues, the program may not run correctly. • In the Trace Window will be shown "XCHW AX, AX". But this has no meaning. <p><u>Workaround</u> Please use hardware breakpoints.</p>
No. 8	Reading of Special Function Registers (SFR) during a software break (Technical Limitation)
	<p><u>Details</u> If Peripheral Break is enabled, the peripheral macro will work one clock longer, when a SFR with retry function will be read</p> <p>For example:</p> <ul style="list-style-type: none"> • The Time SFR may increase, when the SFR window incl. the SFRs with retry function will be refreshed.
No. 9	Illegal program execution after restart due to a software break (Technical Limitation)
	<p><u>Details</u> When the next program data followed on a software break is C0H, the contents of the program counter PC, the program status word PSW and the stack pointer SP are not correct after the software break. Due to this the program will not run correctly when the program execution continues.</p> <p><u>Workaround</u> Please use hardware breakpoints.</p>

(C) Valid Specification

Item	Date published	Document No.	Document Title
1	April 2005	U17534EJ1V0UM0 0 or later	QB-78K0FX1 User's Manual

(D) Revision History

Item	Date published	Document No.	Comment
1	September, 2004	TPS-LE-OP-0TQBFX1	1 st Release
2	May, 2005	TPS-LE-OP-0TQBFX1-1	1 st Update Items 3 and 4 added
3	September, 2005	TPS-LE-OP-0TQBFX1-2	2 nd Update Items 3, 6, 7, 8 and 9 added