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# Connection of SH7660 E10A Emulator

HS7660KCM01H HS7660KCM02H HS7660KCI01H HS7660KCl02H with User System

### 1. Connecting the E10A Emulator with the User System

To connect the E10A emulator (hereinafter referred to as the emulator), the H-UDI port connector must be installed on the user system to connect the user system interface cable. When designing the user system, refer to the recommended circuit between the H-UDI port connector and the MCU. In addition, read the E10A emulator user's manual and hardware manual for the related device.

Table 1.1 shows the type number of the E10A emulator, the corresponding connector type, and the use of AUD function.

Table 1.1 Type Number, AUD Function, and Connector Type

Type Number	Connector	AUD Function
HS7660KCM02H, HS7660KCl02H	36-pin connector	Available
HS7660KCM01H, HS7660KCl01H	14-pin connector	Not available

The H-UDI port connector has the 36-pin and 14-pin types as described below. Use them according to the purpose of the usage.

#### 1. 36-pin type (with AUD function)

The AUD trace function is supported. A large amount of trace information can be acquired in realtime. The E10A emulator supports the window trace function that memory access (memory access address or memory access data) in the specified range can be acquired by tracing.

#### 2. 14-pin type (without AUD function)

The AUD trace function cannot be used because only the H-UDI function is supported. For tracing, only the internal trace function is supported. Since the 14-pin type connector is smaller than the 36-pin type (1/2.5), the area where the connector is installed on the user system can be reduced.

## 2. Installing the H-UDI Port Connector on the User System

Table 2.1 shows the recommended H-UDI port connectors for the emulator.

**Table 2.1 Recommended H-UDI Port Connectors** 

Connector	Type Number	Manufacturer	Specifications
36-pin connector	DX10M-36S	Hirose Electric Co., Ltd.	Screw type
	DX10M-36SE, DX10G1M-36SE	_	Lock-pin type
14-pin connector	2514-6002	Sumitomo 3M Limited	14-pin straight type

Note: When the 36-pin connector is used, do not connect any components under the H-UDI connector. When the 14-pin connector is used, do not install any components within 3 mm of the H-UDI port connector.

# 3. Pin Arrangement of the H-UDI Port Connector

Figures 3.1 and 3.2 show the pin arrangement of the 36-pin and 14-pin H-UDI port connectors, respectively.

Note: Note that the pin number assignment of the H-UDI port connector shown below differs from that of the connector manufacturer.

	Signal	Input/ Output*1	MCU Pin No.	Note	Pin No.	Signal	Input/ Output *1	MCU Pin No.	Note	
1	AUDCK	Output	M16		19	TMS	Input	L1		
2	GND				20	GND				
3	AUDATA0	Output	M17		21 <sup>*2</sup>	/TRST	Input	J1		
4	GND				22	GND				
5	AUDATA1	Output	L16		23	TDI	Input	K1		
6	GND				24	GND				
7	AUDATA2	Output	L17		25	TDO	Output	J3		
8	GND				26	GND				
9	AUDATA3	Output	K14		27 <sup>*2</sup>	/ASEBRKAK	Output	J2		
10	GND				28	GND				
11 <sup>*2</sup>	/AUDSYNC	Output	K15		29	NC				
12	GND				30	GND				
13	NC				31 <sup>*2</sup>	/RESETP	Output	D13	User reset	
14	GND				32	GND				
15	NC				33 <sup>*3</sup>	GND	Output			
16	GND				34	GND				
17	TCK	Input	K2		35	NC				
18	GND				36	GND				
Note	es: 1. Input to									
	2. The sla 3. The en whether	ash (/) me nulator mo er or not th	ans that onitors the ne user:	the sine GN system	gnal is D sign n is co	s active-low. nal of the use	er system		I port connecto	or (top view)

Figure 3.1 Pin Arrangement of the H-UDI Port Connector (36 Pins)

Pin No.	Signal	Input/ Output* <sup>1</sup>	MCU Pin No.	
1	TCK	Input	K2	-
<b>2</b> * <sup>2</sup>	/TRST	Input	J1	-
3	TDO	Output	J3	-
4*2	/ASEBRKAK	Output	J2	-
5	TMS	Input	L1	=
6	TDI	Input	K1	-
<b>7</b> *2	/RESETP	Output	D13	-
11	Not	_		-
	connected			
8 to 10	GND			-
12 to 13				
14*3	GND	Output		-
	evetom and d		GND signal of the	
Pin	is connected.  1 mark		rt connector (top vi	/stem

Figure 3.2 Pin Arrangement of the H-UDI Port Connector (14 Pins)

Pin 1 mark

# 4. Recommended Circuit between the H-UDI Port Connector and MCU

#### 4.1 Recommended Circuit (36-Pin Type)

Figure 4.1 shows a recommended circuit between the H-UDI port connector (36 pins) and the MCU.

Notes: 1. Do not connect anything to the N.C. pin of the H-UDI port connector.

- 2. The processing of the /ASEMD0 pin differs depending on whether the emulator is used or not. As the emulator does not control this pin, it must be controlled by a switch on the board.
  - (1) When the emulator is used: /ASEMD0 = low (ASE mode)
  - (2) When the emulator is not used: /ASEMD0 = high (normal mode)
- 3. The reset signal in the user system is input to the /RESETP pin of the MCU. Connect this signal to the H-UDI port connector as the output from the user system.
- 4. When a joined resistance is used for pull-up, it may be affected by a noise. Separate TCK from other resistances.
- 5. The pattern between the H-UDI port connector and the MCU must be as short as possible. Do not connect the signal lines to other components on the board.
- 6. The resistance values shown in figure 4.1 are recommended.
- 7. For the pin processing in cases where the emulator is not used, refer to the hardware manual of the related device.

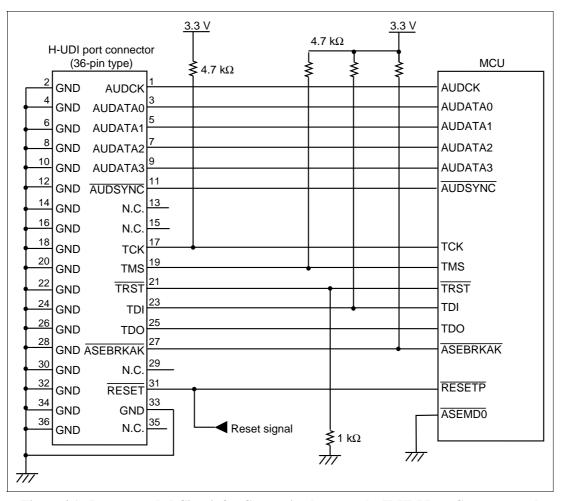


Figure 4.1 Recommended Circuit for Connection between the H-UDI Port Connector and MCU (36-Pin Type)

#### 4.2 Recommended Circuit (14-Pin Type)

Figure 4.2 shows a recommended circuit between the H-UDI port connector and the MCU.

Notes: 1. Do not connect anything to the N.C. pin of the H-UDI port connector.

- 2. The processing of the /ASEMD0 pin differs depending on whether the emulator is used or not. As the emulator does not control this pin, it must be controlled by a switch on the board.
  - (1) When the emulator is used: /ASEMD0 = low (ASE mode)
  - (2) When the emulator is not used: /ASEMD0 = high (normal mode)
- 3. The reset signal in the user system is input to the /RESETP pin of the MCU. Connect this signal to the H-UDI port connector as the output from the user system.
- 4. When a joined resistance is used for pull-up, it may be affected by a noise. Separate TCK from other resistances.
- 5. The pattern between the H-UDI port connector and the MCU must be as short as possible. Do not connect the signal lines to other components on the board.
- 6. The resistance values shown in figure 4.2 are recommended.
- 7. For the pin processing in cases where the emulator is not used, refer to the hardware manual of the related device.

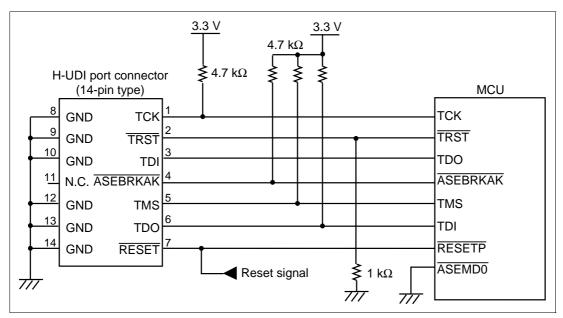


Figure 4.2 Recommended Circuit for Connection between the H-UDI Port Connector and MCU (14-Pin Type)