

# User's Manual

32

# **RX72N** Group

# Renesas Starter Kit+ for RX72N Tutorial Manual For e<sup>2</sup> studio

RENESAS 32-Bit MCU RX Family / RX700 Series

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# General Precautions in the Handling of Microprocessing Unit and Microcontroller Unit Products

The following usage notes are applicable to all Microprocessing unit and Microcontroller unit products from Renesas. For detailed usage notes on the products covered by this document, refer to the relevant sections of the document as well as any technical updates that have been issued for the products.

### 1. Precaution against Electrostatic Discharge (ESD)

A strong electrical field, when exposed to a CMOS device, can cause destruction of the gate oxide and ultimately degrade the device operation. Steps must be taken to stop the generation of static electricity as much as possible, and quickly dissipate it when it occurs. Environmental control must be adequate. When it is dry, a humidifier should be used. This is recommended to avoid using insulators that can easily build up static electricity. Semiconductor devices must be stored and transported in an anti-static container, static shielding bag or conductive material. All test and measurement tools including work benches and floors must be grounded. The operator must also be grounded using a wrist strap. Semiconductor devices must not be touched with bare hands. Similar precautions must be taken for printed circuit boards with mounted semiconductor devices.

### 2. Processing at power-on

The state of the product is undefined at the time when power is supplied. The states of internal circuits in the LSI are indeterminate and the states of register settings and pins are undefined at the time when power is supplied. In a finished product where the reset signal is applied to the external reset pin, the states of pins are not guaranteed from the time when power is supplied until the reset process is completed. In a similar way, the states of pins in a product that is reset by an on-chip power-on reset function are not guaranteed from the time when power is supplied until the power is supplied until the power reaches the level at which reseting is specified.

3. Input of signal during power-off state

Do not input signals or an I/O pull-up power supply while the device is powered off. The current injection that results from input of such a signal or I/O pull-up power supply may cause malfunction and the abnormal current that passes in the device at this time may cause degradation of internal elements. Follow the guideline for input signal during power-off state as described in your product documentation.

4. Handling of unused pins

Handle unused pins in accordance with the directions given under handling of unused pins in the manual. The input pins of CMOS products are generally in the high-impedance state. In operation with an unused pin in the open-circuit state, extra electromagnetic noise is induced in the vicinity of the LSI, an associated shoot-through current flows internally, and malfunctions occur due to the false recognition of the pin state as an input signal become possible.

### 5. Clock signals

After applying a reset, only release the reset line after the operating clock signal becomes stable. When switching the clock signal during program execution, wait until the target clock signal is stabilized. When the clock signal is generated with an external resonator or from an external oscillator during a reset, ensure that the reset line is only released after full stabilization of the clock signal. Additionally, when switching to a clock signal produced with an external resonator or by an external oscillator while program execution is in progress, wait until the target clock signal is stable.

6. Voltage application waveform at input pin

Waveform distortion due to input noise or a reflected wave may cause malfunction. If the input of the CMOS device stays in the area between  $V_{IL}$  (Max.) and  $V_{IH}$  (Min.) due to noise, for example, the device may malfunction. Take care to prevent chattering noise from entering the device when the input level is fixed, and also in the transition period when the input level passes through the area between  $V_{IL}$  (Max.) and  $V_{IH}$  (Min.).

7. Prohibition of access to reserved addresses

Access to reserved addresses is prohibited. The reserved addresses are provided for possible future expansion of functions. Do not access these addresses as the correct operation of the LSI is not guaranteed.

8. Differences between products

Before changing from one product to another, for example to a product with a different part number, confirm that the change will not lead to problems. The characteristics of a microprocessing unit or microcontroller unit products in the same group but having a different part number might differ in terms of internal memory capacity, layout pattern, and other factors, which can affect the ranges of electrical characteristics, such as characteristic values, operating margins, immunity to noise, and amount of radiated noise. When changing to a product with a different part number, implement a system-evaluation test for the given product.

### Disclaimer

By using this Renesas Starter Kit+ (RSK+), the user accepts the following terms:

The RSK+ is not guaranteed to be error free, and the entire risk as to the results and performance of the RSK+ is assumed by the User. The RSK+ is provided by Renesas on an "as is" basis without warranty of any kind whether express or implied, including but not limited to the implied warranties of satisfactory quality, fitness for a particular purpose, title and non-infringement of intellectual property rights with regard to the RSK+. Renesas expressly disclaims all such warranties. Renesas or its affiliates shall in no event be liable for any loss of profit, loss of data, loss of contract, loss of business, damage to reputation or goodwill, any economic loss, any reprogramming or recall costs (whether the foregoing losses are direct or indirect) nor shall Renesas or its affiliates be liable for any other direct or indirect special, incidental or consequential damages arising out of or in relation to the use of this RSK+, even if Renesas or its affiliates have been advised of the possibility of such damages.

### Precautions

The following precautions should be observed when operating any RSK+ product:

This Renesas Starter Kit+ is only intended for use in a laboratory environment under ambient temperature and humidity conditions. A safe separation distance should be used between this and any sensitive equipment. Its use outside the laboratory, classroom, study area or similar such area invalidates conformity with the protection requirements of the Electromagnetic Compatibility Directive and could lead to prosecution.

The product generates, uses, and can radiate radio frequency energy and may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment causes harmful interference to radio or television reception, which can be determined by turning the equipment off or on, you are encouraged to try to correct the interference by one or more of the following measures;

- ensure attached cables do not lie across the equipment
- reorient the receiving antenna
- increase the distance between the equipment and the receiver
- connect the equipment into an outlet on a circuit different from that which the receiver is connected
- power down the equipment when not in use
- consult the dealer or an experienced radio/TV technician for help NOTE: It is recommended that wherever possible shielded interface cables are used.

The product is potentially susceptible to certain EMC phenomena. To mitigate against them it is recommended that the following measures be undertaken;

- The user is advised that mobile phones should not be used within 10m of the product when in use.
- The user is advised to take ESD precautions when handling the equipment.

The Renesas Starter Kit+ does not represent an ideal reference design for an end product and does not fulfil the regulatory standards for an end product.

### How to Use This Manual

### 1. Purpose and Target Readers

This manual is designed to provide the user with an understanding of how to use the e<sup>2</sup> studio IDE to develop and debug software for the RSK+ platform. It is intended for users designing sample code on the RSK+ platform, using the many different incorporated peripheral devices.

The manual comprises of step-by-step instructions to load and debug a project in e<sup>2</sup> studio, but does not intend to be a complete guide to software development on the RSK+ platform. Further details regarding operating the RX72N microcontroller may be found in 'RX72N Group User's Manual: Hardware' and within the provided sample code. The setup procedure for the RSK+ Web installer is described in the Quick Start Guide.

Particular attention should be paid to the precautionary notes when using the manual. These notes occur within the body of the text, at the end of each section, and in the Usage Notes section.

In this manual, the display may differ slightly from screen shots. There is no problem in reading this manual.

The revision history summarizes the locations of revisions and additions. It does not list all revisions. Refer to the text of the manual for details.

The following documents apply to the RX72N Group. Make sure to refer to the latest versions of these documents. The newest versions of the documents listed may be obtained from the Renesas Electronics Web site.

Document Type	Description	Document Title	Document No.
User's Manual	Describes the technical details of the RSK+ hardware.	Renesas Starter Kit+ for RX72N User's Manual	R20UT4443EG
Tutorial Manual	Provides a guide to setting up RSK+ environment, running sample code and debugging programs.	Renesas Starter Kit+ for RX72N Tutorial Manual	R20UT4440EG
Quick Start Guide	Provides simple instructions to setup the RSK+ and run the first sample.	Renesas Starter Kit+ for RX72N Quick Start Guide	R20UT4441EG
Smart Configurator Tutorial	Provides a guide to code generation and importing into the e <sup>2</sup> studio IDE.	Renesas Starter Kit+ for RX72N Smart Configurator Tutorial Manual	R20UT4442EG
Schematics	Full detail circuit schematics of the RSK+.	Renesas Starter Kit+ for RX72N Schematics	R20UT4435EG
Hardware Manual	Provides technical details of the RX72N microcontroller.	RX72N Group User's Manual: Hardware	R01UH0824EJ

### 2. List of Abbreviations and Acronyms

Abbreviation	Full Form
ADC	Analog-to-Digital Converter
API	Application Programming Interface
bps	bits per second
CMT	Compare Match Timer
COM	COMmunications port referring to PC serial port
CPU	Central Processing Unit
E1 / E2 Lite	Renesas On-chip Debugging Emulator
GUI	Graphical User Interface
IDE	Integrated Development Environment
IRQ	Interrupt Request
LCD	Liquid Crystal Display
LED	Light Emitting Diode
LSB	Least Significant Bit
LVD	Low Voltage Detect
MCU	Micro-controller Unit
MSB	Most Significant Bit
PC	Personal Computer
PLL	Phase-locked Loop
Pmod™	This is a Digilent Pmod <sup>™</sup> Compatible connector. Pmod <sup>™</sup> is registered to Digilent Inc. <u>Digilent-Pmod_Interface_Specification</u>
PSU	Power Supply Unit
RAM	Random Access Memory
ROM	Read Only Memory
RSK+	Renesas Starter Kit+
RTC	Real Time Clock
SCI	Serial Communications Interface
SPI	Serial Peripheral Interface
TFT	Thin Film Transistor
UART	Universal Asynchronous Receiver/Transmitter
USB	Universal Serial Bus
WDT	Watchdog Timer

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# RENESAS

### 1. Overview

### 1.1 Purpose

This RSK+ is an evaluation tool for Renesas microcontrollers. This manual describes how to get the RSK+ tutorial started, and basic debugging operations.

### 1.2 Features

This RSK+ provides an evaluation of the following features:

- Renesas microcontroller programming
- User code debugging
- User circuitry such as switches, LEDs and a potentiometer Through the provided set of sample applications.

The RSK+ board contains all the circuitry required for microcontroller operation.



### 2. Introduction

This manual is designed to answer, in tutorial form, the most common questions asked about using a Renesas Starter Kit+ (RSK+). The tutorials help explain the following:

- How do I compile, link, download and run a simple program on the RSK+?
- How do I build an embedded application?
- How do I use Renesas tools?

Files referred to in this manual are installed using the project generator as you work through the tutorials. The tutorial examples in this manual assume that installation procedures described in the RSK+ Quick Start Guide have been completed. Please refer to the Quick Start Guide for details of preparing the configuration.

Due to the project generator, it is possible that line numbers for source code illustrated in this document do not match exactly with that in the actual source files. It is also possible that the source address of instructions illustrated in this manual differ from those in user code compiled from the same source. These differences are minor, and do not affect the functionality of the sample code nor the validity of this manual.

These tutorials are designed to show you how to use the RSK+ and are not intended as a comprehensive introduction to e<sup>2</sup> studio, the compiler toolchains or the E2 emulator Lite. Please refer to the relevant user manuals for more indepth information.

### 2.1 Smart Configurator Plugin

The Smart Configurator plugin for the RX72N has been used to generate the sample code discussed in this document. Smart Configurator for e<sup>2</sup> studio is a plugin tool for generating template 'C' source code and project settings for the RX72N. When using Smart Configurator, it supports the user with a visual way of configuring the target device, clocks, software components, hardware resources and interrupts for the project; thereby bypassing the need, in most cases, to refer to sections of the Hardware Manual.

Once the user has configured the project, the 'Smart Configurator' function is used to generate three code modules for each specific MCU feature selected. These code modules are name 'Config\_xxx.h', 'Config\_xxx.c', and 'Config\_xxx\_user.c', where 'xxx' is an acronym for the relevant MCU feature, for example 'CMT'. Within these code modules, the user is then free to add custom code to meet their specific requirement. However, these files require custom code to be added between the following comment delimiters:

```
/* Start user code for adding. Do not edit comment generated here */
/* End user code. Do not edit comment generated here */
```

Smart Configurator will locate these comment delimiters, and preserve any custom code inside the delimiters on subsequent code generation operations. This is useful if, after adding custom code, the user needs to revisit Smart Configurator to change any MCU operating parameters.

Note: If code is added outside the above user code area, it will be lost if code generation is executed again with Smart Configurator.

In this RSK+ sample project, only some functions are used. For other useful features, refer to the <u>https://www.renesas.com/smart-configurator</u>.



### 3. Tutorial Project Workspace

### 3.1 Introduction

e<sup>2</sup> studio is an open source integrated development tool that allows the user to write, compile, program and debug a software product on many of the Renesas microcontrollers.

### 3.2 Connecting the Debugger

For this tutorial, it is necessary to provide an external power supply to the board. Use the +5V center-positive PSU supplied with this RSK+ to power the board.

The Quick Start Guide provided with the Renesas Starter Kit+ board gives detailed instructions on how to connect the E2 Lite to the host computer. The following assumes that the steps in the Quick Start Guide have been followed and the E2 Lite drivers have been installed.

- Fit the PMOD LCD display to the board. Ensure all the pins of the connector are correctly inserted in the socket.
- Connect the E2 Lite Debugger to a free USB port on your computer.
- Connect the E2 Lite Debugger to the target hardware ensuring that it is plugged into the connector marked 'E1/E2 Lite'.
- Connect the +5V center-positive PSU to the PWR connector on the RSK+.

### 3.3 Starting e<sup>2</sup> studio and Importing Sample Code

To use the program, start e<sup>2</sup>studio:

Windows<sup>™</sup> 8.1: From Apps View , click 'Renesas Electronics e2studio > e2 studio icon Windows<sup>™</sup> 10: Start Menu > All Apps > Renesas Electronics e2studio > e2 studio

<ul> <li>Start e<sup>2</sup> studio by select Windows™ Start Menu to appear will be the W</li> <li>Click 'Browse' and select to store your workspace New Folder' option as 'Launch'.</li> </ul>	i. The first dialog box forkspace Launcher. act a suitable location e, using the 'Make	e <sup>2</sup> studio u <u>W</u> orkspace	irectory as we uses the worksp e: C:\Workspace	ace directory to store its pre	ferences	and development artifacts.	× Prowse Cancel
• The e <sup>2</sup> studio Welcome appear. Click the 'Wor on the far right (circled opposite).	kbench' arrow button	Billingen den Pi (Millingen den Billingen den Bi		C/C ++ project dis C/C ++ project applied project B motification setting motification setting	11  =  >    	Overview Get an everyfere of the fractures Turbane So therwap baratures Tyr surt the samples Tyr surt the samples Tyr surt the samples Tyr surt the samples	An and a set of the se



•	Once the environment has initialized, right click in the 'Project Explorer' window and select 'Import…'	🏠 Project Explorer 🙁 📄 🔄 🔽 🗖
		New
		Show In Alt+Shift+W >
		Copy Ctrl+C
		Copy Qualified Name Paste Ctrl+V
		Delete Delete
		import
		Export Refresh F5
•	The Import dialog box will be shown.	e² Import – – ×
•	Expand the 'General' folder icon, and select 'Existing Projects into Workspace', then click 'Next'.	e2 Import     -     -     ×       Select     Create new projects from an archive file or directory.
		Select an import wizard:
		type filter text
		✓ ➢ General ∧
		🔁 Existing Projects into Workspace
		C File System
		Preferences     Projects from Folder or Archive
		Provide the second s
		😂 Renesas CS+ Project for CA78K0R/CA78K0
		<ul> <li>Renesas CS+ Project for CC-RX and CC-RL</li> <li>Renesas GitHub Amazon FreeRTOS Project</li> </ul>
		> 🍃 C/C++ > 🍃 Code Generator
		C: C: V
		(?) < Back Next > Einish Cancel
•	The Import dialog box will allow you to	e Import – 🗆 X
	specify a project to import. Click the	Import Projects Select a directory to search for existing Eclipse projects.
	'Browse' button and locate the following directory:	Select root directory:     C:\Renesas\Workspace\RSK\RSK+RX72N     Browse.     Browse.
	directory.	O Select archive file:         V         Browse
	C:\Renesas\Workspace\RSK\RSK+RX72N	Projects:           Image: Constraint of the state of the
		Async_Serial (C:\Renesas\Workspace\RSK\RSK+RX72N\Async_Serial)  Low_Power_Mode (C:\Renesas\Workspace\RSK\RSK+RX72N\Low_Power_Mode)  Deselect All
•	Ensure that the 'Copy projects into	
	workspace' option is ticked, and then click	System_Input_Capture (C-\Renesas\Workspace\RSK\RSK+RX72N\System_Input_Capture) Timer_PUMN (C-\Renesas\Workspace\RSK\RSK+RX72N\Timer_PWM)
	'Finish'.	Tutorial (C:Renetas):Workspace(RSK):RSK= RX72N1Utorial) Tutorial_withTFT (C:\Renetas):Workspace\RSK):RSK=RX72N1Utorial_withTFT)
		Options □ Search for nested projects ☑ Copy projects into workspace
		Hide projects that already exist in the workspace
		Working sets Add project to working sets New
		Working sets
		Working sets Add project to working sets New
		Working sets Add project to working sets New
•	Click on Tutorial from the list of projects in	Working sets     Neg       Add project to working sets     Neg       Warking sets:     Sglect       ?     < Back       Neg.     Enish
•	Click on Tutorial from the list of projects in the 'Project Explorer' on the left-hand side.	Working sets     Neg       Warking sets:     V



### 3.4 Build Configurations and Debug Sessions

### 3.4.1 Build Configuration

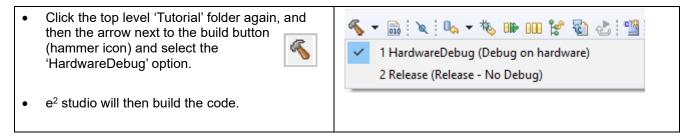
The e<sup>2</sup> studio workspace will be created with two build configurations: 'HardwareDebug' and 'Release'.

### Release

This Build Mode has optimisation turned on, and provides little debug information. The C code execution may appear to be out of order, due to the way the compiler optimises the code. This build configuration is intended for final ROM-programmable code.

### HardwareDebug

This Build Mode has all optimisation turned off, and provides full debug information. This is the best configuration to use whilst developing code as C code execution will be linear.





### 3.4.2 Debug Configuration

•	Click the arrow next to the debug button (bug icon), as highlighted by the red circle. Select 'Debug Configurations'.	(no launch history)
		Debug As >
		Debug Configurations
		Organize Favorites
•	The 'Debug Configurations' dialog box will	e <sup>2</sup> Debug Configurations
	appear. Click the small arrow next to the 'Renesas GDB Hardware Debugging' option.	Create, manage, and run configurations
•	The debug configurations for each project will	Configure launa
	appear. Select the entry for the 'Tutorial	type filter text
	HardwareDebug'.	Java Application
		Launch Group (Deprecated)
		✓ In Renesas GDB Hardware Debugging
		C Application HardwareDebug     Application Release
		Configure launa
		C <sup>®</sup> Async_Serial Release
		C Low_Power_Mode Release
		C RTC HardwareDebug RTC Release
		C <sup>3</sup> System_BootLoader HardwareDebug C <sup>3</sup> System_BootLoader Release
		System_BootLoader_Application HardwareDebug
		C <sup>*</sup> System_BootLoader_Application Release C <sup>*</sup> System_Input_Capture HardwareDebug
		<ul> <li>System_Input_Capture Release</li> <li>Timer_PWM HardwareDebug</li> </ul>
		CT Timer_PWM Release
		C3 Tutorial HardwareDebug C3 Tutorial Release
		C Tutorial_withTFT HardwareDebug
		C <sup>™</sup> Tutorial_withTFT Release C <sup>™</sup> Renesas Simulator Debugging (RX, RL78)
•	The debug configurations control page will	Ain 🕸 Debugger 🕒 Startup 🦆 Source 🔲 Common
	then show for the Tutorial project. Change	Debug hardware: E2 Lite (RX) V Target Device: R5F572NN
	the main tab to 'Debugger' and then select	GDB Settings Connection Settings Debug Tool Settings
	'Connection Settings' on the secondary tab	✓ Clock
	bar that appears.	Main Clock Source EXTAL   Extal Frequency[MHz] 24.0000
٠	There is no need to change the debugger	Operating Frequency [MHz] 240.000
	settings as they are preconfigured with the	Permit Clock Source Change On Writing   Yes  Connection with Target Board
	Tutorial project. Please check "Power Target	Emulator (Auto)
	From The Emulator (MAX 200mA)" is "No".	Connection Type JTag v JTag Clock Frequency[MHz] 6.00 v
•	Refer to the RSK+RX72N User's Manual for	Fine Baud Rate[Mbps] 1.50 v
	details of power supply configuration.	Hot Plug No V
		Power Target From The Emulator (MAX 2( No 🗸
		Supply Voltage (V) 3.3 V V CPU Operating Mode
	<b>Note</b> : e <sup>2</sup> studio will display a warning	Register Setting Single Chip 🗸
	if you attempt to connect with an	Mode pin         Single-chip mode         V           Change startup bank         No         V
	incorrect power supply setting.	Startup bank Bank 0 V
•	Click the 'Debug' button to continue.	
	e <sup>2</sup> studio will be connected to the debugger	Revert Apply
	and download the code to the target.	Debug Close
	and download the code to the larget.	Debug Close
1		



### Renesas Starter Kit+ for RX72N

### 3. Tutorial Project Workspace

•	A firewall warning may be displayed for 'e2- server-gdb.exe'. Check the 'Private networks, such as my home or work network' box and click 'Allow access'. A user account control dialog may be	Windows Security Alert ×  Windows Firewall has blocked some features of this app  Windows Firewall has blocked some features of E2 Server GDB on all public, private and domain  networks.
	displayed. Enter the administrator password and click 'Yes'.	Name:       E2 Server CDB         Publisher:       Renesas Electronics Europe Ltd         Path:       C:\renesas\e2_studio\debugcomp\rx\e2-server-gdb.exe         Allow E2 Server GDB to communicate on these networks:
•	After downloading the code a dialog box will appear asking if you would like to switch to the 'Debug perspective'. Click 'Remember my decision' to prevent this dialog box from appearing in future, then click 'Yes'. e <sup>2</sup> studio will load the new perspective, which is optimised for debugging.	Confirm Perspective Switch       X         It is kind of launch is configured to open the Debug perspective when it suspends.       This Debug perspective is designed to support application debugging. It incorporates views for displaying the debug stack, variables and breakpoint management.         Do you want to open this perspective now?         Remember my decision         Yes
•	To change back to the default 'C/C++' perspective, from the menu bar select Window > Perspective >Open Perspective > Other or click the Open Perspective button. The 'Open Perspective' dialog box will appear. Click on the desired perspective to select it then 'OK'. Perspectives can also be switched with the following shortcuts. C/C++ to Debug	Copen Perspective           X             Code Generator           Code Generator             Code Generator           Subscription             Java           Java             Java           Subscription             Scripting           Scripting             Source           Scripting             Source           Scripting             Scripting           Source             Scripting           Scripting             Source           Scripting             Source           Scripting             Source           Scripting             Source           Scripting             Source           Scripting             Suphate content contracted content c
•	The perspective should be 'Debug', as it will be needed in the next section.	

### 3.5 Running the Tutorial

- Refer to the description.txt file in the doc folder of Tutorial project for instructions on how to configure the RSK+ and run the sample code.
- Once the code has been downloaded, click 'Resume' button 🕩 to run the code to the main function. The main function is set as the program entry point by default. The program counter will stop on the first instruction in the main function.
- Click the 'Resume' button in the 'Debug' perspective to run the rest of the code.
- It is recommended that you run the entire tutorial demo first, before continuing to debug it.

### 4. Reviewing the Tutorial Program

This section will look at each section of the tutorial code and basic debugging functionality in e<sup>2</sup> studio.

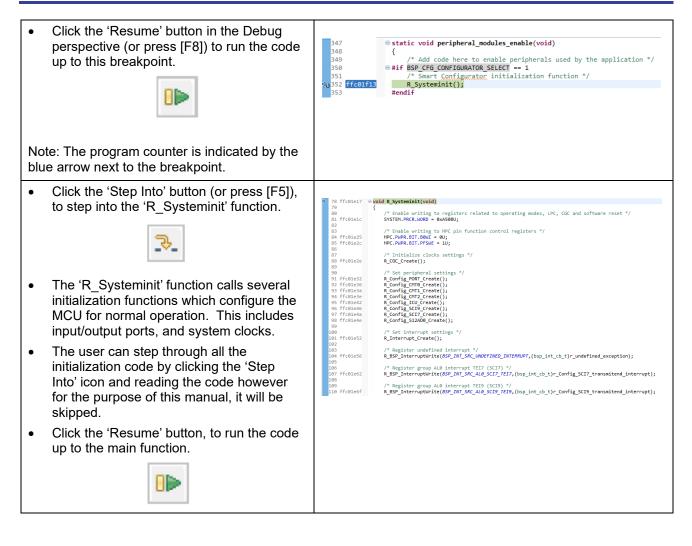
### 4.1 **Program Initialization**

Before the main program can run, the microcontroller must be configured. The following parts of the tutorial program are used exclusively for initializing the RSK+ device so that the main function can execute correctly. The initialization code is run every time the device is reset via the reset switch or from a power cycle.

•	Build and download the code as shown in Section 3.4. On the Project Explorer tab expand the 'Tutorial' folder by clicking on the arrow next to the folder icon, as highlighted by the red circle.	<ul> <li>Project Explorer (2)</li> <li>Application</li> <li>Async_Serial</li> <li>Cow_Power_Mode</li> <li>Cow_Power_Mode</li> <li>RTC</li> <li>System_BootLoader</li> <li>System_BootLoader</li> <li>System_Input_Capture</li> <li>System_Input_Capture</li> <li>Timer_PWM</li> <li>Tutorial [HardwareDebug]</li> <li>Tutorial_withTFT</li> </ul>
•	Click the arrow next to the 'src' folder to show the source files. Expand the folder path 'smc_gen' -> 'r_bsp' -> 'board' -> 'generic_rx72n' as shown and double click on the file 'hwsetup.c'.	<ul> <li>✓ E Tutorial [HardwareDebug]</li> <li>&gt; S Binaries</li> <li>&gt; D Includes</li> <li>✓ S src</li> <li>✓ Config_CMT0</li> <li>&gt; Config_CMT1</li> <li>&gt; Config_CMT2</li> <li>&gt; Config_ICU</li> <li>&gt; Config_ICU</li> <li>&gt; Config_S12AD0</li> <li>&gt; Config_SCI7</li> <li>&gt; Config_SCI9</li> <li>&gt; general</li> <li>✓ p spare</li> <li>✓ board</li> <li>✓ generic_rx72n</li> <li>&gt; hwsetup.h</li> </ul>
•	Breakpoints can be set by double clicking at the left-hand edge of the source window. On the line with instruction 'R_Systeminit();', double click next to the vertical line to set a breakpoint.	<pre>347 348 349 350 351 353 353 347 () static void peripheral_modules_enable(void) { * Add code here to enable peripherals used by the application */ # Add code here to enable peripherals used by the application */ # Signature for the application */ R_Systeminit(); #endif</pre>



### Renesas Starter Kit+ for RX72N



For further details regarding hardware configuration, please refer to 'Renesas Starter Kit+ RX72N User's Manual' and 'RX72N Group User's Manual: Hardware'.



### 4.2 Main Functions

This section will look at the program code called from within the main function, and how it works. It is necessary to connect the G1CUSB0 port on the RSK+ to a PC USB port and open a terminal emulation program, such as Tera Term, with the settings:

Baud Rate: 19200,

Data Length: 8, Parity Bit: None, Stop Bit: 1, Flow Control: None

For information on installation of the RSK+ virtual COM port driver, refer to the file 'description.txt' in the doc folder of the e<sup>2</sup> studio Tutorial project.





<ul> <li>The program counter should come to a halt at the 'R_Config_SCI9_Start' function.</li> <li>Step over the function by clicking the 'Step Over' button. Alternatively, press [F6].</li> <li>The 'R_Config_SCI9_Start' function enables</li> </ul>	<pre>/* Set up SCI9 receive buffer and callback function */ R_Config_SCI9_Serial_Receive((uint8_t *)&amp;g_rx_char, 1); 117 118 /* Enable SCI9 operations */ \$0119 ffc00970 R_Config_SCI9_Start();</pre>
the UART interrupts. The program then proceeds to the main while(1U) loop. The code inside the loop waits for user input from either the SCI or RSK+ switches, and then performs an A/D conversion.	
• Locate the function call to 'lcd_display_adc' inside the while loop.	121
• Set a breakpoint on the 'lcd_display_adc' function call by double-clicking in the break point column.	124 125 126 ffc00a15 ← if (TRUE == g_adc_trigger) 127 128 129 ffc00977 130 131 /* Call the function to perform an A/D conver adc_result = get_adc(); 131 /* Display the result on the LCD */ 1cd_display_adc(adc_result);
In the Project Explorer pane, locate the file 'Config_SCI9_user.c' and double-click to open the source file. Scroll down to the function 'r_Config_SCI9_callback_receiveend'.	<ul> <li>Tutorial [HardwareDebug]</li> <li>Binaries</li> <li>Includes</li> <li>Src</li> <li>Config_CMT0</li> <li>Config_CMT1</li> <li>Config_CMT1</li> <li>Config_ICU</li> <li>Config_ICU</li> <li>Config_SCI7</li> <li>Config_SCI9</li> <li>Config_SCI9.c</li> <li>Config_SCI9.h</li> <li>general</li> </ul>
<ul> <li>Set a breakpoint on the line of code inside the 'r_Config_SCI9_callback_receiveend' function as shown opposite.</li> <li>Continue to execute the program by clicking the 'Resume' button.</li> </ul>	<pre>193 ffc01da7 194 195 195 196 197 198 ffc01da9 197 198 ffc01da9 199 199 199 200 ffc01dce 201 202 203 204 ffc01dd9 205 206 206 207 3 </pre> <pre> 207 3 </pre> <pre> 208 209 209 209 209 209 209 209 209 209 209</pre>



/\* Check the contents of g\_rx\_char \*/ if (('c' == g\_rx\_char) || ('C' == g\_rx\_char)) 197 In the terminal emulation window, press the • 198 ffc01da9 'c' button on the keyboard. 99 200 ffc01dce g adc trigger = TRUE; The program will halt at the breakpoint in • the 'r Config SCI9 callback receiveend' function as shown opposite. Remove the breakpoint by double-clicking on the breakpoint column. \* Display the result on the LCD \*/ • Continue to execute the program by clicking 132 ffc0097c lcd\_display\_adc(adc\_result); 133 the 'Resume' button. 134 /\* Increment the <u>adc</u> count and display 135 ffc00981 😑 if (16 == (++gs\_adc\_count)) 136 { 137 ffc00993 s\_adc\_count = 0; 138 139 ffc0099c led\_display\_count(gs\_adc\_count); The program will halt at the breakpoint in • the main while loop. Remove the breakpoint by double-clicking • on the breakpoint column. Continue to execute the program by clicking the 'Resume' button. UÞ

The program proceeds to display the result of the A/D conversion on the LCD and in the terminal window. In addition, the running count of A/D conversions performed is displayed in binary form using LEDs 0-3 on the RSK+. Adjust the potentiometer and press SW1, SW2 or SW3 on the RSK+ and an additional A/D conversion will be performed.

Press the 'Suspend' button to halt program execution.				
<ul> <li>To change back to the default 'C/C++' perspective, from the menu bar select Window &gt; Perspective &gt;Open Perspective &gt; 'C/C++'.</li> </ul>	Window       Help         New Window       Image: Constraint of the second			
<ul> <li>Alternatively, click on the 'C/C++' button in the top right corner of the screen, as shown opposite.</li> </ul>				
• This is the extent of the tutorial code.				

For further details regarding hardware configuration, please refer to 'Renesas Starter Kit+ for RX72N User's Manual' and 'RX72N Group User's Manual: Hardware'.

The E2 emulator Lite features an advanced logic-based event point trigger system, and full instruction on its use is outside the scope of this tutorial. For further details, please refer to the E2 Emulator Lite User's Manual.



### **5.** Additional Information

### **Technical Support**

For details on how to use $e^2$ studio, refer to the help file by opening $e^2$ studio, then			
selecting Help > Help Contents from the	Window	Help	
menu bar.	~ *	3	Welcome
		?	Help Contents
		20	Search
	× :		Show Contextual Help

Parts of the sample code provided with the RSK+RX72N can be reproduced using the Smart Configurator tool. Smart Configurator is included as a plug in with  $e^2$  studio.

Source files and functions generated by Smart Configurator are prefixed with 'r\_' and 'R\_' or 'Config\_', respectively.

For information about the RX72N Group microcontrollers refer to 'RX72N Group User's Manual: Hardware'.

For information about the RX assembly language, refer to 'RX Family User's Manual: Software'.

### **Technical Contact Details**

### Please refer to the contact details listed in section 8 of the "Quick Start Guide"

General information on Renesas Microcontrollers can be found on the Renesas website at: <u>https://www.renesas.com/</u>

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