Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

Send any inquiries to http://www.renesas.com/inquiry.



|--|

740 FAMILY MASK ROM CONFIRMATION FORM SINGLE-CHIP MICROCOMPUTER M38067MC-XXXFP/GP RENESAS TECHNOLOGY

	Date:	
Receipt	Section head signature	Supervisor signature
Ä		

Note: F	Please	fill i	n all	items	marked	*
---------	--------	--------	-------	-------	--------	---

*	Customer	Company name		TEL (uance nature	Submitted by	Supervisor
AV.	Oustomer	Date issued	Date:		lssı sigr		

* 1. Confirmation

Specify the name of the product being ordered.

Three EPROMs are required for each pattern if this order is performed by EPROMs.

One floppy disk is required for each pattern if this order is performed by a floppy disk.

Microcomputer name:	M38067MC-XXXFP	M38067MC-XXXGF
---------------------	----------------	----------------

☐ Ordering by EPROMs

If at least two of the three sets of EPROMs submitted contain identical data, we will produce masks based on this data. We shall assume the responsibility for errors only if the mask ROM data on the products we produce differs from this data. Thus, extreme care must be taken to verify the data in the submitted EPROMs.

Checksum code for entire EPROM			(hexadecimal notation)

EPROM type (indicate the type used)

□ 27512					
EPROM ac	EPROM address				
000016	Product name ASCII code :				
000F16	'M38067MC-'				
001016					
407F16					
408016	data				
FFFD16	ROM (48K-130) bytes				
FFFE16 FFFF16					

In the address space of the microcomputer, the internal ROM area is from address 408016 to FFFD16. The reset vector is stored in addresses FFFC16 and FFFD16.

- (1) Set the data in the unused area (the shaded area of the diagram) to "FF16".
- (2) The ASCII codes of the product name "M38067MC-" must be entered in addresses 000016 to 000816. And set the data "FF16" in addresses 000916 to 000F16. The ASCII codes and addresses are listed to the right in hexadecimal notation.

Address		Address
000016	'M' = 4D16	000816
000116	'3' = 3316	000916
000216	'8' = 3816	000A16
000316	'0' = 3016	000B16
000416	'6' = 36 ₁₆	000C16
000516	'7' = 37 ₁₆	000D16
000616	'M' = 4D16	000E16
000716	'C' = 4316	000F16

'–' = 2D16

FF16

FF16

FF16

FF16

FF16

FF16 FF16



ask ROM number

740 FAMILY MASK ROM CONFIRMATION FORM SINGLE-CHIP MICROCOMPUTER M38067MC-XXXFP/GP RENESAS TECHNOLOGY

We recommend the use of the following pseudo-command to set the start address of the assembler source program because ASCII codes of the product name are written to addresses 000016 to 000816 of EPROM.

EPROM type	27512
The pseudo-command	*= △\$0000 .BYTE △'M38067MC–'

Note: If the name of the product written to the EPROMs does not match the name of the mask confirmation form, the ROM will not be processed.

responsibility for errors only if the ma treme care must be taken to verify the	sk ROM data on the products mask file in the submitted flop	mask file generating utility. We shall assume the we produce differs from this mask file. Thus, expy disk. Domat. And the number of the mask files must be
File code		(hexadecimal notation)
Mask file name		.MSK (equal or less than eight characters)
		package being ordered. Fill out the appropriate 8067MC-XXXGP) and attach it to the mask ROM
* 3. Usage conditions Please answer the following questions	about usage for use in our pro	duct inspection :
(1) How will you use the XIN-XOUT oscillator	?	
Ceramic resonator	Quartz crystal	
☐ External clock input	☐ Other ()	
At what frequency?	f(XIN) = MHz	
(2) In which operation mode will you use you	ır microcomputer?	
☐ Single-chip mode	☐ Memory expansion mod	e
☐ Microprocessor mode		
* 4. Comments		

