

# UPC834, UPC4064

## Low Power Consumption

## J-FET Input Quad Operational Amplifiers

### DESCRIPTION

The UPC834 and 4064 are low power consumption versions operational amplifier of UPC804 and 4084 series with the general-purpose J-FET input. This revolutionary J-FET input operational amplifier realizes low power consumption of  $I_{CC}$  800  $\mu$ A TYP. and low voltage operation from  $\pm 2$  V. Owing to its J-FET input, the input impedance is high. The AC characteristics are also greatly improved compared to the conventional low power consumption operational amplifier. Therefore, it is ideal for application circuits such as amplifier circuits and active filters in various battery-operated electronic devices.

Depending on the operating ambient temperature, UPC834 is suitable for communication application while UPC4064 is suitable for general-purpose usage.

Along with this series of lineup, the dual type op-amp UPC832 and 4062 with same circuit configuration are also available.

### FEATURES

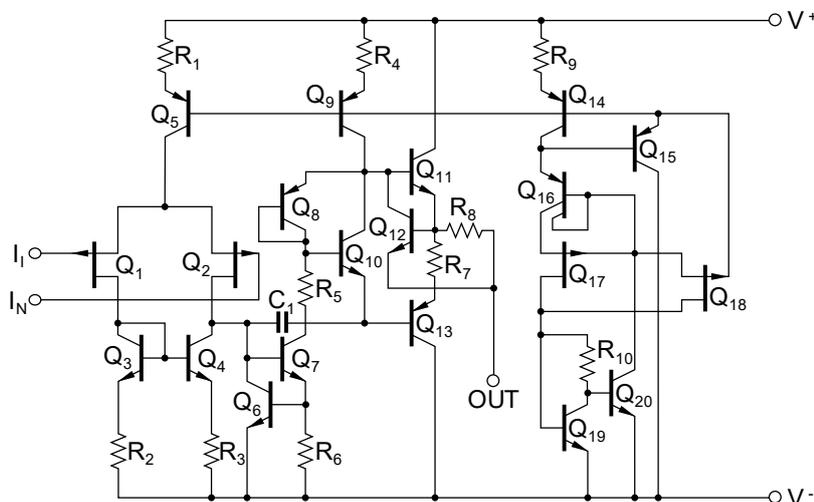
- Input Offset Voltage  $\pm 2$  mV (TYP.)
- Input Bias Current 10 pA (TYP.)
- Slew Rate 3 V/ $\mu$ s (TYP.)
- Unity Gain Frequency 1 MHz (TYP.)
- Circuit Current 800  $\mu$ A (TYP.)
- Operate from  $\pm 2$  V
- Built-In Phase Compensation Circuit
- Built-In Output Short Circuit Protection
- Standard Quad op-amp terminal connection (pin compatible)

### ORDERING INFORMATION

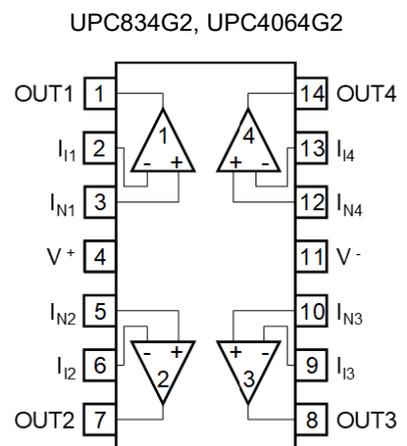
Order Name <sup>(1)</sup>	Package
UPC834G2-AP	14-Pin plastic SOP ( 5.72 mm (225 mil) )
UPC4064G2-AP	14-Pin plastic SOP ( 5.72 mm (225 mil) )

(1) Order names containing E1 or E2 indicate that the packaging format is embossed taping.  
Pin 1 of E1 is on draw-out side, and pin 1 of E2 is at take-up side.

### EQUIVALENT CIRCUIT (1/4 Circuit)



### PIN CONFIGURATION (Top View)



**ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub> = 25 °C)**

Parameter	Symbol	UPC834G2	UPC4064G2	Unit
Supply Voltage <sup>Note1</sup>	V <sup>+</sup> - V <sup>-</sup>	-0.3 to +36		V
Differential Input Voltage	V <sub>ID</sub>	±30		V
Input Voltage <sup>Note2</sup>	V <sub>I</sub>	V <sup>-</sup> -0.3 to V <sup>+</sup> +0.3		V
Output Applied Voltage <sup>Note3</sup>	V <sub>O</sub>	V <sup>-</sup> -0.3 to V <sup>+</sup> +0.3		V
Total Power Dissipation <sup>Note4</sup>	P <sub>T</sub>	550		mW
Output Short Circuit Duration <sup>Note5</sup>		Indefinite		s
Operating Ambient Temperature	T <sub>A</sub>	-40 to +85	-20 to +80	°C
Storage Temperature	T <sub>stg</sub>	-55 to +125		°C

[Note] 1. Note that reverse connections of the power supply may damage the ICs.

2. The input terminal must be applied within the input voltage range to avoid deteriorating or damaging the device characteristic. Do not exceed the ratings including during transition state such as ON/OFF, etc. The Op-Amp input voltage must operate within the electrical characteristics range of input common-mode voltage.
3. The output terminal must be applied within the output voltage range to avoid deteriorating or damaging the device characteristic. Do not exceed the ratings including during transition state such as ON/OFF, etc. The Op-Amp output voltage must operate within the electrical characteristics range of maximum output voltage.
4. This is the value at T<sub>A</sub> ≤ +25 °C. De-rate value at -5.5 mW/°C when T<sub>A</sub> > 25 °C.
5. Please use the total loss and the de-rating value from Note 4.

**RECOMMENDED OPERATING CONDITIONS**

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Power Supply Voltage	V <sup>±</sup>	±2		±16	V
Output Current (SOURCE)	I <sub>O SOURCE</sub>			5	mA
Output Current (SINK)	I <sub>O SINK</sub>			3.5	mA
Load Capacitance (When A <sub>v</sub> = +1)	C <sub>L</sub>			100	pF

**ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25 °C, V<sup>±</sup> = ±15 V)**

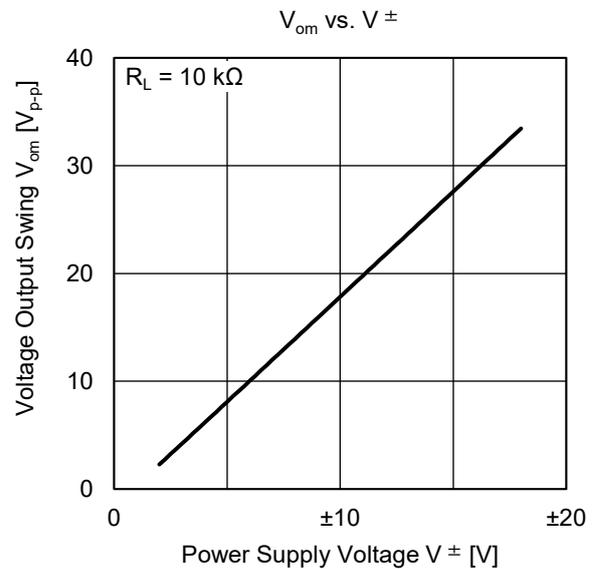
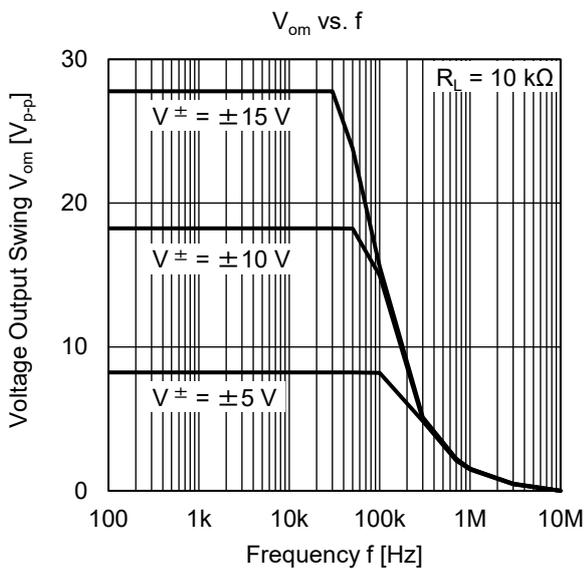
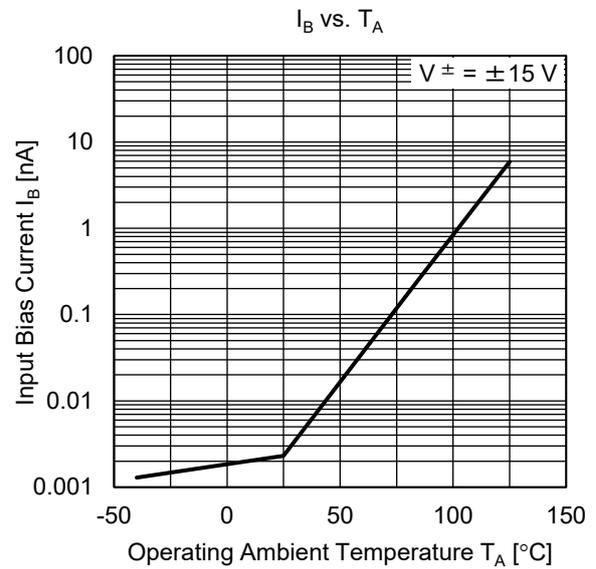
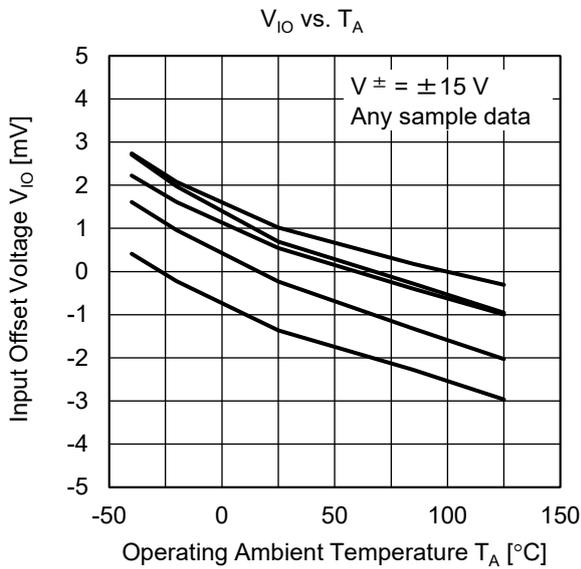
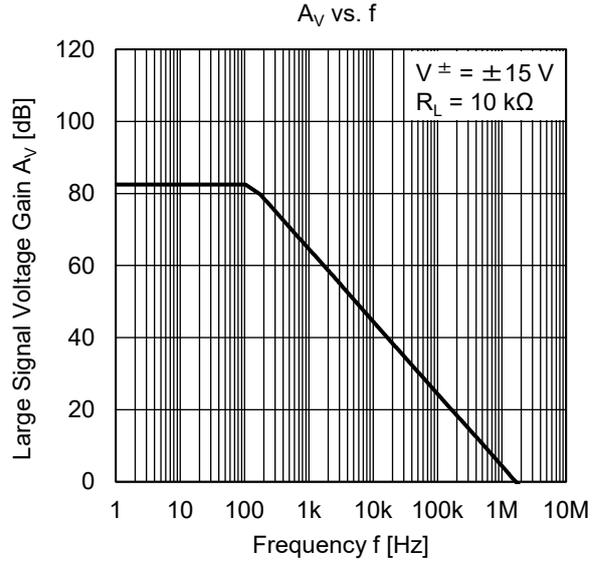
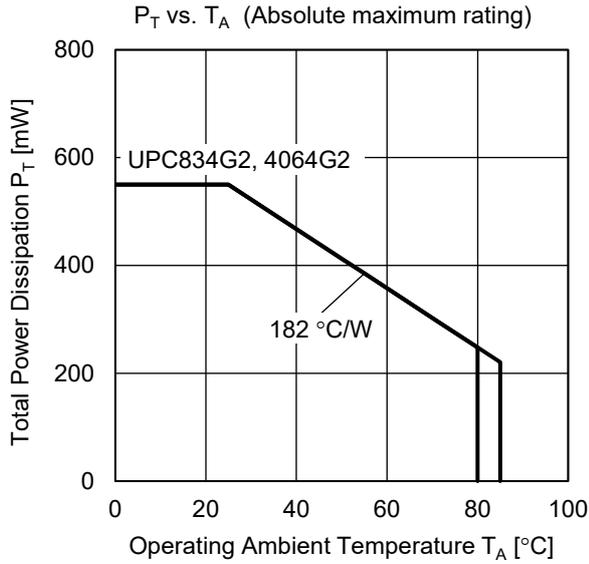
Parameter	Symbol	MIN.	TYP.	MAX.	Unit	Test Conditions
Input Offset Voltage	V <sub>IO</sub>		±2	±10	mV	R <sub>s</sub> ≤ 50 Ω
Input Offset Current	I <sub>IO</sub>		±5	±50	pA	
Input Bias Current <sup>Note6</sup>	I <sub>B</sub>		10	100	pA	
Large Signal Voltage Gain	A <sub>V</sub>	3000	9000			R <sub>L</sub> ≥ 10 kΩ, V <sub>O</sub> = ±10 V
Circuit Current <sup>Note7</sup>	I <sub>CC</sub>		800	1000	μA	I <sub>O</sub> = 0 A
Common Mode Rejection Ratio	CMR	70	90		dB	
Supply Voltage Rejection Ratio	SVR	70	90		dB	
Voltage Output Swing	V <sub>om</sub>	±12	+14.0 -13.6		V	R <sub>L</sub> ≥ 10 kΩ
Input Common-Mode Voltage Range	V <sub>ICM</sub>	±12	+15 -13		V	
Slew Rate	SR		3		V/μs	A <sub>V</sub> = 1
Unity Gain Frequency	f <sub>unity</sub>		1		MHz	
Input Equivalent Noise Voltage Density	e <sub>n</sub>		30		nV/ √Hz	R <sub>s</sub> = 100 Ω, f = 1 kHz
Channel Separation			120		dB	
Input Offset Voltage	V <sub>IO</sub>			±15	mV	R <sub>s</sub> ≤ 50 Ω, T <sub>A</sub> = -20 to +70 °C
Average V <sub>IO</sub> Temperature Drift	Δ V <sub>IO</sub> /Δ T		±10		μV/°C	T <sub>A</sub> = -20 to +70 °C
Input Offset Current	I <sub>IO</sub>			±2	nA	T <sub>A</sub> = -20 to +70 °C
Input Bias Current <sup>Note6</sup>	I <sub>B</sub>			3.5	nA	T <sub>A</sub> = -20 to +70 °C

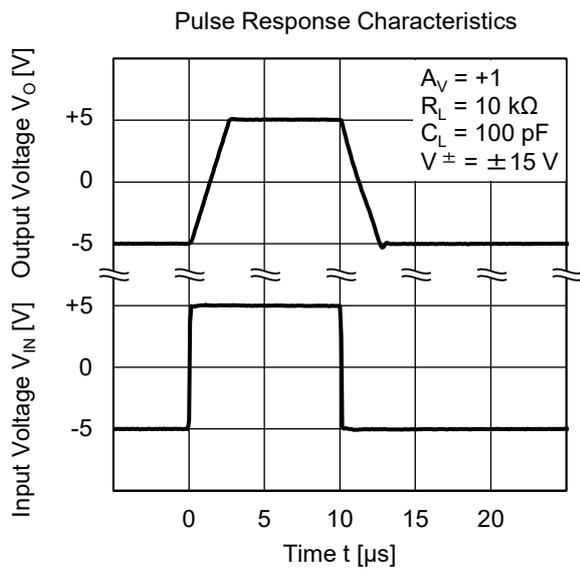
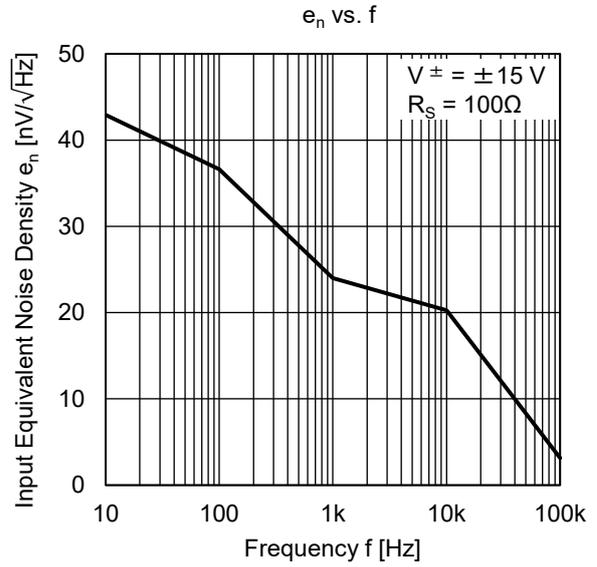
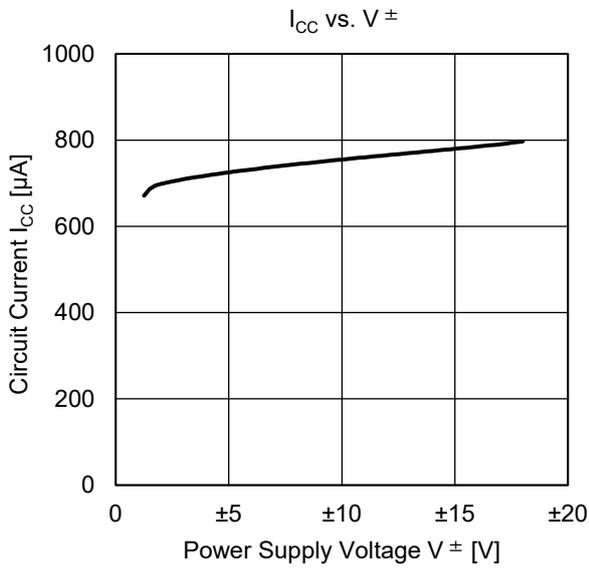
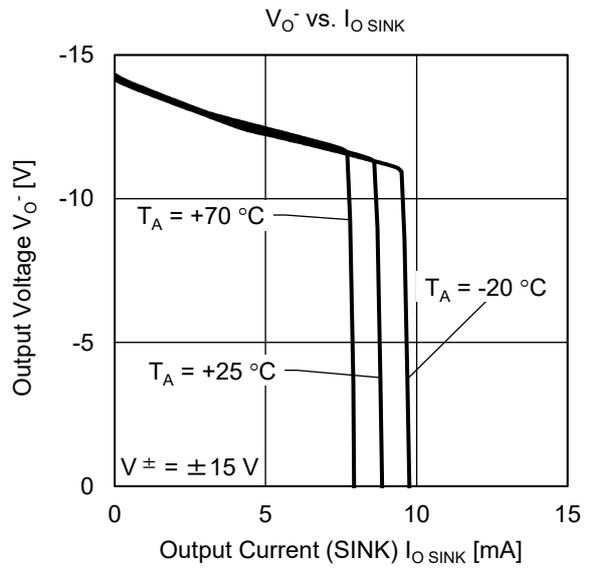
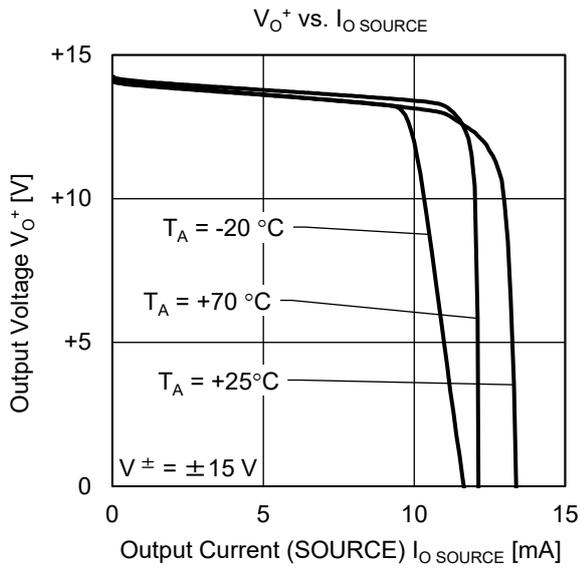
[Note] 6. The direction of the input bias current is the same direction that flows into the IC because the first stage is comprised of Pch J-FET. When T<sub>J</sub> = 25 °C or higher, it increases exponentially with increase in temperature (please see I<sub>B</sub> vs. T<sub>A</sub> characteristics). During measurement, please kindly take care of T<sub>J</sub> ≅ T<sub>A</sub>.

7. It is the current that flows into the internal circuit. This current flow is irrespective of the channel usage.

**Note** Special attention is required for insulation between pins on a board, since the UPC834 has a high-input impedance characteristic.

**ELECTRICAL CHARACTERISTICS CURVE (T<sub>A</sub> = 25 °C, TYP.) (REFERENCE VALUE)**



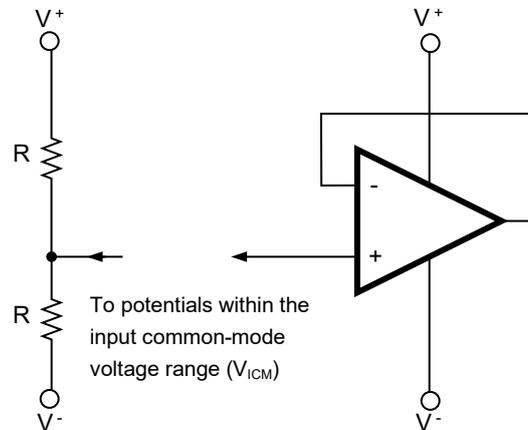


## USE WITH PRECAUTIONS

### Managing unused circuits

If there is an unused circuit, the following connection is recommended.

### Example of handling unused circuit



Note in this example, an intermediate voltage of  $V^+$  and  $V^-$  is applied.

- **Power Supply (Dual Power Supply / Single Power Supply)**

The op-amp operates when a predetermine voltage is applied between  $V^+$  to  $V^-$ . Therefore, while it operates from a single power supply ( $V^- = \text{GND}$ ), it is not possible to operate the input and output near GND. So please be careful of the common-mode input voltage range and maximum output voltage.

- **Ratings of input/output pin voltage**

When the voltage of input/output pin exceeds the absolute maximum rating, the parasitic diode within the IC may conduct, causing characteristics degradation or damage. In addition, if the voltage of input pin is lower than  $V^-$ , or the voltage of output pin exceeds the power supply voltage, it is recommended to make a clamping circuit using a diode with low forward voltage (e.g.: Schottky diode) as protection.

- **Input common-mode voltage range**

When the supply voltage does not meet the condition of electrical characteristics, the input common-mode voltage range is as follows.

$$V_{ICM} \text{ (TYP.)}: V^- + 2 \text{ to } V^+ \text{ [V]} \text{ (} T_A = 25 \text{ }^\circ\text{C)}$$

During designing, do include some margin by considering characteristic variations, temperature characteristics etc.

- **Maximum Output Voltage**

When the supply voltage does not meet the condition of electrical characteristics, the TYP. value range of the maximum output voltage is as follows:

$$V_{om+} \text{ (TYP.)}: V^+ - 1 \text{ [V]} \text{ (} T_A = 25 \text{ }^\circ\text{C}), V_{om-} \text{ (TYP.)}: V^- + 1.4 \text{ [V]} \text{ (} T_A = 25 \text{ }^\circ\text{C)}$$

During designing, do include some margin by considering characteristic variations, temperature characteristics and so on. In addition, also note that the output voltage range ( $V_{om+}$  to  $V_{om-}$ ) will become narrow when the output current increases.

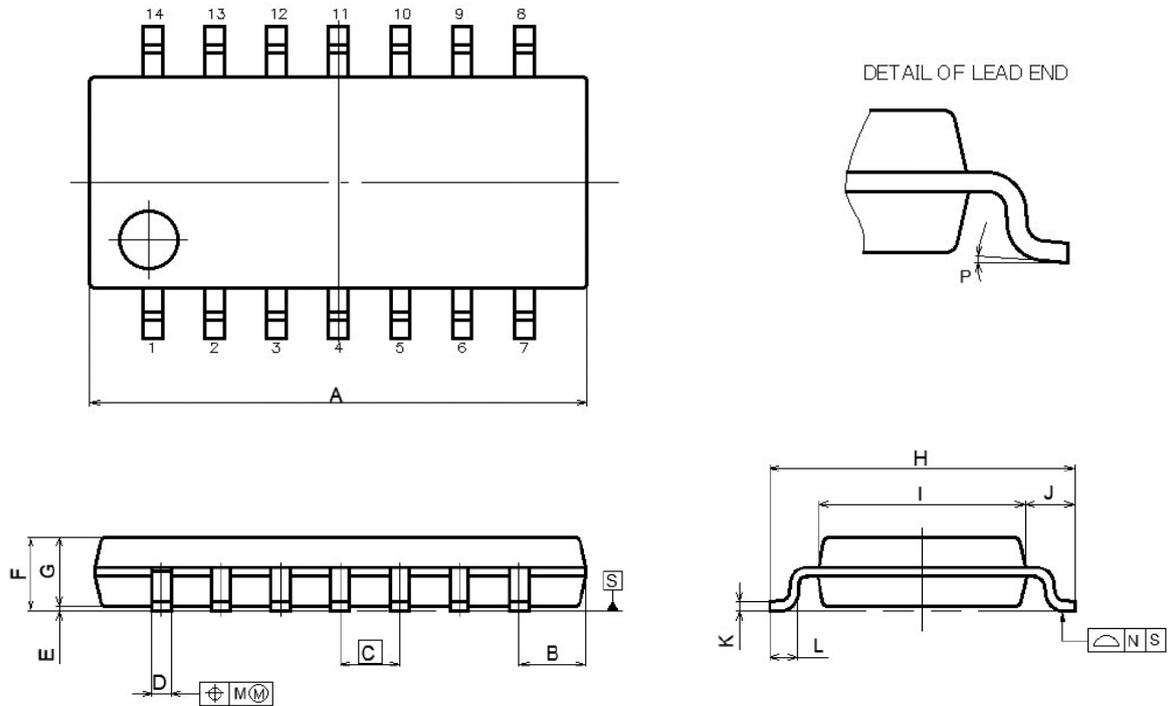
- **Handling of ICs**

When stress is added to the ICs due to warpage or bending of a board, the characteristic may fluctuate due to piezoelectric (piezo) effect. Therefore, pay attention to warpage or bending of a board.

## PACKAGE DRAWINGS

### 14-PIN PLASTIC SOP

JEITA Package code	RENESAS code	MASS (TYP.) [g]
P-LSOP14-4.4×10.2-1.27	PLSP0014DB-A	0.17[g]



NOTE  
EACH LEAD CENTERLINE IS LOCATED WITHIN 0.12 MM OF ITS TRUE POSITION(T.P.) AT MAXIMUM MATERIAL CONDITION.

(UNIT:mm)

ITEM	DIMENSIONS
A	10.2±0.2
B	1.42MAX
C	1.27(T.P)
D	0.40±0.05
E	0.1±0.1
F	1.59±0.20
G	1.49±0.1
H	6.5±0.2
I	4.4±0.1
J	1.05±0.15
K	0.2±0.07
L	0.6±0.20
M	0.1MAX
N	0.1MAX
P	4°±4°

## Notice

1. Descriptions of circuits, software and other related information in this document are provided only to illustrate the operation of semiconductor products and application examples. You are fully responsible for the incorporation or any other use of the circuits, software, and information in the design of your product or system. Renesas Electronics disclaims any and all liability for any losses and damages incurred by you or third parties arising from the use of these circuits, software, or information.
2. Renesas Electronics hereby expressly disclaims any warranties against and liability for infringement or any other claims involving patents, copyrights, or other intellectual property rights of third parties, by or arising from the use of Renesas Electronics products or technical information described in this document, including but not limited to, the product data, drawings, charts, programs, algorithms, and application examples.
3. No license, express, implied or otherwise, is granted hereby under any patents, copyrights or other intellectual property rights of Renesas Electronics or others.
4. You shall be responsible for determining what licenses are required from any third parties, and obtaining such licenses for the lawful import, export, manufacture, sales, utilization, distribution or other disposal of any products incorporating Renesas Electronics products, if required.
5. You shall not alter, modify, copy, or reverse engineer any Renesas Electronics product, whether in whole or in part. Renesas Electronics disclaims any and all liability for any losses or damages incurred by you or third parties arising from such alteration, modification, copying or reverse engineering.
6. Renesas Electronics products are classified according to the following two quality grades: "Standard" and "High Quality". The intended applications for each Renesas Electronics product depends on the product's quality grade, as indicated below.

"Standard": Computers; office equipment; communications equipment; test and measurement equipment; audio and visual equipment; home electronic appliances; machine tools; personal electronic equipment; industrial robots; etc.

"High Quality": Transportation equipment (automobiles, trains, ships, etc.); traffic control (traffic lights); large-scale communication equipment; key financial terminal systems; safety control equipment; etc.

Unless expressly designated as a high reliability product or a product for harsh environments in a Renesas Electronics data sheet or other Renesas Electronics document, Renesas Electronics products are not intended or authorized for use in products or systems that may pose a direct threat to human life or bodily injury (artificial life support devices or systems; surgical implantations; etc.), or may cause serious property damage (space system; undersea repeaters; nuclear power control systems; aircraft control systems; key plant systems; military equipment; etc.). Renesas Electronics disclaims any and all liability for any damages or losses incurred by you or any third parties arising from the use of any Renesas Electronics product that is inconsistent with any Renesas Electronics data sheet, user's manual or other Renesas Electronics document.

7. No semiconductor product is absolutely secure. Notwithstanding any security measures or features that may be implemented in Renesas Electronics hardware or software products, Renesas Electronics shall have absolutely no liability arising out of any vulnerability or security breach, including but not limited to any unauthorized access to or use of a Renesas Electronics product or a system that uses a Renesas Electronics product. RENESAS ELECTRONICS DOES NOT WARRANT OR GUARANTEE THAT RENESAS ELECTRONICS PRODUCTS, OR ANY SYSTEMS CREATED USING RENESAS ELECTRONICS PRODUCTS WILL BE INVULNERABLE OR FREE FROM CORRUPTION, ATTACK, VIRUSES, INTERFERENCE, HACKING, DATA LOSS OR THEFT, OR OTHER SECURITY INTRUSION ("Vulnerability Issues"). RENESAS ELECTRONICS DISCLAIMS ANY AND ALL RESPONSIBILITY OR LIABILITY ARISING FROM OR RELATED TO ANY VULNERABILITY ISSUES. FURTHERMORE, TO THE EXTENT PERMITTED BY APPLICABLE LAW, RENESAS ELECTRONICS DISCLAIMS ANY AND ALL WARRANTIES, EXPRESS OR IMPLIED, WITH RESPECT TO THIS DOCUMENT AND ANY RELATED OR ACCOMPANYING SOFTWARE OR HARDWARE, INCLUDING BUT NOT LIMITED TO THE IMPLIED WARRANTIES OF MERCHANTABILITY, OR FITNESS FOR A PARTICULAR PURPOSE.
8. When using Renesas Electronics products, refer to the latest product information (data sheets, user's manuals, application notes, "General Notes for Handling and Using Semiconductor Devices" in the reliability handbook, etc.), and ensure that usage conditions are within the ranges specified by Renesas Electronics with respect to maximum ratings, operating power supply voltage range, heat dissipation characteristics, installation, etc. Renesas Electronics disclaims any and all liability for any malfunctions, failure or accident arising out of the use of Renesas Electronics products outside of such specified ranges.
9. Although Renesas Electronics endeavors to improve the quality and reliability of Renesas Electronics products, semiconductor products have specific characteristics, such as the occurrence of failure at a certain rate and malfunctions under certain use conditions. Unless designated as a high reliability product or a product for harsh environments in a Renesas Electronics data sheet or other Renesas Electronics document, Renesas Electronics products are not subject to radiation resistance design. You are responsible for implementing safety measures to guard against the possibility of bodily injury, injury or damage caused by fire, and/or danger to the public in the event of a failure or malfunction of Renesas Electronics products, such as safety design for hardware and software, including but not limited to redundancy, fire control and malfunction prevention, appropriate treatment for aging degradation or any other appropriate measures. Because the evaluation of microcomputer software alone is very difficult and impractical, you are responsible for evaluating the safety of the final products or systems manufactured by you.
10. Please contact a Renesas Electronics sales office for details as to environmental matters such as the environmental compatibility of each Renesas Electronics product. You are responsible for carefully and sufficiently investigating applicable laws and regulations that regulate the inclusion or use of controlled substances, including without limitation, the EU RoHS Directive, and using Renesas Electronics products in compliance with all these applicable laws and regulations. Renesas Electronics disclaims any and all liability for damages or losses occurring as a result of your noncompliance with applicable laws and regulations.
11. Renesas Electronics products and technologies shall not be used for or incorporated into any products or systems whose manufacture, use, or sale is prohibited under any applicable domestic or foreign laws or regulations. You shall comply with any applicable export control laws and regulations promulgated and administered by the governments of any countries asserting jurisdiction over the parties or transactions.
12. It is the responsibility of the buyer or distributor of Renesas Electronics products, or any other party who distributes, disposes of, or otherwise sells or transfers the product to a third party, to notify such third party in advance of the contents and conditions set forth in this document.
13. This document shall not be reprinted, reproduced or duplicated in any form, in whole or in part, without prior written consent of Renesas Electronics.
14. Please contact a Renesas Electronics sales office if you have any questions regarding the information contained in this document or Renesas Electronics products.

(Note1) "Renesas Electronics" as used in this document means Renesas Electronics Corporation and also includes its directly or indirectly controlled subsidiaries.

(Note2) "Renesas Electronics product(s)" means any product developed or manufactured by or for Renesas Electronics.

(Rev.5.0-1 October 2020)

## Corporate Headquarters

TOYOSU FORESIA, 3-2-24 Toyosu,  
Koto-ku, Tokyo 135-0061, Japan  
[www.renesas.com](http://www.renesas.com)

## Trademarks

Renesas and the Renesas logo are trademarks of Renesas Electronics Corporation. All trademarks and registered trademarks are the property of their respective owners.

## Contact information

For further information on a product, technology, the most up-to-date version of a document, or your nearest sales office, please visit:  
[www.renesas.com/contact/](http://www.renesas.com/contact/).