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April 1st, 2010 Renesas Electronics Corporation

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RENESAS

MOS FIELD EFFECT TRANSISTOR μ **ΡΑ678ΤΒ**

P-CHANNEL MOS FIELD EFFECT TRANSISTOR FOR SWITCHING

DESCRIPTION

The μ PA678TB is a switching device, which can be driven directly by a 2.5 V power source.

The μ PA678TB features a low on-state resistance and excellent switching characteristics, and is suitable for applications such as power switch of portable machine and so on.

FEATURES

- 2.5 V drive available
- Low on-state resistance
- $R_{DS(on)1} = 1.45 \Omega MAX. (V_{GS} = -4.5 V, I_{D} = -0.20 A)$
- $R_{DS(on)2} = 1.55 \Omega MAX. (V_{GS} = -4.0 V, I_{D} = -0.20 A)$
- RDS(on)3 = 2.98 Ω MAX. (VGS = -2.5 V, ID = -0.15 A)
- Two MOS FET circuits in same size package as SC-70

ORDERING INFORMATION

PART NUMBER	PACKAGE
μ PA678TB	SC-88 (SSP)

Marking: XA

ABSOLUTE MAXIMUM RATINGS ($T_A = 25^{\circ}C$)

Drain to Source Voltage (Vgs = 0 V)	VDSS	-20	V
Gate to Source Voltage (VDS = 0 V)	Vgss	∓12	V
Drain Current (DC)	ID(DC)	∓0.25	А
Drain Current (pulse) ^{Note1}	D(pulse)	∓1.00	Α
Total Power Dissipation (2 units) Note2	Ρτ	0.2	W
Channel Temperature	Tch	150	°C
Storage Temperature	Tstg	–55 to +150	°C

Notes 1. PW \leq 10 μ s, Duty Cycle \leq 1%

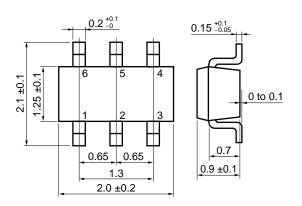
2. Mounted on FR-4 board of 2500 mm² x 1.1 mm

Caution This product is electrostatic-sensitive device due to low ESD capability and shoud be handled with caution for electrostatic discharge. VESD = ± 100 V TYP. (C = 200 pF, R = 0 Ω , Single pulse)

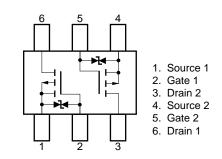
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Document No. G16607EJ1V0DS00 (1st edition) Date Published February 2003 NS CP(K) Printed in Japan

PACKAGE DRAWING (Unit: mm)



PIN CONNECTION (Top View)

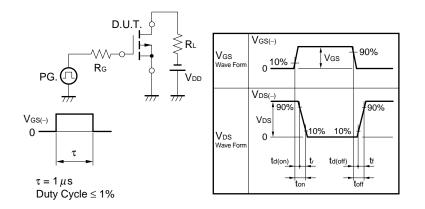


ELECTRICAL CHARACTERISTICS (T_A = 25°C)

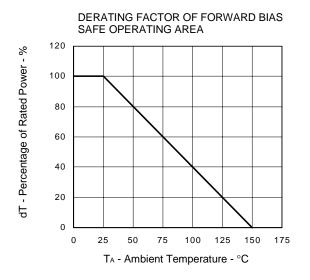
CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	Idss	Vds = -20.0 V, Vgs = 0 V			-1.0	μA
Gate Leakage Current	lgss	Vgs = ∓12.0 V, Vds = 0 V			∓10	μA
Gate Cut-off Voltage Note	V _{GS(off)}	$V_{DS} = -10.0 \text{ V}, \text{ ID} = -1.0 \text{ mA}$	-0.8	-1.3	-1.8	V
Forward Transfer Admittance Note	y _{fs}	Vds = -10.0 V, Id = -0.20 A	0.2	0.6		S
Drain to Source On-state Resistance Note	RDS(on)1	Vgs = -4.5 V, Id = -0.20 A		1.17	1.45	Ω
	RDS(on)2	V _{GS} = −4.0 V, I _D = −0.20 A		1.25	1.55	Ω
	RDS(on)3	V _{GS} = −2.5 V, I _D = −0.15 A		2.25	2.98	Ω
Input Capacitance	Ciss	V _{DS} = -10.0 V		29		pF
Output Capacitance	Coss	V _G s = 0 V		15		pF
Reverse Transfer Capacitance	Crss	f = 1.0 MHz		3		pF
Turn-on Delay Time	td(on)	$V_{DD} = -10.0 \text{ V}, \text{ Id} = -0.20 \text{ A}$		23		ns
Rise Time	tr	V _{GS} = -4.0 V		39		ns
Turn-off Delay Time	td(off)	R _G = 10 Ω		50		ns
Fall Time	tr			33		ns
Body Diode Forward Voltage	VF(S-D)	IF = 0.25 A, VGs = 0 V		0.88		V

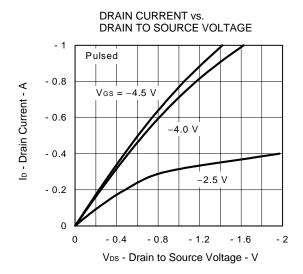
Note Pulsed: PW \leq 350 μ s, Duty cycle \leq 2%

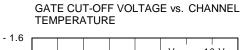
TEST CIRCUIT SWITCHING TIME

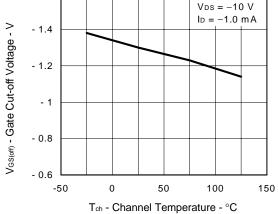


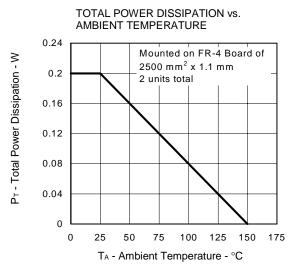
TYPICAL CHARACTERISTICS (TA = 25^{\circ}C)



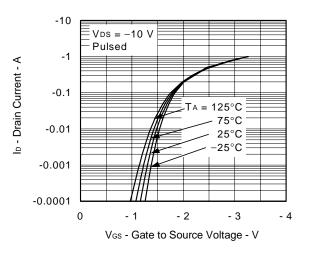




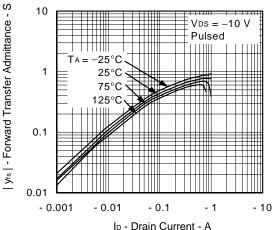




FORWARD TRANSFER CHARACTERISTICS



FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT

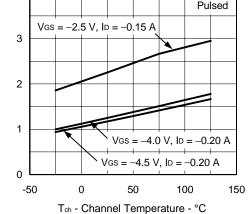




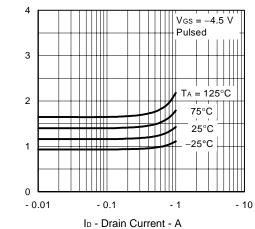
 $\mathsf{R}^{\mathsf{DS}(\mathsf{on})}$ - Drain to Source On-state Resistance - Ω

 $\mathsf{R}^{\mathsf{DS}(m)}$ - Drain to Source On-state Resistance - Ω

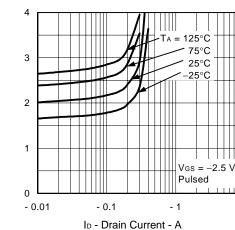
DRAIN TO SOURCE ON-STATE RESISTANCE vs. CHANNEL TEMPERATURE



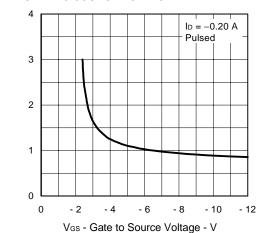
DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT



DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT



DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE

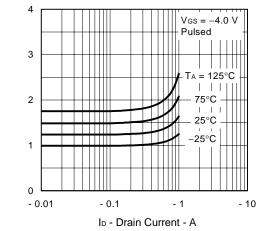


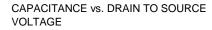
 $R_{DS(cn)}$ - Drain to Source On-state Resistance - Ω

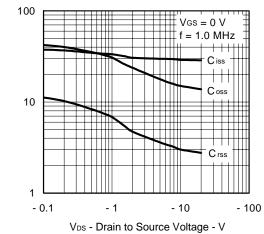
 $\mathsf{R}_{\mathsf{DS}(m)}$ - Drain to Source On-state Resistance - Ω

Ciss, Coss, Crss - Capacitance - pF

DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT

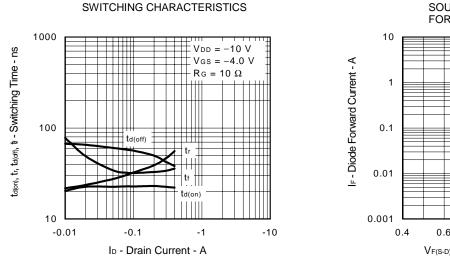




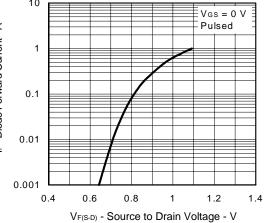


- 10

 $R_{DS(m)}$ - Drain to Source On-state Resistance - Ω



SOURCE TO DRAIN DIODE FORWARD VOLTAGE



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