Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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RENESAS

RQJ0602EGDQS

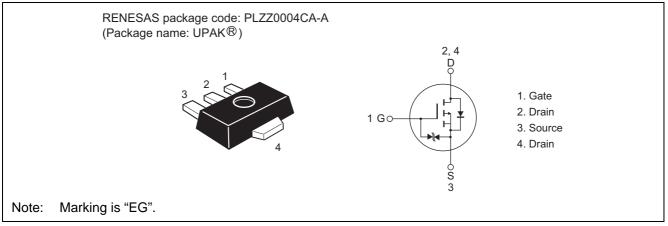
Silicon P Channel MOS FET Power Switching

> REJ03G1268-0300 Rev.3.00 Jun 05, 2006

Features

- Low on-resistance $R_{DS(on)} = 485 \text{ m}\Omega \text{ typ } (V_{GS} = -10 \text{ V}, I_D = -0.75 \text{ A})$
- Low drive current
- High speed switching
- 4.5 V gate drive

Outline



*UPAK is a trademark of Renesas Technology Corp.

Absolute Maximum Ratings

			$(Ta = 25^{\circ}C)$
Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	-60	V
Gate to source voltage	V _{GSS}	+10 / -20	V
Drain current	ID	-1.5	А
Drain peak current	Note1 I _{D (pulse)}	-2.2	А
Body - drain diode reverse drain current	I _{DR}	-1.5	А
Channel dissipation	Pch ^{Note2}	1.5	W
Channel dissipation	Pch (pulse) Note1	5	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. $PW \le 1 \text{ s}$, duty cycle $\le 1\%$

2. When using the glass epoxy board (FR-4: 40 x 40 x 1 mm)



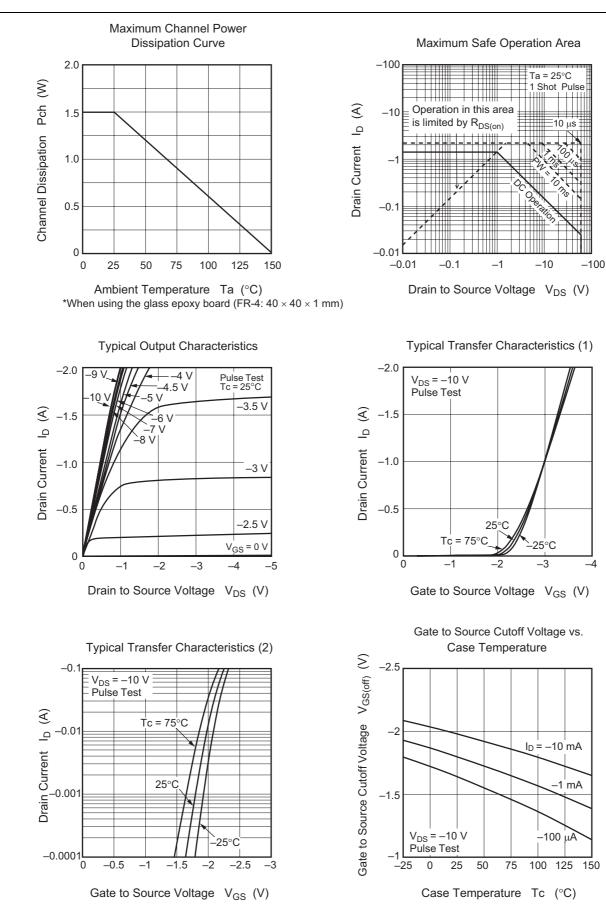
Electrical Characteristics

Item	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source breakdown voltage	V _{(BR)DSS}	-60	—		V	$I_D = -10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	V _{(BR)GSS}	+10	—		V	$I_{G} = +100 \ \mu A, V_{DS} = 0$
Gate to source breakdown voltage	V _{(BR)GSS}	-20	_		V	$I_{\rm G} = -100 \ \mu {\rm A}, \ V_{\rm DS} = 0$
Gate to source leak current	I _{GSS}	_	—	+10	μΑ	$V_{GS} = +8 V, V_{DS} = 0$
Gate to source leak current	I _{GSS}	_	—	-10	μΑ	$V_{GS} = -16 V, V_{DS} = 0$
Drain to source leak current	I _{DSS}	_	—	-1	μΑ	$V_{DS} = -60 \text{ V}, \text{ V}_{GS} = 0$
Gate to source cutoff voltage	V _{GS(off)}	-1.0	—	-2.0	V	$V_{DS} = -10 \text{ V}, \text{ I}_{D} = -1 \text{ mA}$
Drain to source on state resistance	R _{DS(on)}	_	485	607	mΩ	$I_D = -0.75 \text{ A}, V_{GS} = -10 \text{ V}^{\text{Note3}}$
	R _{DS(on)}		620	868	mΩ	$I_D = -0.75 \text{ A}, V_{GS} = -4.5 \text{ V}^{Note3}$
Forward transfer admittance	y _{fs}	0.8	1.4		S	$I_D = -0.75 \text{ A}, V_{DS} = -10 \text{ V}^{\text{Note3}}$
Input capacitance	Ciss	_	135	—	pF	$V_{DS} = -10 V$ $V_{GS} = 0$ $f = 1 MHz$
Output capacitance	Coss		24	_	pF	
Reverse transfer capacitance	Crss	_	12		pF	
Turn - on delay time	t _{d(on)}	_	11	—	ns	$I_{D} = -1 A$ $V_{GS} = -10 V$ $R_{L} = 6.6 \Omega$ $Rg = 4.7 \Omega$
Rise time	tr	_	28	—	ns	
Turn - off delay time	t _{d(off)}	_	29	—	ns	
Fall time	t _f	_	3.6	—	ns	
Total gate charge	Qg		2.9	_	nC	$V_{DD} = -10 V$ $V_{GS} = -10 V$ $I_D = -1.5 A$
Gate to source charge	Qgs		0.6	—	nC	
Gate to drain charge	Qgd		0.3	_	nC	
Body - drain diode forward voltage	V _{DF}	_	-0.9	_	V	$I_F = -1.5 \text{ A}, V_{GS} = 0^{\text{Note3}}$

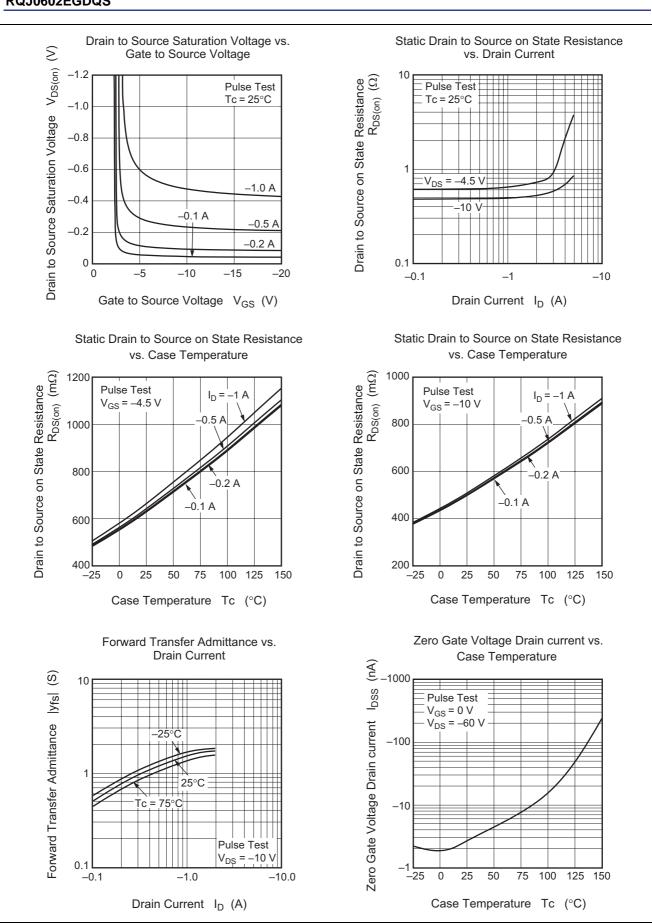
Notes: 3. Pulse test



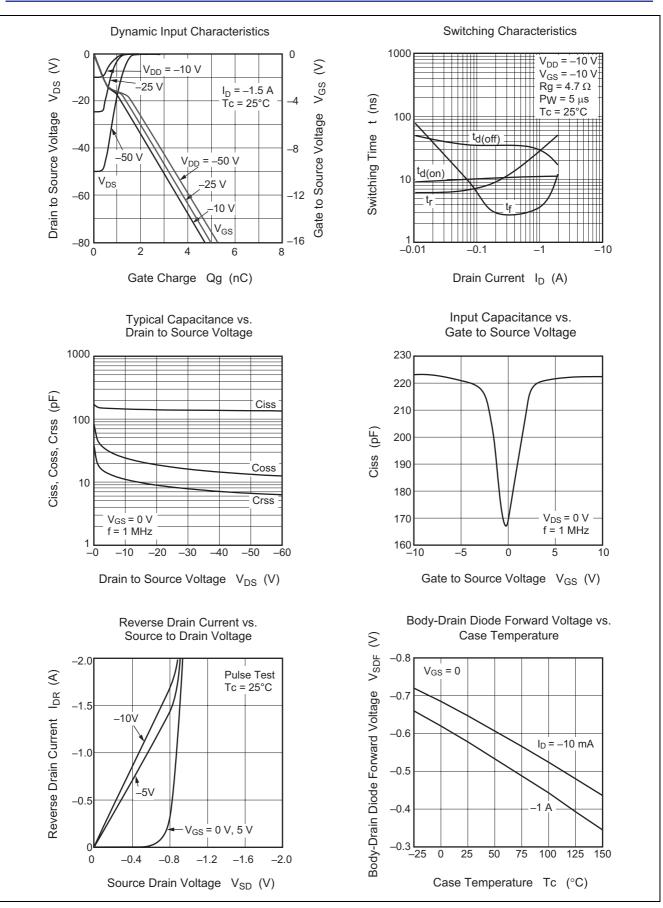
Main Characteristics





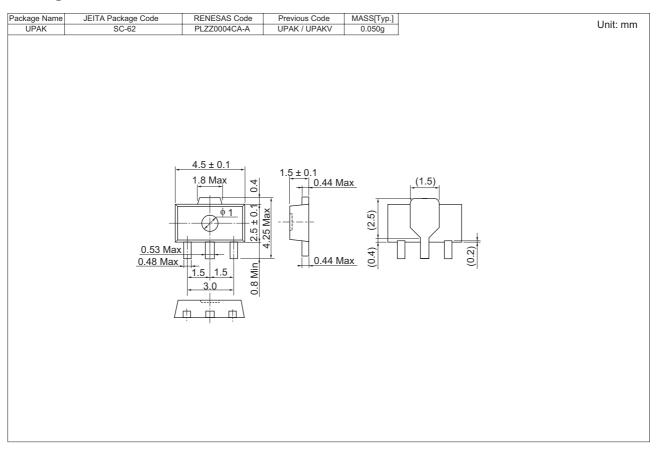








Package Dimensions



Ordering Information

Part Name	Quantity	Shipping Container
RQJ0602EGDQSTL-E	1000 pcs.	φ178 reel, 12 mm Emboss taping



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