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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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RQJ0306FQDQS

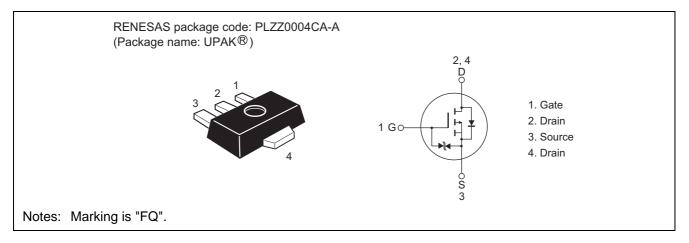
Silicon P Channel MOS FET Power Switching

> REJ03G1780-0100 Rev.1.00 Mar 16, 2009

Features

- Low gate drive V_{DSS} : -30 V and 2.5 V gate drive
- Low drive current
- High speed switching
- Small traditional power package (UPAK)

Outline



Absolute Maximum Ratings

			$(Ta = 25^{\circ}C)$
Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	-30	V
Gate to source voltage	V _{GSS}	+8 / -12	V
Drain current	ID	-4	A
Drain peak current	Note1 D(pulse)	-16	A
Body - drain diode reverse drain current	I _{DR}	4	A
Channel dissipation	Pch Note2	1.5	W
Thermal resistance	Rth(ch-a) Note2	83	°C /W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	–55 to +150	°C

Notes: 1. $PW \le 10 \ \mu s$, Duty cycle $\le 1\%$

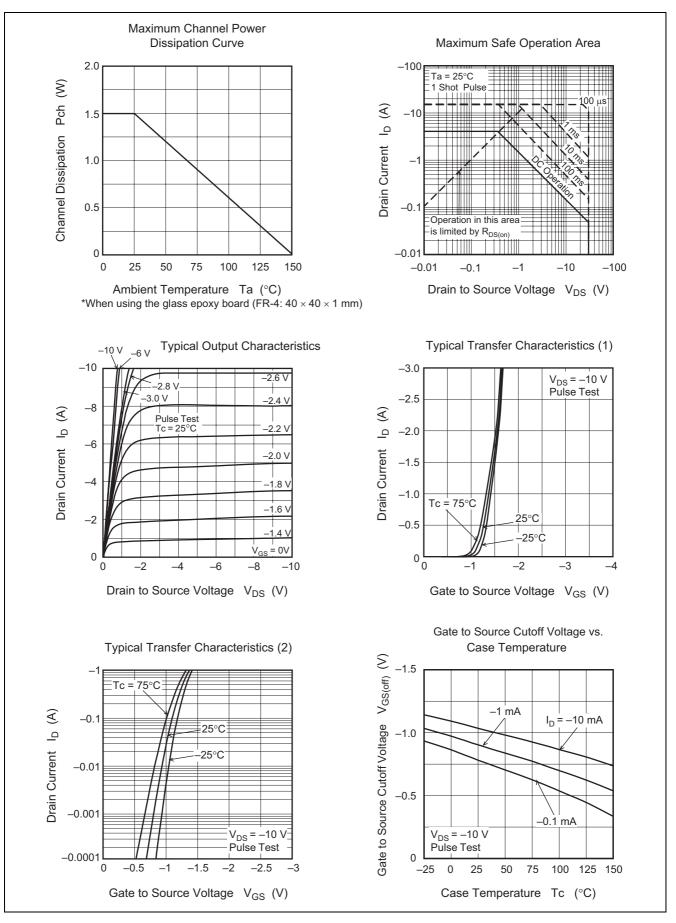
2. When using the glass epoxy board (FR-4 $40 \times 40 \times 1$ mm)

Electrical Characteristics

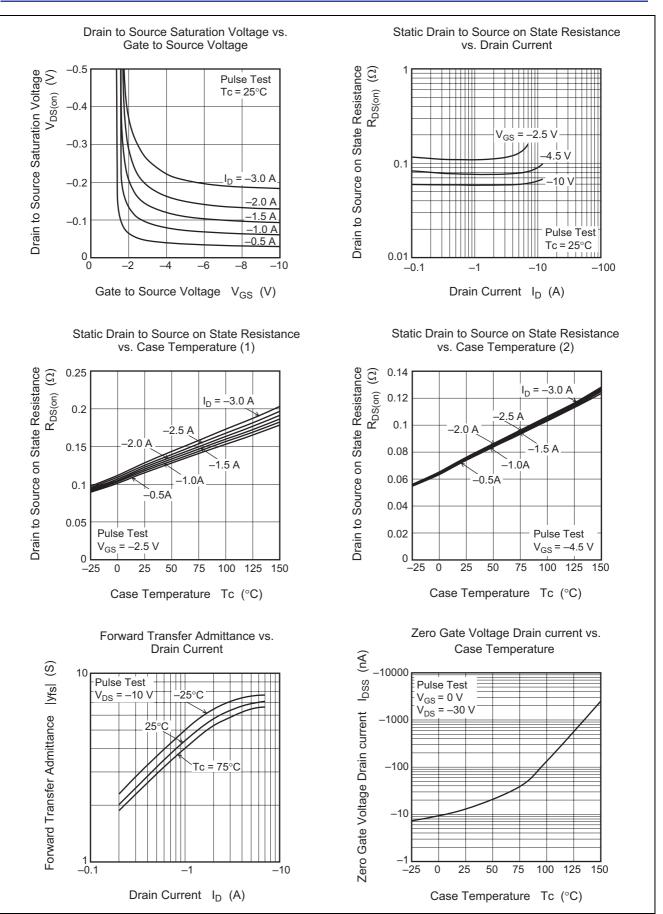
				-	-	$(Ta = 25^{\circ}C)$
Item	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source breakdown voltage	V _{(BR)DSS}	-30	_	—	V	$I_D = -10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	V _{(BR)GSS}	+8		—	V	$I_{G} = +100 \ \mu A, V_{DS} = 0$
Gate to source breakdown voltage	V _{(BR)GSS}	-12	_	—	V	$I_G = -100 \ \mu A, \ V_{DS} = 0$
Gate to source leak current	I _{GSS}			+10	μA	$V_{GS} = +6 V, V_{DS} = 0$
Gate to source leak current	I _{GSS}			-10	μA	$V_{GS} = -10 V, V_{DS} = 0$
Drain to source leak current	I _{DSS}			-1	μA	$V_{DS} = -30 V, V_{GS} = 0$
Gate to source cutoff voltage	V _{GS(off)}	-0.4		-1.4	V	$V_{DS} = -10 \text{ V}, I_D = -1 \text{ mA}$
Drain to source on state resistance	R _{DS(on)}	_	75	95	mΩ	$I_D = -2.0 \text{ A}, V_{GS} = -4.5 \text{ V}^{\text{Note3}}$
Drain to source on state resistance	R _{DS(on)}	_	120	165	mΩ	$I_D = -2.0 \text{ A}, V_{GS} = -2.5 \text{ V}^{\text{Note3}}$
Forward transfer admittance	y _{fs}	4.5	6.0	—	S	$I_D = -2.0 \text{ A}, V_{DS} = -10 \text{ V}^{\text{Note3}}$
Input capacitance	Ciss		510	_	pF	$V_{DS} = -10 V, V_{GS} = 0,$
Output capacitance	Coss		100	_	pF	f = 1 MHz
Reverse transfer capacitance	Crss		58	_	pF	
Turn - on delay time	t _{d(on)}		18	_	ns	$I_{D} = -1.5 \text{ A}$
Rise time	tr		48	_	ns	$V_{GS} = -4.5 V$
Turn - off delay time	t _{d(off)}		47	_	ns	$R_L = 6.7 \Omega$
Fall time	t _f		13	_	ns	$R_g = 4.7 \Omega$
Total gate charge	Qg		4.8	_	nC	$V_{DD} = -10 V$
Gate to Source charge	Qgs		0.8	_	nC	$V_{GS} = -4.5 V$
Gate to drain charge	Qgd		1.8	—	nC	$I_{D} = -3.0 \text{ A}$
Body - drain diode forward voltage	V _{DF}	_	-0.9	-1.3	V	$I_F = -4.0 \text{ A}, V_{GS} = 0^{Note3}$

Notes: 3. Pulse test

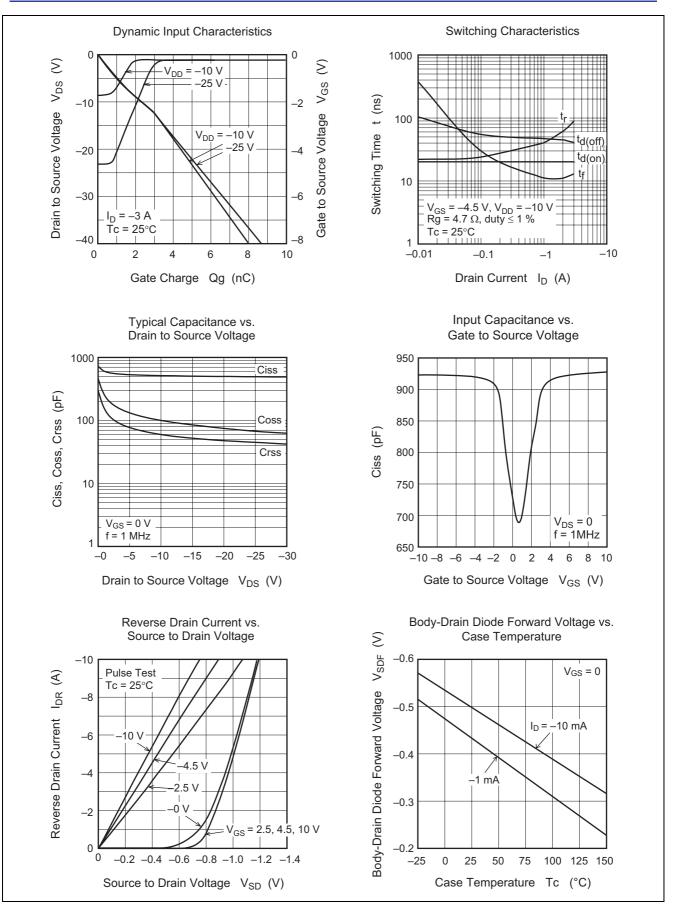
Main Characteristics



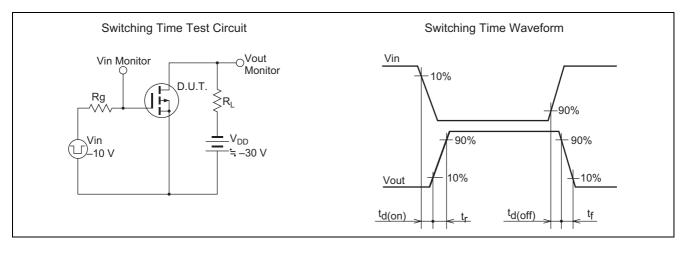
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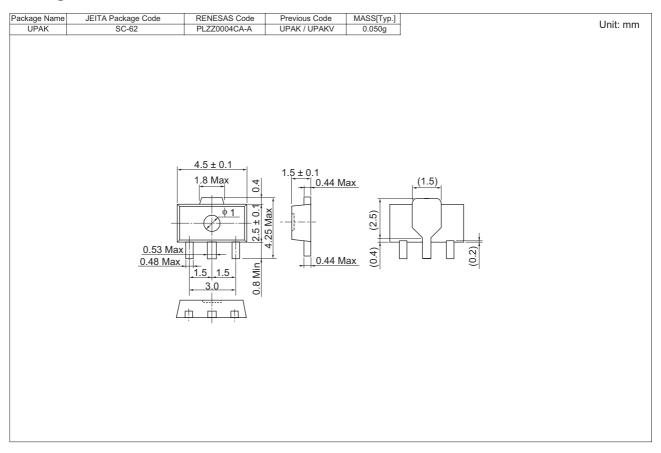
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Package Dimensions



Ordering Information

Part No.	Quantity	Shipping Container
RQJ0306FQDQSTL-E	1000 pcs.	¢178 mm reel, 12 mm Emboss taping

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