

# RJU65F27DWA / RJU65F27DWS

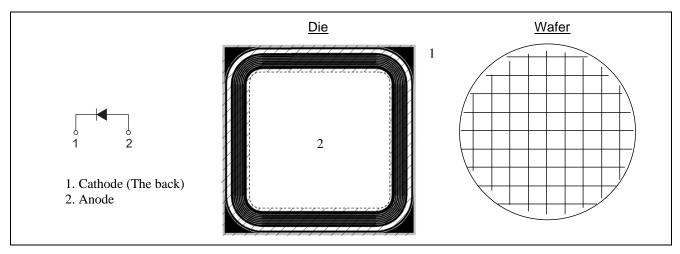
650V - 150A - Fast Recovery Diode Application: Inverter

R07DS1485EJ0201 Rev.2.01 Apr.10.2020

## Features

- Low forward voltage  $V_F = 1.7 \text{ V typ.}$  (at  $I_F = 150 \text{ A}$ ,  $Tc = 25^{\circ}C$ )
- Fast recovery (soft recovery)
  - $t_{rr} = 100 \text{ ns typ.}$  (at  $I_F = 100 \text{ A}$ ,  $di/dt = 100 \text{ A}/\mu\text{s}$ ,  $Tc = 25^{\circ}\text{C}$ )

#### Outline



#### **Absolute Maximum Ratings**

 $(Tc = 25^{\circ}C \text{ unless otherwise noted})$ 

			(	
Item		Symbol	Ratings	Unit
Maximum reverse voltage		Vrm	650	V
Forward current	Tc = 25°C	IF Notes1	300	А
	Tc = 100°C	IF Notes1	150	A
Junction temperature		Tj <sup>Notes2</sup>	175	°C

Notes: 1. Depends on thermal properties of assembly.

 Please use this device in the thermal condition which the junction temperature does not exceed 175 °C. IGBT Application Note is disclosed about reliability test and application condition up to Tj = 175 °C.

Continuous heavy condition (e.g. high temperature/voltage/current or high variation of temperature) may
affect a reliability even if it is within the absolute maximum ratings. Please consider derating condition for
appropriate reliability in reference Renesas Semiconductor Reliability Handbook (Recommendation for
Handling and Usage of Semiconductor Devices) and individual reliability data.



#### Electrical Characteristics (These data are an actual measurement value in a package.)

 $(Tc = 25^{\circ}C \text{ unless otherwise noted})$ 

ltem	Symbol	Min	Тур	Max	Unit	Test conditions
Forward voltage	VF		1.7	2.2	V	IF = 150 A Notes4, Notes5
Reverse current	IR			2	μA	$V_R = 650 \text{ V}^{\text{Notes6}}$
Reverse recovery time	trr		100		ns	I <sub>F</sub> = 100 A, di/dt = 100 A/µs Notes5, Notes7

Notes: 4. Pulse test

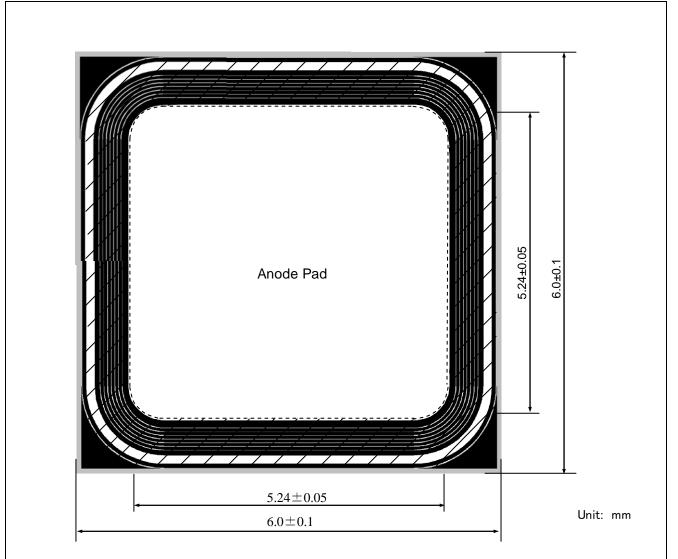
5. Designed target value on Renesas measurement condition. (Not tested)

6. Tested on wafer

7. Tested to be mounted on Renesas single test vehicle.



#### **Die Dimension**



Note 1 :

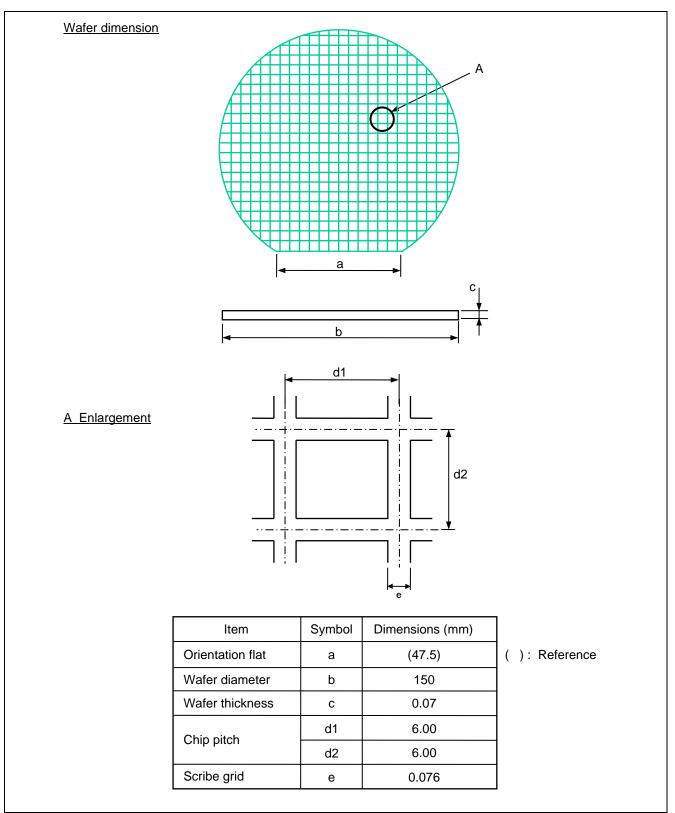
Illustration	Definition		
Part of white	AI pattern		
Part of dotted line	Bonding area		
Part of hatching	Final passivation		

Note 2: The back of the chip is processed with Au evaporation.

Note 3 : Recognition, target and any other patterns which are not related to FRD operation, may be changed without notice.



#### **Wafer Dimension**



## **Ordering Information**

Orderable Part Number	Shipment form		
RJU65F27DWA-00#W0	Unsawn wafer		
RJU65F27DWS-00#W0	Sawn wafer		



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