# RENESAS

# HD74LV1GW17A

# Dual Buffer with Schmitt Trigger Inputs

# R04DS0033EJ0300

Data Sheet

Rev.3.00 Jan 10, 2014

# Description

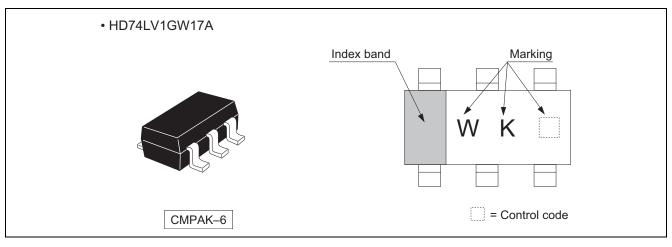
The HD74LV1GW17A has dual buffer with schmitt-trigger input in a 6 pin package. Low voltage and high-speed operation is suitable for the battery powered products (e.g., notebook computers), and the low power consumption extends the battery life.

# Features

- The basic gate function is lined up as Renesas uni logic series.
- Supplied on emboss taping for high-speed automatic mounting.
- Supply voltage range : 1.65 to 5.5 V Operating temperature range : -40 to +85°C
- All inputs  $V_{IH}$  (Max.) = 5.5 V (@V<sub>CC</sub> = 0 V to 5.5 V) All outputs V<sub>0</sub> (Max.) = 5.5 V (@V<sub>CC</sub> = 0 V)
- Output current  $\pm 6 \text{ mA}$  (@V<sub>CC</sub> = 3.0 V to 3.6 V),  $\pm 12 \text{ mA}$  (@V<sub>CC</sub> = 4.5 V to 5.5 V)
- All the logical input has hysteresis voltage for the slow transition.
- Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)	
HD74LV1GW17ACME	CMPAK-6 pin	PTSP0006JA-A (CMPAK-6V)	СМ	E (3,000 pcs / Reel)	

### **Outline and Article Indication**



## **Function Table**

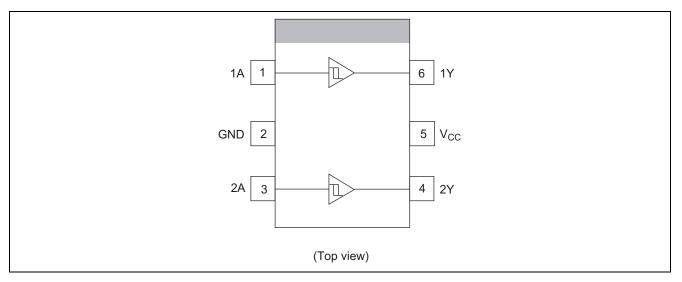
Input A	Output Y				
Н	Н				
L	L				

H : High level

L : Low level



### **Pin Arrangement**



### **Absolute Maximum Ratings**

Item	Symbol	Ratings	Unit	Test Conditions
Supply voltage range	V <sub>CC</sub>	-0.5 to 7.0	V	
Input voltage range <sup>*1</sup>	VI	-0.5 to 7.0	V	
Output voltage range *1, 2	N/	-0.5 to V <sub>CC</sub> + 0.5	- V	Output : H or L
Output voltage range	Vo	-0.5 to 7.0		V <sub>CC</sub> : OFF
Input clamp current	I <sub>IK</sub>	-20	mA	V <sub>1</sub> < 0
Output clamp current	Ι <sub>οκ</sub>	±50	mA	$V_0 < 0 \text{ or } V_0 > V_{CC}$
Continuous output current	lo	±25	mA	$V_0 = 0$ to $V_{CC}$
Continuous current through $V_{CC}$ or GND	I <sub>CC</sub> or I <sub>GND</sub>	±50	mA	
Maximum power dissipation at Ta = 25°C (in still air) $^{*3}$	PT	200	mW	
Storage temperature	Tstg	-65 to 150	°C	

Notes: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore no two of which may be realized at the same time.

1. The input and output voltage ratings may be exceeded if the input and output clamp-current ratings are observed.

2. This value is limited to 5.5 V maximum.

3. The maximum package power dissipation was calculated using a junction temperature of 150°C.



Item	Symbol	Min	Max	Unit	Conditions
Supply voltage range	V <sub>CC</sub>	1.65	5.5	V	
Input voltage range	VI	0	5.5	V	
Output voltage range	Vo	0	V <sub>CC</sub>	V	
		—	1		V <sub>CC</sub> = 1.65 to 1.95 V
	I <sub>OL</sub>	—	2		$V_{CC}$ = 2.3 to 2.7 V
		—	6		$V_{CC} = 3.0$ to 3.6 V
Output ourroat		—	12		$V_{CC} = 4.5$ to 5.5 V
Output current		—	-1	mA	V <sub>CC</sub> = 1.65 to 1.95 V
		_	-2		$V_{CC}$ = 2.3 to 2.7 V
	IOH	_	-6		$V_{CC} = 3.0$ to 3.6 V
		—	-12		$V_{CC} = 4.5$ to 5.5 V
Operating free-air temperature	Ta	-40	85	°C	

### **Recommended Operating Conditions**

Note: Unused or floating inputs must be held high or low.

### **Electrical Characteristics**

<b>K</b>		V 00 ±		-			$Ta = -40 \text{ to } 85^{\circ}C$
ltem	Symbol	V <sub>cc</sub> (V) *	Min	Тур	Max	Unit	Test condition
		1.65 to 1.95	—		V <sub>CC</sub> ×0.75		
	V <sub>T</sub> <sup>+</sup>	2.5	—		1.75		
		3.3	—	—	2.31		
		5.0	—		3.50		
		1.65 to 1.95	V <sub>CC</sub> ×0.25		—		
Threshold voltage	V <sub>T</sub> <sup>-</sup>	2.5	0.75		—	V	
Theshold voltage	VI	3.3	0.99		—	v	
		5.0	1.5		_		
		1.65 to 1.95	0.1	—	V <sub>CC</sub> ×0.4		
	$\Delta V_T$	2.5	0.25	—	1.0		
		3.3	0.33	_	1.32		
		5.0	0.5	_	2.0		
		Min to Max	V <sub>CC</sub> -0.1	_	—		I <sub>OH</sub> = -50 μA
		1.65	1.4	_	—		$I_{OH} = -1 \text{ mA}$
	V <sub>OH</sub>	2.3	2.0	_	—		I <sub>OH</sub> = -2 mA
		3.0	2.48	_	—		I <sub>OH</sub> =6 mA
Output valtage		4.5	3.8	_	—	V	I <sub>OH</sub> = -12 mA
Output voltage		Min to Max	—	_	0.1	v	I <sub>OL</sub> = 50 μA
		1.65	—	_	0.3		I <sub>OL</sub> = 1 mA
	V <sub>OL</sub>	2.3	—	_	0.4		$I_{OL} = 2 \text{ mA}$
		3.0	—	_	0.44		$I_{OL} = 6 \text{ mA}$
		4.5	—	_	0.55		I <sub>OL</sub> = 12 mA
Input current	I <sub>IN</sub>	0 to 5.5	—	_	±1	μA	$V_{IN} = 5.5 \text{ V or GND}$
Quiescent supply current	I <sub>CC</sub>	5.5	_		10	μΑ	$V_{IN} = V_{CC} \text{ or } GND,$ $I_O = 0$
Output leakage current	I <sub>OFF</sub>	0	_	_	5	μA	$V_{IN}$ or $V_O = 0$ to 5.5 V
Input capacitance	CIN	3.3		3.0		pF	$V_{IN} = V_{CC}$ or GND

Note: For conditions shown as Min or Max, use the appropriate values under recommended operating conditions.



# **Switching Characteristics**

#### $V_{CC}=1.8\pm0.15~V$

ltom	Symbol		Ta = 25°C		Ta = -40	-40 to 85°C		Test	FROM	то
ltem	Symbol	Min	Тур	Max	Min	Max	Unit	Conditions	(Input)	(Output)
Propagation	t <sub>PLH</sub>	_	16.8	32.0	1.0	34.0	20	C <sub>L</sub> = 15 pF	٨	V
delay time	t <sub>PHL</sub>	_	23.8	43.0	1.0	46.0	ns	C <sub>L</sub> = 50 pF	A	ſ

 $V_{CC}=2.5\pm0.2~V$ 

ltom	Symbol		Ta = 25°C		Ta = -40	) to 85°C	l lmit	Test	FROM	то
ltem	Symbol	Min	Тур	Max	Min	Max	Unit	Conditions	(Input)	(Output)
Propagation	t <sub>PLH</sub>	_	10.5	19.7	1.0	22.0	20	C <sub>L</sub> = 15 pF	٨	V
delay time	t <sub>PHL</sub>		14.0	24.0	1.0	27.0	ns	C <sub>L</sub> = 50 pF	A	ſ

$V_{CC} =$	33	+	Δ	3	V
V CC -	5.5	Ŧ	υ.	5	v

Itom	Symbol		Ta = 25°C		Ta = -40	) to 85°C	Unit	Test	FROM	то
nem	Item Symbol	Min	Тур	Max	Min	Max	Unit	Conditions	(Input)	(Output)
Propagation	t <sub>PLH</sub>	_	8.3	12.8	1.0	15.0	20	C <sub>L</sub> = 15 pF	٨	V
delay time	t <sub>PHL</sub>	—	10.8	16.3	1.0	18.5	ns	$C_L = 50 \text{ pF}$	А	Ŷ

$V_{CC} =$	5.0	± (	0.5	V
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ltom	Symbol		Ta = 25°C		Ta = -40	) to 85°C	Unit	Test	FROM	то
ltem	Symbol	Min	Тур	Max	Min	Max	Unit	Conditions	(Input)	(Output)
Propagation	t <sub>PLH</sub>	_	5.5	8.6	1.0	10.0	20	C <sub>L</sub> = 15 pF	٨	V
delay time	t <sub>PHL</sub>	—	7.0	10.6	1.0	12.0	ns	$C_L = 50 \text{ pF}$	A	ř

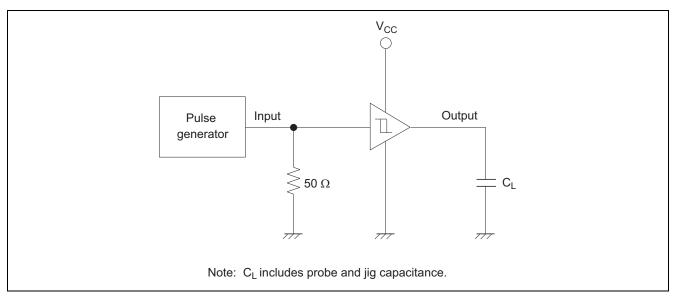
# **Operating Characteristics**

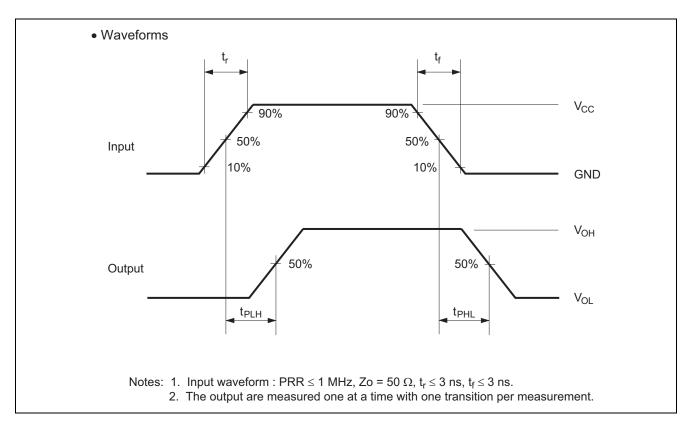
 $C_L = 50 \text{ pF}$ 

ltom	Item Symbol V <sub>cc</sub> (V)			Ta = 25°C			Test Conditions	
item	Symbol	V <sub>cc</sub> (V)	Min	Тур	Max Unit Tes		Test Conditions	
Power dissipation	<u> </u>	3.3	-	8.5		<b>~</b> Г	f = 10 MHz	
capacitance	C <sub>PD</sub>	5.0		10.0	_	р⊦		



## **Test Circuit**

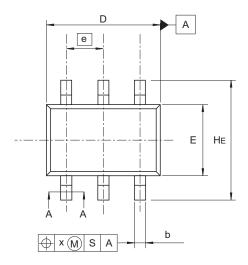


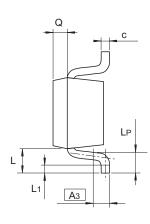


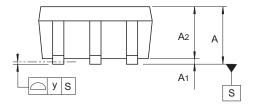


# Package Dimensions

JEITA Package Code	RENESAS Code	Previous Code	MASS (Typ) [g]
SC-88	PTSP0006JA-A	CMPAK-6 / CMPAK-6V	0.006









A-A Section

Reference	Dimensions in millimeters			
Symbol	Min	Nom	Max	
А	0.8		1.1	
A <sub>1</sub>	0		0.1	
A <sub>2</sub>	0.8	0.9	1.0	
A <sub>3</sub>	—	0.25		
b	0.15	0.2	0.25	
С	0.1	0.15	0.25	
D	1.8	2.0	2.2	
E	1.15	1.25	1.35	
е		0.65		
HE	2.0	2.1	2.2	
L	0.3		0.7	
L <sub>1</sub>	0.1		0.5	
LP	0.2		0.6	
Х			0.05	
у			0.05	
Q		0.25		

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