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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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RENESAS

HD74HC352

Dual 4-to-1-line Data Selectors/Multiplexers

REJ03D0611-0200 (Previous ADE-205-490) Rev.2.00 Jan 31, 2006

Description

Each of these data selectors/multiplexers contains inverters and drivers to supply fully complementary binary decoding data selection to the AND-OR-invert gates. Separate strobe inputs (G) are provided for each of the two four-line sections.

Features

- High Speed Operation: t_{pd} (Data to Y) = 16 ns typ ($C_L = 50 \text{ pF}$)
- High Output Current: Fanout of 10 LSTTL Loads
- Wide Operating Voltage: $V_{CC} = 2 \text{ to } 6 \text{ V}$
- Low Input Current: 1 µA max
- Low Quiescent Supply Current: I_{CC} (static) = 4 μ A max (Ta = 25°C)
- Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
HD74HC352RPEL	SOP-16 pin (JEDEC)	PRSP0016DG-A (FP-16DNV)	RP	EL (2,500 pcs/reel)

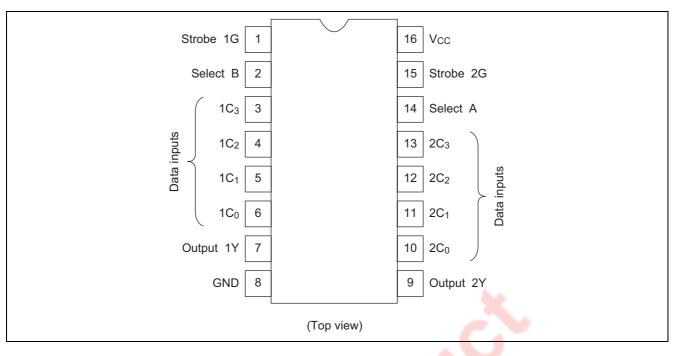
Function Table

Selec	t Input		Data		Strobe	Output	
В	Α	C ₀	C ₁	C ₂	C ₃	G	Y
Х	Х	Х	Х	Х	Х	Н	н
L	L	L	Х	Х	Х	L	Н
L	L	Н	X	Х	Х	L	L
L	н	X	L	Х	Х	L	Н
L	н	Х	Н	Х	Х	L	L
Н	L	Х	Х	L	Х	L	Н
Н	L	X	Х	н	Х	L	L
Н	Н	Х	Х	Х	L	L	Н
Н	Н	Х	Х	Х	Н	L	L

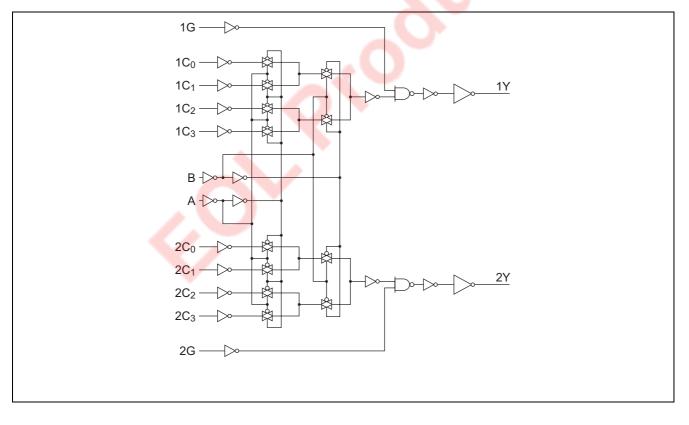
Select inputs A and B are common to both sections



Pin Arrangement



Logic Diagram





Absolute Maximum Ratings

Item	Symbol	Ratings	Unit
Supply voltage range	V _{CC}	-0.5 to 7.0	V
Input / Output voltage	V _{IN} , V _{OUT}	–0.5 to V _{CC} +0.5	V
Input / Output diode current	I _{IК} , I _{ОК}	±20	mA
Output current	lo	±25	mA
V _{CC} , GND current	I _{CC} or I _{GND}	±50	mA
Power dissipation	PT	500	mW
Storage temperature	Tstg	-65 to +150	°C

Note: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

Recommended Operating Conditions

Item	Symbol	Ratings	Unit	Conditions
Supply voltage	V _{CC}	2 to 6	V	
Input / Output voltage	V _{IN} , V _{OUT}	0 to V _{CC}	V	
Operating temperature	Та	-40 to 85	°C	
Input rise / fall time ^{*1}	t _r , t _f	0 to 1000	ns	V _{CC} = 2.0 V
		0 to 500		$V_{CC} = 4.5 V$
		0 to 400		V _{CC} = 6.0 V

Notes: 1. This item guarantees maximum limit when one input switches. Waveform: Refer to test circuit of switching characteristics.

Electrical Characteristics	
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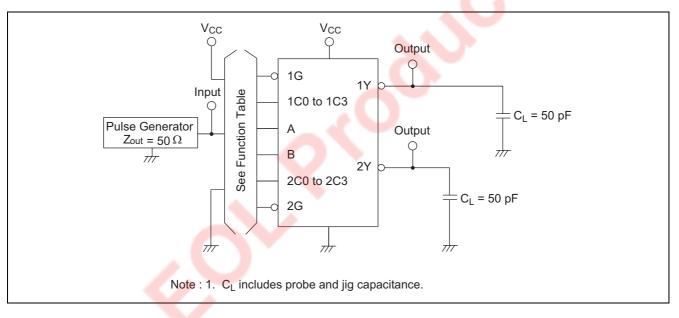
Item	Symbol		Т	a = 25°	С	Ta = -40	to+85°C	Unit	Test Conditions	
nem	Symbol	V _{cc} (V)	Min	Тур	Max	Min	Max	Unit	Test Cor	lations
Input voltage	VIH	2.0	1.5	-	$D \neq 0$	1.5	_	V		
		4.5	3.15	ľ	-	3.15	_			
		6.0	4.2		ł	4.2	_			
	VIL	2.0	ł		0.5	_	0.5	V		
		4.5	T		1.35	_	1.35			
		6.0	J		1.8	_	1.8			
Output voltage	V _{он}	2.0	1.9	2.0	_	1.9		V	$Vin = V_{IH} \text{ or } V_{IL}$	I _{OH} = -20 μA
		4.5	4.4	4.5	—	4.4				
		6.0	5.9	6.0	—	5.9	_			
		4.5	4.18		_	4.13				$I_{OH} = -4 \text{ mA}$
		6.0	5.68	_	_	5.63	_			I _{OH} = -5.2 mA
	V _{OL}	2.0		0.0	0.1	—	0.1	V	$Vin = V_{IH} \text{ or } V_{IL}$	I _{OL} = 20 μA
		4.5		0.0	0.1	_	0.1			
		6.0	_	0.0	0.1	_	0.1			
		4.5	_	_	0.26	—	0.33			I _{ОН} = 4 mA
		6.0	_	_	0.26	—	0.33			I _{OH} = 5.2 mA
Input current	lin	6.0		_	±0.1	—	±1.0	μΑ	Vin = V _{CC} or GND	
Quiescent supply current	I _{CC}	6.0	_		4.0	_	40	μA	$Vin = V_{CC} \text{ or } GN$	D, Iout = $0 \mu A$

Switching Characteristics

 $(C_L = 50 \text{ pF}, \text{ Input } t_r = t_f = 6 \text{ ns})$

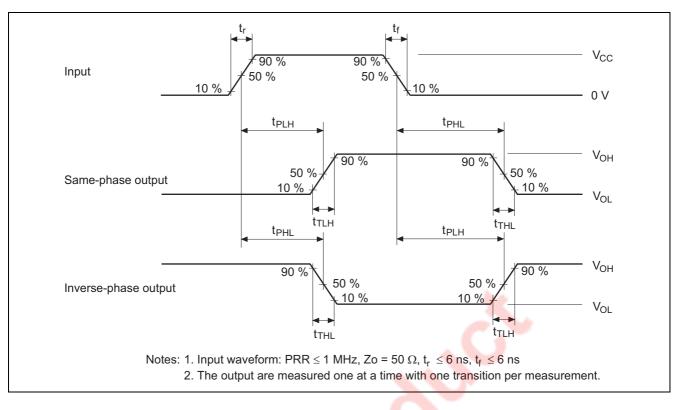
Item	Symbol	Symbol	V., 00	Т	a = 25°	С	Ta = -40 to		Unit	Test Conditions
nem		V _{cc} (V)	Min	Тур	Max	Min	Max			
Propagation delay	t _{PLH}	2.0	_	_	125	—	155	ns	Data to Y	
time	t _{PHL}	4.5	—	16	25	—	31			
		6.0	_	_	21	_	26			
		2.0	_	_	160	_	200	ns	A or B to Y	
		4.5	_	18	32	_	40			
		6.0	_	_	27	_	34			
		2.0	_	_	100	_	125	ns	G to Y	
		4.5	_	10	20	_	25			
		6.0	_	_	17	_	21			
Output rise/fall	t _{TLH}	2.0	_	_	75	—	95	ns		
time	t _{THL}	4.5	_	5	15	—	19			
		6.0		_	13	—	16			
Input capacitance	Cin	—	—	5	10	—	10	рF		

Test Circuit



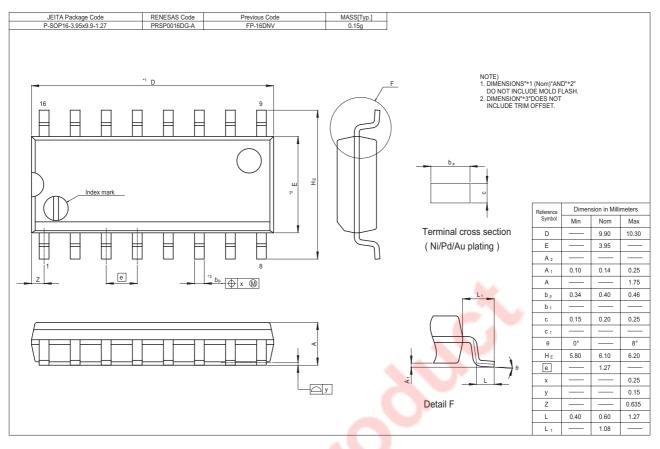


Waveforms





Package Dimensions





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