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April 1st, 2010 Renesas Electronics Corporation

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RENESAS

HD74HC240

Octal Buffers/Line Drivers/Line Receivers (with inverted 3-state outputs)

REJ03D0594-0200 (Previous ADE-205-471) Rev.2.00 Jan 31, 2006

Description

The HD74HC240 is an inverting buffer and has two active low enables ($1\overline{G}$ and $2\overline{G}$). Each enable independently controls 4 buffers. This device does not have schmitt trigger inputs.

Features

- High Speed Operation: $t_{pd} = 10 \text{ ns typ} (C_L = 50 \text{ pF})$
- High Output Current: Fanout of 15 LSTTL Loads
- Wide Operating Voltage: $V_{CC} = 2 \text{ to } 6 \text{ V}$
- Low Input Current: 1 µA max
- Low Quiescent Supply Current: I_{CC} (static) = 4 μ A max (Ta = 25°C)
- Ordering Information

| Part Name | Package Type | Package Code (Previous Code) | Package Abbreviation | Taping Abbreviation (Quantity) |
|---------------|--------------------|---------------------------------|-------------------------|-----------------------------------|
| HD74HC240P | DILP-20 pin | PRDP0020AC-B (DP-20NEV) | Ρ | _ |
| HD74HC240FPEL | SOP-20 pin (JEITA) | PRSP0020DD-B (FP-20DAV) | FP | EL (2,000 pcs/reel) |
| HD74HC240RPEL | SOP-20 pin (JEDEC) | PRSP0020DC-A (FP-20DBV) | RP | EL (1,000 pcs/reel) |
| HD74HC240TELL | TSSOP-20 pin | PTSP0020JB-A (TTP-20DAV) | т | ELL (2,000 pcs/reel) |

Note: Please consult the sales office for the above package availability.

Function Table

| Inp | Output | | |
|-----|--------|---|--|
| G | А | Y | |
| Н | Х | Z | |
| L | Н | L | |
| L | L | Н | |

H : high level

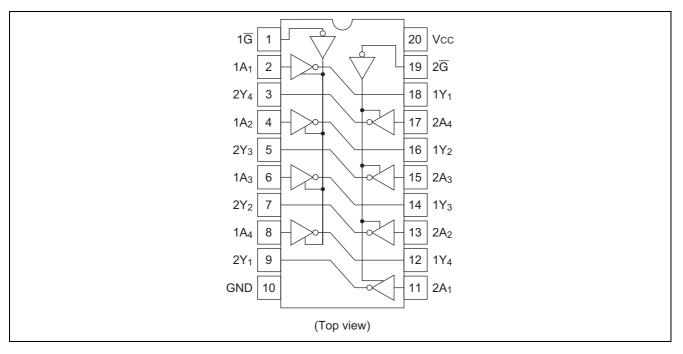
L : low level

X : irrelevant

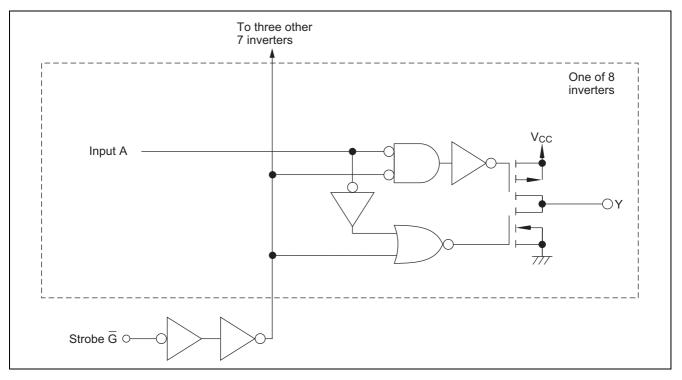
Z : off (high-impedance) state of a 3-state output



Pin Arrangement



Logic Diagram





Absolute Maximum Ratings

| Item | Symbol | Ratings | Unit |
|-------------------------------|-------------------------------------|------------------------------|------|
| Supply voltage range | V _{CC} | -0.5 to 7.0 | V |
| Input / Output voltage | V _{IN} , V _{OUT} | –0.5 to V _{CC} +0.5 | V |
| Input / Output diode current | I _{IК} , I _{ОК} | ±20 | mA |
| Output current | lo | ±35 | mA |
| V _{CC} , GND current | I _{CC} or I _{GND} | ±75 | mA |
| Power dissipation | PT | 500 | mW |
| Storage temperature | Tstg | -65 to +150 | °C |

Note: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

Recommended Operating Conditions

| Item | Symbol | Ratings | Unit | Conditions | |
|--------------------------------------|------------------------------------|----------------------|------|------------------|--|
| Supply voltage | V _{CC} | 2 to 6 | V | | |
| Input / Output voltage | V _{IN} , V _{OUT} | 0 to V _{CC} | V | | |
| Operating temperature | Та | -40 to 85 | °C | | |
| Input rise / fall time ^{*1} | t _r , t _f | 0 to 1000 | ns | $V_{CC} = 2.0 V$ | |
| | | 0 to 500 | | $V_{CC} = 4.5 V$ | |
| | | 0 to 400 | | $V_{CC} = 6.0 V$ | |

Notes: 1. This item guarantees maximum limit when one input switches. Waveform: Refer to test circuit of switching characteristics.

Electrical Characteristics

| ltom | Symbol | V 00 | Т | a = 25° | С | Ta = -40 | to+85°C | Unit | Test Conditions | |
|-----------------------------|-----------------|---------------------|------|---------|------|----------|---------|------|---|----------------------------|
| ltem | Symbol | V _{cc} (V) | Min | Тур | Max | Min | Max | | Test Cor | ations |
| Input voltage | VIH | 2.0 | 1.5 | _ | — | 1.5 | — | V | | |
| | | 4.5 | 3.15 | _ | - | 3.15 | — | | | |
| | | 6.0 | 4.2 | _ | - | 4.2 | — | | | |
| | V _{IL} | 2.0 | _ | _ | 0.5 | — | 0.5 | V | | |
| | | 4.5 | _ | _ | 1.35 | — | 1.35 | | | |
| | | 6.0 | | | 1.8 | | 1.8 | | | |
| Output voltage | V _{OH} | 2.0 | 1.9 | 2.0 | - | 1.9 | _ | V | $Vin = V_{IH} \text{ or } V_{IL}$ | $I_{OH} = -20 \ \mu A$ |
| | | 4.5 | 4.4 | 4.5 | - | 4.4 | _ | | | |
| | | 6.0 | 5.9 | 6.0 | - | 5.9 | _ | | | |
| | | 4.5 | 4.18 | | - | 4.13 | _ | | | $I_{OH} = -6 \text{ mA}$ |
| | | 6.0 | 5.68 | _ | - | 5.63 | — | | | $I_{OH} = -7.8 \text{ mA}$ |
| | V _{OL} | 2.0 | _ | 0.0 | 0.1 | — | 0.1 | V | $Vin = V_{IH} \text{ or } V_{IL}$ | $I_{OL} = 20 \ \mu A$ |
| | | 4.5 | | 0.0 | 0.1 | _ | 0.1 | | | |
| | | 6.0 | _ | 0.0 | 0.1 | — | 0.1 | | | |
| | | 4.5 | _ | _ | 0.26 | — | 0.33 | | | $I_{OL} = 6 \text{ mA}$ |
| | | 6.0 | | | 0.26 | _ | 0.33 | | | I _{OL} = 7.8 mA |
| Off-state output | l _{oz} | 6.0 | _ | _ | ±0.5 | — | ±5.0 | μΑ | $Vin = V_{IH} \text{ or } V_{IL},$ | |
| current | | | | | | | | | Vout = V _{CC} or GND | |
| Input current | lin | 6.0 | — | — | ±0.1 | — | ±1.0 | μA | Vin = V _{CC} or GND | |
| Quiescent supply current | I _{CC} | 6.0 | _ | — | 4.0 | _ | 40 | μA | Vin = V_{CC} or GND, lout = 0 μ A | |



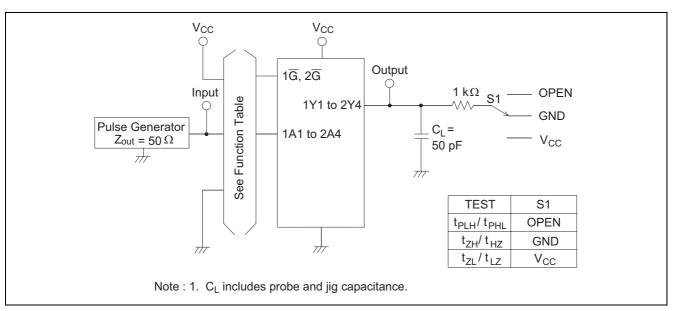
Switching Characteristics

 $(C_L = 50 \text{ pF}, \text{ Input } t_r = t_f = 6 \text{ ns})$

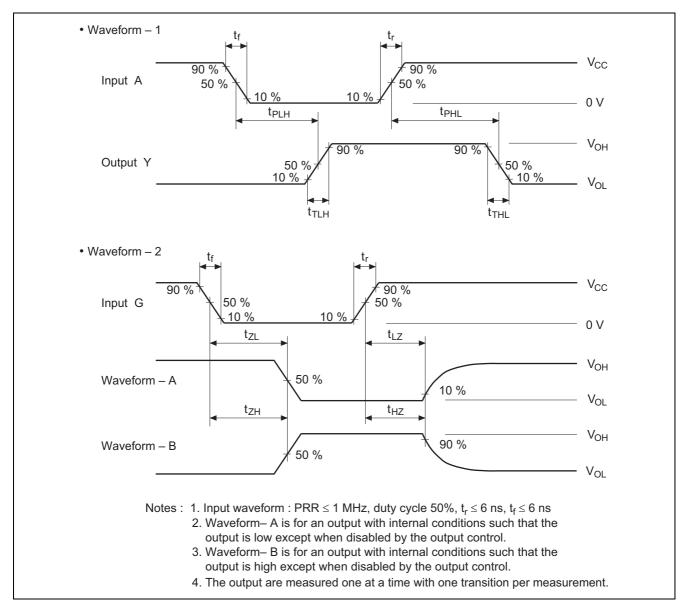
| ltem | Symbol | V _{cc} (V) | Ta = 25°C | | Ta = -40 to +85°C | | 11 | Test Conditions | |
|--------------------|------------------|---------------------|-----------|-----|-------------------|-----|-----|-----------------|-----------------|
| | | | Min | Тур | Max | Min | Max | Unit | Test Conditions |
| Propagation delay | t _{PHL} | 2.0 | _ | _ | 90 | — | 115 | ns | |
| time | | 4.5 | _ | 10 | 18 | — | 23 | | |
| | | 6.0 | | | 15 | — | 20 | | |
| | t _{PLH} | 2.0 | | | 90 | — | 115 | ns | |
| | | 4.5 | | 10 | 18 | — | 23 | | |
| | | 6.0 | | | 15 | — | 20 | | |
| Output enable time | t _{ZL} | 2.0 | | | 150 | — | 190 | ns | |
| | | 4.5 | | 11 | 30 | — | 38 | | |
| | | 6.0 | | | 26 | _ | 33 | | |
| | t _{ZH} | 2.0 | | | 150 | — | 190 | ns | |
| | | 4.5 | l | 12 | 30 | — | 38 | | |
| | | 6.0 | l | | 26 | — | 33 | | |
| Output disable | t _{LZ} | 2.0 | l | | 150 | — | 190 | ns | |
| time | | 4.5 | l | 16 | 30 | — | 38 | | |
| | | 6.0 | | _ | 26 | — | 33 | | |
| | t _{HZ} | 2.0 | | _ | 150 | — | 190 | ns | |
| | | 4.5 | | 19 | 30 | — | 38 | | |
| | | 6.0 | l | | 26 | — | 33 | | |
| Output rise/fall | t _{TLH} | 2.0 | | | 60 | — | 75 | ns | |
| time | t _{THL} | 4.5 | | 4 | 12 | — | 15 | | |
| | | 6.0 | — | _ | 10 | — | 13 | | |
| Input capacitance | Cin | — | | 5 | 10 | — | 10 | pF | |



Test Circuit

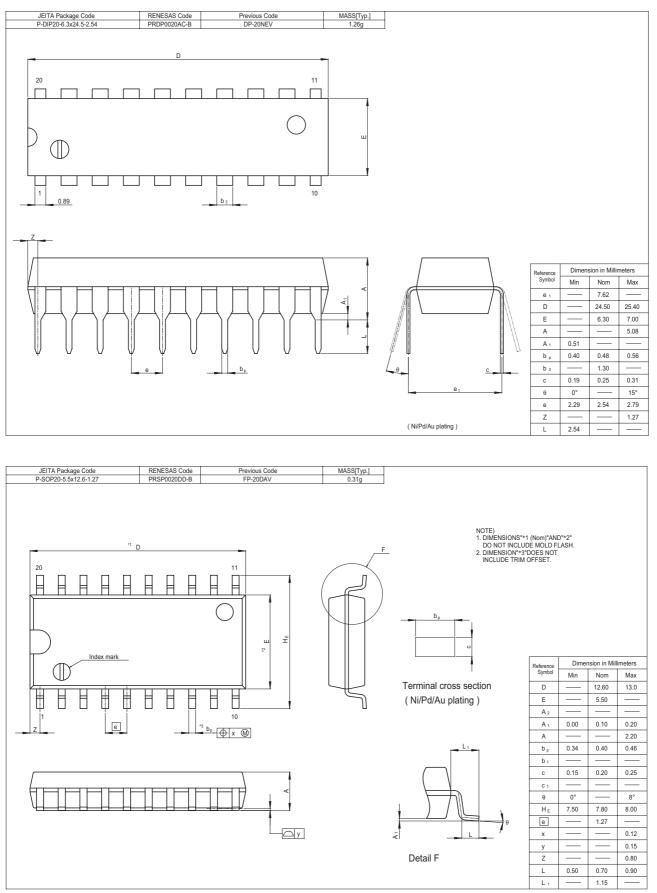


Waveforms



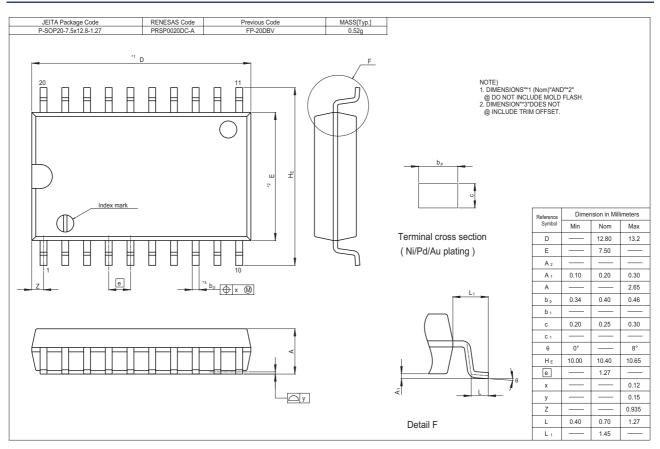


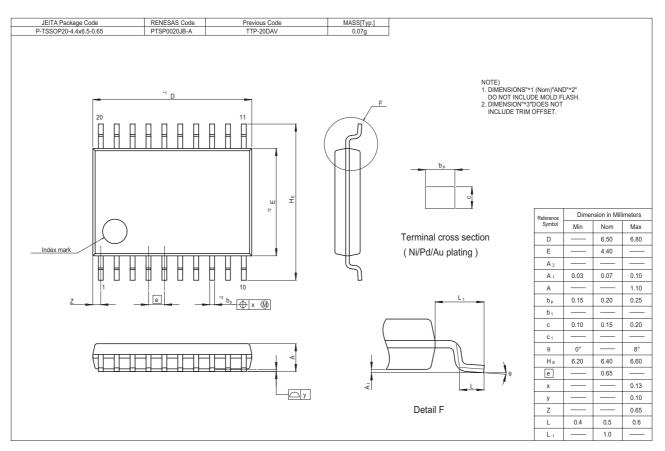
Package Dimensions





HD74HC240







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