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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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HD74HC10 Triple 3-input NAND Gates

REJ03D0538-0200 (Previous ADE-205-410) Rev.2.00 Oct 06, 2005

Features

- High Speed Operation: $t_{pd} = 10.5$ ns typ ($C_L = 50$ pF)
- High Output Current: Fanout of 10 LSTTL Loads
- Wide Operating Voltage: $V_{CC} = 2 \text{ to } 6 \text{ V}$
- Low Input Current: 1 µA max
- Low Quiescent Supply Current: I_{CC} (static) = 1 μ A max (Ta = 25°C)
- Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
HD74HC10P	DILP-14 pin	PRDP0014AB-B (DP-14AV)	Р	_
HD74HC10FPEL	SOP-14 pin (JEITA)	PRSP0014DF-B (FP-14DAV)	FP	EL (2,000 pcs/reel)
HD74HC10RPEL	SOP-14 pin (JEDEC)	PRSP0014DE-A (FP-14DNV)	RP	EL (2,500 pcs/reel)

Note: Please consult the sales office for the above package availability.

Function Table

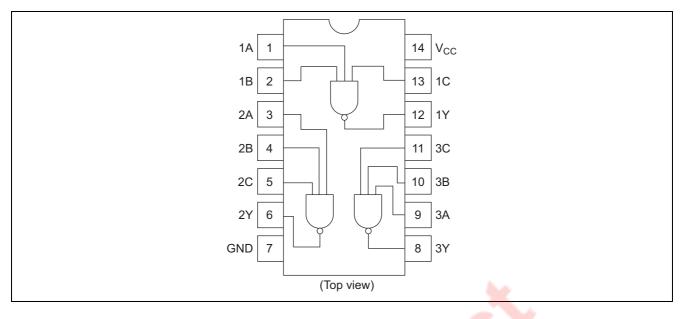
	Inputs								
A	В	С	Y						
L	L	L	Н						
Н	L	L	Н						
L	Н	L	Н						
Н	Н	L	Н						
L	L	Н	Н						
н		Н	Н						
L	Н	Н	Н						
Н	Н	Н	L						

H: High level

L: Low level



Pin Arrangement



Absolute Maximum Ratings

ltem	Symbol	Patinga	Unit
nem	Symbol	Ratings	Unit
Supply voltage range	Vcc	-0.5 to 7.0	V
Input / Output voltage	Vin, Vout	– <mark>0.5</mark> to V _{CC} +0.5	V
Input / Output diode current	I _{ік} , I _{ок}	±20	mA
Output current	lo	±25	mA
V _{CC} , GND current	I _{CC} or I _{GND}	±50	mA
Power dissipation	PT	500	mW
Storage temperature	Tstg	-65 to +150	°C

Note: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

Recommended Operating Conditions

Item	Symbol	nbol Ratings		Conditions
Supply voltage	Vcc	2 to 6	V	
Input / Output voltage	VIN, VOUT	0 to V _{CC}	V	
Operating temperature	Та	-40 to 85	°C	
		0 to 1000		V _{CC} = 2.0 V
Input rise / fall time ^{*1}	t _r , t _f	0 to 500	ns	$V_{CC} = 4.5 V$
		0 to 400		V _{CC} = 6.0 V

Note: 1. This item guarantees maximum limit when one input switches. Waveform: Refer to test circuit of switching characteristics.



			Т	a = 25°	С	Ta = -40 to+85°C				
ltem	Symbol	V _{cc} (V)	Min	Тур	Max	Min	Max	Unit	Test Cor	nditions
Input voltage	VIH	2.0	1.5	_	_	1.5	—	V		
		4.5	3.15	_	—	3.15	_			
		6.0	4.2	_	—	4.2	_			
	VIL	2.0	_	_	0.5		0.5	V		
		4.5	_	_	1.35		1.35			
		6.0	_	_	1.8	_	1.8			
Output voltage	V _{OH}	2.0	1.9	2.0	_	1.9	—	V	$Vin = V_{IH} \text{ or } V_{IL}$	I _{OH} = –20 µА
		4.5	4.4	4.5	—	4.4	_			
		6.0	5.9	6.0	—	5.9	_			
		4.5	4.18	_	—	4.13	—			I _{ОН} = -4 mA
		6.0	5.68	_	_	5.63	_			I _{OH} = -5.2 mA
	V _{OL}	2.0	_	0.0	0.1		0.1	V	$Vin = V_{IH} \text{ or } V_{IL}$	I _{OL} = 20 μA
		4.5	_	0.0	0.1	_	0.1			
		6.0	_	0.0	0.1		0.1			
		4.5	_	_	0.26	_	0.33			$I_{OL} = 4 \text{ mA}$
		6.0	_	_	0.26		0.33			I _{OL} = 5.2 mA
Input current	lin	6.0			±0.1		±1.0	μA	Vin = V _{CC} or GN	ID
Quiescent supply	Icc	6.0	_	_	1.0		10	μA	Vin = V_{CC} or GN	ID, Iout = $0 \mu A$
current										

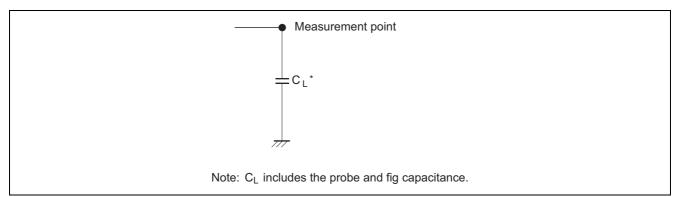
Electrical Characteristics

Switching Characteristics ($C_L = 50 \text{ pF}$, Input $t_r = t_f = 6 \text{ ns}$)

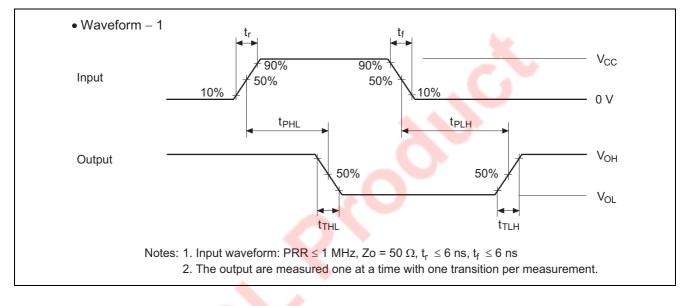
			Ta = 25°C		Ta = -40 to +85°C				
ltem	Symbol	V _{cc} (V)	Min	Тур	Max	Min	Max	Unit	Test Conditions
Propagation delay	t _{PLH}	2.0	_	-	100	-	125	ns	
time		4.5	_	11	20	-	25		
		6.0		_	17	—	21		
	t _{PHL}	2.0	<u></u>	_	100		115	ns	
		4.5	-	10	20		25		
		6.0	-	1	17		21		
Output rise time	t _{тLH}	2.0	-	_	75		95	ns	
		4.5	4	5	15	—	19		
		6.0	_	_	13	_	16		
Output fall time	t _{THL}	2.0	_	_	75		95	ns	
		4.5		5	15		19		
		6.0		_	13		16		
Input capacitance	Cin	—		5	10		10	pF	



Test Circuit

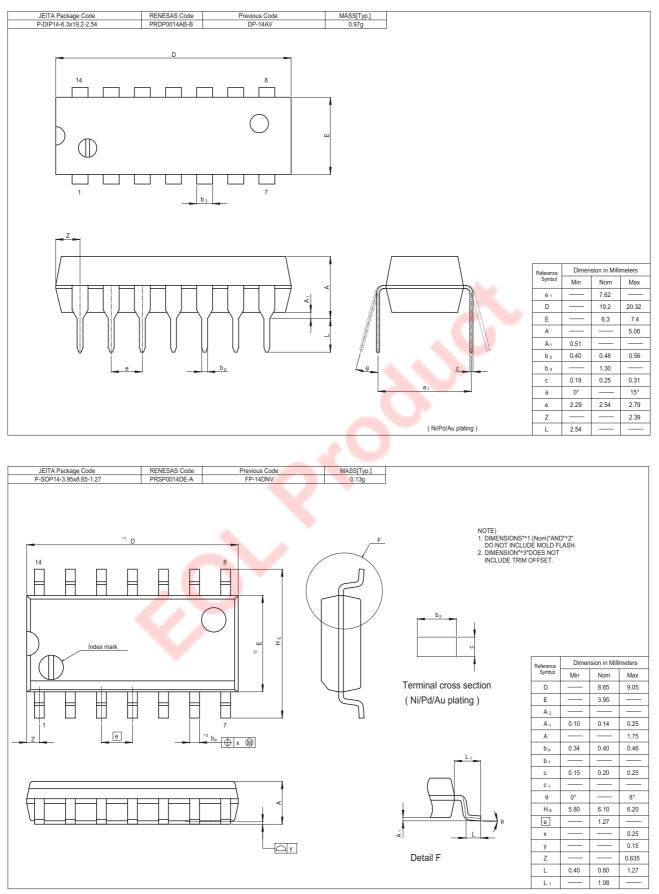


Waveforms



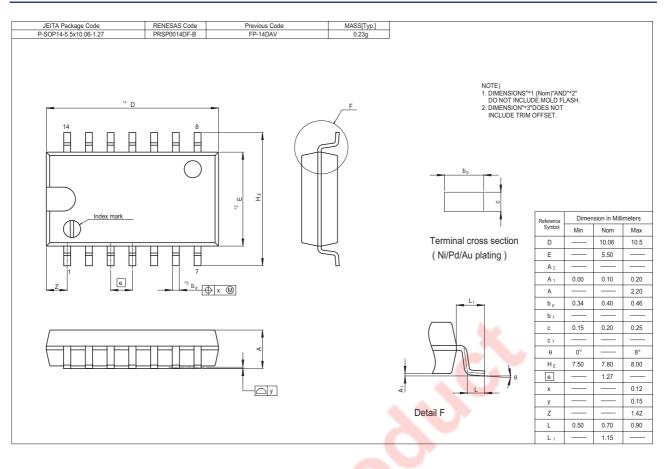


Package Dimensions





HD74HC10



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