

## **Description**

This document details the custom configuration that is programmed into the one-time-programmable (OTP) memory of the 9FGV1005CQ505LTGI. Please refer to the device datasheet for further information about the device.

## **General Configuration**

Parameter	Value	Units
Device I <sup>2</sup> C Address	0xD0	_
Default Configuration at I <sup>2</sup> C Mode	0	_

## **Frequency Overview**

Parameter	Configuration 0	Configuration 1	Configuration 2	Configuration 3	Units
Input	50	50	50	50	MHz
REF 0	_	_	_	_	_
Output 0	50	100	125	156.25	MHz
Output 1	50	100	125	156.25	MHz

### Configuration 0 Parameters: SEL[1:0] = 00

Input frequency: 50MHz; crystal load capacitance: 7.74pF

Parameter	REF 0	Output 0	Output 1	Units
Frequency	50	50	50	MHz
Default Output Status	Off	On	On	_
VDDO Voltage	Low	3.3	3.3	V
Output Type	LVCMOS	LP-HCSL	LP-HCSL	_
Spread Spectrum	_	_	_	%
Spread Spectrum Modulation	_	_	_	kHz
LP-HCSL Impedance	_	100	100	Ω



### Configuration 1 Parameters: SEL[1:0] = 01

Input frequency: 50MHz; crystal load capacitance: 7.74pF

Parameter	REF 0	Output 0	Output 1	Units
Frequency	50	100	100	MHz
Default Output Status	Off	On	On	_
VDDO Voltage	Low	3.3	3.3	V
Output Type	LVCMOS	LP-HCSL	LP-HCSL	_
Spread Spectrum	_	_	_	%
Spread Spectrum Modulation	_	_	_	kHz
LP-HCSL Impedance	_	100	100	Ω

## Configuration 2 Parameters: SEL[1:0] = 10

Input frequency: 50MHz; crystal load capacitance: 7.74pF

Parameter	REF 0	Output 0	Output 1	Units
Frequency	50	125	125	MHz
Default Output Status	Off	On	On	_
VDDO Voltage	Low	3.3	3.3	V
Output Type	LVCMOS	LP-HCSL	LP-HCSL	_
Spread Spectrum	_	_	_	%
Spread Spectrum Modulation	_	_	_	kHz
LP-HCSL Impedance	_	100	100	Ω

# Configuration 3 Parameters: SEL[1:0] = 11

Input frequency: 50MHz; crystal load capacitance: 7.74pF

Parameter	REF 0	Output 0	Output 1	Units
Frequency	50	156.25	156.25	MHz
Default Output Status	Off	On	On	_
VDDO Voltage	Low	3.3	3.3	V
Output Type	LVCMOS	LP-HCSL	LP-HCSL	_
Spread Spectrum	_	_	_	%
Spread Spectrum Modulation	_	_	_	kHz
LP-HCSL Impedance	_	100	100	Ω



# **Package Outline Drawings and Marking Diagrams**

Refer to the datasheet for detailed information pertaining to package outline drawings and marking diagrams.

## **Ordering Information**

Orderable Part Number	Carrier Type	Temperature	Crystal
9FGV1005CQ505LTGI	Tray	-40° to +85°C	50MHz Internal
9FGV1005CQ505LTGI8	Tape and Reel	-40° to +85°C	50MHz Internal

# **Revision History**

Revision Date	Description of Change
September 16, 2020	Initial release.

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### **Corporate Headquarters**

TOYOSU FORESIA, 3-2-24 Toyosu, Koto-ku, Tokyo 135-0061, Japan www.renesas.com

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