

# 2SK1581C

## N-CHANNEL MOSFET FOR SWITCHING

R07DS1287EJ0200 Rev.2.00 Jul 24, 2015

#### Description

The 2SK1581C, N-channel vertical type MOSFET designed for general-purpose switch, is a device which can be driven directly by a 2.5 V power source.

#### Features

• Directly driven by a 2.5 V power source.

Low on-state resistance

 $\begin{array}{l} {\sf R}_{DS(on)1} = 0.57 \; \Omega \; {\sf MAX}. \; ({\sf V}_{GS} = 4.5 \; {\sf V}, \; {\sf I}_{D} = 0.3 \; {\sf A}) \\ {\sf R}_{DS(on)2} = 0.60 \; \Omega \; {\sf MAX}. \; ({\sf V}_{GS} = 4.0 \; {\sf V}, \; {\sf I}_{D} = 0.3 \; {\sf A}) \\ {\sf R}_{DS(on)3} = 0.88 \; \Omega \; {\sf MAX}. \; ({\sf V}_{GS} = 2.5 \; {\sf V}, \; {\sf I}_{D} = 0.15 \; {\sf A}) \\ \end{array}$ 

#### **Ordering Information**

Part Number	Lead Plating	Packing	Package	
2SK1581C-T1B-A/AT	-A:Sn-Bi , -AT:Pure Sn	3000p/Reel	SC-59 (3pMM)	

Remark "-A/AT" indicates Pb-free. This product does not contain Pb in external electrode and other parts.

#### Marking XL

#### Absolute Maximum Ratings (T<sub>A</sub> = 25°C)

Drain to Source Voltage ( $V_{GS} = 0 V$ )	Vdss	20	V
Gate to Source Voltage (V <sub>DS</sub> = 0 V)	Vgss	±12	V
Drain Current (DC)	ID(DC)	±500	mA
Drain Current (pulse) <sup>Note</sup>	ID(pulse)	±2	А
Total Power Dissipation	Рт	200	mW
Channel Temperature	Tch	150	°C
Storage Temperature	Tstg	-55 to +150	°C

**Note** PW  $\leq$  10  $\mu$ s, Duty Cycle  $\leq$  1%

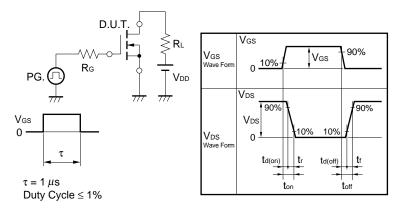


### Electrical Characteristics (T<sub>A</sub> = 25°C)

Characteristics	Symbol	Test Conditions	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	IDSS	V <sub>DS</sub> = 20 V, V <sub>GS</sub> = 0 V			1	μA
Gate Leakage Current	lgss	$V_{GS} = \pm 12 \text{ V}, \text{ V}_{DS} = 0 \text{ V}$			±10	μA
Gate to Source Cut-off Voltage	VGS(off)	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	0.5	1.0	1.5	V
Forward Transfer Admittance Note	y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 0.3 A	0.25	0.75		S
Drain to Source On-state Resistance Note	RDS(on)1	V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 0.3 A		0.38	0.57	Ω
	RDS(on)2	Vgs = 4.0 V, ID = 0.3 A		0.41	0.6	Ω
	RDS(on)2	Vgs = 2.5 V, Ib = 0.15 A		0.6	0.88	Ω
Input Capacitance	Ciss	V <sub>DS</sub> = 10 V,		28		pF
Output Capacitance	Coss	V <sub>GS</sub> = 0 V,		11		pF
Reverse Transfer Capacitance	Crss	f = 1.0 MHz		7		pF
Turn-on Delay Time	td(on)	V <sub>DD</sub> = 10 V,		20		ns
Rise Time	tr	ID = 0.3 A,		51		ns
Turn-off Delay Time	td(off)	V <sub>GS</sub> = 4 V,		94		ns
Fall Time	tr	R <sub>G</sub> = 10 Ω		87		ns
Body Diode Forward Voltage <sup>Note</sup>	VF(S-D)	IF = 0.5 A, VGs = 0 V		0.87		V

Note Pulsed

### **Test Circuit Switching Time**





### Typical Characteristics (T<sub>A</sub> = 25°C)

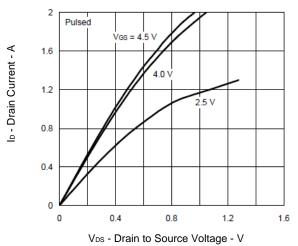
SAFE OPERATING AREA

120 dT - Percentage of Rated Power - % 100 80 60 40 20 0 0 25 50 75 100 125 150 175

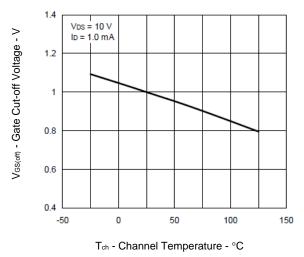
DERATING FACTOR OF FORWARD BIAS

 $T_A-Ambient$  Temperature -  $^\circ C$ 

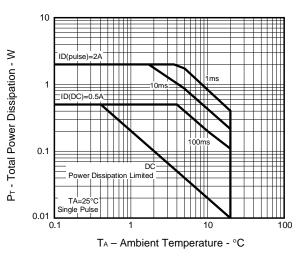




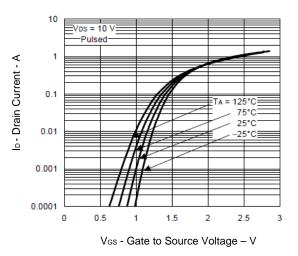




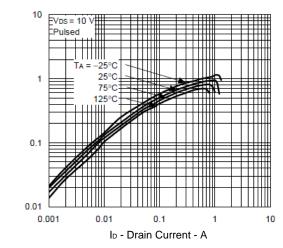
FORWARD BIAS SAFE OPERATING AREA



#### FORWARD TRANSFER CHARACTERISTICS

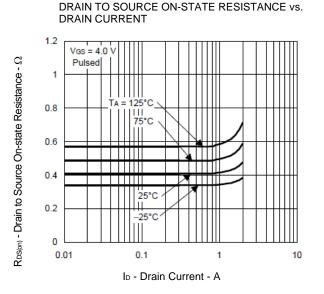


# FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT

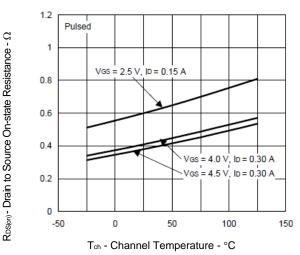




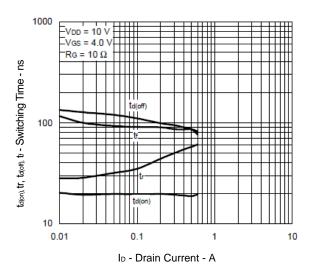
| y<sub>fs</sub> | - Forward Transfer Admittance - S

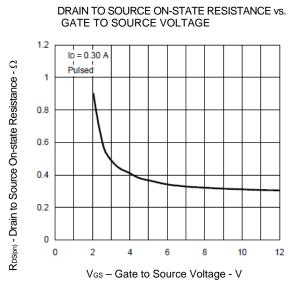


DRAIN TO SOURCE ON-STATE RESISTANCE vs. CHANNEL TEMPERATURE

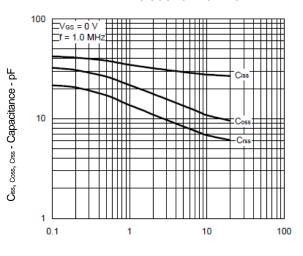






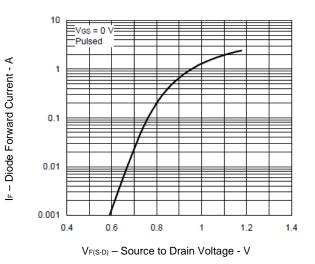


CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE



V<sub>DS</sub> - Drain to Source Voltage - V

SOURCE TO DRAIN DIODE FORWARD VOLTAGE



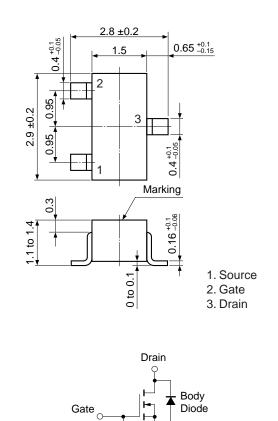
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### Package Drawings (Unit: mm)

#### SC-59 (Mini Mold)

**Equivalent Circuit** 



**Remark** The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

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Source

Gate

Diode

Protection

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2SK1581C
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		Description		
Rev.	Date	Page	Summary	
1.00	Sep , 2013	-	First Edition Issued	
2.00	Jul, 2015	3	Changed FORWARD BIAS SAFE OPERATING AREA	

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