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April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)
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Sample Program Guideline

DMA transfer (A/D → internal RAM) (V850/SA1TM, V850/SB1TM, V850/SB2TM)

This document describes the sample program that performs application-independent general-purpose control in the V850/SA1, V850/SB1, and V850/SB2.

This document consists of the following two sections.

- -- Outline
- -- Flow chart
- *Sample codes can be downloaded from the Microcomputer web page.

<Related document>

V850/SA1 Application Note (Document number: U13851E)

Caution

This software is for reference only; NEC does not guarantee the operation. Therefore, evaluate it sufficiently beforehand with the set to be actually used.

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DMA transfer (A/D → internal RAM)

Outline

This program stores the variable resistance value to on-chip RAM using INTAD as the DMA transfer trigger source.

Flow chart

