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7542 Group

Sample Program Serial I/O 1(UART)-LED Pattern Control

Introduction

This sample program uses the following functions:

- Timer X (Timer mode): The period of the main loop function.
- INT0 (Falling edge active): Transmit trigger.
- Serial I/O 1 (UART mode): Full duplex communication between two MCUs.
- Output Port (P30-P32, P03): LED0, LED1, LED2, LED3 control.

Target Device

The explanation of this issue is applied to the following condition:

- Target MCU: 7542 Group
- Oscillation frequency: 8MHz
- Memory size: ROM 32KB, RAM 1KB

Contents

1.	Operation	2
	Contents	
Ζ.	Contents	5
3.	Flow Chart	7
4.	Sample Program	14
5.	Reference	20



1. Operation

The program is explained in following.

- Serial I/O 1(UART) is used in this sample program, communication format is shown below:
 - Transfer Baud Rate: 9615bps;
 - Transfer Data Length: 8 bits;
 - Stop Bit: 1 bit;
 - Parity Bit: Even Parity.

One byte is transferred from the transmitter to the receiver when SW1 is pressed. Initial value of transmitting data is 00001111B. It decreases by one when SW1 pressed each time until the data equals 0, then initial value is reloaded.

Circuit diagram is shown in the figure 1.

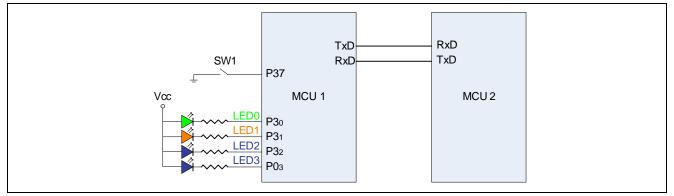


Figure 1 Circuit Diagram

The receiver turns on corresponding LED according to received data.

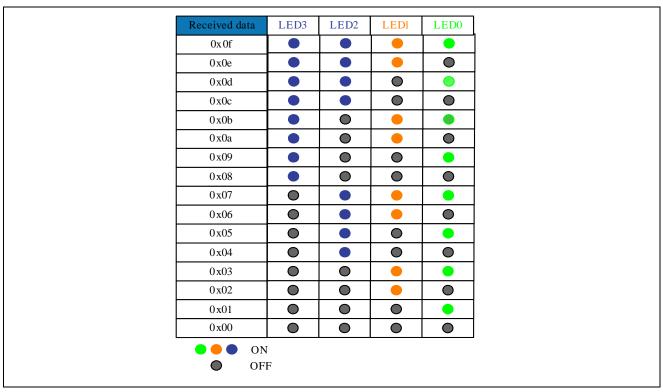


Figure 2 Received Data - LED Display Pattern



2. Contents

2.1 Global Variables

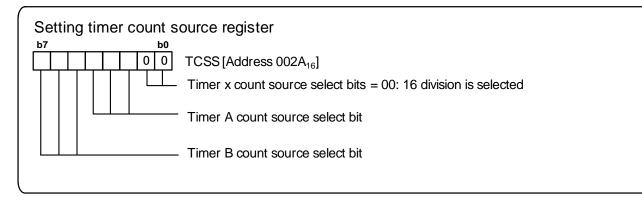
Table 1. Global variable

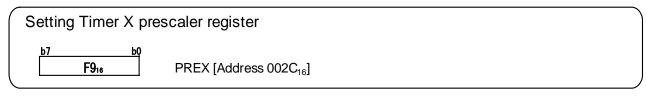
Name	Initial Value	Size (Byte)	Description	Min (H)	Max (H)	Flag
g_mode	00H	1	Mode flag: 0 Receive; 1 Transmit	00	01	
g_error	00H	1	Error flag	00	FF	
g_tr_data	0FH	1	Transmit data buffer	00	0F	
g_re_data	00H	1	Receive data buffer	00	FF	
g_key_state	00H	1	Key state flag	00	04	

2.2 Timer X (Timer mode)

Timer X is used in timer mode. The standard period of 20ms is used for main cycle. Interrupt of Timer X isn't being used.

 $8MHz \times f16 \times 250 \times 40 = 20ms$ Main cycle 20ms





Setting Timer X count register					
b7 b0 27 ₁₈	TX [Address 002D ₁₆]				



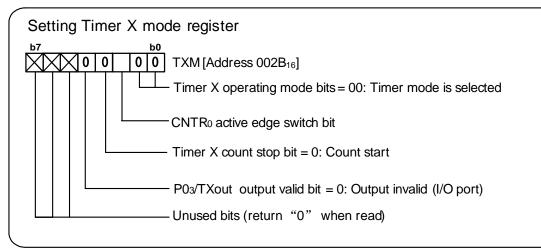
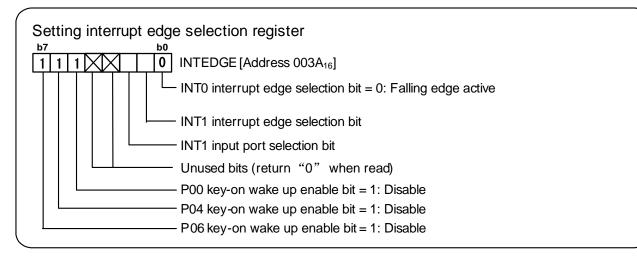


Figure 3 Setting of Timer X

2.3 INT0 (falling edge active)

Interrupt of INT0 is used. The setup of INT0 is shown in the following.



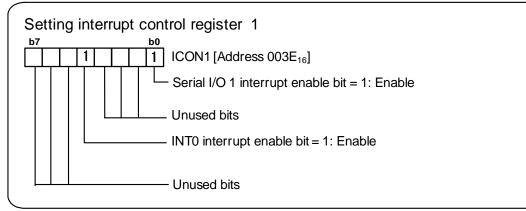


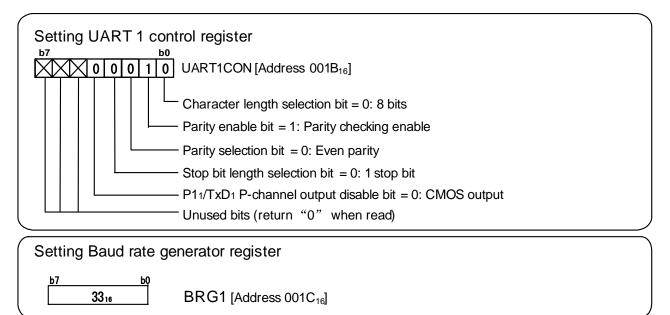
Figure 4 Setting of INT0



2.4 Serial I/O 1 (UART mode)

For clock asynchronous serial I/O (UART), the baud rate and transfer formats used by a transmitter and receiver must be identical. When a byte is received, an interrupt will happen.

The setup of Serial I/O 1 is shown in the following.



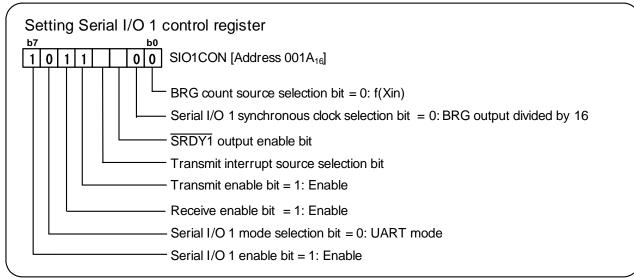


Figure 5 Setting of Serial I/O 1

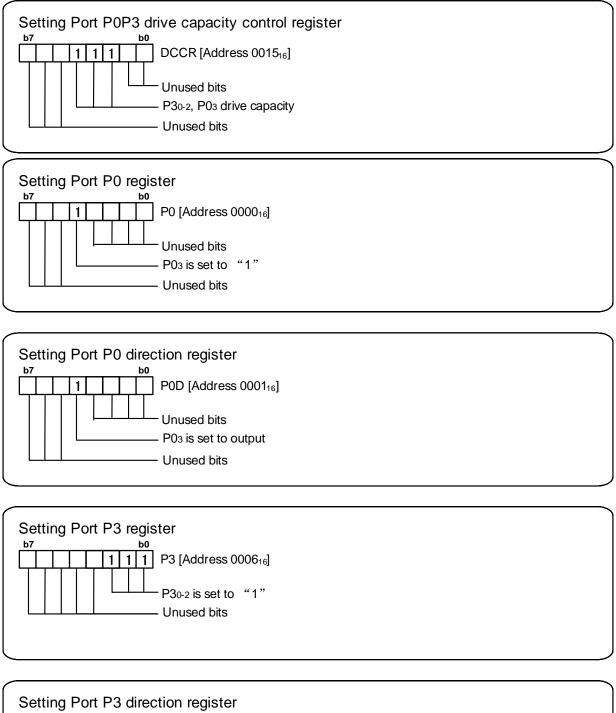
2.5 I/O Ports

LED0, LED1, LED2, LED3 are controlled by P30, P31, P32, P03.

Output of the ports are refreshed every 20ms.

The setup of the ports is shown in the following.





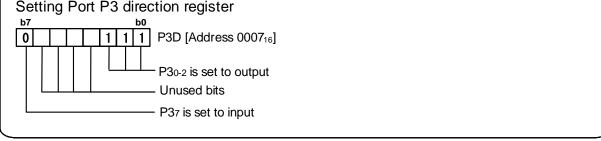
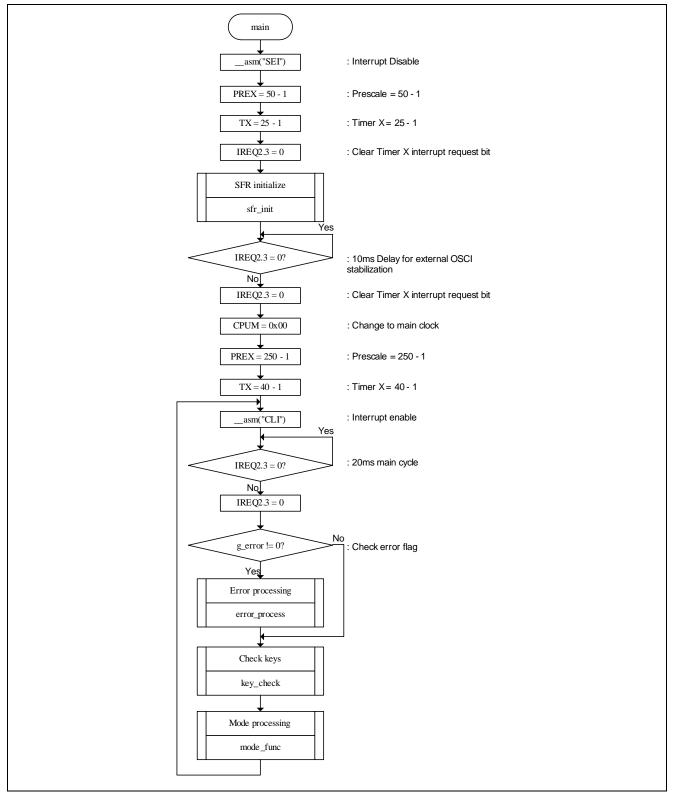


Figure 6 Setting of I/O Ports



3. Flow Chart

3.1 Initialize & Main Loop







3.2 SFR Initialize

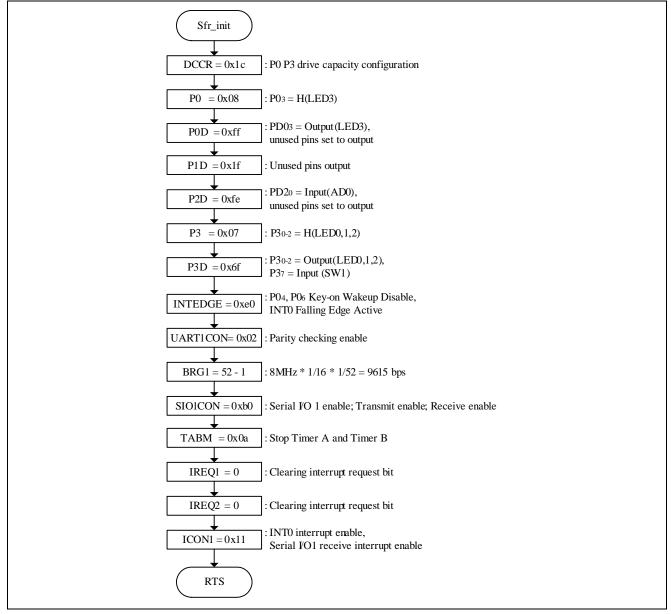


Figure 8 SFR Initialize Flow Chat



3.3 Mode Processing

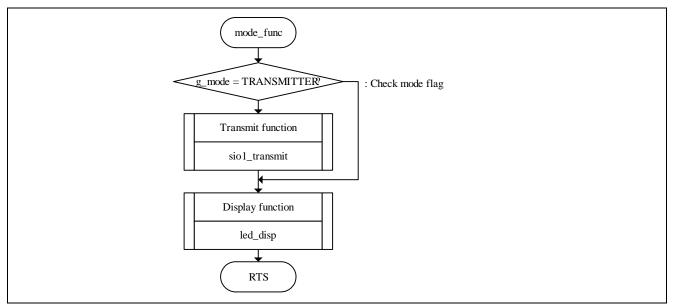


Figure 9 Mode Processing Flow Chat



3.4 Output Ports (P30-2, P03): LED Control

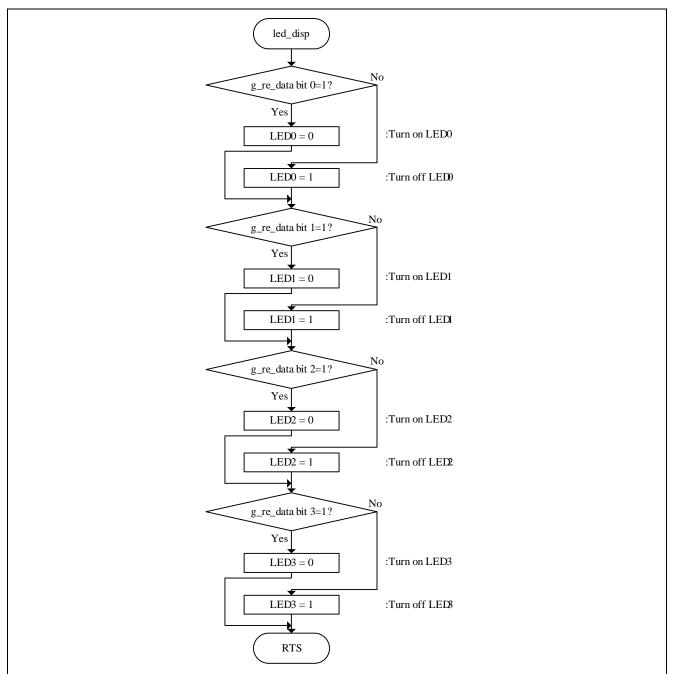


Figure 10 LED Control Flow Chat



3.5 Key Check

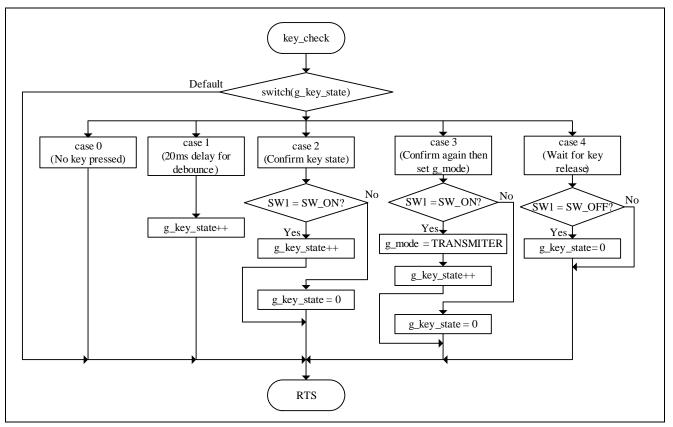


Figure 11 Key Check Flow Chat



3.6 Serial I/O 1 Transmit

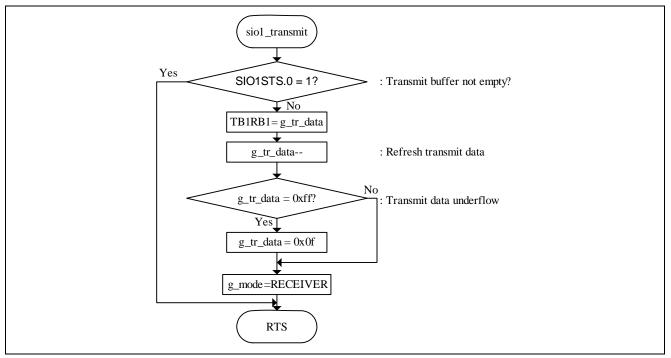


Figure 12 Serial I/O 1 Transmit Flow Chat

3.7 INT0 & Serial I/O 1 Interrupt Handler

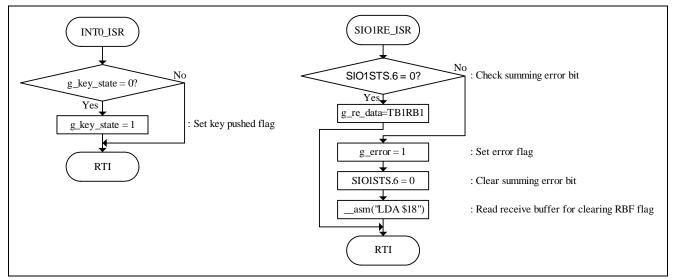


Figure 13 INT0 & Serial I/O 1 Interrupt Handler Flow Chat



3.8 Error Processing

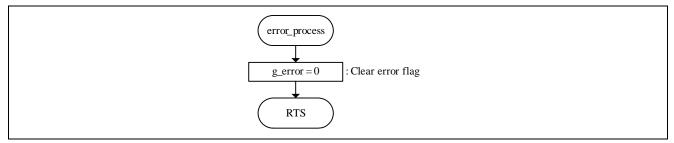


Figure 14 Error Processing Flow Chat



4. Sample Program

```
*
                                                                  *
                                                                  *
*
 File Name: main.c
                                                                  *
*
  Contents : Main program of serial I/O sample program
                                                                  *
  Copyright, 2005 RENESAS SYSTEM SOLUSTIONS (BEIJING) CO., LTD.
*
*
 Version: 1.00
#include "sfr_7542.h"
#include "sio.h"
void main(void)
{
  ___asm("SEI");
                         /* Interrupt Disable */
  /* Delay for external OSCI stabilization */
  PREX = 50 - 1;
                        /* 2MHz(On-chip) * 1/16 * 1/50 * 1/25 = 10ms */
  TX = 25 - 1;
  IREQ2.3 = 0;
                         /* Clear Timer X interrupt request bit */
  sfr_init();
  while (IREQ2.3 == 0){}
                         /* Wait for Timer X underflow */
  IREQ2.3 = 0;
                         /* Clear Timer X interrupt request bit */
  CPUM = 0x00;
                         /* Change to main clock */
  /* Setting main cycle timer */
  PREX = 250 - 1; /* 8MHz * 1/16 * 1/250 * 1/40 = 20ms */
  TX = 40 - 1;
  while(1)
  {
     __asm("CLI"); /* Enable interrupt */
     while (IREQ2.3 == 0){} /* Waiting for Timer X underflow */
     IREQ2.3 = 0;
                        /* Clear Timer X interrupt request bit */
     if (g_error != 0)
     {
        error_process(); /* Error processing */
     }
                        /* Check keys */
     key_check();
                         /* Mode processing */
     mode_func();
  }
}
; Name:
           sfr_init
```



```
; Parameter:
           None
; Return:
           None
; Description: Initial setting of SFR registers
void sfr_init(void)
{
  DCCR = 0x1c;
                         /* P0 P3 drive capacity configuration for LED */
  P0 = 0x08;
                         /* P03 = H(LED3) */
  POD = 0xff;
                         /* PD03 = Output(LED3), unused pins set to output */
  P1D = 0x1f;
                         /* Unused pins output */
  P2D = 0xfe;
                         /* PD20 = Input(AD0), unused pins set to output */
  P3 = 0x07;
                         /* P30-2 = H(LED0,1,2) */
                         /* P30-2 = Output(LED0,1,2), P37 = Input (SW1) */
  P3D = 0x6f;
  INTEDGE = 0xe0;
                         /* P04, P06 Key-on Wakeup Disable;
                         INTO Falling Edge Active */
  UART1CON = 0 \times 02;
                         /* Even parity checking enable */
  BRG1 = 52 - 1;
                         /* 8MHz * 1/16 * 1/52 = 9615 bps */
  SIO1CON = 0xb0;
                         /* Serial I/0 1 enable;
                         Transmit enable; Receive enable */
  TABM = 0x0a;
                        /* Stop Timer A and Timer B */
  IREQ1 = 0;
                         /* Clearing interrupt request bit */
  IREO2 = 0;
  ICON1 = 0 \times 11;
                         /* INT0 interrupt enable;
                         Serial I/01 receive interrupt enable */
}
mode_func
; Name:
; Parameter: None
          None
; Return:
; Description: Mode processing
void mode_func(void)
{
  if (g_mode == TRANSMITTER)
  {
                        /* Call transmit function */
     sio1_transmit();
  }
  led_disp();
                        /* Call display function */
}
; Name:
          key_check
; Parameter: None
; Return:
         None
; Description: Confirm pressed key twice and change g_mode
```



```
void key_check(void)
{
  switch (g_key_state)
  {
     case 0:
        break;
                            /* 20ms delay */
        case 1:
           g_key_state++;
           break;
        case 2:
           if (SW1 == SW_ON)
                           /* Confirm key state */
           {
              g_key_state++;
           }
           else
           {
              g_key_state = 0;
           }
           break;
        case 3:
           if (SW1 == SW_ON) /* Confirm key state again */
           {
              g_mode = TRANSMITER;
             g_key_state++;
           }
           else
           {
              g_key_state = 0;
           }
           break;
        case 4:
           if (SW1 == SW_OFF) /* If key is released */
           {
              g_key_state = 0; /* Set to no key pushed state */
           }
           break;
        default:
           break;
  }
}
; Name:
           led_disp
; Parameter: None
           None
; Return:
; Description: Display receive data by LED
void led_disp(void)
```



```
{
  if (g_re_data & 0x01)
  {
    LED0 = 0;
                     /* Turn on LED0 */
  }
  else
  {
                       /* Turn off LED0 */
    LED0 = 1;
  }
  if (g_re_data & 0x02)
  {
    LED1 = 0;
                       /* Turn on LED1 */
  }
  else
  {
                      /* Turn off LED0 */
    LED1 = 1;
  }
  if (g_re_data & 0x04)
  {
    LED2 = 0;
                       /* Turn on LED2 */
  }
  else
  {
    LED2 = 1;
                      /* Turn off LED2 */
  }
  if (g_re_data & 0x08)
  {
    LED3 = 0;
                      /* Turn on LED3 */
  }
  else
  {
    LED3 = 1;
                       /* Turn on LED3 */
  }
}
siol_transmit
; Name:
; Parameter: None
; Return:
         None
; Description: Transmit g_tr_data by Serial I/O 1
void sio1_transmit(void)
{
  if (SIO1STS.0 == 1) /* Check transmit buffer full flag */
  {
     TB1RB1 = g_tr_data;
     g_tr_data--;
                      /* Reduce transmit data by 1 */
     if (g_tr_data == 0xff)
     {
```



```
g_tr_data = 0x0f;
    }
                    /* When complete return to receive mode */
    g_mode = RECEIVER;
  }
}
; Name:
         error_process
; Parameter:
         None
; Return:
         None
; Description: Receive error processing
void error_process(void)
{
  /* User define */
  g_error = 0;
                  /* Clear error flag */
}
; Name:
         INT0_ISR
; Parameter: None
; Return:
         None
; Description: INTO interrupt handler
interrupt void INT0_ISR(void)
{
  if (g_key_state == 0) /* If no key pressed */
  {
    g_key_state = 1;
  }
}
; Name:
         SIO1RE_ISR
; Parameter: None
; Return:
         None
; Description: Serial I/O 1 receive interrupt handler
interrupt void SIO1RE_ISR(void)
{
                    /* Check summing error bit */
  if (SIO1STS.6 == 0)
  {
    g_re_data = TB1RB1;
  }
  else
  {
    g_error = 1;
                    /* Set error flag */
    IO1STS.6 = 0;
                    /* Clear summing error bit */
    ___asm("LDA $18");
                    /* Read receive buffer for clearing RBF flag*/
  }
}
```

RENESAS

```
*
*
    File Name : sio.h
    Contents : Definition of sample program
   Copyright, 2005 RENESAS SYSTEM SOLUSTIONS (BEIJING) CO., LTD.
*
    Version: 1.00
#ifndef SIO H
#define SIO_H
/* Definition of globle variable */
                                         /* Mode flag */
unsigned char g_mode;
unsigned char g_error; /* Error flag */
unsigned char g_tr_data = 0x0f; /* Transmit data buffer (Initialized as 0x0f) */
unsigned char g re data; /* President data buffer (Initialized as 0x0f) */
                                           /* Receive data buffer */
unsigned char g_re_data;
unsigned char g_key_state;
                                     /* Key state */
/* Declaration of function prototype */
extern void sfr_init(void); /* SFR initialize routine */
extern void mode_func(void); /* Mode processing */
extern void key_check(void); /* Check key input */
extern void led_disp(void); /* LED control */
extern void siol_transmit(void); /* Transmit routine */
extern void error_process(void); /* Error processing */
/* Definition of macros */
#define SW_ON
                         0
#define SW_OFF
                         1
#define RECEIVER
                          0
#define TRANSMITTER
                          1
#define LED0
                        P3 0
#define LED1
                         P3_1
#define LED2
                         P3_2
#define LED3
                         P0_3
#define SW1
                         P3_7
#endif
                                                 /* SIO_H */
```



5. Reference

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Data Sheet

7542 Group Rev.3.00 (Use the latest version on the home page:http://www.renesas.com)



Revision Record

		Description	
Rev.	Date	Page	Summary
1.00	Dec.28.05	—	First edition issued



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