

# RX62T

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## 180-degree Sinusoidal-wave with Hall sensor

July 12, 2012

### Introduction

This application note describes a method of driving a sensed Brushless DC Motor (BLDCM) with 180-degree sinusoidal wave controlled by RX62T. The motor control firmware algorithm and experimental result are shown in the following chapters.

### Target Device

RX62T

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### 1. System Configuration

Target board is RX62T Evaluation Board which is made in Renesas Electronics Taiwan. E1 connector interface to debug program, hall sensor input circuit to capture hall signal; inverter circuit to drive BLDC motor; variable resistor (VR) to control the motor rotational speed; UART port to output MCU information to terminal; switch button to control the motor spinning direction.

In this application note, we'll use 120-degree trapezoidal control for startup only (The solution of this control method please refer application note "RX62T 120-degree Trapezoidal-wave with Hall Sensor"). Once the commutating is successful and revolution error is smaller than 60 rpm (i.e. Speed Command – Speed < 60 rpm), change control method from 120-degree trapezoidal to 180-degree sinusoidal control. The flowchart of sine-wave control is shown in chapter 3.



Figure 1-1 System Configuration

Table 1-1 lists the jumper setting for the hall signals input. Table 1-2 lists the jumper setting for the CPU mode selection.

Jumper	J9	J10	J11
Pins	2-3	2-3	2-3

Table 1-1 Jumper Setting for Hall Sensor Control

Jumper	J1	J2	J3	J4
Single chip mode	-	1-2	1-2	2-3
Boot mode	-	1-2	2-3	2-3

Table 1-2 CPU Mode Setting

## 2. Block Diagram

MTU3 is mainly used module in this application note. Channel 3 and channel 4 are used for PWM output, which is set as reset-synchronized mode. Channel 6 is used for calculating rotational speed. Channel 1 is an auxiliary counter for 1ms and 2ms timing. Three GPIOs connect with three hall sensor input signals. ADC receives VR value to control rotational speed.

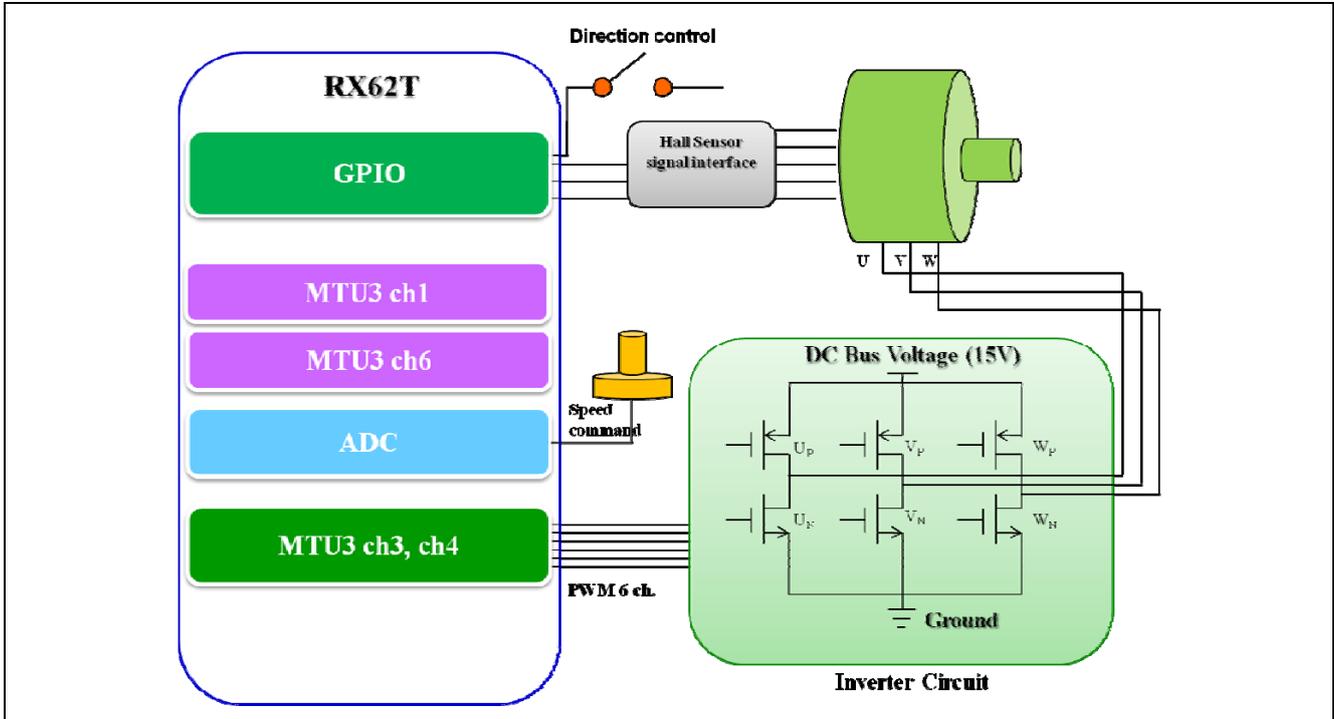


Figure 2-1 Block Diagram

### 3. Flowchart

Chapter 3 is going to explain how program is designed. Figure 3-1 shows the main routine that describes how program is executed. The carrier frequency is 15 KHz, which stands for one PWM duty cycle will be calculated 15K times in one second, and each interrupt operating procedure shows in Figure 3-6. The maximum and minimum duties of the PWM are limited at 80% and 10% respectively. The limitation of PWM duty should depend on each motor's specification. Figure 3-3 and 3-4 show how mechanical speed is measured.

As described in chapter 1, paragraph 2. We start the motor with 120-degree trapezoidal control. For angle being estimated precisely, we change to sinusoidal control while revolution error smaller than 60 rpm. Besides, once direction is changed during sinusoidal control, PWM output will be disabled and motor status will be changed to STOP for restarting control procedure.

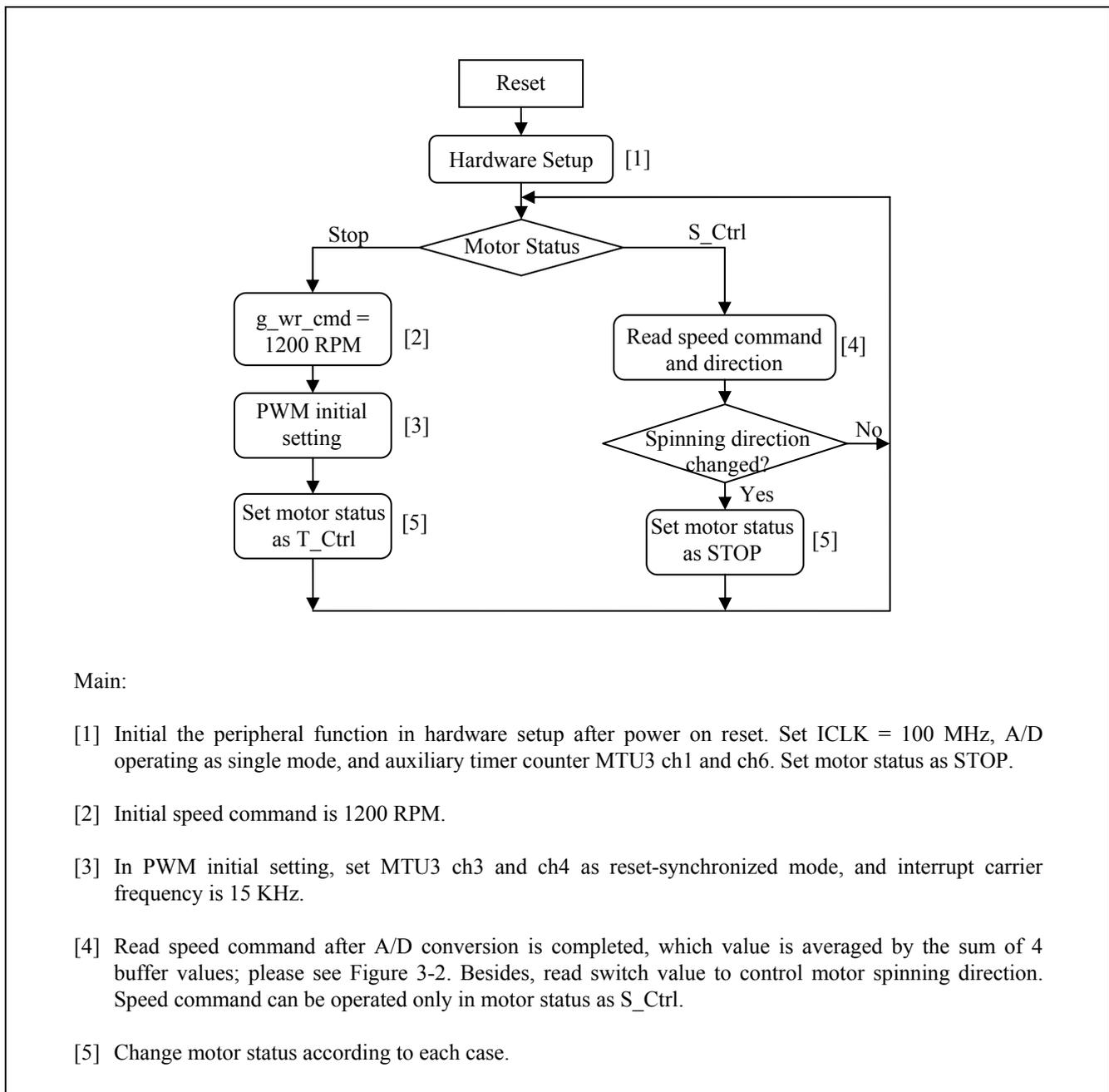


Figure 3-1 Main Control Procedure

To get more precisely speed command value, we set four variables for saving A/D conversion results. In Fig 3-2, the interrupt procedure is triggered while A/D conversion is completed. Conversion result will be stored in buf1, and others will be shifted in sequence. Four variables will be summed up and divided by 4, which used as speed command.

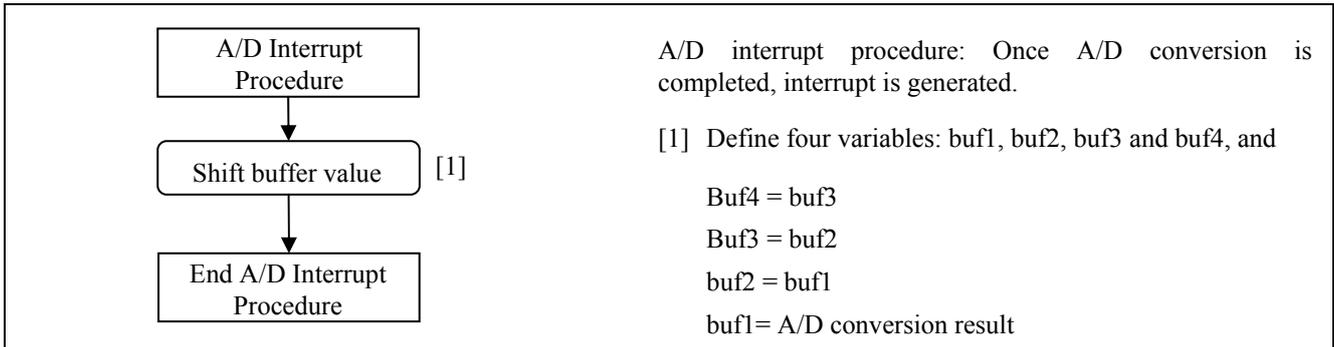


Figure 3-2 Speed Command Value form A/D

Speed calculation is one of the most important issues in motor control process. In this example, a two-pole-pairs motor is used, which generates 2 pulses in one revolution. One timer is selected to calculate motor rotation speed. As shown in Figure 3-3, timer operating frequency is 390625Hz, counter value will be saved in TGRA at rising and falling edge.

In this case, motor rotation speed is 60 rpm (i.e. 1 rps, Revolutions Per Second). While rising and falling edge is to trigger timer for capturing counter value, the TGRA value should be 97656. Depending on this condition, we calculate the ratio between 97656 and TGRA, and this is how we estimate motor rotation speed in this program. Speed calculation procedure is shown in Figure 3-4.

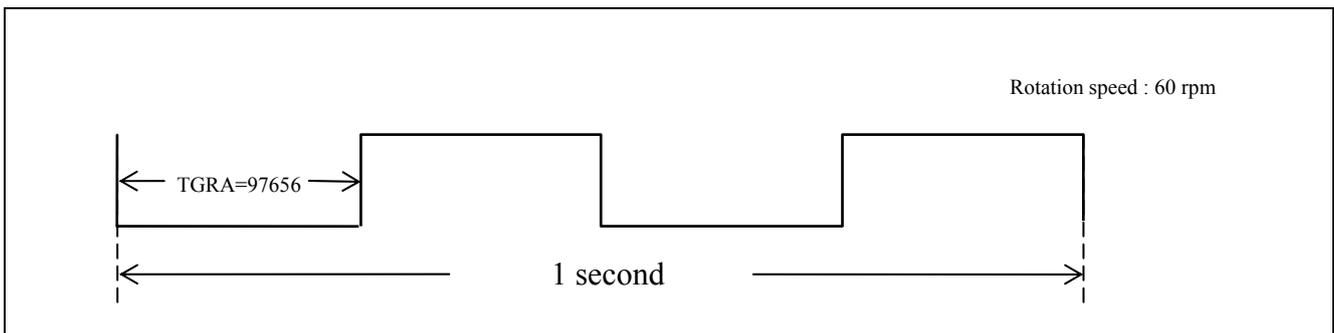


Figure 3-3 Hall Signal of 2 pole-pairs' Motor in One Revolution

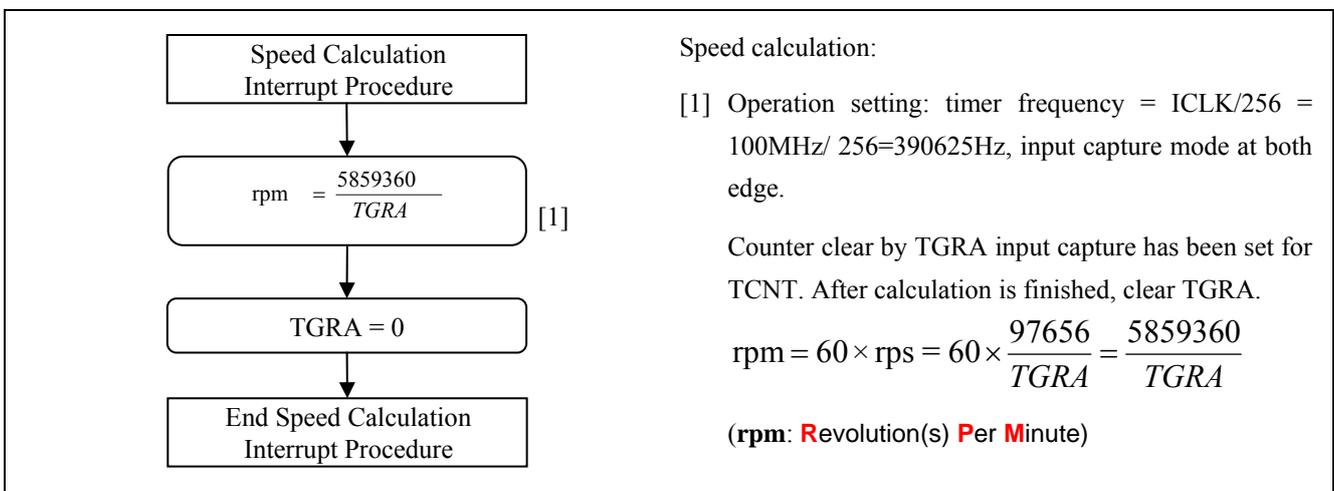


Figure 3-4 Motor Speed Calculation Procedure

In sinusoidal control, we set a look-up table which has  $2^{10}$  points between  $0^\circ$  and  $360^\circ$  in X-axis and magnification of Y-axis from  $1 \sim -1$  to  $2^{13} \sim -2^{13}$ , the figure explanation is shown in Fig3-5. 3-phase hall sensors provide angular resolution of 60-degree, so before the transition to next phase, each PWM duty cycle will accord with speed to estimate how far it has changed and sets a specific angle for a suitable output, the equation is shown in Fig3-6.

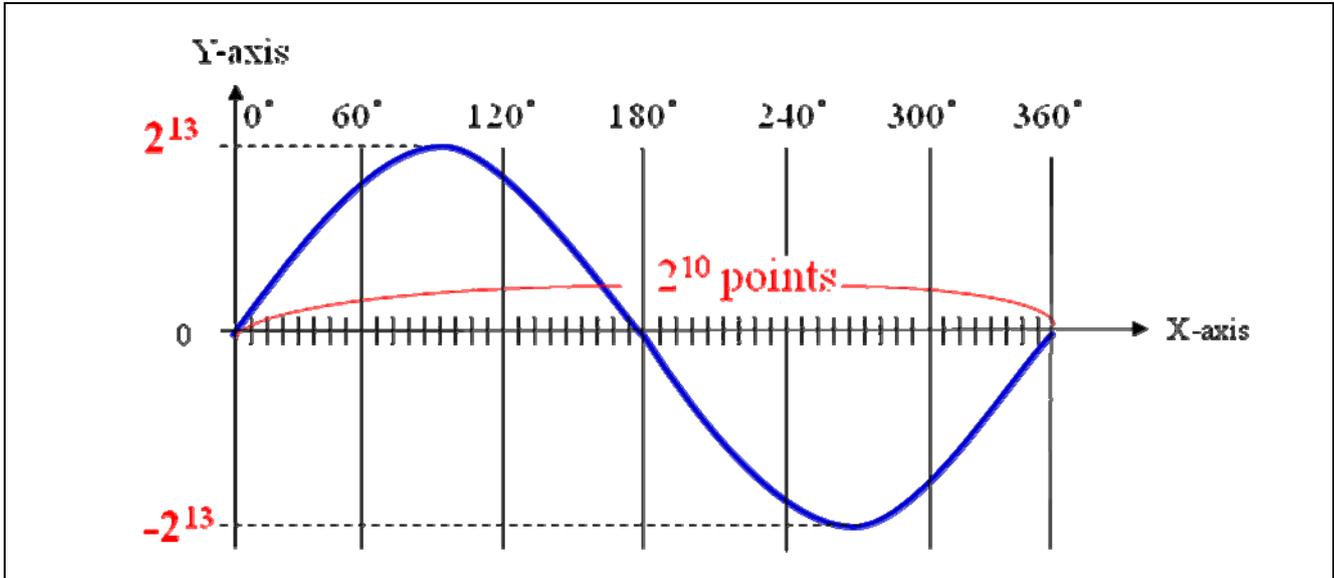


Figure 3-5 Resolution of Sine Wave

Sinusoidal PWM duty calculation and angle calculation procedure are shown in Figure 3-6 to Figure 3-8.

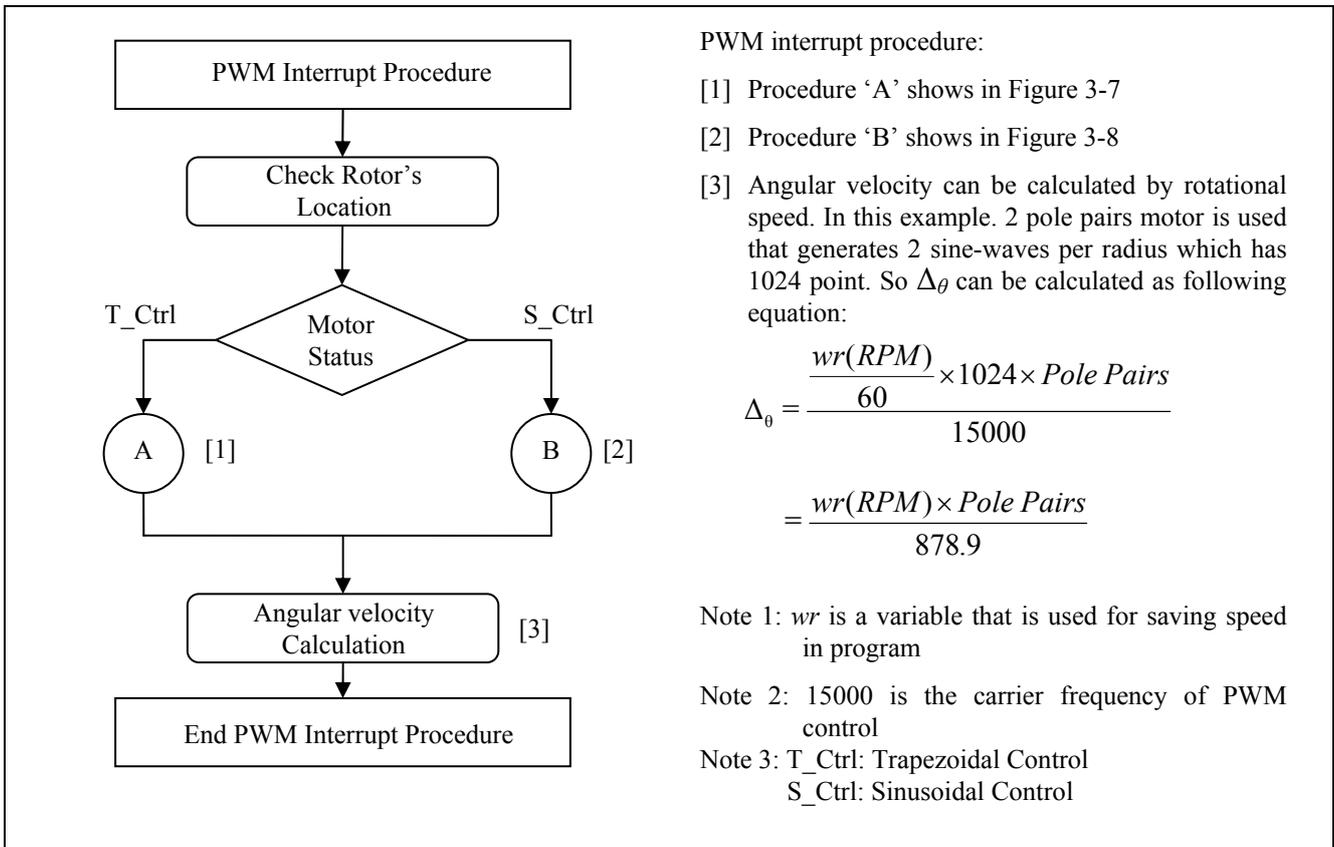
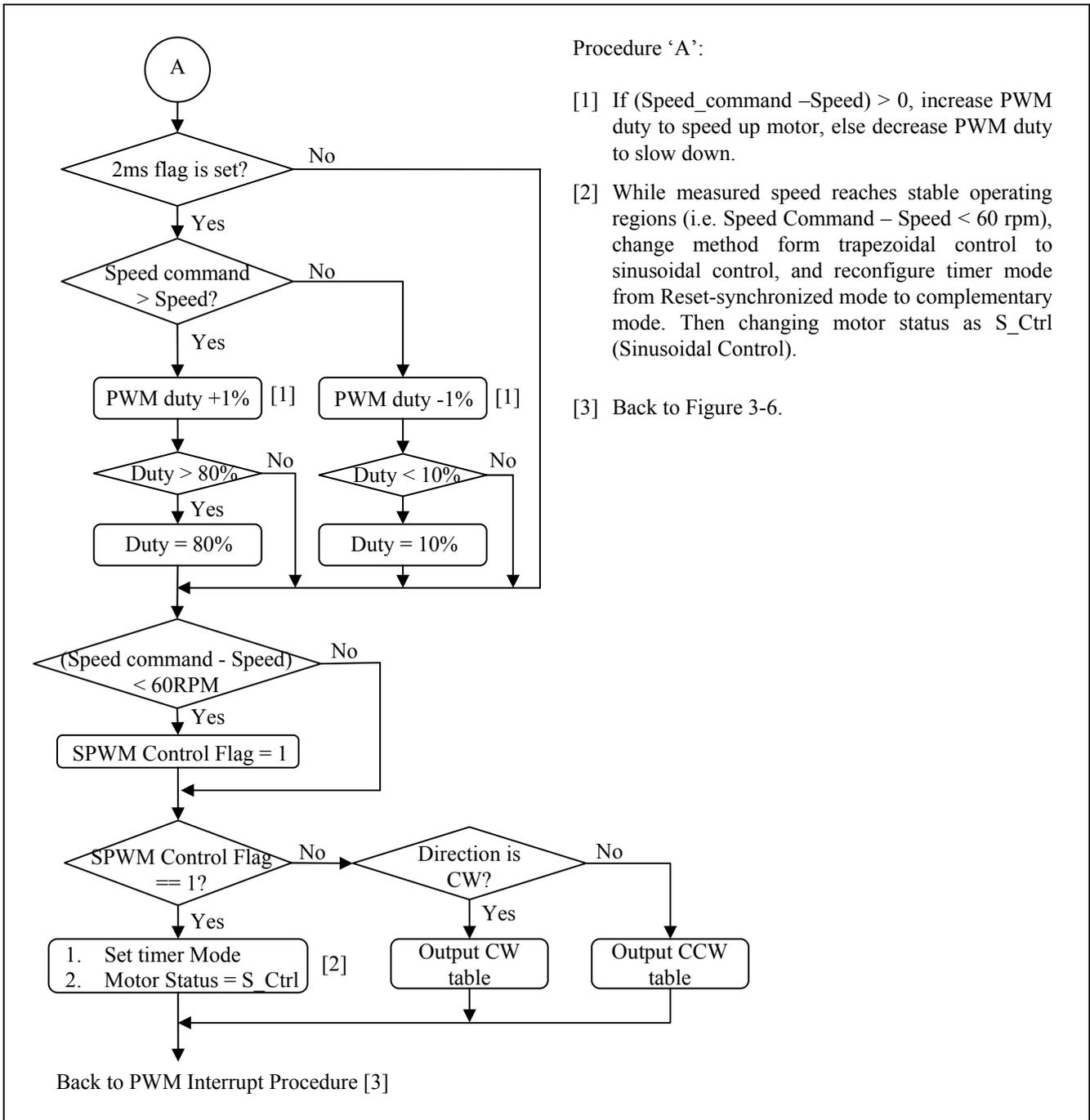


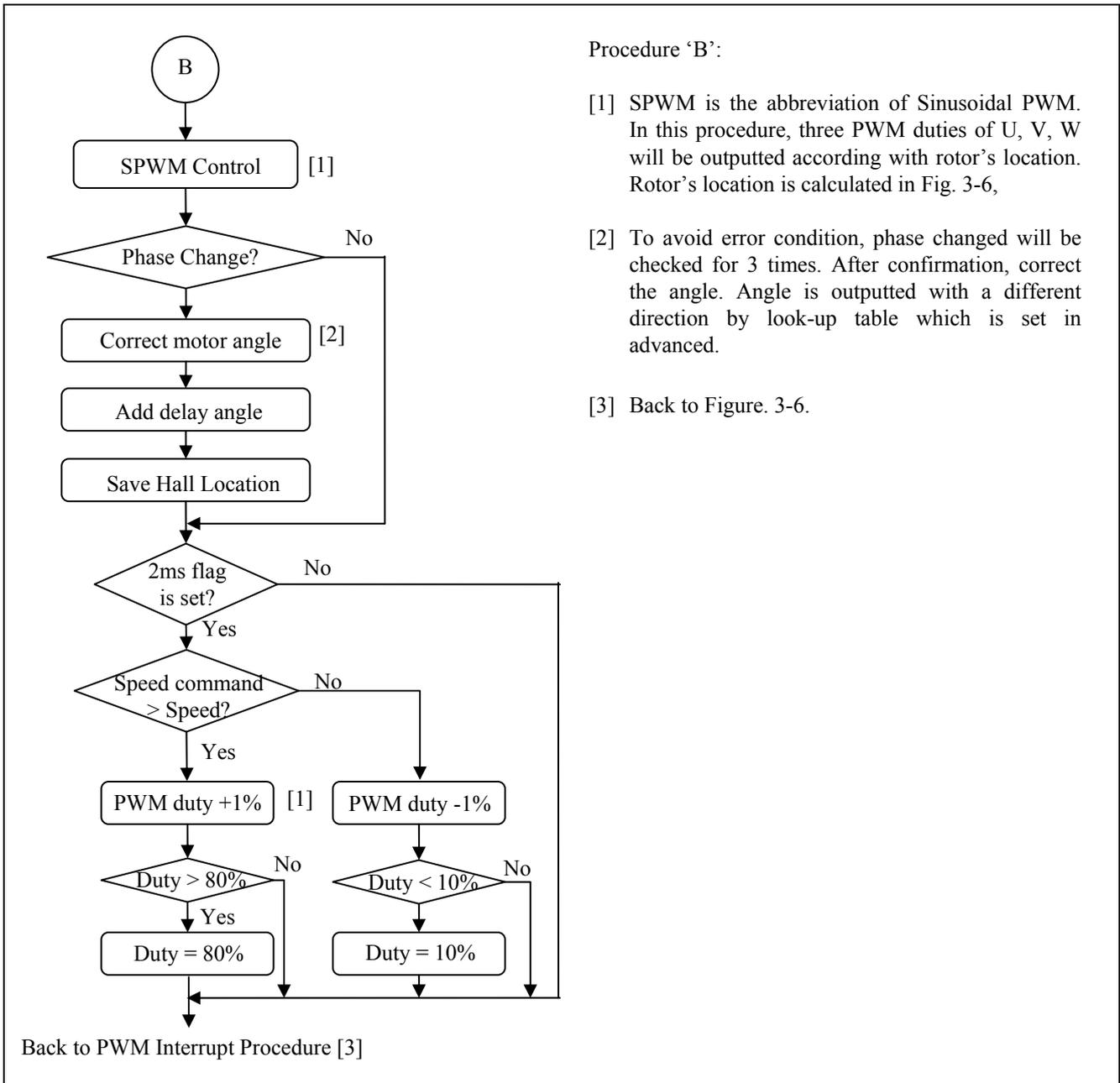
Figure 3-6 MTU3 Ch3 and Ch4 PWM Duty Calculation Procedure



Procedure 'A':

- [1] If (Speed\_command - Speed) > 0, increase PWM duty to speed up motor, else decrease PWM duty to slow down.
- [2] While measured speed reaches stable operating regions (i.e. Speed Command - Speed < 60 rpm), change method from trapezoidal control to sinusoidal control, and reconfigure timer mode from Reset-synchronized mode to complementary mode. Then changing motor status as S\_Ctrl (Sinusoidal Control).
- [3] Back to Figure 3-6.

Figure 3-7 Procedure 'A' while Motor Status as T\_Ctrl



Procedure 'B':

[1] SPWM is the abbreviation of Sinusoidal PWM. In this procedure, three PWM duties of U, V, W will be outputted according with rotor's location. Rotor's location is calculated in Fig. 3-6,

[2] To avoid error condition, phase changed will be checked for 3 times. After confirmation, correct the angle. Angle is outputted with a different direction by look-up table which is set in advanced.

[3] Back to Figure. 3-6.

Figure 3-8 Procedure 'B' while Motor Status as S\_Ctrl

## 4. Implementation and Testing

### 4.1 Specification of BLDC motor

The specification of BLDC motor is shown in Figure 4-1 and Figure 4-2. The model number of target motor is BLWR110S-15V-8000. Maximum speed can up to 8000 rpm in spec. In this experimental result, we can drive target motor up to 7200 rpm under PWM duty is 75%, the minimum speed of this motor without any loading is 600 rpm.

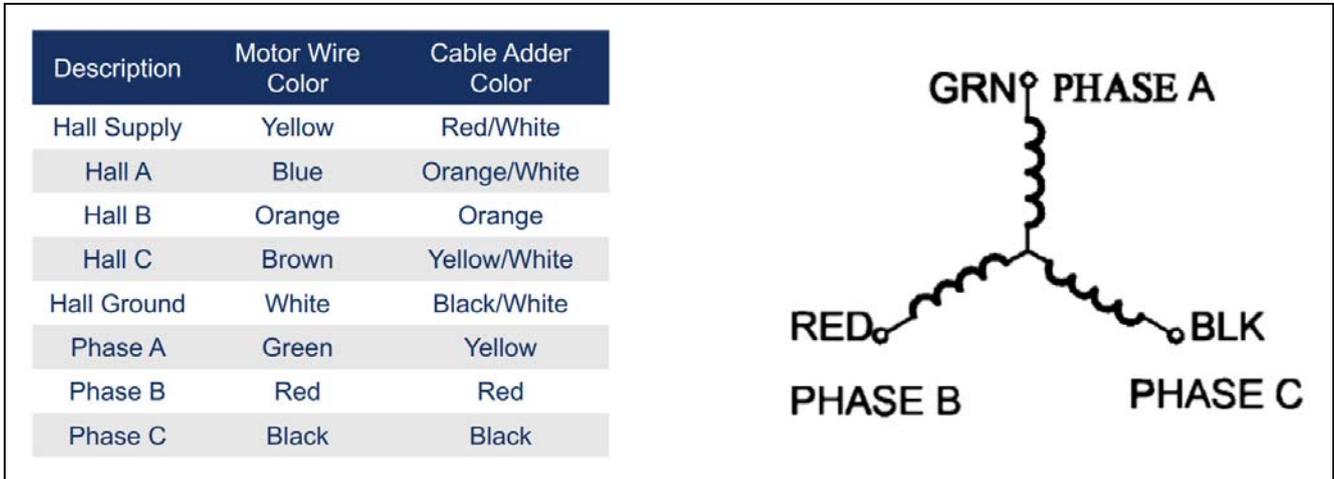


Figure 4-1 U, V, and W Description of BLWR110S

Winding Type:	Star, 4 Poles	Max. Radial Force:	15N @ 10mm from the flange
Hall Effect Angle:	120 degree electrical angle	Max. Axial Force:	10N
Shaft Run Out:	0.025mm	Insulation Class:	Class B
Radial Play:	0.02mm@450g	Dielectric Strength:	500VDC for one minute
End Play:	0.08mm@450g	Insulation Resistance:	100MOhm, 500VDC

Figure 4-2 BLWR110S Specification

The Capability of this target board is shown in Table 3-1.

Ietm	Value	Unit
CPU loading	3.41	%
Max. Speed	7200	rpm
Min. Speed	600	rpm
Timer Used	4	channel
Input BUS voltage	15	V

Table 4-1 Capability of RX62T Evaluation Board for Sinusoidal Control

### 4.2 Experimental result

In this application note, we used 120 degree trapezoidal control to startup only. When rotational speed reaches stable operating regions, stop timer operation and PWM output, then changing mode form Reset-synchronized to complementary mode. As shown in Fig 4-3, high-side and low-side MOSFET will input PWM signal simultaneously after changing control method form trapezoidal control to sinusoidal control. In addition, we added low pass filters at three phases output to get result Fig 4-4, each phase PWM duty according with rotor's location and reference table to output a corresponding value to inverter. As you can see in Fig 4-5, different PWM duty cycle is outputted simultaneously in each phase after control method form trapezoidal to simusoidal control.

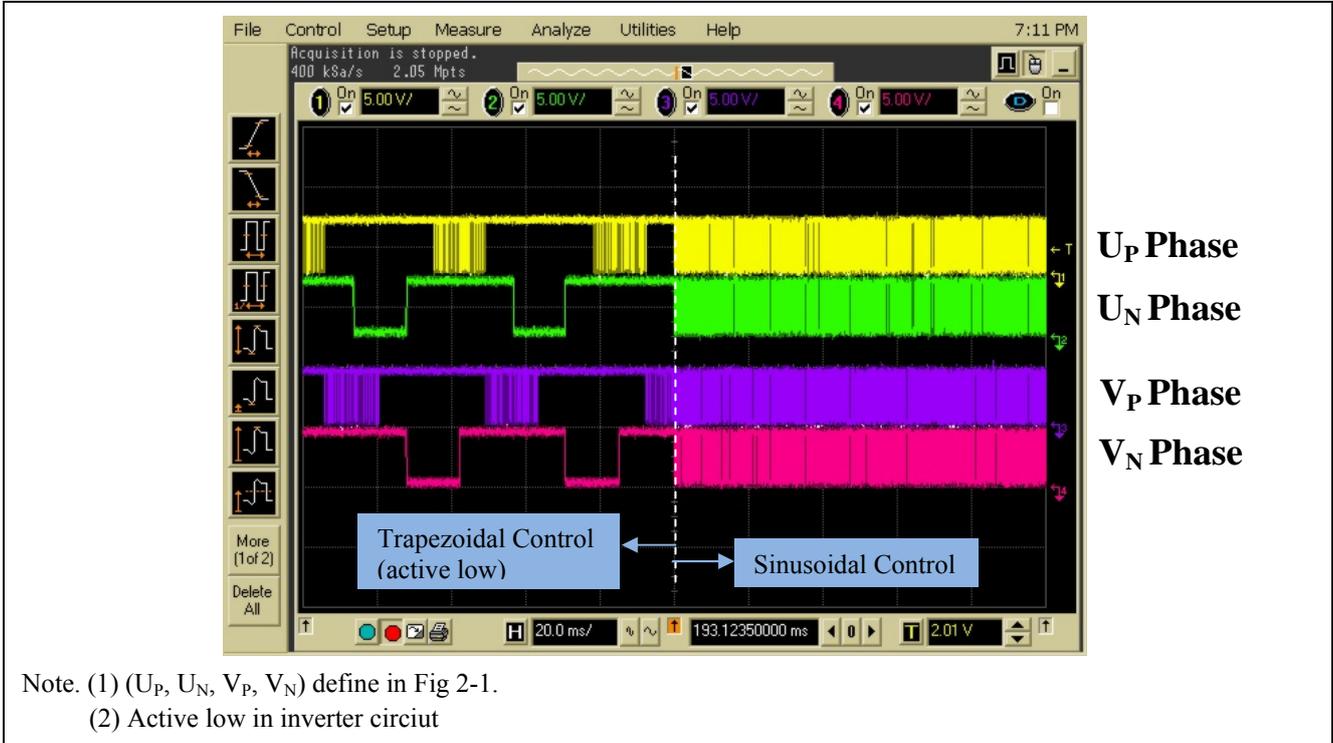


Figure 4-3 Changing PWM Control

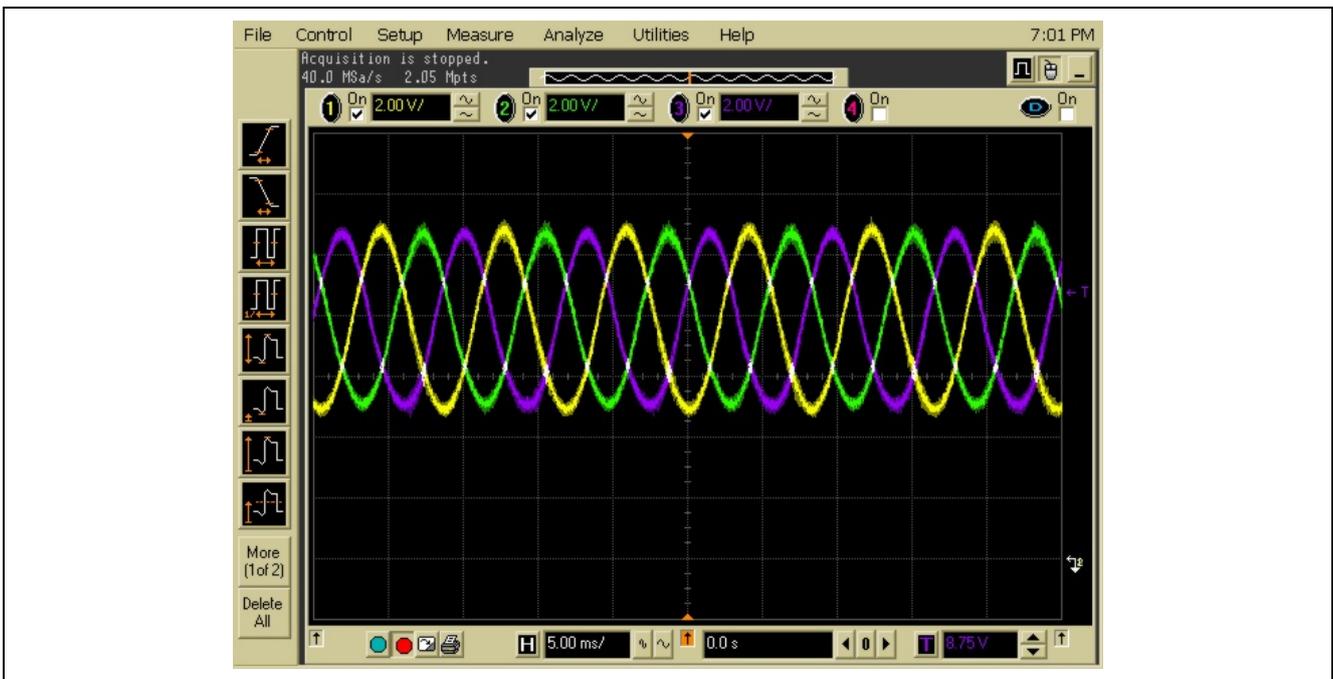


Figure 4-4 Sinusoidal Wave Control on U, V, W

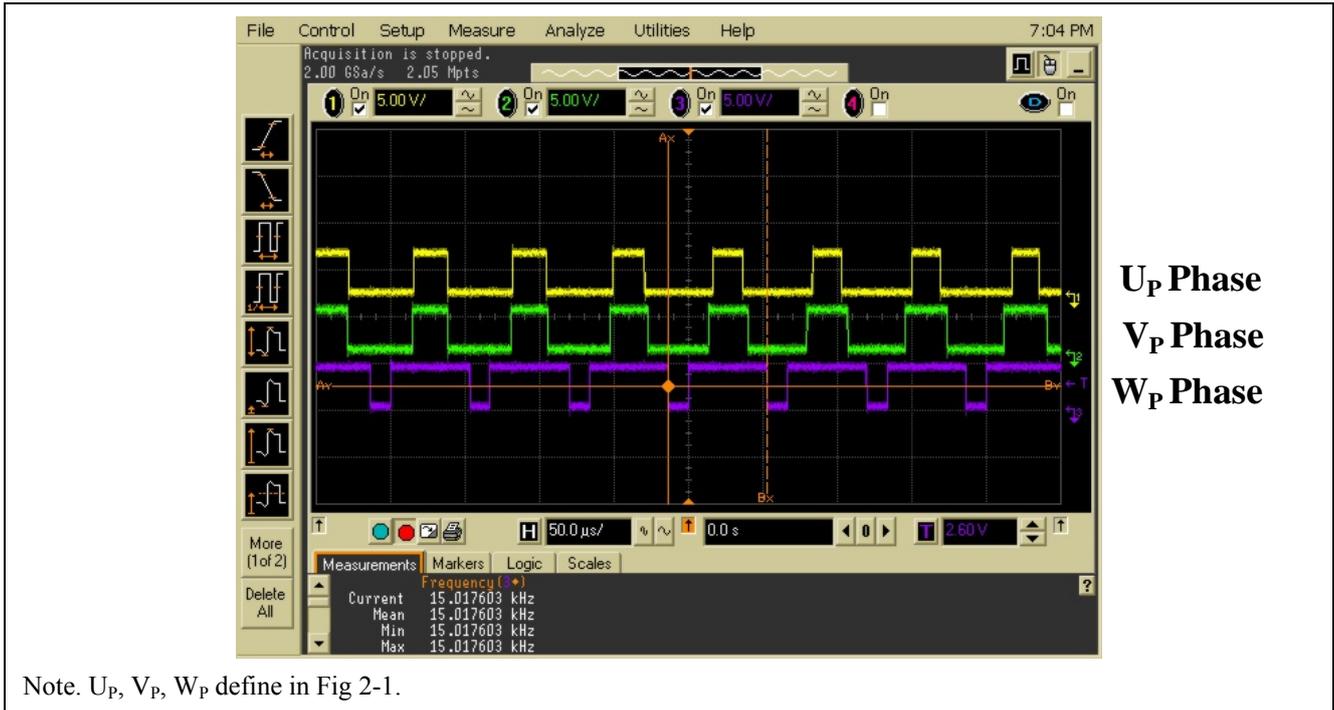


Figure 4-5 PWM Output Result of U<sub>P</sub>, V<sub>P</sub>, W<sub>P</sub>

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## Revision Record

Rev.	Date	Description	
		Page	Summary
1.00	July.12.12	—	First edition issued

## General Precautions in the Handling of MPU/MCU Products

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- The input pins of CMOS products are generally in the high-impedance state. In operation with an unused pin in the open-circuit state, extra electromagnetic noise is induced in the vicinity of LSI, an associated shoot-through current flows internally, and malfunctions occur due to the false recognition of the pin state as an input signal become possible. Unused pins should be handled as described under Handling of Unused Pins in the manual.

### 2. Processing at Power-on

The state of the product is undefined at the moment when power is supplied.

- The states of internal circuits in the LSI are indeterminate and the states of register settings and pins are undefined at the moment when power is supplied.  
In a finished product where the reset signal is applied to the external reset pin, the states of pins are not guaranteed from the moment when power is supplied until the reset process is completed. In a similar way, the states of pins in a product that is reset by an on-chip power-on reset function are not guaranteed from the moment when power is supplied until the power reaches the level at which resetting has been specified.

### 3. Prohibition of Access to Reserved Addresses

Access to reserved addresses is prohibited.

- The reserved addresses are provided for the possible future expansion of functions. Do not access these addresses; the correct operation of LSI is not guaranteed if they are accessed.

### 4. Clock Signals

After applying a reset, only release the reset line after the operating clock signal has become stable. When switching the clock signal during program execution, wait until the target clock signal has stabilized.

- When the clock signal is generated with an external resonator (or from an external oscillator) during a reset, ensure that the reset line is only released after full stabilization of the clock signal. Moreover, when switching to a clock signal produced with an external resonator (or by an external oscillator) while program execution is in progress, wait until the target clock signal is stable.

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Tel: +1-408-588-6000, Fax: +1-408-588-6130

#### Renesas Electronics Canada Limited

1101 Nicholson Road, Newmarket, Ontario L3Y 9C3, Canada  
Tel: +1-905-898-5441, Fax: +1-905-898-3220

#### Renesas Electronics Europe Limited

Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K  
Tel: +44-1628-585-100, Fax: +44-1628-585-900

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Arcadiastrasse 10, 40472 Düsseldorf, Germany  
Tel: +49-211-65030, Fax: +49-211-6503-1327

#### Renesas Electronics (China) Co., Ltd.

7th Floor, Quantum Plaza, No.27 ZhichunLu Haidian District, Beijing 100083, P.R.China  
Tel: +86-10-8235-1155, Fax: +86-10-8235-7679

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Unit 204, 205, AZIA Center, No.1233 Lujiazui Ring Rd., Pudong District, Shanghai 200120, China  
Tel: +86-21-5877-1818, Fax: +86-21-6887-7858 / -7898

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Unit 1601-1613, 16/F., Tower 2, Grand Century Place, 193 Prince Edward Road West, Mongkok, Kowloon, Hong Kong  
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