
RL78/G14

R01AN1328EG0100

Rev1.00

Utilising the Timer RD Unit Sample Code for e2studio

Oct 12, 2012

Introduction

The purpose of this Application Note is to show the user how to add the associated RL78/G14 sample code to a new or existing e2studio workspace; as well as give an explanation of what the sample code does.

The sample code provided with this Application Note runs on the RL78/G14 Renesas Starter Kit (RSK) and demonstrates usage of the Timer RD to output two 1KHz PWM signals with fixed 50% duty cycle.

Target Device

RL78/G14

Contents

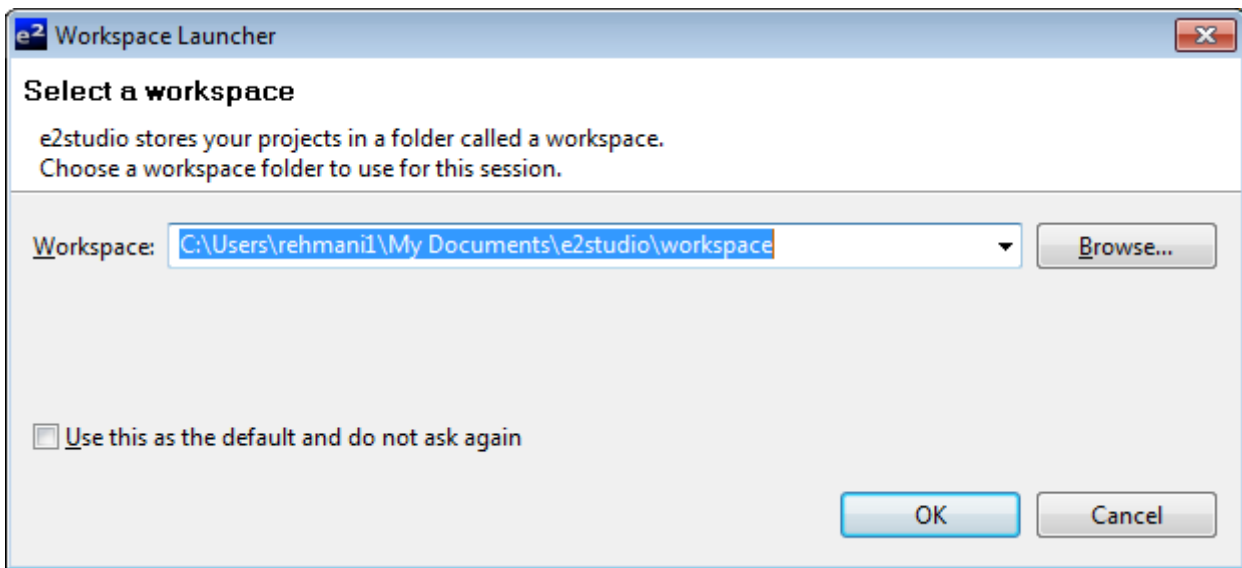
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1. Installation

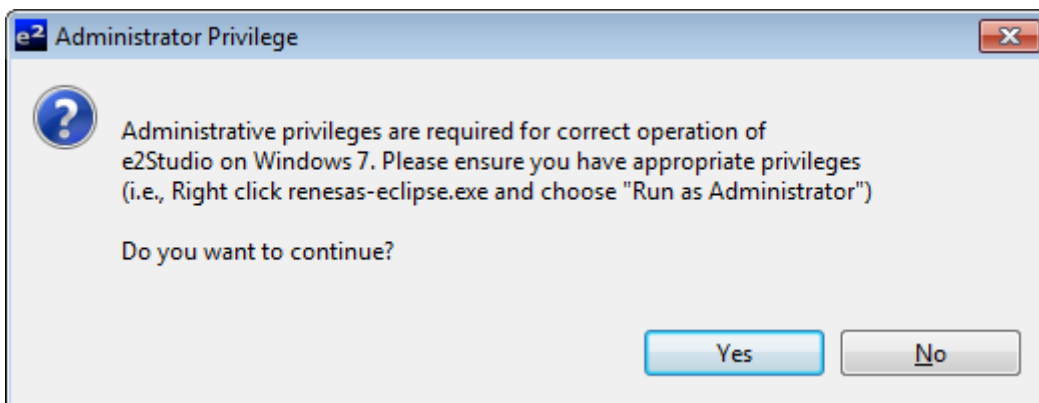
This section assumes e2studio IDE is already installed on the user's personal computer (PC). Create a new folder and name it as 'RSKRL78G14_Workspace'. Copy the zipped file PWM_TimerRD.zip, available in the Application Note package downloaded from the website, to this folder. Extract the PWM_TimerRD.zip file to the RSKRL78G14_Workspace folder.

2. Creating the Project Workspace

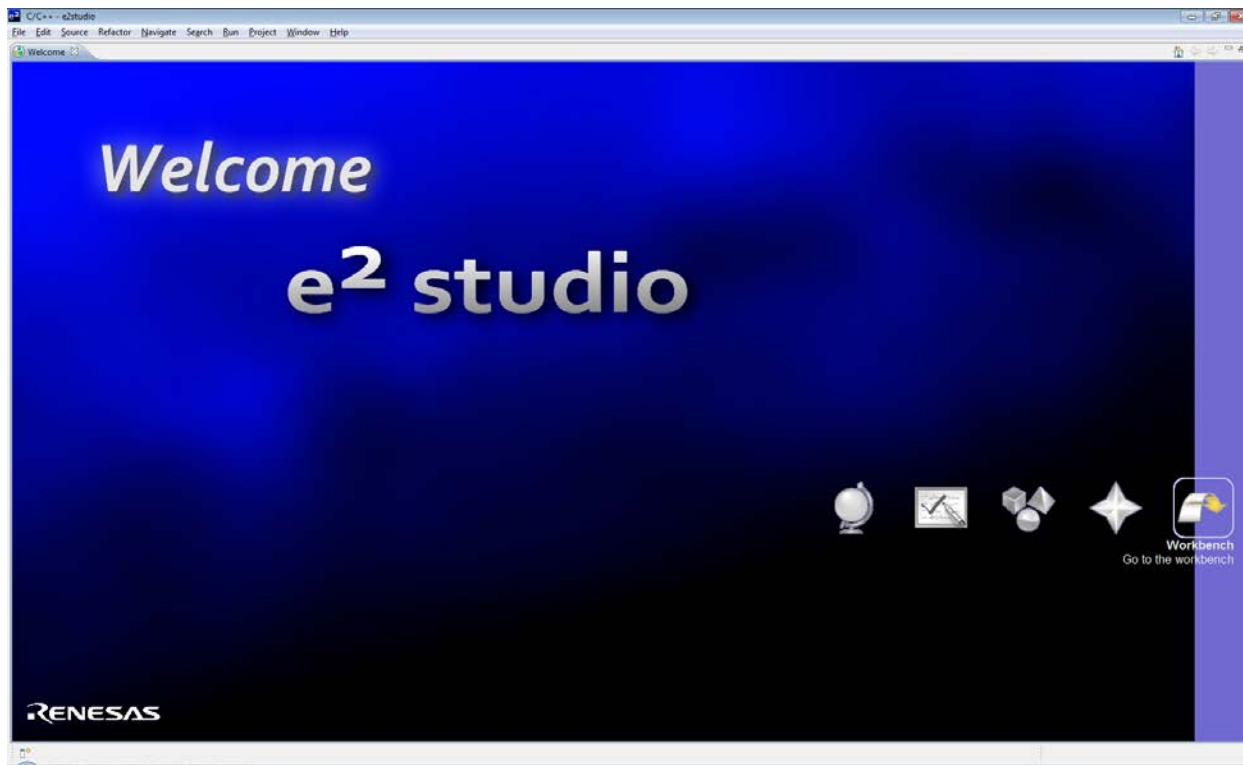
Open E2studio IDE by clicking the Windows Start button, select All Programs > Renesas Electronics e2studio > Renesas e2studio.



Select <OK>.

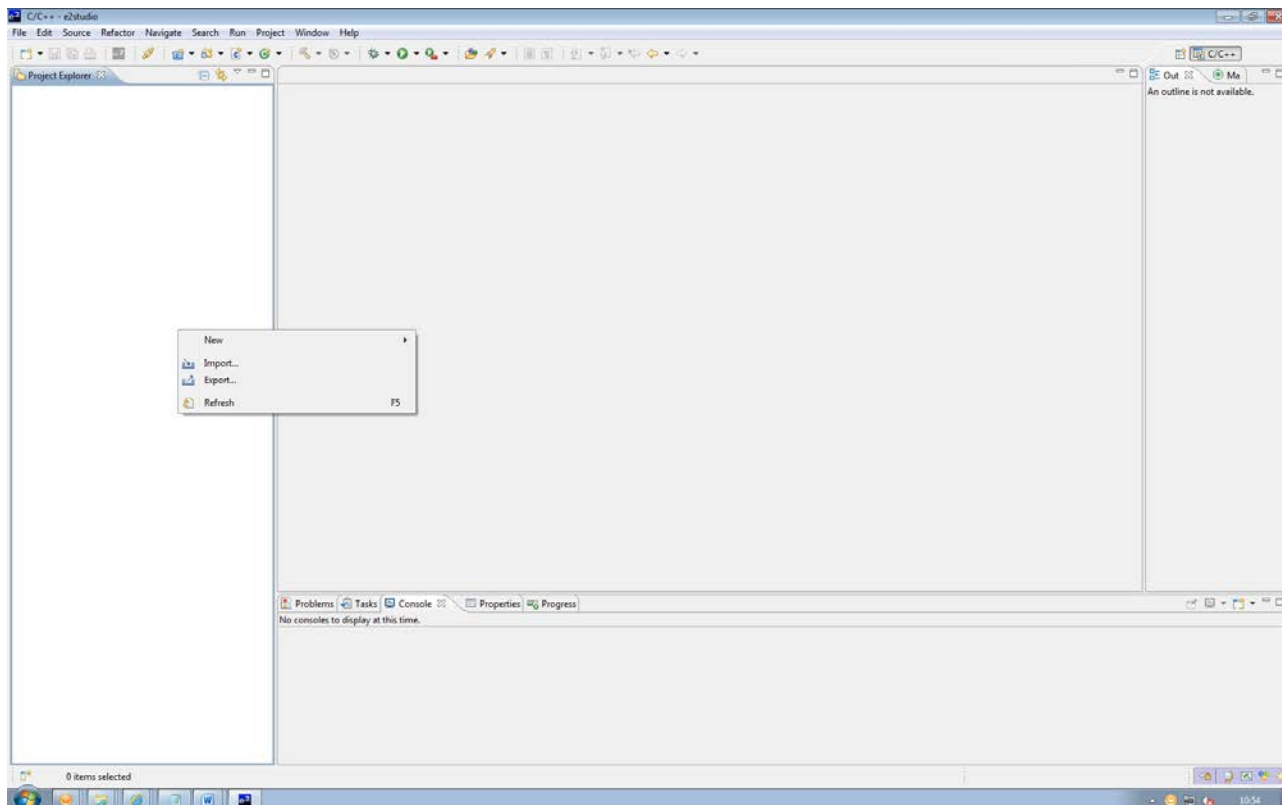


Select <Yes> to Administrator Privilege dialog.

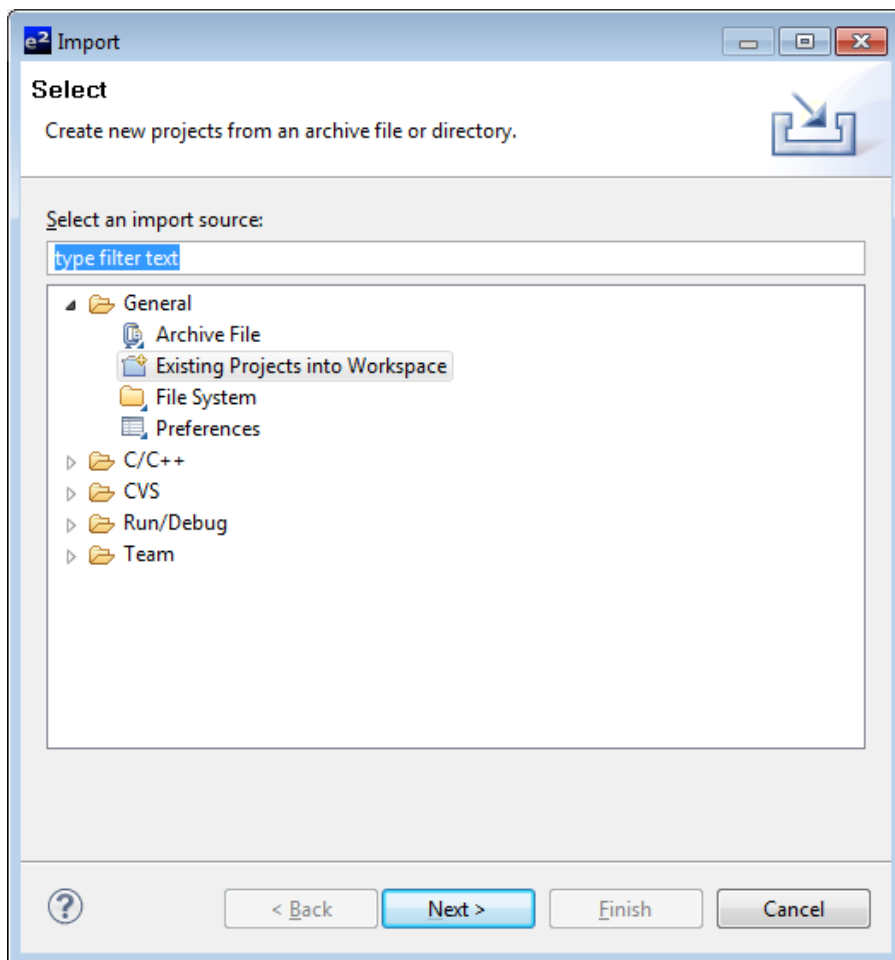


On the welcome screen select Go the Workbench icon as shown above.

1. Once the e2studio environment has initialised, right click in the project explorer window and click <Import...>



2. The Import dialog will now appear . Expand the “General” folder icon, and select “Existing Projects into Workspace”, then click ‘Next’.

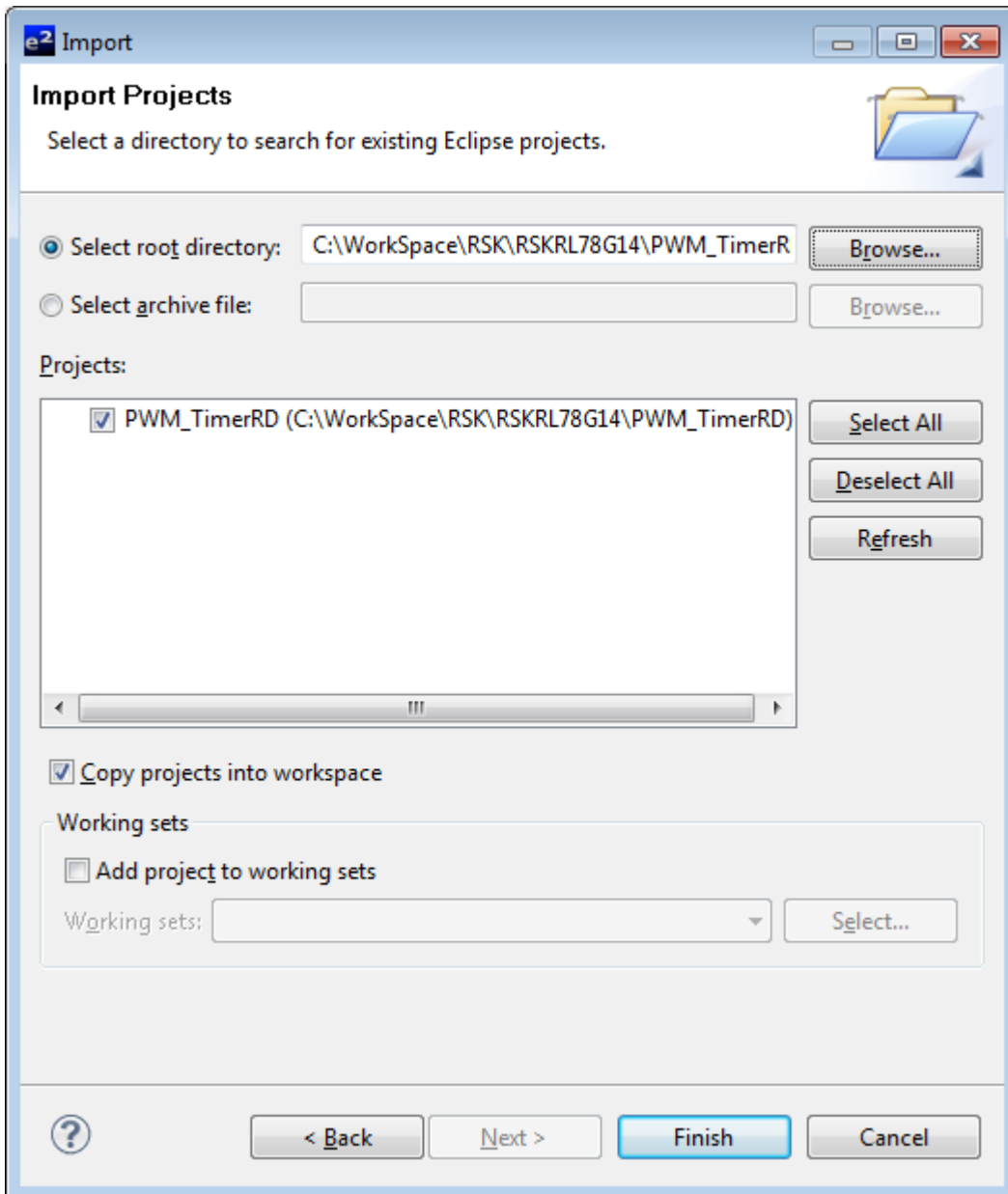


3. The Import Dialog will now appear ,specify the project to import .Click the “Browse” button and locate the directory: C:\Workspace\RSK\RSKRL78G14

And also ensure that the ‘Copy projects into workspace’ option is ticked, and then click <Finish>

Navigate to the unzipped PWM_TimerRD folder located in RSKRL78G14 Workspace folder.

Select the PWM_TimerRD folder



Click <Finish>.

The IDE e2studio will load the project.

5. Code Execution

1. Connect an oscilloscope to JA2-13 and JA6-13.
2. Compile the sample code, and download to the RSK. Click the 'Restart' button to start the program execution.
3. This application outputs a square wave signals with 50% duty cycle.

6. Website, Inquiries and Support

Renesas Electronics Website

<http://www.renesas.com/>

Inquiries

<http://www.renesas.com/inquiry>

Support

<http://www.renesas.com/rskr178g14>

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Revision Record

Rev.	Date	Description	
		Page	Summary
1.00	Oct 12, 2012	—	First edition issued

General Precautions in the Handling of MPU/MCU Products

The following usage notes are applicable to all MPU/MCU products from Renesas. For detailed usage notes on the products covered by this manual, refer to the relevant sections of the manual. If the descriptions under General Precautions in the Handling of MPU/MCU Products and in the body of the manual differ from each other, the description in the body of the manual takes precedence.

1. Handling of Unused Pins

Handle unused pins in accord with the directions given under Handling of Unused Pins in the manual.

- The input pins of CMOS products are generally in the high-impedance state. In operation with an unused pin in the open-circuit state, extra electromagnetic noise is induced in the vicinity of LSI, an associated shoot-through current flows internally, and malfunctions occur due to the false recognition of the pin state as an input signal become possible. Unused pins should be handled as described under Handling of Unused Pins in the manual.

2. Processing at Power-on

The state of the product is undefined at the moment when power is supplied.

- The states of internal circuits in the LSI are indeterminate and the states of register settings and pins are undefined at the moment when power is supplied.

In a finished product where the reset signal is applied to the external reset pin, the states of pins are not guaranteed from the moment when power is supplied until the reset process is completed.

In a similar way, the states of pins in a product that is reset by an on-chip power-on reset function are not guaranteed from the moment when power is supplied until the power reaches the level at which resetting has been specified.

3. Prohibition of Access to Reserved Addresses

Access to reserved addresses is prohibited.

- The reserved addresses are provided for the possible future expansion of functions. Do not access these addresses; the correct operation of LSI is not guaranteed if they are accessed.

4. Clock Signals

After applying a reset, only release the reset line after the operating clock signal has become stable. When switching the clock signal during program execution, wait until the target clock signal has stabilized.

- When the clock signal is generated with an external resonator (or from an external oscillator) during a reset, ensure that the reset line is only released after full stabilization of the clock signal. Moreover, when switching to a clock signal produced with an external resonator (or by an external oscillator) while program execution is in progress, wait until the target clock signal is stable.

5. Differences between Products

Before changing from one product to another, i.e. to one with a different type number, confirm that the change will not lead to problems.

- The characteristics of MPU/MCU in the same group but having different type numbers may differ because of the differences in internal memory capacity and layout pattern. When changing to products of different type numbers, implement a system-evaluation test for each of the products.

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