

# RL78/G14

# APPLICATION NOTE

Utilising the Event Link Controller Sample Code for e2studio

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## Introduction

The purpose of this Application Note is to show the user how to add the associated RL78/G14 sample code to a new or existing e2studio workspace; as well as give an explanation of what the sample code does.

The sample code provided with this Application Note runs on the RL78/G14 Renesas Starter Kit (RSK) and demonstrates the Event Link Controller (ELC) by linking the TAU1 with the ADC without having to interrupt the CPU.

#### Target Device

RL78/G14

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#### 1. Installation

This section assumes e2studio IDE is already installed on the user's personal computer (PC). Create a new folder and name it as 'RSKRL78G14\_Workspace'. Copy the zipped file ELC.zip, available in the Application Note package downloaded from the website, to this folder. Extract the ELC.zip file to the RSKRL78G14\_Workspace folder.

## 2. Creating the Project Workspace

Open E2studio IDE by clicking the Windows Start button, select All Programs > Renesas Electronics e2studio > Renesas e2studio.

e <sup>2</sup> Workspace Launcher				
Select a workspace				
e2studio stores your projects in a folder called a workspace. Choose a workspace folder to use for this session.				
Workspace: C:\Users\rehmani1\My Documents\e2studio\workspace	▼ <u>B</u> rowse			
Use this as the default and do not ask again	OK Cancel			

Select <OK>.

e² Adm	inistrator Privilege	×
?	Administrative privileges are required for correct operation of e2Studio on Windows 7. Please ensure you have appropriate privileges (i.e., Right click renesas-eclipse.exe and choose "Run as Administrator")	
	Do you want to continue?	
	Yes <u>N</u> o	

Select <Yes> to Administrator Privilege dialog.





On the welcome screen select Go the Workbench icon as shown above.

1. Once the e2studio environment has initialised, right click in the project explorer window and click <Import...>

	C/C++ + e2studio			
No consides to display at this time.	Project Explorer 3	New	15	An outline is not available.
No consides to display at this time.			🚹 Problems 🕘 Tasks 🖾 Console 🗵 🔪 🗇 Properties 🔫 Progress	S Q - 13 - 90
			The consists to stuppy at this time.	
	0 items selected		II.	(a) 3 K 9 0
				- 😋 🍋 🍓 10-54



2. The Import dialog will now appear . Expand the "General" folder icon, and select "Existing Projects into Workspace", then click 'Next'.

e2 Import					
Select Create new projects from an archive file or directory.					
Select an import source:					
Image: Ward of the second state of					
(?) < <u>Back</u> <u>Next</u> > <u>Finish</u>	Cancel				

3. The Import Dialog will now appear ,specify the project to import .Click the "Browse" button and locate the directory: C:\Workspace\RSK\RSKRL78G14

And also ensure that the 'Copy projects into workspace' option is ticked, and then click <Finish>



Navigate to the unzipped ELC folder located in RSKRL78G14 Workspace folder.

Select the ELC folder

e2 Import				
Import Projects Select a directory to sear	ch for existing Eclipse projects.			
<ul> <li>Select roo<u>t</u> directory:</li> <li>Select <u>a</u>rchive file:</li> <li><u>P</u>rojects:</li> </ul>	C:\WorkSpace\RSK\RSKRL78G14\ELC	Browse		
ELC (C:\WorkSpa	<u>S</u> elect All <u>D</u> eselect All <u>Re</u> fresh			
Copy projects into wo Working sets Add projec <u>t</u> to work W <u>o</u> rking sets:		S <u>e</u> lect		
?	< <u>B</u> ack <u>N</u> ext > Finish	Cancel		

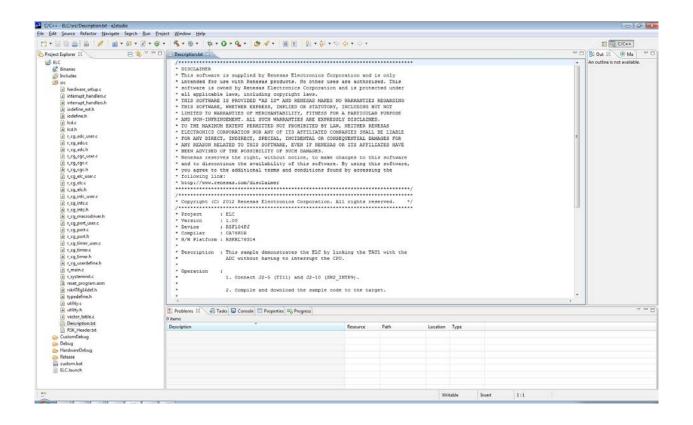
Click <Finish>.

The IDE e2studio will load the project.



#### 3. Opening Sample Code and Source Files

Once the project has been opened, the source code and all dependant files can be opened in the editor by expanding the folders in the Project Tree window and double clicking the files listed. All files have been grouped according to their file type.



#### 4. Source Code Functionality

The source code project is specifically written to run on the appropriate RSK. However this source code can be useful as an example even without the RSK.

The project was written using source files containing API functions generated using Code Generator. The project will contain a C source file 'r\_main.c'. This source file includes the C function main(). All source files and dependant files whose filenames are prefixed with 'r\_' were generated using Code Generator.



#### 5. Code Execution

1. Connect J2-5 (TI11) and J2-10 (SW2\_INTP9).

2. Compile and download the sample code to the target.

3. Click 'Restart' icon on the 'Debug' toolbar to start the software.

4. Adjust the potentiometer, RV1, press and hold switch SW2 for a period of up to 50 seconds.

5. The number of events is displayed on the first line of the LCD. The duration of the switch press will be updated on the second line of the LCD once the switch has been released.

6. Press SW1 to display the ADC value on the second line of the LCD. Press SW3 to display the last recorded switch press duration.

7. Go to Step 4 to repeat the test.

### 6. Website, Inquiries and Support

Renesas Electronics Website <u>http://www.renesas.com/</u> Inquiries <u>http://www.renesas.com/inquiry</u> Support <u>http://www.renesas.com/rskrl78g14</u>

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# **Revision Record**

		Description	
Rev.	Date	Page	Summary
1.00	Oct 12, 2012	—	First edition issued

# General Precautions in the Handling of MPU/MCU Products

The following usage notes are applicable to all MPU/MCU products from Renesas. For detailed usage notes on the products covered by this manual, refer to the relevant sections of the manual. If the descriptions under General Precautions in the Handling of MPU/MCU Products and in the body of the manual differ from each other, the description in the body of the manual takes precedence.

- 1. Handling of Unused Pins
  - Handle unused pins in accord with the directions given under Handling of Unused Pins in the manual.
  - The input pins of CMOS products are generally in the high-impedance state. In operation with an unused pin in the open-circuit state, extra electromagnetic noise is induced in the vicinity of LSI, an associated shoot-through current flows internally, and malfunctions occur due to the false recognition of the pin state as an input signal become possible. Unused pins should be handled as described under Handling of Unused Pins in the manual.
- 2. Processing at Power-on

The state of the product is undefined at the moment when power is supplied.

 The states of internal circuits in the LSI are indeterminate and the states of register settings and pins are undefined at the moment when power is supplied.

In a finished product where the reset signal is applied to the external reset pin, the states of pins are not guaranteed from the moment when power is supplied until the reset process is completed. In a similar way, the states of pins in a product that is reset by an on-chip power-on reset function are not guaranteed from the moment when power is supplied until the power reaches the level at which resetting has been specified.

- 3. Prohibition of Access to Reserved Addresses
  - Access to reserved addresses is prohibited.

The reserved addresses are provided for the possible future expansion of functions. Do not access
these addresses; the correct operation of LSI is not guaranteed if they are accessed.

4. Clock Signals

After applying a reset, only release the reset line after the operating clock signal has become stable. When switching the clock signal during program execution, wait until the target clock signal has stabilized.

 When the clock signal is generated with an external resonator (or from an external oscillator) during a reset, ensure that the reset line is only released after full stabilization of the clock signal. Moreover, when switching to a clock signal produced with an external resonator (or by an external oscillator) while program execution is in progress, wait until the target clock signal is stable.

#### 5. Differences between Products

Before changing from one product to another, i.e. to one with a different type number, confirm that the change will not lead to problems.

— The characteristics of MPU/MCU in the same group but having different type numbers may differ because of the differences in internal memory capacity and layout pattern. When changing to products of different type numbers, implement a system-evaluation test for each of the products.

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