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April 1<sup>st</sup>, 2010  
Renesas Electronics Corporation

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# H8/300H Tiny Series

## Power-On Reset Operation Using Internal Circuit

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### Introduction

An optional internal circuit in the H/3687 is used to perform power-on reset operations.

### Target Device

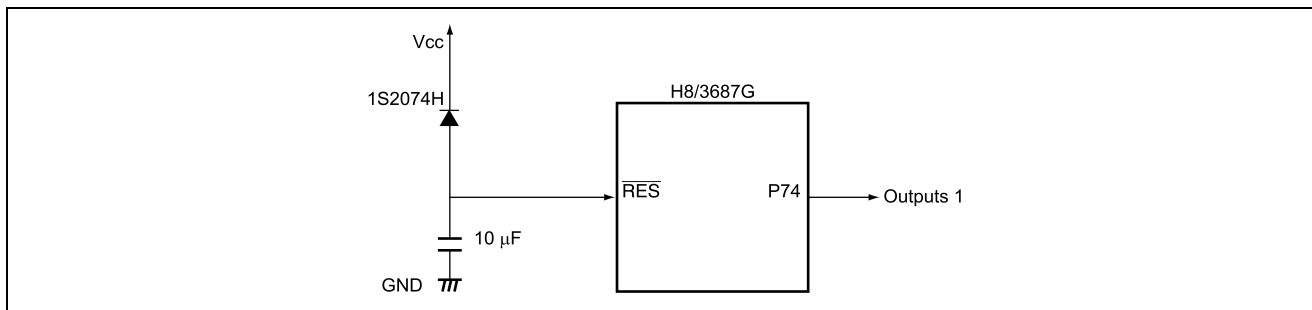
H8/3687G

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### 1. Specifications

1. An optional internal circuit in the H8/3687G is used for the H8/3687 externally to perform power-on reset operation. Figure 1.1 shows an example of microcomputer connections when using an internal power-on reset circuit.
2. 1 is output from pin P74 after reset cancellation.

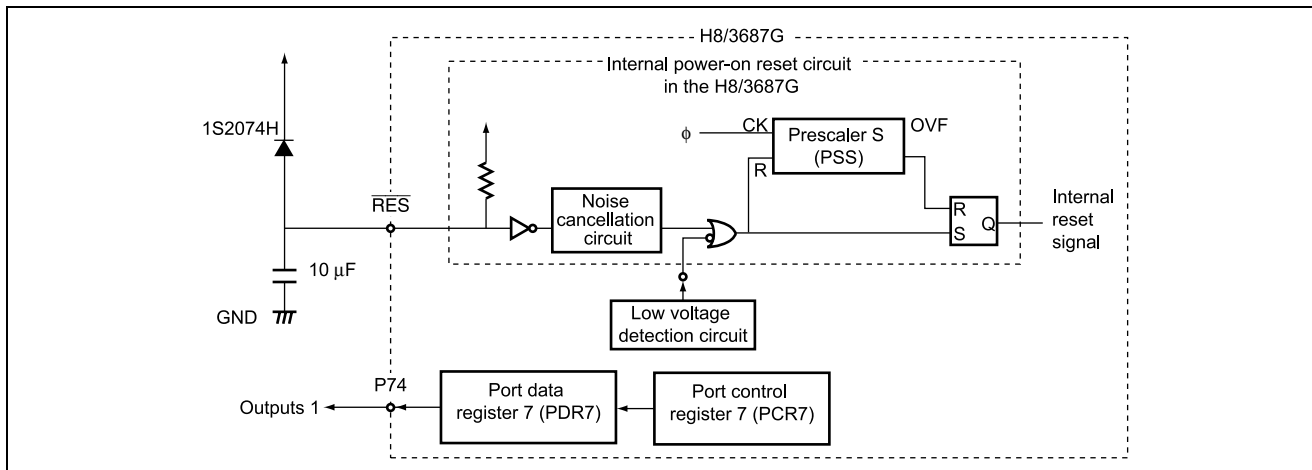


**Figure 1.1 Microcomputer connections when using an internal power-on reset circuit**

### 2. Description of Functions

In this sample task, an optional internal circuit in the H8/3687 is used to perform power-on reset operation. Figure 2.1 is a block diagram of the internal power-on reset circuit. Below the block diagram of the internal power-on reset circuit is described.

- System clock ( $\phi$ ) is a 16 MHz clock which serves as the reference clock for operation of the CPU and peripheral functions.
- Prescaler S (PSS): is functions as a 13-bit counter when  $\phi$  is input, counting up one each cycle.
- Port data register 7 (PDR7) is P74 of port 7 is set to 1 in order to confirm reset cancellation.
- Port control register 7 (PCR7) is sets P74 of port 7 to function as an output pin.



**Figure 2.1 Block diagram of internal power-on reset circuit in H8/3687**

### 3. Description of Operation

Power-on reset operation by this sample circuit is illustrated in figure 3.1.

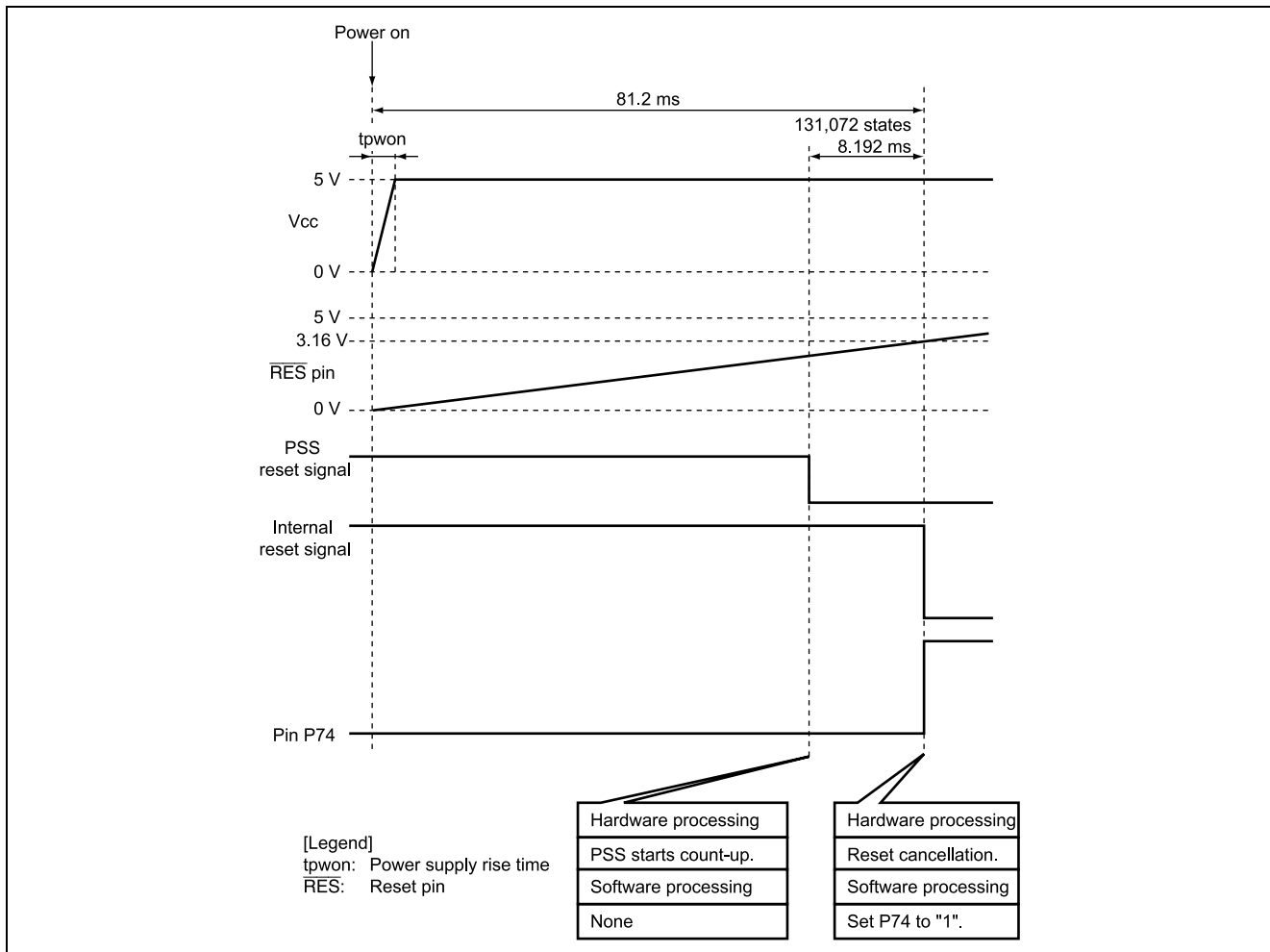


Figure 3.1 Description of operation

## 4. Description of Software

### 4.1 Description of module

The module used in this sample task is described in table 4.1.

**Table 4.1 Description of module**

Module name	Label name	Function
Main routine	main	Output 1 from pin P74

### 4.2 Description of arguments

In this sample task, no arguments are used.

### 4.3 Description of internal registers used

The internal registers used in this sample task are described below.

- PDR7 Port data register 7 Address: 0xFFDA

Bit	Bit name	Setting	Description
4	P74	1	Port data register 74 P74 = 0: Pin P74 output level Low P74 = 1: Pin P74 output level High

- PCR7 Port control register 7 Address: 0xFFEA

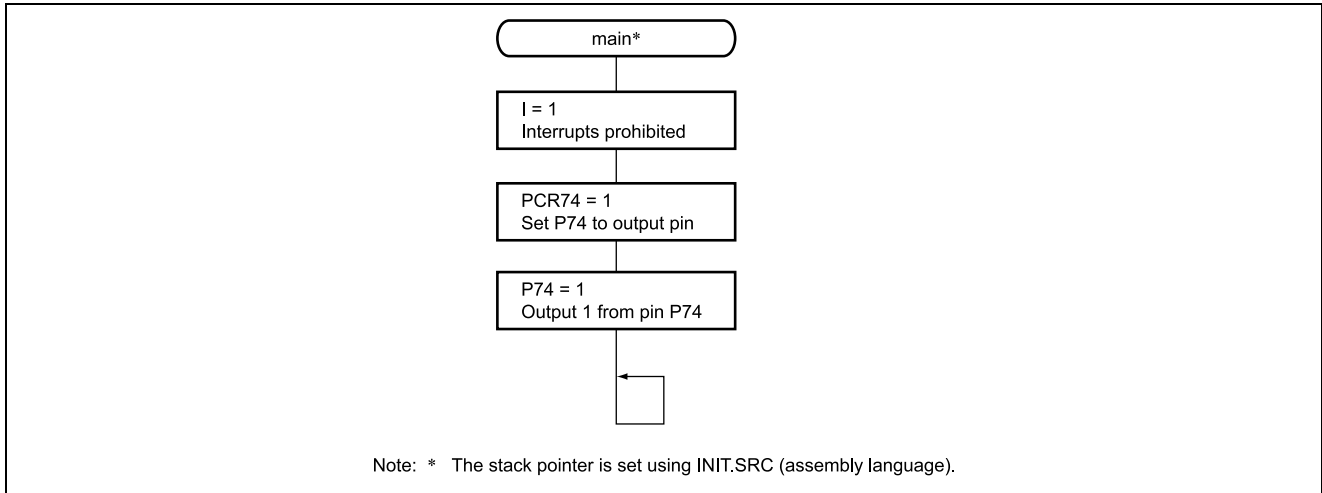
Bit	Bit name	Setting	Description
4	PCR74	1	Port control register 74 PCR74 = 0: Set pin P74 as P74 input pin PCR74 = 1: Set pin P74 as P74 output pin

### 4.4 Description of RAM used

In this sample task, RAM is not used.

### 5. Flowcharts

Main routine



## 6. Program Listing

```

/*****
/*
/* H8/300HN Series -H8/3687G-
/* Application Note
/*
/* Function
/* : Power on Reset ◆◆◆◆
/*
/* External Clock : 16MHz
/* Internal Clock : 16MHz
/* Sub Clock      : 32.768kHz
/*
/*****

#include <machine.h>

/*****
/* Symbol Definition
/*****

struct BIT {
    unsigned char  b7:1;      /* bit7 */
    unsigned char  b6:1;      /* bit6 */
    unsigned char  b5:1;      /* bit5 */
    unsigned char  b4:1;      /* bit4 */
    unsigned char  b3:1;      /* bit3 */
    unsigned char  b2:1;      /* bit2 */
    unsigned char  b1:1;      /* bit1 */
    unsigned char  b0:1;      /* bit0 */
};

#define PDR7_BIT      (*(struct BIT *)0xPFDA)      /* Port Data Register 7
#define P74            PDR7_BIT.b4                /* Port Data Register 7 bit4
#define PCR7_BIT      (*(struct BIT *)0xPFEA)      /* Port Control Register 7
#define PCR74         PCR7_BIT.b4                /* Port Control Register 7 bit4
/*****
/* Function define
/*****

extern void INIT ( void );      /* SP Set
void main ( void );

/*****
/* Vector Address
/*****

#pragma section V1              /* VECTOR SECTOIN SET
void (*const VEC_TBL1[])(void) = {
    INIT                          /* 00 Reset
};

#pragma section                  /* P

```



```
/*-----*/  
/* Main Program */  
/*-----*/  
void main ( void )  
{  
    PCR74 = 1;  
    P74 = 1;  
    while(1);  
}
```

## Link address specifications

Section Name	Address
CV1	0x0000
P	0x0100

### Revision Record

Rev.	Date	Description	
		Page	Summary
1.00	Sep.29.03	—	First edition issued

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