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M16C/28, 29 Group

Operation of SI/O3, 4

1. Abstract

In transmitting data in clock-synchronous serial I/O mode, choose functions from those listed in Table1. Operations of the checked items are described below.

Table 1. Choosed Functions

Item		Set-up	Item	Set-up		
Transfer clock	Yes	Internal clock (f1/f2/f8/f32)	SOUTi initial value	Yes	Yes Not used	
source		External clock (CLKi pin)	function		Used	
Transfer format	Yes	LSB first				
		MSB first				

2. Introduction

The explanation of this issue is applied to the following condition:

Applicable MCU: M16C/28, M16C/29 Group

This program can also be used when operating other microcomputers within the M16C family, provided they have the same SFR (Special Function Registers) as the M16C/28, M16C/29 microcomputers. However, some functions may have been modified.

Refer to the User's Manual for details. Use functions covered in this Application Note only after careful evaluation.

3. Operation of Serial I/O

- (1) Transfer begins upon writing the SI/Oi transmit data. The transmit data is sent out from the S_{OUTi} pin synchronously with falling edges of the transfer clock.
- (2) When Souti finishes sending one byte of data, the interrupt request bit is set to "1".
- (3) After the transfer is completed, S_{OUTi} holds the last data for a 1/2 transfer clock period before going to a high-impedance state.

Supplementation

- Do not write data to the SI/Oi transmit/receive register ($i=3,\ 4$; addresses $0360_{16},\ 0364_{16}$) during a transfer.
- Data can only be written to the SI/Oi transmit/receive register when the device is idle neither sending nor receiving data.

Figure 1 shows the operation timing.



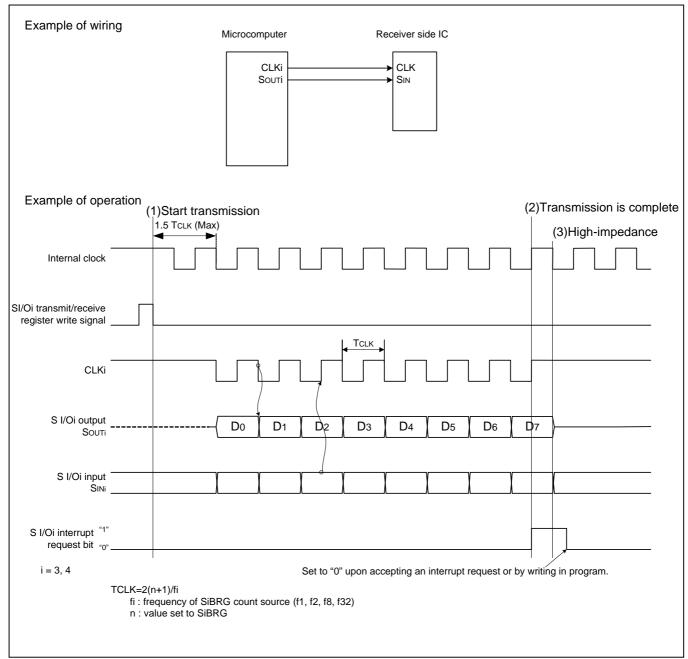


Figure 1. Operation Timing of Transmission, Reception Timing



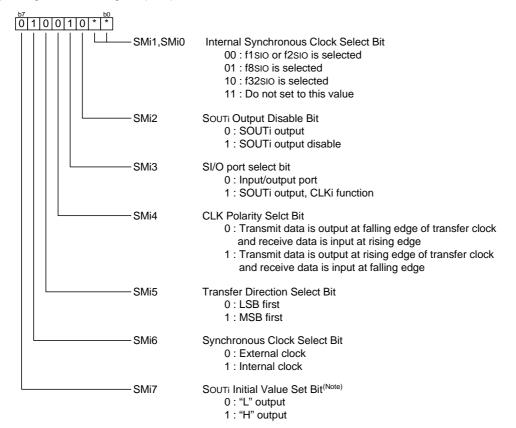
3.1 Register Setting

To enable the operation defined in "Section 3. Operation of timer A", the following register settings must be taken place step by step. For detail configuration of each register, please refer to M16C/26 Group hardware manual, M16C/26A Group hardware manual, M16C/28 Group hardware manual, M16C/29 Group hardware manual.

(1) If used SI/O4, write enable.

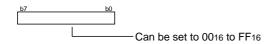


(2) Setting SI/Oi control register (i=3,4)



Note: The SI/Oi initialize function permits the output level to be set from when the SMi3 bit is set to "1" after setting the SMi7 bit till output of the first byte of data starts. After one byte is transmitted, the level output of the last bit (D7) is retained no matter how the SMi7 bit is set until clock for the next data is input.

(3) SI/Oi baud rate generation register (i=3,4)



(4) SI/Oi transmit/receive register (i=3,4)





4. Sample Program

```
(SI/O4)
```

```
*************
    FILE NAME :
   CPU : M16C/Tiny series
Function : Operation of SIO3,4
Version : 1.00
   Copyright (C)2004, Renesas Technology Corp.
 * Copyright (C)2004, Renesas Solutions Corp.
* include file
**********
#include "sfr28.h"
* Function Definition
voidport_init(void);
* main
***********
void main(void) {
   unsigned char out_data = 0;
   port_init();
   s4ic = 0;
   prc2 = 1;
                  /* Protect bit 2 Write enable */
   s4c = 0x49; /* f8sio select
               Sout, Sclk outupt enable
               Transmit data is output at falling edge of transfer clock
               LSB first
               Internal clock */
   s4brg = 7;
   while (1) {
       s4trr = out_data; /* Writing transfer data --> transfer begin */
       while ( !ir_s4ic ) { /* Check SI/O interrupt request */
       ir_s4ic = 0;
                      /* Clear interrupt request bit */
                      /* Transfer data up_date */
       out data++;
voidport_init() {
   p0 = 0;
   p1 = 0;
   p2 = 0;
   p3 = 0;
   p7 = 0;
   p8 = 0;
   p9 = 0;
   p10 = 0;
   pd0 = 0x00;
   pd1 = 0x00;
   pd2 = 0xff;
   pd3 = 0x00;
   pd7 = 0x00;
   pd8 = 0x00;
   pd9 = 0x00;
   pd10 = 0x00;
```



5. Usage Example

The following example shows SI/O4 output.

Conditions: Supply voltage = 5V,

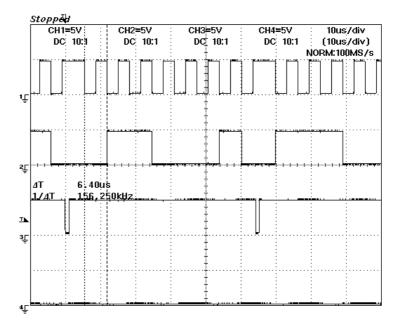
Main clock $(X_{IN}) = 20MHz$

Internal synchronous clock= f_{8SIO}

SI/O4 baud rate generation register set value = 7

 S_{CLK4} output

 $S_{\text{OUT4}} \ output$





6. Reference

Renesas Technology Corporation Home Page http://www.renesas.com/

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Hardware Manual M16C/28, M16C/29 Group Hardware Manual (Use the latest version on the home page: http://www.renesas.com)

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