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# H8/300H Tiny Series

## Block Transfer Using Block Transfer Instruction (EEPROM)

## Introduction

Transfers a block of data (up to 65535 bytes) to any even-numbered address by using the block transfer instruction (EEPMOV.W).

## **Target Device**

H8/300H Tiny Series

## **Contents**

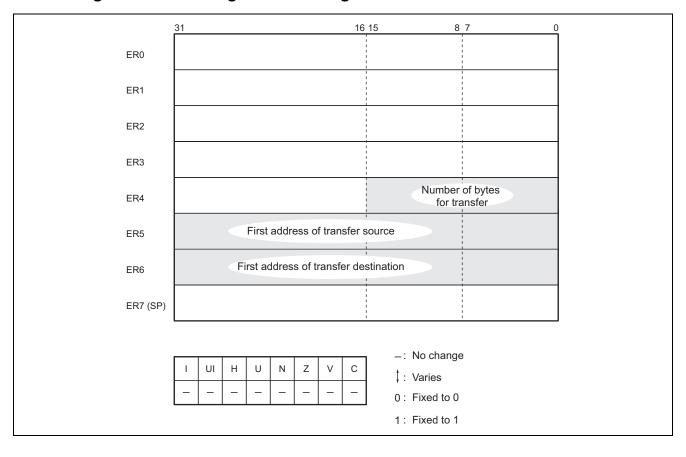
1.	Arguments	2
	Changes to Internal Registers and Flags	
	Programming Specifications	
4.	Note	. 3
5.	Description	. 4
6.	Flowchart	6
7	Program Listing	6



## 1. Arguments

Description		Storage Location	Data Length (Bytes)	
Input	First address of the transfer source	ER5	4	
	First address of the transfer destination	ER6	4	
	Number of bytes to be transferred	R4	2	
Output	_	_	<u> </u>	

## 2. Changes to Internal Registers and Flags



# 3. Programming Specifications

Γ	Program memory (bytes)
ŀ	4
f	Data memory (bytes)
ţ	0
F	Stack (bytes)
f	0
t	Number of cycles
f	262148
f	Re-entrant
Ī	Yes
	Relocatable
	Yes
	Interrupts during execution
	No

## 4. Note

The number of cycles given in the programming specifications is the value when H'FFFF bytes are transferred.



#### 5. **Description**

#### **Description of Functions** 5.1

- 1. The arguments are as follows:
  - ER5: Set the first address (even address) of the transfer source.
  - ER6: Set the first address (even address) of the transfer destination.
  - R4: Set the number of bytes to be transferred.
- 2. The following figure illustrates the execution of the EEPROM subroutine.

When the input arguments are set as shown, the data at the transfer source is transferred, as a block, to the transfer destination.

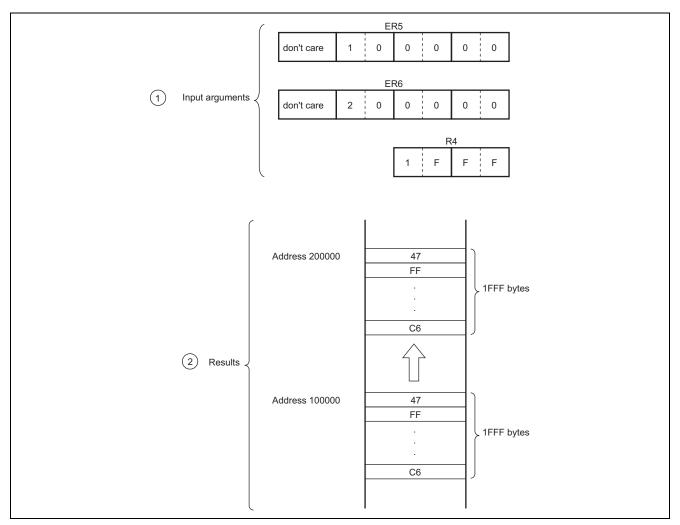


Figure 1 Example of EEPROM Execution



## 5.2 Usage Notes

- 1. Since R2 is 2 bytes, set data within the range  $H'0001 \le R2 \le H'FFFF$ .
- 2. Interrupts cannot be detected while EEPROM is being executed.
- 3. Set the input arguments so that the block of data at the transfer source (area A in the figure) and the block of data at the transfer destination (area B) do not overlap. When there is an overlap, as shown in the figure, the source data for transfer in the area of overlap (area C) will be lost.

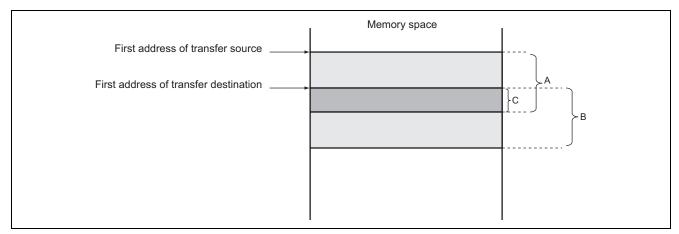


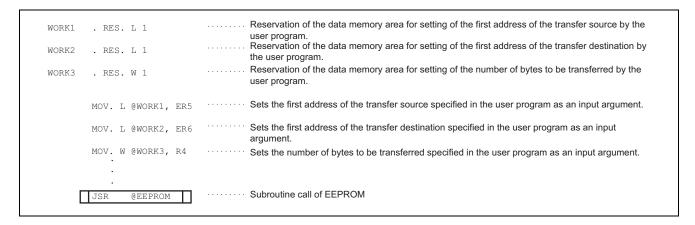
Figure 2 Block Transfer with Overlapping Data

## 5.3 Description of Data Memory

No data memory is used by EEPROM.

## 5.4 Example of Usage

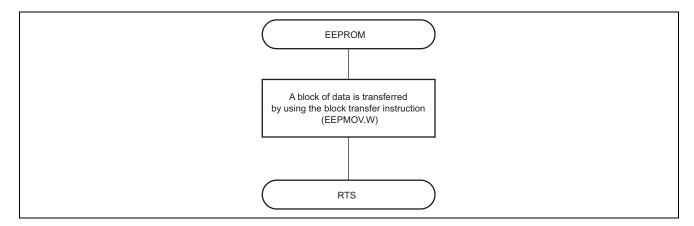
After setting the first address of the transfer source, the first address of the transfer destination and the number of bytes to be transferred, call the EEPROM subroutine.



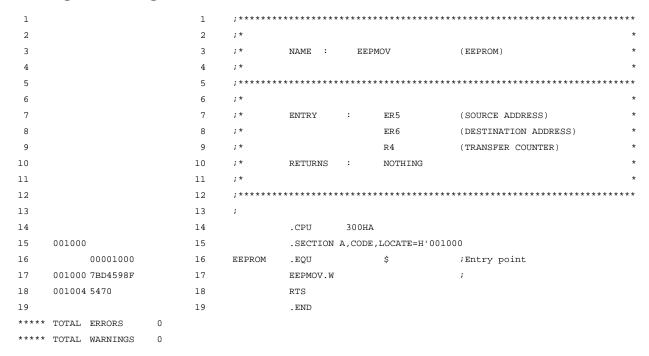
## 5.5 Principle of Operation

The block transfer instruction (EEPMOV.W) is used.

## 6. Flowchart



## 7. Program Listing



Note: The program listing included in this application note assumes compilation under the option for the advanced mode of H8/300H CPU. If you use this sample program with an H8/300H Tiny Series product, make the following change to the program code:

.CPU 300HA → .CPU 300HN

# H8/300H Liny Series Block Transfer Using Block Transfer Instruction (EEPROM)

## **Revision Record**

		Description			
Rev.	Date	Page	Summary		
2.00	Feb.28.06	_	Format has been changed from Hitachi version to Renesas version.		



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