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7544 Group A-D Converter

1. Abstract

The following article introduces and shows an application example of converting the analog input voltage input to digital values.

2. Introduction

The explanation of this issue is applied to the following condition: Applicable MCU: 7544 Group



3. Contents

3.1 Setting of A-D converter

Figure 1 shows the relevant registers setting.

Process 1: Disable A-D conversion interrupt.					
b7 b0 Interrupt control register 2 (ICON2) [Address 3F16]					
A-D conversion interrupt disabled					
Process 2: Set A-D control register.					
A-D control register (ADCON) [Address 3416] Analog input pins selected ^{b2b1b0} 0 0 0: P20/AN ₀ 0 0 1: P21/AN1 0 1 0: P22/AN2 0 1 1: P23/AN3 1 0 0: P24/AN4 1 0 1: P25/AN5 1 1 0: Not available 1 1 1: Not available					
Process 3: In order not to execute the no requested interrupt processing, set "0" (no requested) to the A-D conversion interrupt request bit.					
b7 b0 Interrupt request register 2 (IREQ2) [Address 3D16]					
Process 4: When the interrupt is used, set "1" (interrupt enabled) to the A-D conversion interrupt enable bit.					
Process 5: Start A-D conversion.					





3.2 Example of Control Procedure

Outline: The analog input voltage input from a sensor is converted to digital values. Specifications: •The analog input voltage input from a sensor is converted to digital values. •P2₀/AN₀ pin is used as an analog input pin.

Figure 2 shows a connection diagram, and Figure 3 shows an example of control procedure.

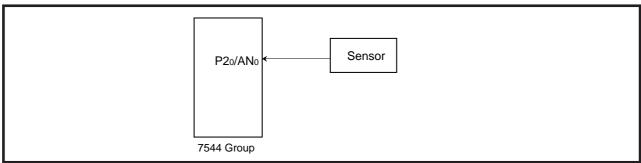


Figure 2 Connection diagram

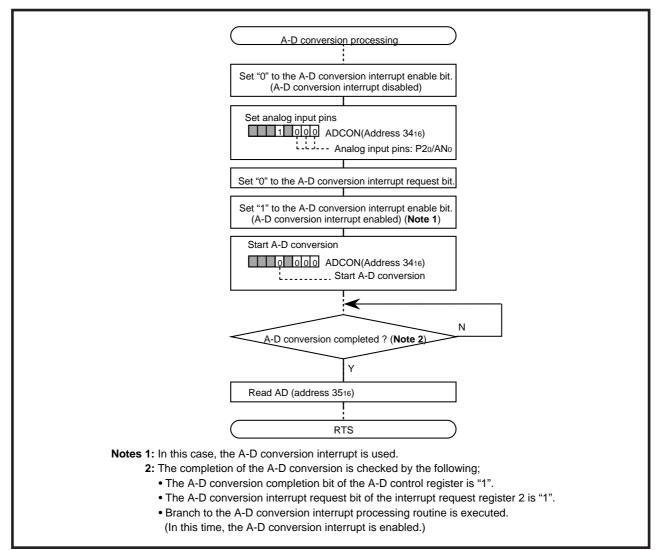


Figure 3 Control procedure



4. Sample Programming Code

```
[Reset Start ••• Main Routine Process]
RESET:
          SEI
                                    ; Interrupt disable
          CLD
          CLT
;
          LDX #$FF
                                    ; Set stack bottom
          TXS
;
          LDM #%1000000,CPUM ; Set CPU mode register
; Wait f(XIN) oscillation stabilizing time
          LDM #%0000000,CPUM ; Set CPU mode register
;
          LDA #0
          LDX #>RAM_top
RAM_clear:STA $00,X
          INX
          BNE RAM_clear
;
;
      _____
                    _____
; --
 _MAIN:
         JSR AD_CONVERT
___MAIN_00:
         BRA MAIN
;
[A-D Conversion Process]
AD CONVERT:
          CLB 4,ICON2 ; disable A/D converter interrupt

LDM #%00010000,ADCON ; set analog input pin (P2_0/AN0)

CLB 4,IREQ2 ; clear A/D converter interrupt request
          CLB 4, IREQ2
                                   ; enable A/D converter interrupt
;
          SEB 4,ICON2
                                    ; (When A/D interrupt is used)
; start A/D convert
          CLB 4, ADCON
;
AD_CONVERT_00:
               4, ADCON, AD CONVERT 00; completed A/D convert ? -> no
          BBC
          LDA AD
          STA Read_Data ; read A/D convert value
          RTS
;
```



5. Reference

Data Sheet 7544 Group Data sheet 7544 Group Data sheet (QzROM Version)

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REVISION HISTORY

7544 Group A-D Converter

Rev.	Date	Description	
		Page	Summary
1.00	Apr 01, 2003	-	First Edition issued
2.00	Nov 12, 2004	4	Sample Programming Code added.



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