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# 7542 Group

### Termination of Unused Pins

#### 1. Abstract

The following article introduces and shows the termination of unused pins on the 7542 Group device.

#### 2. Introduction

The explanation of this issue is applied to the following condition: Applicable MCU: 7542 Group



#### 3. Contents

#### 3.1 Termination of Unused Pins

Table 3.1 Termination of Unused Pins

Pin/Port Name	Termination		
P0, P1, P2, P3	<ul> <li>Set a port to the input mode and connect the port to Vcc or Vss through a 1 kΩ kΩ resistor. The port which can select a built-in pull-up resistor can also use th in pull-up resistor.</li> <li>Set a port to the output mode, and leave the port open at "H" or "L".</li> </ul>		
VREF	Connect to Vss (GND).		
Xin	When an on-chip oscillator is used as the main clock, connect this pin to Vcc through a 1 k $\Omega$ to 10 k $\Omega$ resistor.		
Хоит	<ul><li>When an external clock is used, leave this pin open.</li><li>When an on-chip oscillator is used as the main clock, leave this pin open.</li></ul>		

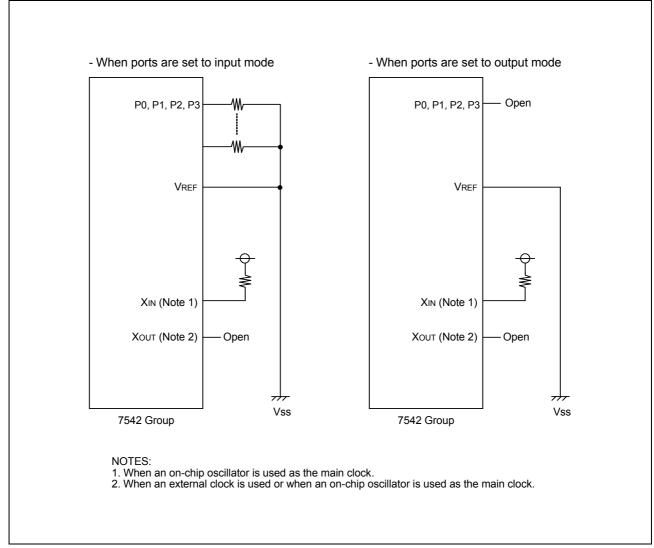


Figure 3.1 Termination Example of Unused Pins



#### 4. Reference

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Data Sheet 7542 group

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7542 Group Termination of Unused Pins Application Note

Rev.	Date		Description
		Page	Summary
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