

# AN-1208 Overcurrent Protection Device

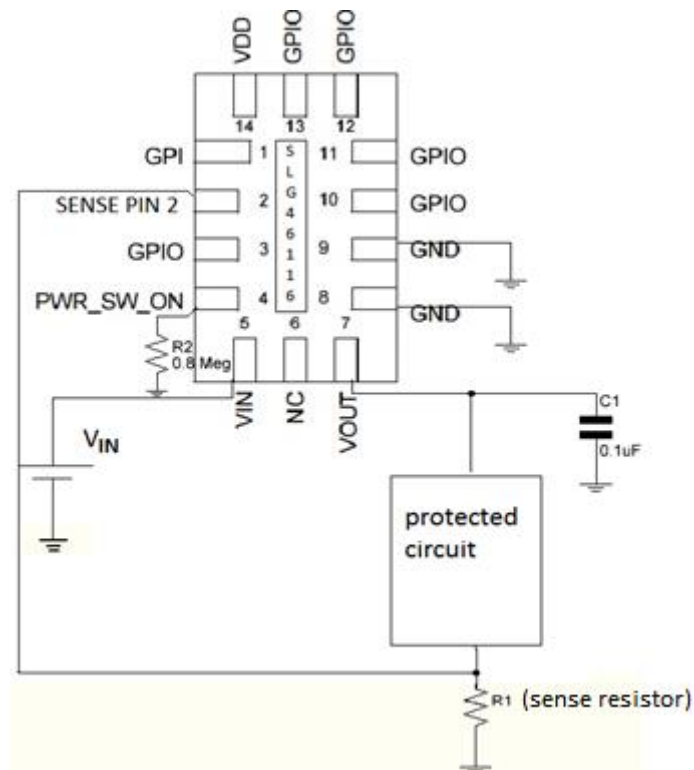
# Introduction

In this application note, we'll build an overcurrent protection device. Overcurrent protection is protection against excessive currents or current beyond the acceptable current rating of the device. To protect downstream loads, the point of overcurrent protection should occur at the FET switch.

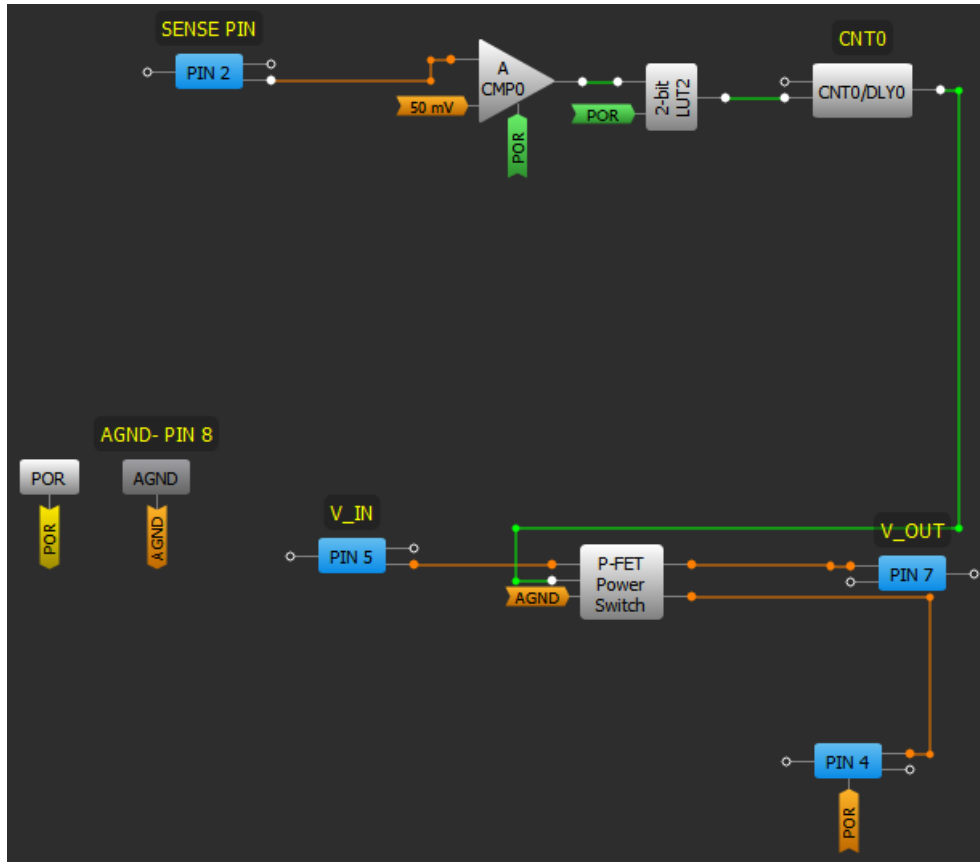
An overcurrent protection device can be implemented using a GreenPAK™ 3 SLG46116V chip. The SLG46116 includes a P-FET Power Switch designed for load switching applications. The P-FET Power Switch can be controlled internally via the ON digital input of the P-FET Power Switch component in GreenPAK Designer, allowing the user to generate integrated mixed-signal control circuits.

The P-FET Power Switch contains a 28.5mΩ RDSON, 1.25 A P-channel MOSFET with fixed slew rate control. The device has a built-in soft-start, which controls the output rise time. This minimizes inrush current when the switch is enabled.

For this implementation, the protected circuit is connected to the voltage supply via the SLG46116V chip P-FET. When the current in the protection circuit reaches the preset maximum current rating value, the GreenPAK device can almost instantly turn off the P-FET power switch and stop the flow of “overcurrent”.



### Figure 1. Overcurrent Protection Application Circuit with the SLG4616V chip



**Figure 2. Overcurrent Protection Circuit GPAK3: SLG46116V Design**

## Circuit Design and Analysis

An overcurrent protection application circuit with the block diagram of the SLG46116V chip is shown in Figure 1, and the internal design of the chip is shown in Figure 2.

As seen in Figure 2, besides the P-FET switch, the design requires additional components, one ACMP, one 2-bit LUT and one DLY.

To use the ACMP in a GreenPAK design, the power up signal (PWR UP) needs to be active (HIGH). Before the power-up signal POR, the ACMP is powered down. Its output is low, the output of LUT2 is low, and the ON signal of P-FET is inactive. After the power-up signal POR, if the current does not exceed the rated limit, then the ON signal will be active (high) and the switch is closed providing the power supply to the connected circuit.

The overcurrent limit threshold can be programmed/designed by selection of external resistor R1 in Figure 1, which is connected between the protected circuit and GND, and by selection of the IN- Vref value of ACMP, which has 24 internal reference sources (50 mV – 1200 mV) and VDD/3, VDD/4, PIN 4.

The current limit can be calculated as the  $I_{N-}$   $V_{ref}$  divided by the external resistor  $R1$ . In this design example  $I_{N-}$  is 50mV and  $R1 = 0.2 \Omega$ , thus the current limit is equal to 250 mA.

If the current exceeds 250mA the output of ACMP is low, the ON signal is low (inactive) and the P-FET switch is open, thus the overcurrent flow is interrupted.

Once the overcurrent problem has been interrupted, the delay time of DLY0 (adjustable) will define the recovery time after which the P-FET will switch into the "ON" position and the protected circuit will resume being powered. This can cause a continuous cycle of switching on and off the PFET if the overcurrent is not resolved.

The domain for  $V_{IN}$ : 1.5 V to 5.0 V. When ON is configured to be controlled internally, PWR\_SW\_ON (PIN 4) is configured as Push-Pull output.

When  $V_{IN}$  is not used in the same voltage domain as  $V_{DD}$ , using a large pull-down resistor on PWR\_SW\_ON (PIN 4) is recommended to prevent current from flowing through the P-FET Power Switch while the device is not powered. Also, to prevent glitches at the output, it is recommended to connect at least 0.1  $\mu F$  capacitor from VOUT pin to the GND.

## Conclusion

An overcurrent protection device can be easily implemented using a GreenPAK 3 that includes a P-FET Power Switch. It has low power consumption and few external components.

## IMPORTANT NOTICE AND DISCLAIMER

RENESAS ELECTRONICS CORPORATION AND ITS SUBSIDIARIES ("RENESAS") PROVIDES TECHNICAL SPECIFICATIONS AND RELIABILITY DATA (INCLUDING DATASHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES "AS IS" AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS OR IMPLIED, INCLUDING, WITHOUT LIMITATION, ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, OR NON-INFRINGEMENT OF THIRD-PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for developers who are designing with Renesas products. You are solely responsible for (1) selecting the appropriate products for your application, (2) designing, validating, and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, or other requirements. These resources are subject to change without notice. Renesas grants you permission to use these resources only to develop an application that uses Renesas products. Other reproduction or use of these resources is strictly prohibited. No license is granted to any other Renesas intellectual property or to any third-party intellectual property. Renesas disclaims responsibility for, and you will fully indemnify Renesas and its representatives against, any claims, damages, costs, losses, or liabilities arising from your use of these resources. Renesas' products are provided only subject to Renesas' Terms and Conditions of Sale or other applicable terms agreed to in writing. No use of any Renesas resources expands or otherwise alters any applicable warranties or warranty disclaimers for these products.

(Disclaimer Rev.1.01)

### Corporate Headquarters

TOYOSU FORESIA, 3-2-24 Toyosu,  
Koto-ku, Tokyo 135-0061, Japan  
[www.renesas.com](http://www.renesas.com)

### Contact Information

For further information on a product, technology, the most up-to-date version of a document, or your nearest sales office, please visit [www.renesas.com/contact-us/](http://www.renesas.com/contact-us/).

### Trademarks

Renesas and the Renesas logo are trademarks of Renesas Electronics Corporation. All trademarks and registered trademarks are the property of their respective owners.