Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

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April 1st, 2010 Renesas Electronics Corporation

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740 FAMILY MASK ROM CONFIRMATION FORM SINGLE-CHIP MICROCOMPUTER M38027M8DXXXSP/FP RENESAS TECHNOLOGY

	Date:	
eipt	Section head signature	Supervisor signature
Receipt		

Note: Please fill in all items marked	Note: I	Please	fill in	all items	marked	*
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*	Customer	Company name		TEL ()	uance nature	Submitted by	Supervisor
AV.	Oustomer	Date issued	Date:		Issi		

* 1. Confirmation

Specify the name of the product being ordered.

Three EPROMs are required for each pattern if this order is performed by EPROMs.

One floppy disk is required for each pattern if this order is performed by a floppy disk.

Microcomputer name:	☐ M38027M8DXXXSP	☐ M38027M8DXXXFF
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☐ Ordering by EPROMs

If at least two of the three sets of EPROMs submitted contain identical data, we will produce masks based on this data. We shall assume the responsibility for errors only if the mask ROM data on the products we produce differs from this data. Thus, extreme care must be taken to verify the data in the submitted EPROMs.

Checksum code for entire EPROM			(hexadecimal notation

EPROM type (indicate the type used)

□ 27512					
EPROM ac	ldress				
000016	Product name ASCII code :				
000F16	'M38027M8D'				
001016					
807F16					
808016	data				
FFFD16	ROM (32K-130) bytes				
FFFE16 FFFF16					

In the address space of the microcomputer, the internal ROM area is from address 808016 to FFFD16. The reset vector is stored in addresses FFFC16 and FFFD16.

- (1) Set the data in the unused area (the shaded area of the diagram) to "FF16".
- (2) The ASCII codes of the product name "M38027M8D" must be entered in addresses 000016 to 000816. And set the data "FF16" in addresses 000916 to 000F16. The ASCII codes and addresses are listed to the right in hexadecimal notation.

Address	
000016	'M' = 4D16
000116	'3' = 3316
000216	'8' = 3816
000316	'0' = 3016
000416	'2' = 3216
000516	'7' = 37 ₁₆
000616	'M' = 4D16
000716	'8' = 3816

Address	
000816	'D' = 4416
000916	FF16
000A16	FF16
000B16	FF16
000C16	FF16
000D16	FF16
000E16	FF16
000F16	FF16



(1/2)

Mask ROM number	
Mask Roll Hullibel	

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We recommend the use of the following pseudo-command to set the start address of the assembler source program because ASCII codes of the product name are written to addresses 000016 to 000816 of EPROM.

EPROM type	27512
The pseudo-command	*= △\$0000 .BYTE △'M38027M8D'

Note: If the name of the product written to the EPROMs does not match the name of the mask confirmation form, the ROM will not be processed.

responsibility for errors only if the ma treme care must be taken to verify the	sk ROM data on the products vermask file in the submitted flopp	nask file generating utility. We shall assume the we produce differs from this mask file. Thus, expy disk. Irmat. And the number of the mask files must be
File code		(hexadecimal notation)
Mask file name		.MSK (equal or less than eight characters)
mark specification form (64P4B for M3 ROM confirmation form. ** 3. Usage conditions	38027M8DXXXSP, 64P6N for	package being ordered. Fill out the appropriate M38027M8DXXXFP) and attach it to the mask
Please answer the following questions		duct inspection:
(1) How will you use the XIN-XOUT oscillator' Ceramic resonator	? □ Quartz crystal	
External clock input	Other ()	
At what frequency?	f(XIN) = MHz	
(2) In which operation mode will you use you	ur microcomputer?	
☐ Single-chip mode	☐ Memory expansion mode	е
☐ Microprocessor mode		
* 4. Comments		

