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Renesas Starter Kit+ for SH7203

Tutorial Manual
RENESAS SINGLE-CHIP MICROCOMPUTER
SuperH RISC Engine

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Chapter 1. Preface

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Glossary

RSK	Renesas Starter Kit	PC	Program Counter
RSK+	Renesas Starter Kit Plus	ROM	Read Only Memory
E10A	'E10A for Starter Kit' Emulator	USB	Universal Serial Bus
HEW	High performance Embedded Workshop	ADC	Analog to Digital Converter
LED	Light Emitting Diode	LCD	Liquid Crystal Display
CPU	Central Processing Unit	SDRAM	Synchronous dynamic random access memory
CD	Compact Disk		

Chapter 2. Introduction

This manual is designed to answer, in tutorial form, the most common questions asked about using a Renesas Starter Kit (RSK): The tutorials help explain the following:

- How do I compile, link, download, and run a simple program on the RSK?
- How do I build an embedded application?
- How do I use Renesas' tools?

The project generator will create a tutorial project with two selectable build configurations

- 'Debug' is a project built with the debugger support included.
- 'Release' build demonstrating code suitable for release in a product.

Files referred to in this manual are installed using the project generator as you work through the tutorials. The tutorial examples in this manual assume that installation procedures described in the RSK Quick Start Guide have been completed. Please refer to the Quick Start Guide for details of preparing the configuration.

NOTE: These tutorials are designed to show you how to use the RSK and are not intended as a comprehensive introduction to the High performance Embedded Workshop (HEW) debugger or the compiler toolchains or E10A Emulator – please consult the relevant user manuals for more in-depth information.

Chapter 3. Tutorial Project Workspace

The workspace includes all of the files for two build configurations. The tutorial code is common to both the Debug and the Release build configurations. The tutorial is designed to show how code can be written, debugged then downloaded without the debug monitor in a 'Release' situation.

The build configuration menu in High-performance Embedded Workshop (HEW) allows the project to be configured such that certain files may be excluded from each of the build configurations. This allows the inclusion of the debug monitor within the Debug build, and its exclusion in the Release build. Contents of common C files are controlled with defines set up in the build configuration options and #ifdef statements within the source files.

Maintaining only one set of project files means that projects are more controllable.

Chapter 4. Project Workspace

4.1. Introduction

HEW is an integrated development tool that allows the user to write, compile, program and debug a software project on any of the Renesas Microcontrollers. HEW will have been installed during the software installation for the RSK product.

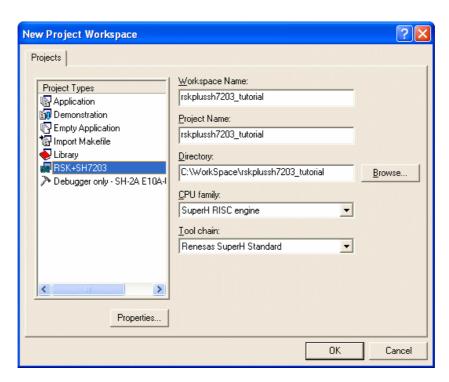
To begin using the RSK, this manual will describe the stages required to create and debug the supplied tutorial code.

4.2. Creating a new Project Workspace

To look at the program, start High-performance Embedded Workshop from the Windows Start Menu or from its icon:



Open a new tutorial workspace from the [File -> New Workspace...] menu or select 'Create a new project workspace' when presented with the 'Welcome!' dialog.



The example above shows the New Project Workspace dialog with the RSK+SH7203 selected.

- Select the 'SuperH RISC Engine' CPU family and 'Renesas SuperH Standard' Toolchain for the RSK
- Select the 'RSK+SH7203' Project type for the RSK from the project list.
- Enter a name for the workspace, all your files will be stored under a directory with this name.
- The project name field will be pre-filled to match the workspace name above; this name may be changed.
 Note: HEW allows you to add multiple projects to a workspace. You may add the sample code projects later so you may wish to choose a suitable name for the Tutorial project now.
- Click OK to start the RSK Project Generator wizard.

The next dialog presents the example projects available. Choose the Tutorial code which will be explained later in this manual. There is also an option for Sample code which provides examples for using various peripherals. This will open a new dialog allowing the selection of many code examples for the peripheral modules of the device. The final option is for an application build where the debugger is configured but there is no program code. This project is suitable for the user to add code without having to configure the debugger.

- Select 'Tutorial' as the type of project to generate and then click <Next>.
- Click <Finish> to create the project

The project generator wizard will display a confirmation dialog. Press <OK> to create the project and insert the necessary files.

A tree showing all the files in this project will appear in the HEW Workspace window.

To view the file main.c double click on the file in the Workspace window. A new window will open showing the code.

4.3. Build Configurations and Debug Sessions

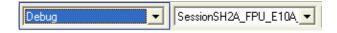
The workspace that has been created contains two Build Configurations and two Debug Sessions. The Build Configuration allows the same project to be built but with different compiler options. The options available to the user are described fully in the HEW Users Manual.

4.3.1. Build Configuration

The build configurations are selected from the left hand drop down list on the tool bar. The options available are Debug and Release. The debug build is configured for use with the debugger. The Release build is configured for final ROM-able code for the RSK.

A common difference between the two builds may be the optimisation settings. With Optimisation turned on the Debugger may seem to execute code in an unexpected order. To assist in debugging it is often helpful to turn off optimisation on the code being debugged.

• Select the Debug Build Configuration.



4.3.2. Debug Session

The debug sessions are selected from the right hand drop down list on the tool bar. The options vary between RSK however one will always start Debug and include the type of debug interface. The alternate selection will be 'DefaultSession'. This purpose of the debug session is to allow the use of different debugger targets or different debugger settings on the same project.

Select the session 'SessionSH2A_FPU_E10A_USB'



Chapter 5. Building the Tutorial Project

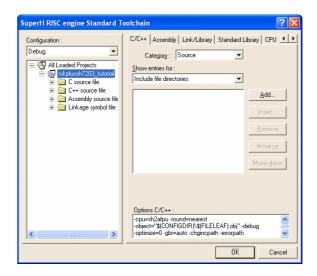
The tutorial project build settings have been pre-configured in the toolchain options. To view the tool chain options select the 'Build' Menu item and the relevant toolchain. This should be the first option(s) on the drop down menu.

The dialog that is displayed will be specific to the selected toolchain.

The configuration pane on the left hand side will exist on all the toolchain options. It is important when changing any setting to be aware of the current configuration that is being modified. If you wish to modify multiple or all build configurations this is possible by selecting 'All' or 'Multiple' from the 'Configuration' drop down list.

 Review the options on each of the tabs and 'Category' drop down lists to be aware of the options available.

When complete close the dialog box by clicking <OK>.



5.1. Building Code

There are three short cuts available for building the project.

- Select the 'Build All' tool bar button.

 This will build everything in the project that has not been excluded from the build. This includes the standard library.
- Select the 'Build' tool bar button.

 This will build all files that have changed since the last build. The standard library will not be built unless an option has been changed.
- Press 'F7'
 This is equivalent to pressing the 'Build' button described above.
- Build the project now by pressing 'F7' or pressing one of the build icons as shown above.

During the build each stage will be reported in the Output Window.

The build will complete with an indication of errors and warnings encountered during the build.

5.2. Connecting the debugger

For this tutorial connect an external power supply to the board, taking care to ensure the correct polarity and voltage.

The Quick Start Guide provided with the RSK board gives detailed instructions on how to connect the E10A to the host computer. The following assumes that the steps in the Quick Start Guide have been followed and the E10A drivers have been installed.

- Connect the E10A debugger to the USB port on your computer.
- Connect the E10A Debugger to the target hardware ensuring that it is plugged into the connector marked E10A
- Turn on the external power supply to the board.

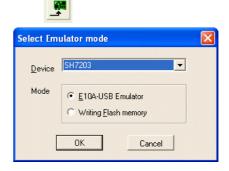
5.3. Connecting to the target

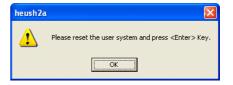
This section will take you through the process of connecting to the device, programming the Flash and executing the code.

- Click the connect button from the Tool Bar.
- On 'Select 'Emulator mode' dialog
 For <u>Device select 'SH7203'</u>
 For Mode select 'E10A_USB Emulator'
- Click <OK>
- Press the RES button on the RSK board.
- Click <OK>
- The system should then connect.
- Click Emulator System from Tool bar.

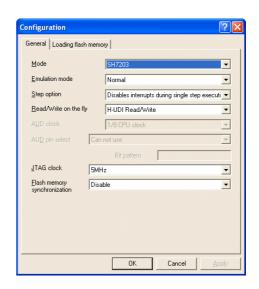
Switch to the 'Loading flash memory' Tab

Note: Please ensure all settings for both tabs of the 'Configuration Dialog' match the diagrams shown.

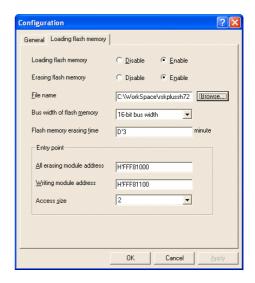








- Select 'Enable' for 'Loading and Erasing flash memory'
- Browse and select the 'fmtool.mot' file in your workspace.
- Click <OK>

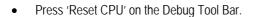


Chapter 6. Downloading and Running the Tutorial

Now the code has been built in HEW it needs to be downloaded to the RSK.

Now that you are connected to the target you should see an additional category in the workspace view called 'Download Modules'

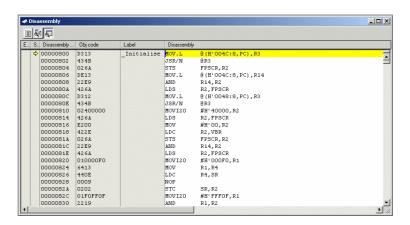
- Right click on the download module listed and select 'Download'
- On completion the debugger and code are ready to be executed







The File window will open the Tutorial code at the entry point. An arrow and a yellow highlight marks the current position of the program counter.



Use these buttons to switch between 'source, disassembly and mixed modes'.





We will now skip over the initialisation code and proceed to the main tutorial.

- In 'Source View' scroll down the file until you see the call to main.
- Place an event at the call to main(); by double clicking in the **Event Column** next to the line to stop at.
- Press 'Reset Go' on the Debug Tool Bar.



The code will execute to the event point. At this point all the device initialisation will have been completed.

Press 'Step In' on the Debug Tool Bar.

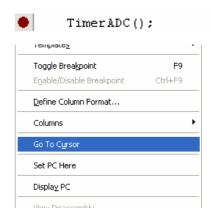


The code window will open 'main.c' and show the new position of the program counter.

```
53
54
55
                            User Program Code
56
57
58
59
                            Function Name : main
60
                            Description : Main function.
                                   This function calls timer, ADC & LCD initialisation functions. The user
61
62
                                   LEDs flashes until the user presses a switch on the RSK.
63
                            Parameters
                                        : none
64
                            Return value : none
                            *****************************
65
   00001A2C
66
                            void main(void)
67
68
                                unsigned char ucCount:
69
                                unsigned char ucConvert:
70
                                unsigned short usDelav:
71
72
                                /* Reset the LCD module. */
73
   00001A30
                                InitialiseDisplay();
74
75
                                /* Display Renesas Splash Screen. */
76
   00001A34
                                DisplayString(LCD_LINE1,"Renesas");
                                DisplayString (LCD_LINE2, NICKNAME);
77
   00001244
78
79
                                  Flash the user LEDs for some time or until a key is pressed. */
80
   00001A52
                                FlashLEDs();
```

Support for the LCD display is included in the tutorial code. We do not need to be concerned about the details of the LCD interface – except that the interface is write-only and so is not affected if the LCD display is attached or not.

- Insert a event on the 'TimerADC();' function call.
- Right click on the 'FlashLEDs();' function and select 'Go to cursor'.



The code will run to the selected line and stop. A temporary event point was automatically inserted in the code and then removed when the program stopped at the event point.

• Press 'Step Over' on the Debug Tool Bar.



The code will run and flash the LEDs 200 times. The debugger will not stop running until all 200 flashes have completed or a button is pressed on the RSK.

• If the LEDs are still flashing press the SW1 button on the RSK to exit the FlashLEDs() function.

The code will run to the event point we previously set on the 'TimerADC()' function call.

There are several versions of the timer function depending upon the peripherals available in the device. The default function is TimerADC which we shall demonstrate here.

The timer function initialises an interrupt on an available internal timer. On a compare match in the timer module an interrupt is generated. In the TimerADC code version the interrupt reads the last ADC conversion from the external potentiometer and uses the result to set the next compare match value. The ADC conversion is then re-started.

Clear all Event Points.

The interrupt initialisation is performed as part of the hardware setup. This is located in the file 'interrupts.c'.

- Open the file 'interrupts.c' by double clicking on the file in the workspace view.
- Review this file and find the interrupt function that changes the LED pins, INT_MTU2_MTU2_TGI2A(void)
- Set a event point on the line where the LED pins are modified.
- Press 'Go' or 'F5' to run the code from the current PC position.

The code will stop in the interrupt routine. It is now possible to step through the interrupt function.

- Remove the event point in the interrupt by double clicking again before exiting the function.
- Press 'Step Over' to step over the instruction and observe the LEDs turn off.
- Press 'Go' to run the code from the current PC position.



The code will now run to the infinite loop at the end of Main(). The user LEDs should now be flashing. If the RSK supports an ADC you can modify the flashing rate by adjusting the potentiometer on the board.

Press 'Stop' on the debug tool bar.



Clear all event points

Open the file 'main.c' and insert an event point on 'StaticsTest();'.

The statics test is used to demonstrate that the initialisation has successfully copied all initialised variables from storage in flash to RAM.

• Press 'Reset Go' on the Debug Tool Bar.



The code will stop at the event point. (Press a button to bypass the flashing LED test.)

• Press 'Step In' on the Debug Tool Bar.



It is possible to monitor variables during debugging of the code. To set up a 'watch' on a variable place the mouse over the variable. If the variable is available in the current context a tool-tip will be displayed with the current value of the variable.

• Hover the mouse over the 'ucStr' variable to see the tooltip value. Then Right click on the variable name and select 'Instant Watch'.

A dialog will open showing the variable and allowing further details to be explored.

Press 'Add'

The dialog will close and a new pane will open in the workspace containing the variable.

It is possible to see that the string has been successfully initialised to 'STATIC'.

- Set a event point on the 'DisplayString();' function call inside the loop.
- Press 'Go' to run the code from the current PC position.



When the program stops you can see the modified string displayed on the second line of the LCD.

Inspection of the watch pane will show that the first character of the variable string has been replaced with the first character of the constant replacement string.

- Remove the event point
- Right click on the 'DisplayString();' function call after the loop and select 'Go to cursor'.

This shows that the variable was initialised at program start up and can be overwritten with 'TESTTEST'.

The modified string is also displayed on the LCD

You have now run the tutorial code and used many of the common features of the debugger. We suggest that you review the rest of the tutorial code as many functions have important information on the operation of the code, the compiler directives and comments on when they should or must be used. Please refer to Chapter 7 for more information on the project files.

Chapter 7. Project Files

7.1. Standard Project Files

The RSK tutorials are configured so that it is possible to provide the same tutorial code on multiple RSK products. This allows the evaluation of the different processor cores using equivalent code. To achieve this, the following files are common between all device cores / Toolchains.

Each of the tutorial files has expanded comment text describing the function of each code entry. Please refer to the source code for greater detail on the purpose and operation of the compiler specific details.

7.1.1. Initialisation code (resetprg.c)

This is the entry point of the code after a power on reset. 'Initialise' is the entry point defined by the Power on reset vector.

```
S/W Breakpoints Source
          #define FPSCR_Init
                              0x00040000
          #define SR Init
                          0x000000F0
          ISR Name : Initialise
          Description : Power on reset function.
                 This function executes following to power on reset.
                 It first calls hardware initialisation function & then 'main()' function.
          Parameters : none
          Return value: none
          #pragma entry Initialise
          #pracma section ResetPRG
          void Initialise (void)
              /* Set up the hardware */
           HardwareSetup();
              /* Copy initilizes data from ROM to RAM */
              _INITSCT();
          #ifdef CACHE_WRITEBACK
             cache_writeback();
           #endif
              /* Initialize FPSCR */
              set_fpscr(FPSCR_Init);
              /* Initialize VBR */
              set_vbr((void *)((char *)0));
              /* Initialize Status register */
              set_cr(SR_Init);
              nop();
```

The call to 'hardwaresetup()' configures the external bus interface to access external memory and initialises device hardware and peripherals before they are accessed by the tutorial software.

After initialisation the 'main' function is called.

The call to 'main()' will start the main demonstration code.

7.1.2. External bus and Memory configuration (bus-init.c/bus-init.h)

```
*******************
                                                                                                     bus init.h
                                                                                     DESCRIPTION
                                                                                                     Header file for external bus initialization.
Function Name
Description
             : ConfigureBuskndMemory 
: Configures the external bus and memory.
                                                                                     Copyright : 2007 Renesas Technology Europe Ltd.
Copyright : 2007 Renesas Technology Corporation.
Return value : none.
                                                                                     All Rights Reserved
   /* Configure the CSO space. */
init_bsc_cs0();
                                                                                      /-----
                                                                                     Revision History
/* To use the SDRAM in clock mode 0 or 1, AC characteristics switching register
    must be initialized */
#ifdef USE_CLKMODEO
    init purem_section();
    set_acswr();
#endif"
                                                                                     DD. HM. YTYY OSO-UID Description
23.02.2007 RTE-WNA First Release.
                                                                                     #ifndef BUS_INIT_H_INCLUDED
#define BUS_INIT_H_INCLUDED
#endif
/* Configure CS3 space for SDRAM */
#ifinder USE 32BIT_SDRAM_INTERFACE
init_sdram();
#else
init_sdram32();
#endif
                                                                                      /******************************
                                                                                     Library includes
                                                                                     SDRAM Mode register addresses
                                                                                     /* The address when writing in a SDRAM mode register */
#define SDRAM_MODE (*(volatile unsigned short *)(0xfffc5060))
#define SDRAM_MODE32 (*(volatile unsigned short *)(0xfffc5000))
End of function ConfigureBusAndMemory
                                                                                     Macro Defines
                                                                                     #define USE_32BIT_SDRAM_INTERFACE
                                                                                     //#define USE_CACHE_MEMORY
//#define CACHE_WRITEBACK
```

7.1.3. Board initialisation code (hwsetup.c/hwsetup.h)

Further hardware initialisation should be performed in this function. For debug purposes, the interrupt priority registers are initialised to zero (as they would be after a power on reset). In the case of the tutorial the interrupts are configured and the LCD interface is initialised.

```
hwsetup.h
                                                                                                                                 FILE NAME
                                                                                                                                              TION Hardware Setup header file.
This file declares prototype for hardware setup function.
                                 Function Name: HardwareSetup
Description: Sets up the hardware.
This function makes a call to initialisation functions to
operating frequency, port pins & relevant on-chip modules
timer etc.) in order to setup the RSK for the main applice
                                                                                                                                  Copyright : 2007 Renesas Technology Europe Ltd.
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                                 Parameters : none
Return value : none
                                                                                                                                  All Rights Reserved
00001310
0000131E
00001322
0000132E
00001338
00001342
0000134E
                                      ConfigureOperatingFrequency();
ConfigureBusAndHemory();
ConfigurePortPins();
EnablePeripheralModules();
ConfigureInterrupts();
                                                                                                                                 Revision History
DD.MM.YYYY OSO-UID Description
23.02.2007 RTE-VNA First Release.
                                                                                                                                 #ifndef HWSETUP_H_INCLUDED
#define HWSETUP_H_INCLUDED
                                 End of function HardwareSetup
                                                                                                                                  Function Prototypes
                                                                                                                                  void HardwareSetup(void);
                                                                                                                                 #endif
                                                                                                                                               /* HWSETUP H INCLUDED */
```

7.1.4. Main tutorial code (main.c)

The display initialisation and string display functions operate on the LCD display module. In addition to the tutorial code several samples of use of the on chip peripherals are supplied. Check compatibility with a ks0066u controller and pin connection on the schematic before connecting an LCD module not supplied by Renesas.

```
FILE NAME
DESCRIPTION
                Header file for main user application.
Copyright : 2007 Renesas Technology Europe Ltd.
Copyright : 2007 Renesas Technology Corporation.
All Rights Reserved
/*********************
Revision History
DD.HM.YYYY OSO-UID Description
23.02.2007 RTE-VNA First Release.
#ifndef MAIN_H_INCLUDED #define MAIN H INCLUDED
Macro Defines
#define NICKNAME " SH7203 "
Function Prototypes
void main(void);
void Statics_Test(void);
#endif
           /* MAIN H INCLUDED */
```

Chapter 8. Additional Information

For details on how to use High-performance Embedded Workshop (HEW), refer to the HEW manual available on the CD or from the web site.

Further information available for this product can be found on the Renesas website at: http://www.renesas.com/renesas_starter_kits

General information on Renesas Microcontrollers can be found at the following websites.

Global: http://www.renesas.com/

Regional (English language) sites can be accessed from the Global site, or directly by going to:

Europe: http://renesas.eu

Americas: http://america.renesas.com

Asia: http://sq.renesas.com

Renesas Starter Kit+ for SH7203

Tutorial Manual

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