

RQK2501YGDQA

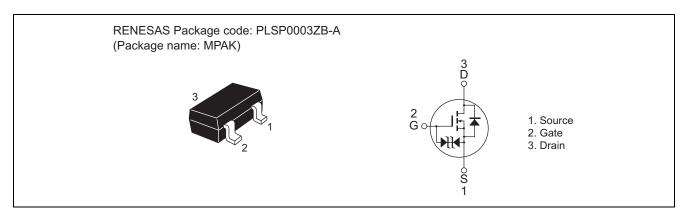
Silicon N Channel MOS FET Power Switching

R07DS0312EJ0400 Rev.4.00 Jan 10, 2014

Features

- High drain to source voltage and Low gate drive
 V_{DSS}: 250 V and 2.5 V gate drive
- Low drive current
- High speed switching
- Small traditional package (MPAK)

Outline



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	250	V
Gate to source voltage	V _{GSS}	±10	V
Drain current	I _D	0.4	А
Drain peak current	I _{D(pulse)} Note1	1.6	А
Body - drain diode reverse drain current	I _{DR}	0.4	Α
Channel dissipation	Pch Note2	0.8	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW \leq 10 μ s, Duty cycle \leq 1%

2. When using the glass epoxy board (FR-4 $40 \times 40 \times 1$ mm)

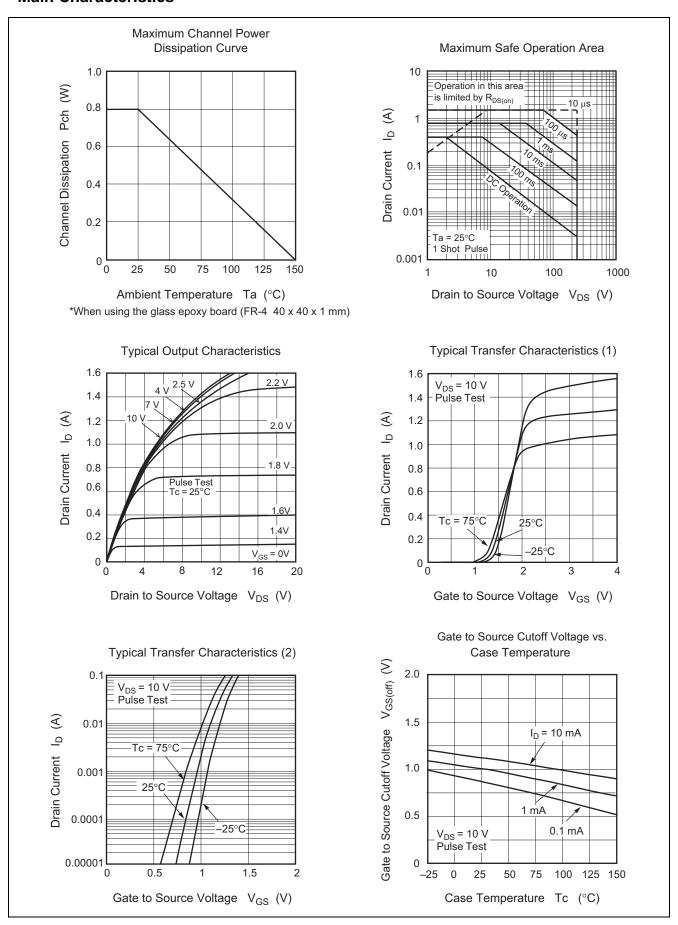
Electrical Characteristics

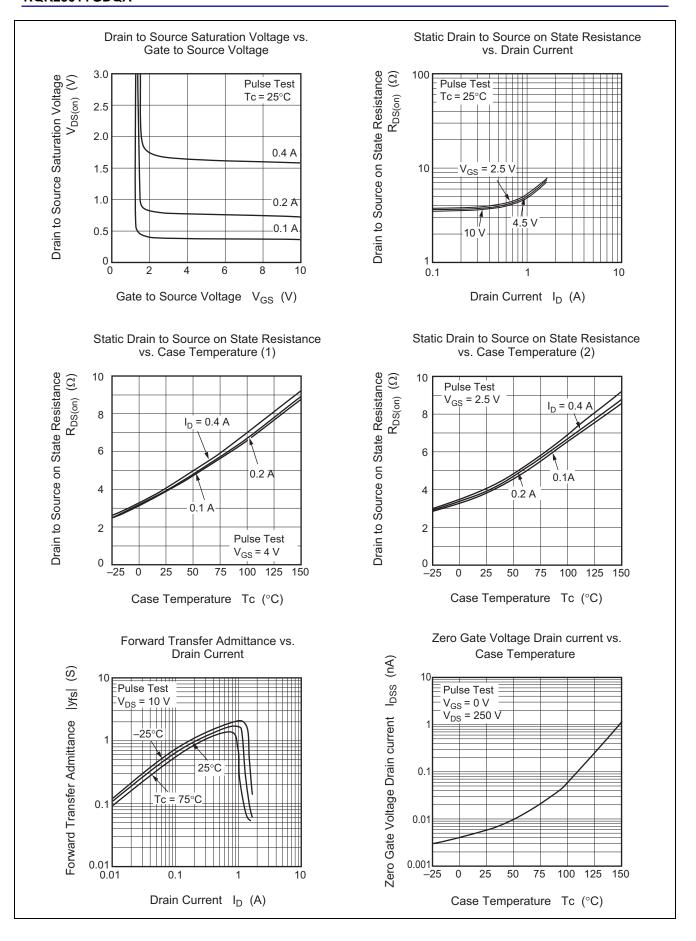
 $(Ta = 25^{\circ}C)$

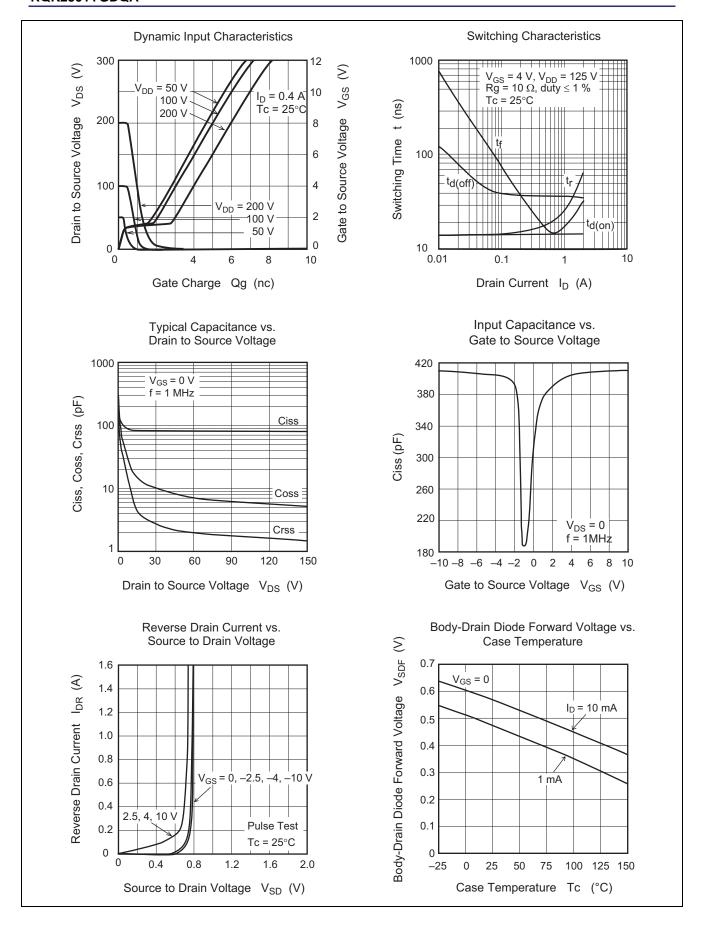
ltem	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	250	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	+10	_	_	V	$I_G = +100 \mu A, V_{DS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	-10	_	_	V	$I_G = -100 \ \mu A, \ V_{DS} = 0$
Gate to source leak current	I _{GSS}	1	_	+10	μΑ	$V_{GS} = +8 \text{ V}, V_{DS} = 0$
Gate to source leak current	I _{GSS}	1	_	-10	μΑ	$V_{GS} = -8 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I _{DSS}	1	_	1	μΑ	$V_{DS} = 250 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	0.5	_	1.5	V	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$
Drain to source on state resistance	R _{DS(on)}	1	4.0	5.4	Ω	$I_D = 0.2 \text{ A}, V_{GS} = 4 \text{ V}^{\text{Note3}}$
Drain to source on state resistance	R _{DS(on)}	_	4.1	5.6	Ω	$I_D = 0.2 \text{ A}, V_{GS} = 2.5 \text{ V}^{\text{Note3}}$
Forward transfer admittance	y _{fs}	0.6	0.95	_	S	$I_D = 0.2 \text{ A}, V_{DS} = 10 \text{ V}^{\text{Note3}}$
Input capacitance	Ciss	_	80	_	pF	V _{DS} = 25 V
Output capacitance	Coss	_	10	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	3	_	pF	f = 1 MHz
Turn - on delay time	t _{d(on)}	_	15	_	ns	V _{DD} = 125 V,V _{GS} = 4 V
Rise time	t _r	_	16	_	ns	$I_D = 0.2 \text{ A}$
Turn - off delay time	t _{d(off)}	_	40	_	ns	$R_L = 625 \Omega$
Fall time	t _f	_	38	_	ns	$Rg = 10 \Omega$
Total gate charge	Qg	_	4.0	_	nC	V _{DD} = 200 V
Gate to Source charge	Qgs	_	0.5	_	nC	$V_{GS} = 4 V$
Gate to drain charge	Qgd	_	2.6	_	nC	$I_D = 0.4 A$
Body - drain diode forward voltage	V_{DF}	_	0.8	1.2	V	$I_F = 0.4 \text{ A}, V_{GS} = 0^{\text{Note3}}$

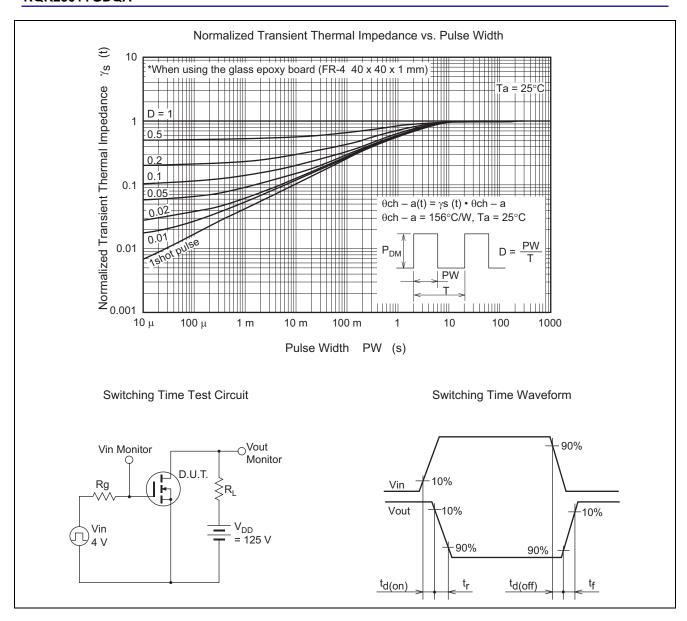
Notes: 3. Pulse test

Main Characteristics



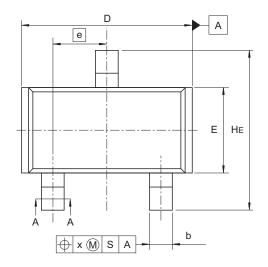


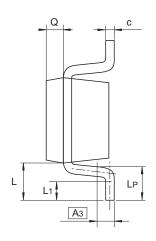


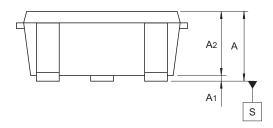


Package Dimensions

JEITA Package Code	RENESAS Code	Previous Code	MASS (Typ) [g]
SC-59A	PLSP0003ZB-A	MPAK(T) / MPAK(T)V	0.011









Reference	Dimensions in millimeters			
Symbol	Min	Nom	Max	
Α	1.0	_	1.3	
A ₁	0	_	0.1	
A ₂	1.0	1.1	1.2	
A_3		0.25	_	
b	0.35	0.4	0.5	
С	0.1	0.16	0.26	
D	2.7	_	3.1	
E	1.35	1.5	1.65	
е		0.95		
HE	2.2	2.8	3.0	
L	0.35	_	0.75	
L ₁	0.15	_	0.55	
Lp	0.25		0.65	
Х			0.05	
Q	_	0.3	_	

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Ordering Information

Orderable Part Number	Quantity	Shipping Container
RQK2501YGDQATL-E	3000 pcs.	φ178 mm reel, 8 mm Emboss taping
RQK2501YGDQATL-H		

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Renesas Electronics America Inc. 2880 Scott Boulevard Santa Clara, CA 95050-2554, U.S.A. Tel: +1-408-588-6000, Fax: +1-408-588-6130

Renesas Electronics Canada Limited 1101 Nicholson Road, Newmarket, Ontario L3Y 9C3, Canada Tel: +1-905-898-5441, Fax: +1-905-898-3220

Renesas Electronics Europe Limited Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K Tel: +44-1628-651-709, Fax: +44-1628-651-804

Renesas Electronics Europe GmbH

Arcadiastrasse 10, 40472 Düsseldorf, Germany Tel: +49-211-65030, Fax: +49-211-6503-1327

Renesas Electronics (China) Co., Ltd. 7th Floor, Quantum Plaza, No.27 ZhiChunLu Haidian District, Beijing 100083, P.R.China Tel: +86-10-2035-1155, Fax: +86-10-8235-7679

Renesas Electronics (Shanghai) Co., Ltd.
Unit 301, Tower A, Central Towers, 555 LanGao Rd., Putuo District, Shanghai, China
Tel: +86-21-2226-088, Fax: +86-21-2226-0999

Renesas Electronics Hong Kong Limited
Unit 1601-1613, 161F., Tower 2, Grand Century Place, 193 Prince Edward Road West, Mongkok, Kowloon, Hong Kong
Tel: +852-2886-9318, Fax: +852 2886-9022/9044

Renesas Electronics Taiwan Co., Ltd. 13F, No. 363, Fu Shing North Road, Taipei, Taiv Tel: +886-2-8175-9600, Fax: +886 2-8175-9670

Renesas Electronics Singapore Pte. Ltd. 80 Bendemeer Road, Unit #06-02 Hyflux Innovation Centre Singapore 339949 Tel: +65-6213-0200, Fax: +65-6213-0300

Renesas Electronics Malaysia Sdn.Bhd.
Unit 906, Block B, Menara Amcorp, Amcorp Trade Centre, No. 18, Jln Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia
Tel: +60-3-7955-9390, Fax: +60-3-7955-9510

Renesas Electronics Korea Co., Ltd. 12F., 234 Teheran-ro, Gangnam-Gu, Seoul, 135-080, Korea Tel: +82-2-558-3737, Fax: +82-2-558-5141

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