## RJK0380DPA

30V, 45A, 3.2m max.
Built in SBD N Channel Power MOS FET High Speed Power Switching

## Features

- High speed switching
- Capable of 4.5 V gate drive
- Low drive current
- High density mounting
- Low on-resistance
- Pb-free
- Halogen-free


## Outline

RENESAS Package code: PWSN0008DE-A
(Package name: WPAK(3F))


4321


1, 2, 3 Source
4 Gate
5, 6, 7, 8 Drain

## Absolute Maximum Ratings

$\left(\mathrm{Ta}=25^{\circ} \mathrm{C}\right)$

| Item | Symbol | Ratings | Unit |
| :---: | :---: | :---: | :---: |
| Drain to source voltage | $V_{\text {DSS }}$ | 30 | V |
| Gate to source voltage | $V_{\text {Gss }}$ | $\pm 20$ | V |
| Drain current | ID | 45 | A |
| Drain peak current | $\mathrm{I}_{\mathrm{D} \text { (pulse) }}{ }^{\text {Note1 }}$ | 180 | A |
| Body-drain diode reverse drain current | $\mathrm{I}_{\mathrm{DR}}$ | 45 | A |
| Avalanche current | $\mathrm{I}_{\mathrm{AP}}{ }^{\text {Note } 2}$ | 25 | A |
| Avalanche energy | $\mathrm{E}_{\text {AR }}{ }^{\text {Note } 2}$ | 62.5 | mJ |
| Channel dissipation | Pch ${ }^{\text {Note3 }}$ | 50 | W |
| Channel to Case Thermal Resistance | $\theta \mathrm{ch}-\mathrm{C}$ | 2.5 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
| Channel temperature | Tch | 150 | ${ }^{\circ} \mathrm{C}$ |
| Storage temperature | Tstg | -55 to +150 | ${ }^{\circ} \mathrm{C}$ |

Notes: 1. PW $\leq 10 \mu \mathrm{~s}$, duty cycle $\leq 1 \%$
2. Value at Tch $=25^{\circ} \mathrm{C}, \mathrm{Rg} \geq 50 \Omega$
3. $\mathrm{Tc}=25^{\circ} \mathrm{C}$

## Electrical Characteristics

$\left(\mathrm{Ta}=25^{\circ} \mathrm{C}\right)$

| Item | Symbol | Min | Typ | Max | Unit | Test Conditions |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Drain to source breakdown voltage | $\mathrm{V}_{\text {(BR)DSs }}$ | 30 | - | - | V | $\mathrm{I}_{\mathrm{D}}=10 \mathrm{~mA}, \mathrm{~V}_{\mathrm{GS}}=0$ |
| Gate to source leak current | Igss | - | - | $\pm 0.1$ | $\mu \mathrm{A}$ | $V_{G S}= \pm 20 \mathrm{~V}, \mathrm{~V}_{\mathrm{DS}}=0$ |
| Zero gate voltage drain current | IDss | - | - | 1 | m A | $\mathrm{V}_{\mathrm{DS}}=30 \mathrm{~V}, \mathrm{~V}_{\mathrm{GS}}=0$ |
| Gate to source cutoff voltage | $\mathrm{V}_{\mathrm{GS} \text { (off) }}$ | 1.2 | - | 2.5 | V | $\mathrm{V}_{\mathrm{DS}}=10 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=1 \mathrm{~mA}$ |
| Static drain to source on state resistance | $\mathrm{R}_{\mathrm{DS} \text { (on) }}$ | - | 2.4 | 3.2 | $\mathrm{m} \Omega$ | $\mathrm{I}_{\mathrm{D}}=22.5 \mathrm{~A}, \mathrm{~V}_{\mathrm{GS}}=10 \mathrm{~V}^{\text {Note } 4}$ |
|  | $\mathrm{R}_{\mathrm{DS} \text { (on) }}$ | - | 3.3 | 4.7 | $\mathrm{m} \Omega$ | $\mathrm{I}_{\mathrm{D}}=22.5 \mathrm{~A}, \mathrm{~V}_{\mathrm{GS}}=4.5 \mathrm{~V}^{\text {Note } 4}$ |
| Forward transfer admittance | $\left\|y_{\text {ts }}\right\|$ | - | 95 | - | S | $\mathrm{I}_{\mathrm{D}}=22.5 \mathrm{~A}, \mathrm{~V}_{\mathrm{DS}}=10 \mathrm{~V}^{\text {Note4 }}$ |
| Input capacitance | Ciss | - | 3350 | - | pF | $\begin{aligned} & V_{D S}=10 \mathrm{~V}, \mathrm{~V}_{\mathrm{GS}}=0, \\ & \mathrm{f}=1 \mathrm{MHz} \end{aligned}$ |
| Output capacitance | Coss | - | 730 | - | pF |  |
| Reverse transfer capacitance | Crss | - | 330 | - | pF |  |
| Gate Resistance | Rg | - | 1.6 | - | $\Omega$ |  |
| Total gate charge | Qg | - | 24 | - | nC | $\begin{aligned} & \mathrm{V}_{\mathrm{DD}}=10 \mathrm{~V}, \mathrm{~V}_{\mathrm{GS}}=4.5 \mathrm{~V}, \\ & \mathrm{I}_{\mathrm{D}}=45 \mathrm{~A} \end{aligned}$ |
| Gate to source charge | Qgs | - | 9.2 | - | nC |  |
| Gate to drain charge | Qgd | - | 6.7 | - | nC |  |
| Turn-on delay time | $\mathrm{t}_{\mathrm{d}(\mathrm{On})}$ | - | 14 | - | ns | $\begin{aligned} & \mathrm{V}_{\mathrm{GS}}=10 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=22.5 \mathrm{~A}, \\ & \mathrm{~V}_{\mathrm{DD}} \cong 10 \mathrm{~V}, \mathrm{R}_{\mathrm{L}}=0.44 \Omega, \\ & \mathrm{Rg}=4.7 \Omega \end{aligned}$ |
| Rise time | $\mathrm{t}_{\mathrm{r}}$ | - | 16 | - | ns |  |
| Turn-off delay time | $\mathrm{t}_{\text {d(off) }}$ | - | 58 |  | ns |  |
| Fall time | $\mathrm{t}_{\mathrm{f}}$ | - | 11.5 | - | ns |  |
| Body-drain diode forward voltage | $V_{\text {DF }}$ | - | 0.39 | - | V | $\mathrm{I}_{\mathrm{F}}=2 \mathrm{~A}, \mathrm{~V}_{\mathrm{GS}}=0^{\text {Note4 }}$ |
| Body-drain diode reverse recovery time | $\mathrm{t}_{\mathrm{rr}}$ | - | 30 |  | ns | $\begin{aligned} & \mathrm{I}_{\mathrm{F}}=45 \mathrm{~A}, \mathrm{~V}_{\mathrm{GS}}=0 \\ & \mathrm{di}_{\mathrm{F}} / \mathrm{dt}=100 \mathrm{~A} / \mu \mathrm{S} \end{aligned}$ |

Notes: 4. Pulse test

## Main Characteristics





## Package Dimensions



## Ordering Information

| Orderable Part Number | Quantity | Shipping Container |
| :--- | :--- | :--- |
| RJK0380DPA-00-J5A | 3000 pcs | Taping |

Note: The symbol of 2nd "-" is occasionally presented as "\#".

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