

RJK0380DPA

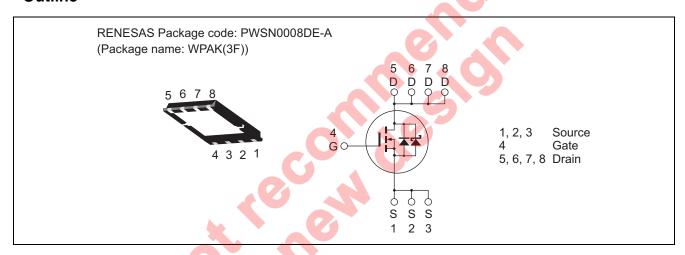
30V, 45A, $3.2m\Omega$ max. Built in SBD N Channel Power MOS FET High Speed Power Switching

R07DS0938EJ0400 Rev.4.00 Mar 21, 2013

Features

- High speed switching
- Capable of 4.5 V gate drive
- Low drive current
- High density mounting
- Low on-resistance
- Pb-free
- Halogen-free

Outline



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Ratings	Unit	
Drain to source voltage	V _{DSS}	30	V	
Gate to source voltage	V _{GSS}	±20	V	
Drain current	I _D	45	А	
Drain peak current	I _{D(pulse)} Note1	180	А	
Body-drain diode reverse drain current	I _{DR}	45	А	
Avalanche current	I _{AP} Note 2	25	А	
Avalanche energy	E _{AR} Note 2	62.5	mJ	
Channel dissipation	Pch Note3	50	W	
Channel to Case Thermal Resistance	θch-C	2.5	°C/W	
Channel temperature	Tch	150	°C	
Storage temperature	Tstg	-55 to +150	°C	

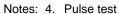
Notes: 1. PW \leq 10 μ s, duty cycle \leq 1%

- 2. Value at Tch = 25°C, Rg \geq 50 Ω
- 3. $Tc = 25^{\circ}C$

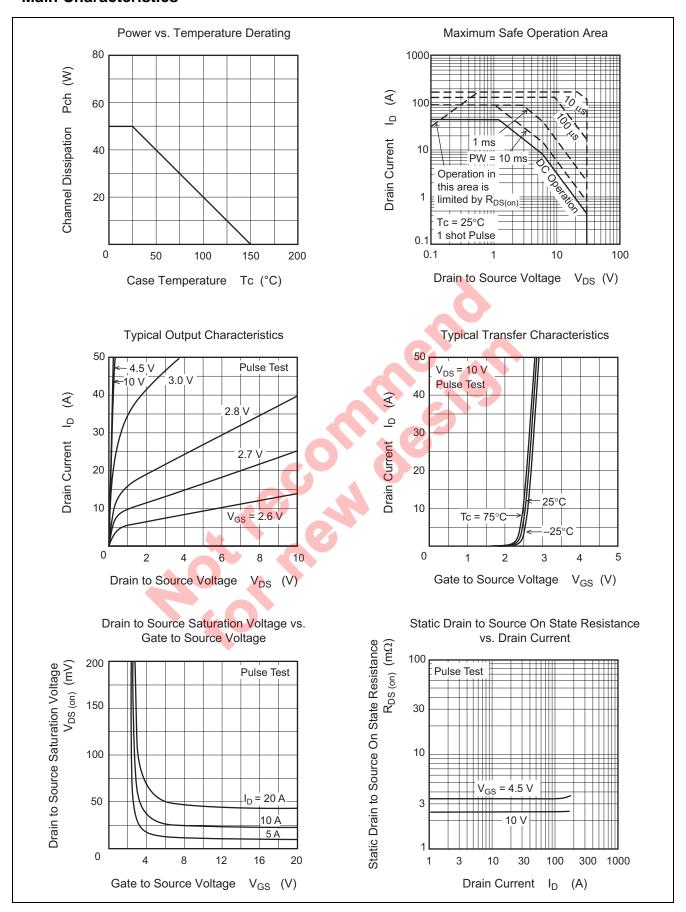
Electrical Characteristics

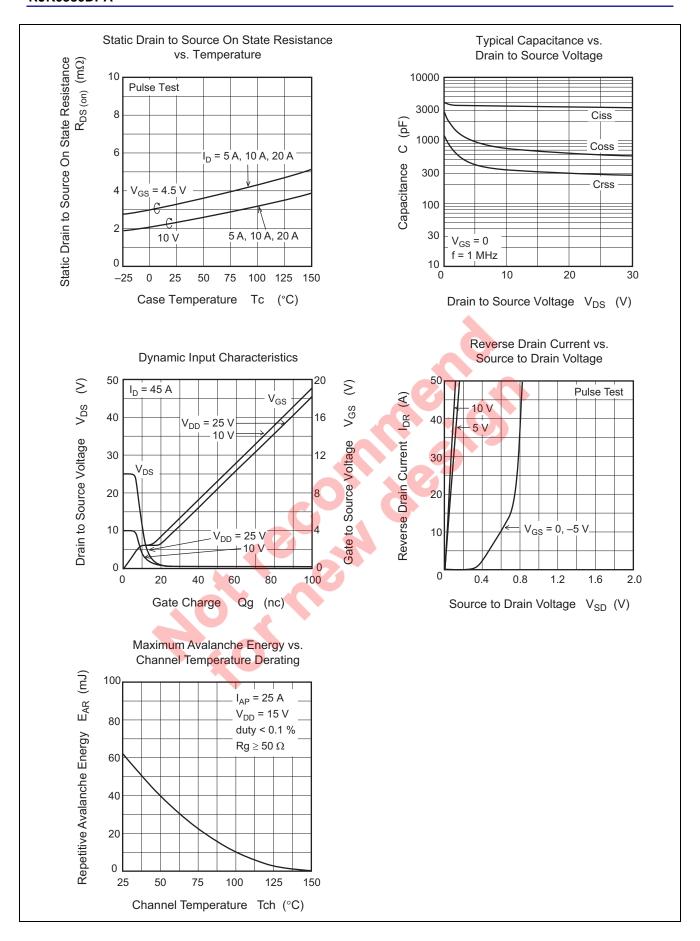
 $(Ta = 25^{\circ}C)$

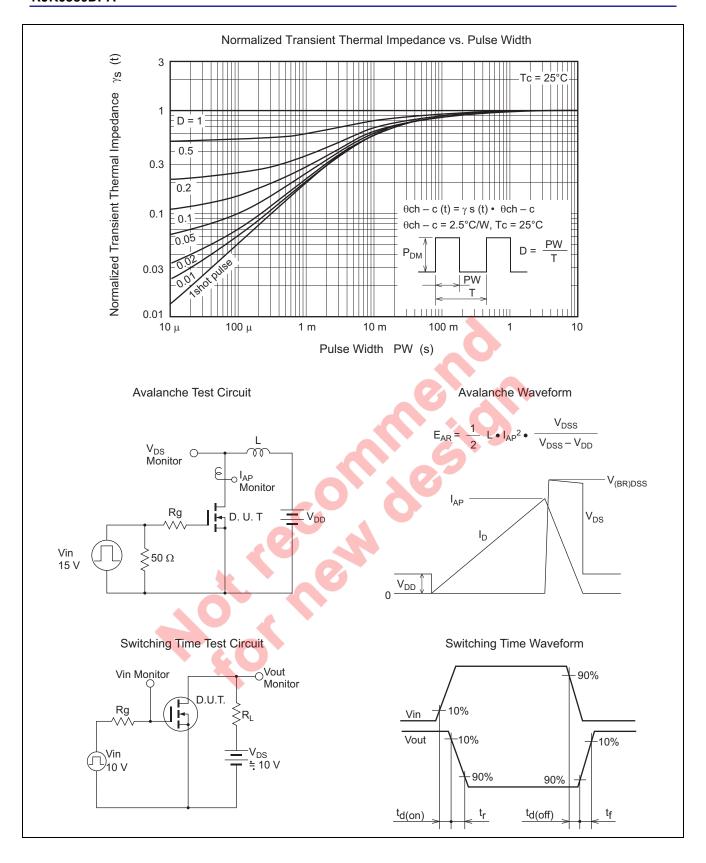
Item	Symbol	Min	Тур	Max	Unit	Test Conditions	
Drain to source breakdown voltage	V _{(BR)DSS}	30	_	-	V	$I_D = 10 \text{ mA}, V_{GS} = 0$	
Gate to source leak current	I _{GSS}		_	±0.1	μΑ	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0$	
Zero gate voltage drain current	I _{DSS}		_	1	m A	$V_{DS} = 30 \text{ V}, V_{GS} = 0$	
Gate to source cutoff voltage	$V_{GS(off)}$	1.2	_	2.5	V	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$	
Static drain to source on state	R _{DS(on)}		2.4	3.2	mΩ	$I_D = 22.5 \text{ A}, V_{GS} = 10 \text{ V}^{Note4}$	
resistance	R _{DS(on)}		3.3	4.7	mΩ	$I_D = 22.5 \text{ A}, V_{GS} = 4.5 \text{ V}^{\text{Note4}}$	
Forward transfer admittance	y _{fs}		95	_	S	$I_D = 22.5 \text{ A}, V_{DS} = 10 \text{ V}^{\text{Note4}}$	
Input capacitance	Ciss		3350	_	pF	$V_{DS} = 10 \text{ V}, V_{GS} = 0,$	
Output capacitance	Coss		730	_	pF	f = 1 MHz	
Reverse transfer capacitance	Crss		330		pF		
Gate Resistance	Rg		1.6		Ω		
Total gate charge	Qg		24		nC	$V_{DD} = 10 \text{ V}, V_{GS} = 4.5 \text{ V},$	
Gate to source charge	Qgs		9.2		nC	I _D = 45 A	
Gate to drain charge	Qgd		6.7		nC		
Turn-on delay time	t _{d(on)}		14	_	ns	$V_{GS} = 10 \text{ V}, I_D = 22.5 \text{ A},$	
Rise time	t _r		16		ns	$V_{DD}\cong 10 \text{ V}, \text{ R}_L=0.44 \Omega,$ $\text{Rg}=4.7 \Omega$	
Turn-off delay time	t _{d(off)}		58		ns		
Fall time	t _f		11.5	_	ns		
Body-drain diode forward voltage	V_{DF}		0.39		V	$I_F = 2 \text{ A}, V_{GS} = 0^{\text{Note4}}$	
Body-drain diode reverse	t _{rr}		30	_6	ns	$I_F = 45 \text{ A}, V_{GS} = 0$	
recovery time						$di_F/dt = 100 A/ \mu s$	
Notes: 4. Pulse test							



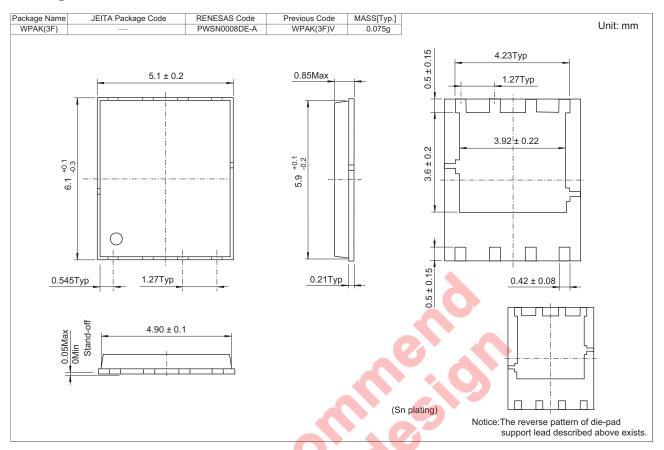
Main Characteristics







Package Dimensions



Ordering Information

Orderable Part Number	Quantity	Shipping Container
RJK0380DPA-00-J5A	3000 pcs	Taping

Note: The symbol of 2nd "-" is occasionally presented as "#".

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