

RBN50H65T1FPQ-A0

650V - 50A - IGBT Power Switching R07DS1381EJ0120 Rev.1.20 Aug.03.2020

Features

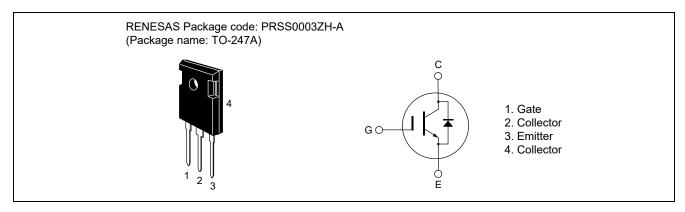
- Trench gate and thin wafer technology (G8H series)
- Built in fast recovery diode in one package
- Low collector to emitter saturation voltage
 V_{CE(sat)} = 1.5 V typ. (at I_C = 50 A, V_{GE} = 15 V, Ta = 25°C)
- Quality grade: Standard

- High speed switching
- Non-specification for short circuit
- Applications: UPS, Welding, photovoltaic inverters, Power converter system

Key Performance

Туре	V _{CES}	Ic	V _{CE(sat)} , T _C =25°C	I _F	Tj
RBN50H65T1FPQ-A0	650 V	50 A	1.5 V	50 A	175 °C

Outline



Absolute Maximum Ratings

 $(Tc = 25^{\circ}C)$

Ite	em	Symbol	Ratings	Unit
Collector to emitter voltage	ge	Vces	650	V
Gate to emitter voltage		Vges	±30	V
Collector current	Tc = 25 °C	lc	100	Α
	Tc = 100 °C	lc	50	Α
Collector peak current		IC(peak) Notes1	200	Α
Diode forward current	Tc = 25 °C	lF	100	Α
	Tc = 100 °C	lF	50	Α
Diode forward peak curre	ent	IF(peak) Notes1	200	Α
Collector power dissipation	on	Pc Notes2	250	W
Junction temperature		T _j Notes2	175	°C
Storage temperature		T _{stg}	-55 to +150	°C

Note: Continuous heavy condition (e.g. high temperature/voltage/current or high variation of temperature) may affect a reliability even if it is within the absolute maximum ratings. Please consider derating condition for appropriate reliability in reference Renesas Semiconductor Reliability Handbook (Recommendation for Handling and Usage of Semiconductor Devices) and individual reliability data.

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1%

2. Please use this device in the thermal conditions which the junction temperature does not exceed 175 °C. Renesas IGBT Application Note is disclosed about reliability test and application condition up to 175 °C.

Thermal Resistance Characteristics

 $(Tc = 25^{\circ}C)$

Item	Symbol	Max. Value Notes3	Unit
Junction to case thermal resistance (IGBT)	R _{th(j-c)}	0.6	°C/W
Junction to case thermal resistance (Diode)	R _{th(j-c)}	0.97	°C/W

Notes: 3. Designed target value on Renesas measurement condition. (Not tested)

Electrical Characteristics

 $(Tc = 25^{\circ}C)$

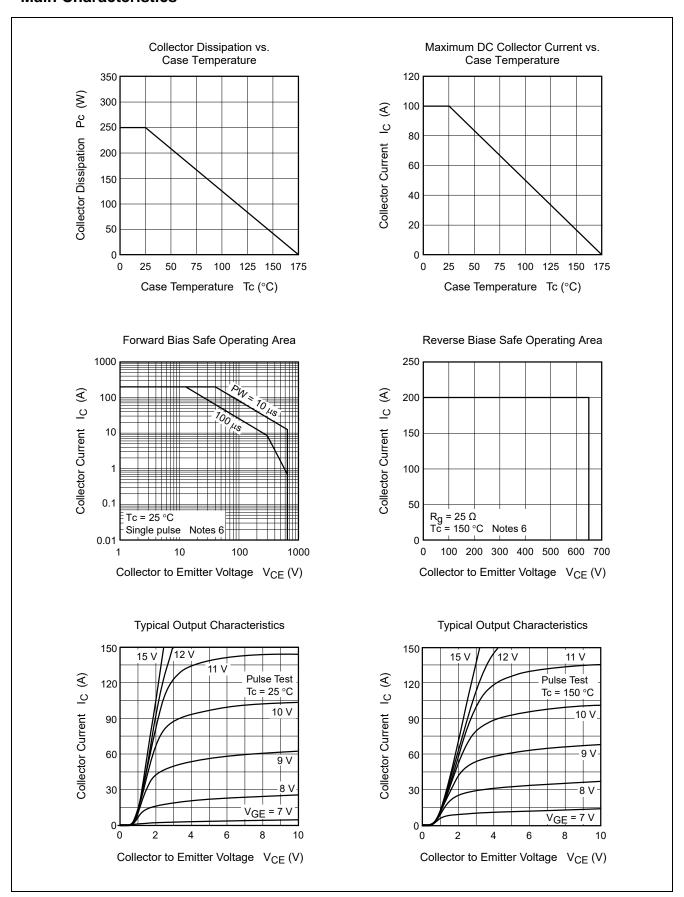
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Collector to emitter leakage current	Ices	_	_	200	μΑ	V _{CE} = 650 V, V _{GE} = 0 V
Gate to emitter leakage current	Iges	_	_	±1	μΑ	V _{GE} = ±30 V, V _{CE} = 0 V
Gate to emitter threshold voltage	V _{GE(th)}	4.1	_	5.9	V	V _{CE} = 10 V, I _C = 1.0 mA
Collector to emitter saturation voltage	VcE(sat)	_	1.5	2.0	V	I _C = 50 A, V _{GE} = 15 V Notes4
Input capacitance	Cies		980	_	pF	V _{CE} = 25 V
Output capacitance	Coes		118	_	pF	V _{GE} = 0 V
Reverse transfer capacitance	Cres		11	_	pF	f = 1 MHz
Total gate charge	Qg		36	_	nC	V _{GE} = 15 V
Gate to emitter charge	Qge	_	9	_	nC	V _{CE} = 400 V
Gate to collector charge	Qgc	_	17	_	nC	I _C = 50 A
Turn-on delay time	t _{d(on)}		20		ns	V _{CC} = 400 V
Rise time	tr	_	18	_	ns	V _{GE} = +15 V/–5 V
Turn-off delay time	t _{d(off)}		93	_	ns	Ic = 50 A
Fall time	tf		40	_	ns	$R_g = 16 \Omega$
Turn-on loss energy	Eon	_	0.83	_	mJ	T _C = 25 °C
Turn-off loss energy	E _{off}	_	0.67	_	mJ	Inductive load Notes5
Total switching energy	Etotal		1.5	_	mJ	
Turn-on delay time	t _{d(on)}		20		ns	V _{CC} = 400 V
Rise time	tr	_	19	_	ns	V _{GE} = +15 V/–5V
Turn-off delay time	t _{d(off)}	_	113	_	ns	I _C = 50 A
Fall time	t _f	_	59	_	ns	$R_g = 16 \Omega$
Turn-on loss energy	Eon	_	1.2	_	mJ	Tc = 150 °C
Turn-off loss energy	E _{off}	_	1	_	mJ	Inductive load Notes5
Total switching energy	Etotal	_	2.2	_	mJ	
Die de fermond ook on			1 00	0.0	T	L FO A Notes/

Diode forward voltage	VF	_	2.0	2.6	V	I _F = 50 A Notes4
Diode reverse recovery time	t _{rr}	_	65	_	ns	$I_F = 50 \text{ A}, d_{iF}/d_t = 300 \text{ A}/\mu\text{s}$
Diode reverse recovery charge	Qrr	_	0.24	_	μC	
Diode peak reverse recovery current	I _{rr}		7		Α	

Notes: 4. Pulse test

5. Switching time test circuit and waveform are shown below.

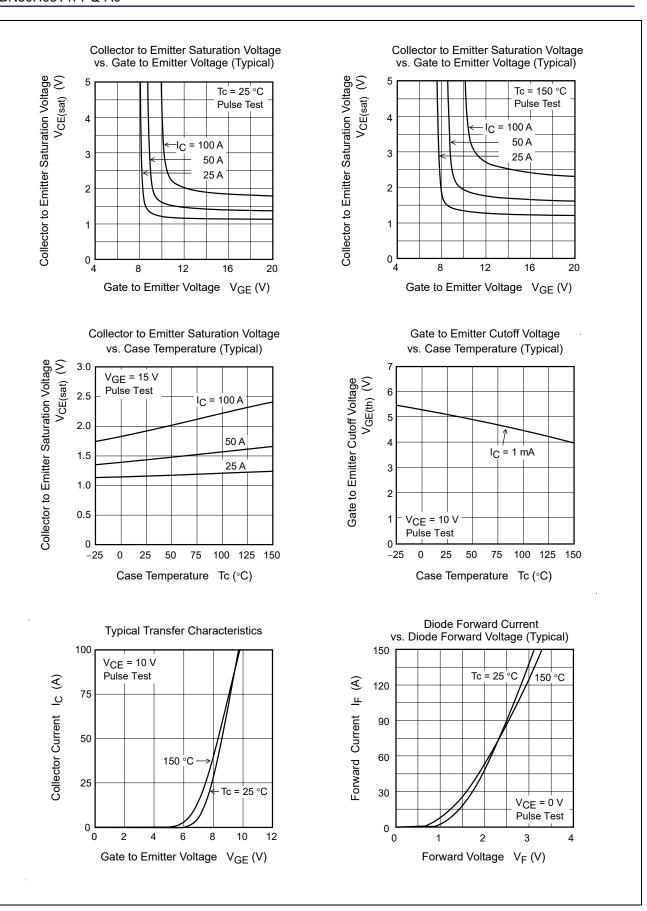
Main Characteristics

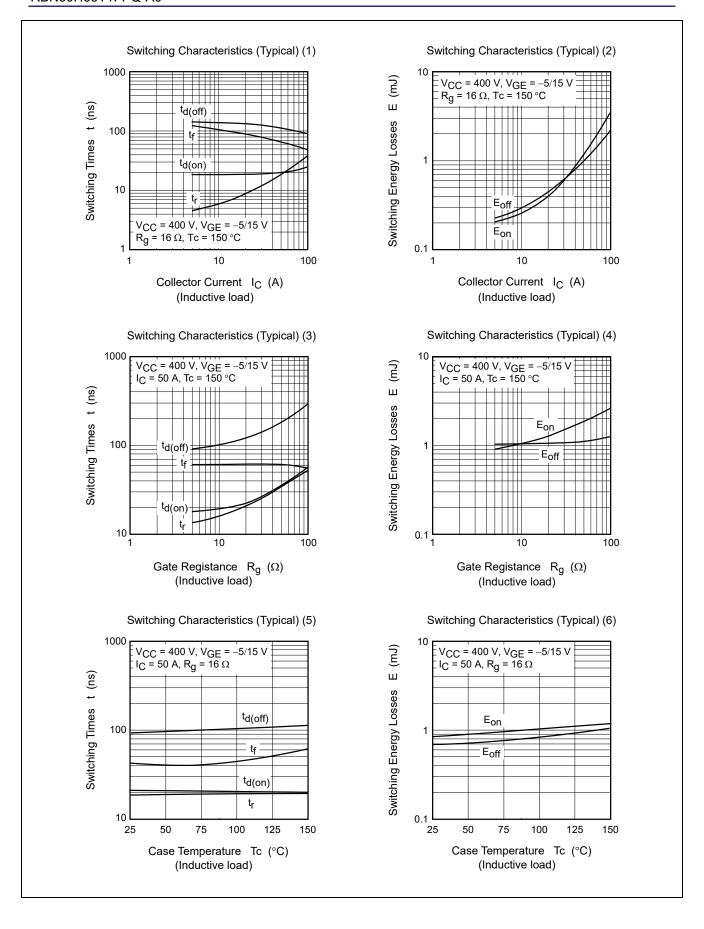


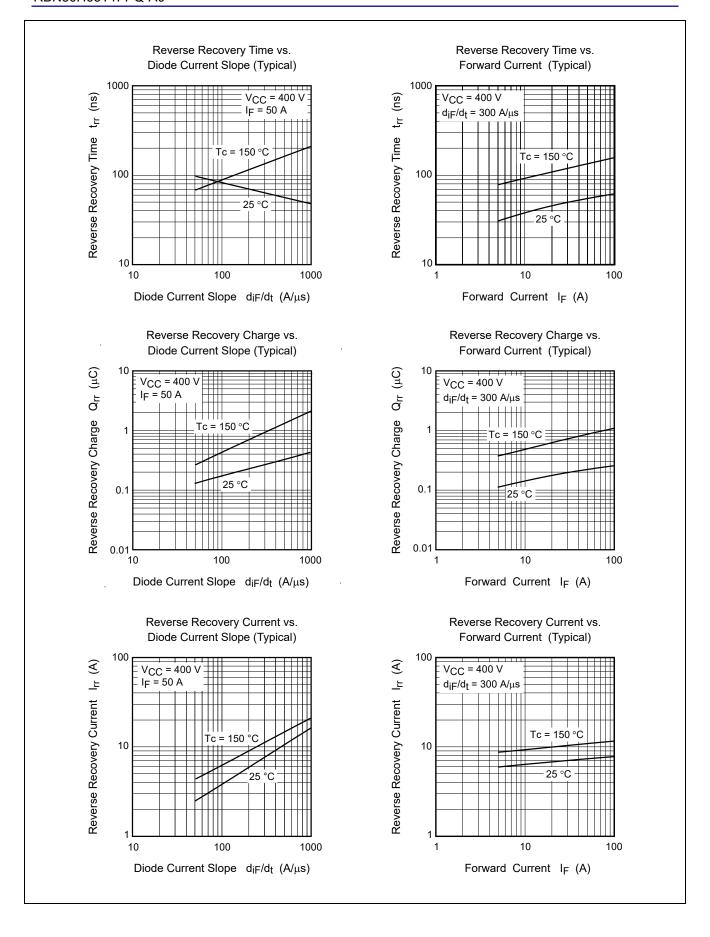
Notes: 6. Designed target value on Renesas measurement condition. (Not tested)

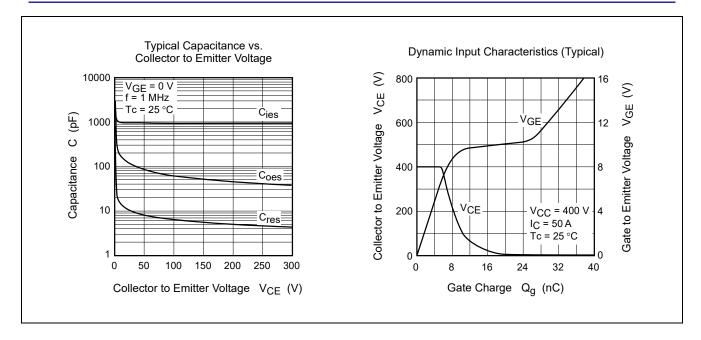
Renesas recommends that operating conditions are designed according to a document "Power MOS FET •

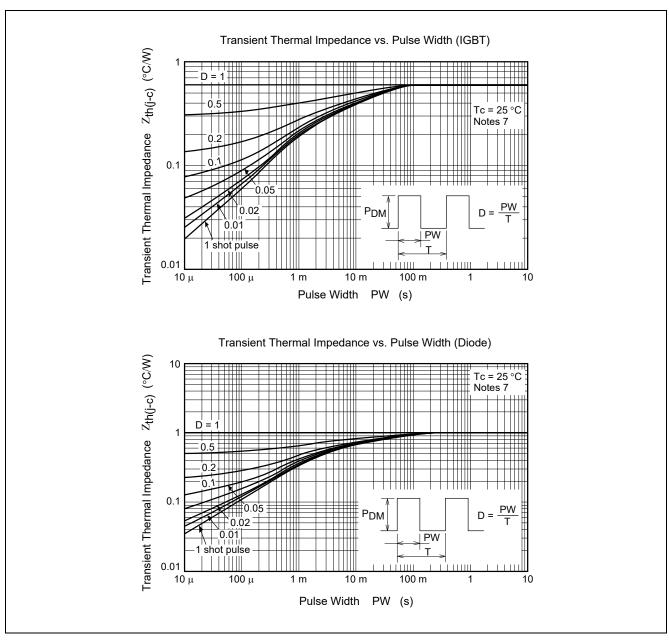
IGBT Attention of Handling Semiconductor Devices".



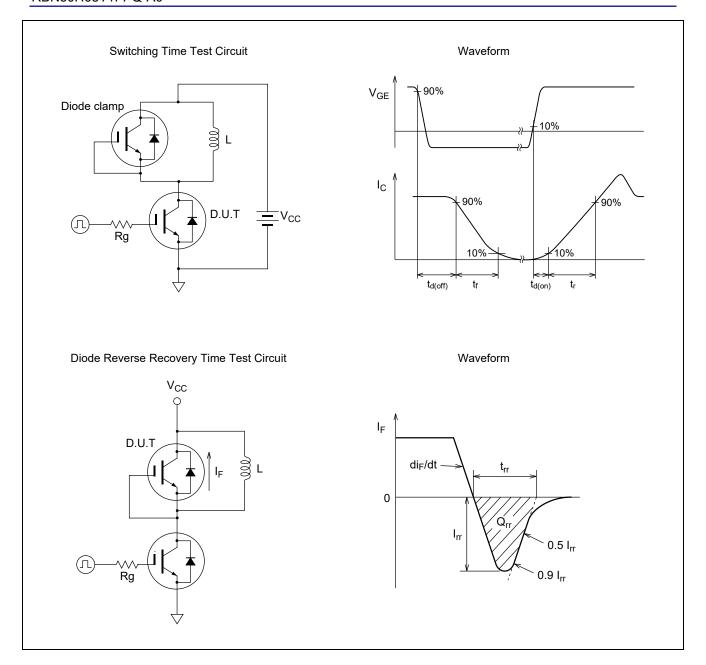




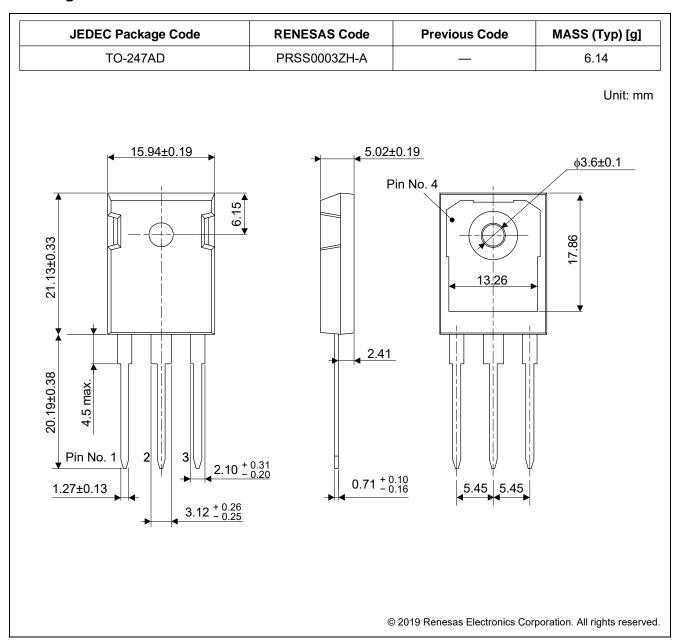




Notes: 7. Designed target value on Renesas measurement condition. (Not tested)



Package Dimensions



Ordering Information

Orderable Part Number	Quantity	Shipping Container
RBN50H65T1FPQ-A0#CB0	240 pcs	Box (Tube)

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