

RX Family

R01AN2296EJ0120 Rev.1.20 Jun 1, 2020

Sample Program using USB Peripheral Communication Device Class Driver (PCDC) for USB Mini Firmware to communicate via USB with USB Host Firmware Integration Technology

Introduction

This document describes the following sample firmware: USB Peripheral Communication Devices Class Driver using Firmware Integration Technology. The sample firmware is referred to below as the PCDC.

When developing an actual software, be sure to use the "USB Basic Mini Host and Peripheral Driver (USB Mini Firmware) using Firmware Integration Technology Application Note" (Document number: R01AN2166) together with the user's manual for each MCU (Hardware). In addition, also refer to the "USB Peripheral Communication Device Class Driver (PCDC) for USB Mini Firmware Firmware Integration Technology Application Note" (Document number: R01AN2170), if necessary. "USB Basic Mini Host and Peripheral Driver (USB Mini Firmware) using Firmware Integration Technology Application Note" (Document number: R01AN2170), if necessary. "USB Basic Mini Host and Peripheral Driver (USB Mini Firmware) using Firmware Integration Technology Application Note" (Document number: R01AN2166) is located in the "reference documents" folder within the package.

Target Device

RX111 Group RX113 Group RX231 Group RX23W Group

The operation of this program has been confirmed using the Renesas Starter Kits (RSK) or the Renesas Solution Starter Kit (RSSK).

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1. Introduction

1.1 Functions

The PCDC conforms to the Abstract Control Model of the USB communication device class (CDC) specification and implements communication with the USB host PC.

The PCDC provides the following functionalities:

- Implements USB-serial conversion functionality and USB loopback communication functionality (echo mode).
- It is recognized as a communication class (virtual COM) when connected to a USB host.
- Supports communication by designating a virtual COM port in the terminal software.

1.2 FIT Module Configuration

The PCDC comprises the following FIT modules and a sample application:

Table 1-1 FIT Module Configuration

FIT Module	Folder Name
RX Family Board Support Package Module Using Firmware Integration Technology	r_bsp
RX Family USB Basic Mini Host and Peripheral Driver (USB Mini Firmware) using Firmware Integration Technology	r_usb_basic_mini
RX Family USB Peripheral Communications Devices Class Driver(CDC) for USB Mini Firmware using Firmware Integration Technology	r_usb_pcdc_mini
RX Family DTC Module Using Firmware Integration Technology	r_dtc_rx
RX Family DMA Controller DMACA Control Module Firmware Integration Technology	r_dmaca_rx
RX Family BYTEQ Module Using Firmware Integration Technology	r_byteq
RX Family SCI Multi-Mode Module Using Firmware Integration Technology	r_sci_rx
RX Family LPC (Low Power Consumption) Module Firmware Integration Technology	r_lpc_rx

Refer to the related documentation for details of each FIT module. Note that the latest versions of the FIT modules used by the sample firmware are available for download from the following website:

Renesas Electronics website: http://www.renesas.com/

1.3 Note

This driver is not guaranteed to provide USB communication operation. The customer should verify operation when utilizing it in a system and confirm the ability to connect to a variety of different types of devices.



1.4 Operating Confirmation Environment

The operating confirmation environment for the PCDC is described below:

Item	Contents		
C compiler	Renesas Electronics C/C++ compiler for RX Family V.3.02.00		
	(The option "-lang=C99" is added to the default setting of IDE)		
	GCC for Renesas RX 8.3.0.201904		
	(The option "-std=gnu99" is added to the default setting of IDE)		
	IAR C/C++ Compiler for Renesas RX version 4.14.1		
Real-Time OS	FreeRTOS V.10.0.0		
	RI600V4 V.1.06		
Endian	Little Endian, Big Endian		
USB Driver Revision Number	Rev.1.20		
Using Board	Renesas Starter Kit for RX111		
	Renesas Starter Kit for RX113		
	Renesas Starter Kit for RX231		
	Renesas Solution Starter Kit for RX231		
Host Environment	The operation of this USB Driver module connected to the following OSes		
	has been confirmed.		
	1. Windows® 8.1		
	2. Windows® 10		

Table 1-2 Operation Confirmation Environment



2. Software Configuration

2.1 Module Configuration

The PCDC has a USB-serial converter mode and a USB loopback mode (echo mode). In USB-serial converter mode it uses the serial communication interface (SCI). LCD display and low-power control processing are implemented as a sample application.

Figure 2-1 shows the module configuration of the PCDC, and Table 2-1 lists the functions of the modules.

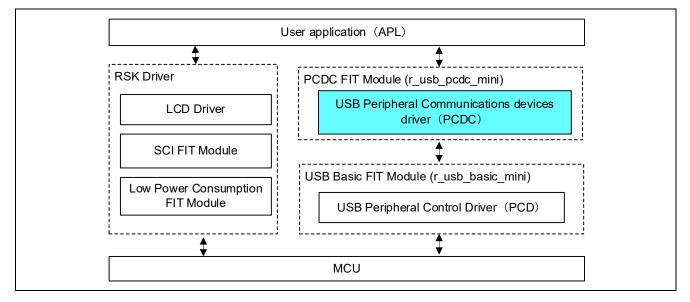


Figure 2-1 Module Configuration

Table 2-1	Functions of Modules
-----------	----------------------

Module Name	Function
APL	Sample application program
RSK driver	Sample application for using the peripheral functions of the RSK.
PCDC (r_usb_pcdc_mini)	CDC class driver
	Interprets requests from the USB host.
	• Provides data transfer services between the APL and the USB host, via the PCD.
PCD (r_usb_basic_mini)	USB peripheral hardware control driver



3. Setup

3.1 Hardware

3.1.1 Example Operating Environment

Figure 3-1 and Figure 3-2 shows an example operating environment for the PCDC. Refer to the associated instruction manuals for details on setting up the evaluation board and using the emulator, etc.

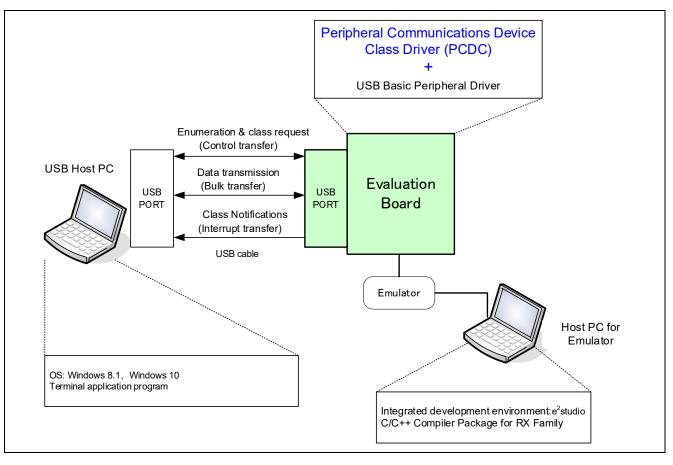


Figure 3-1 Example Operating Environment (Echo Mode)



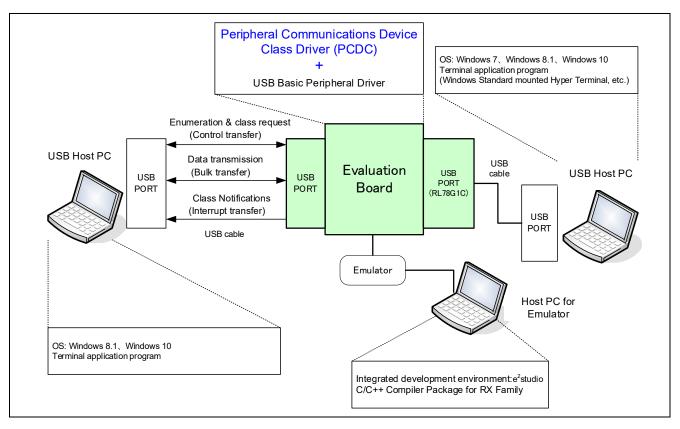


Figure 3-2 Example Operating Environment (USB-Serial Converter Mode)

Table 3-1 shows the evaluation board on which operation has been confirmed.

MCU	Evaluation Board
RX111	RSKRX111
RX113	RSKRX113
RX231	RSKRX231
RX23W	RSSKRX23W

Table 3-1 Evaluation Board on which PCDC Operation Has Been Verified

3.1.2 RSK / RSSK Setting

It is necessary to set RSK/RSSK to operate in the peripheral mode. Please refer to the following.

Table 3-2 RSK / RSSK Setting

RSK / RSSK	Jumper Setting
RSKRX111	J12: Shorted Pin2-3
RSKRX113	J12: Shorted Pin2-3
RSKRX231	J15: Shorted Pin2-3
RSSKRX23W	J5: Shorted Pin1-2

Note:

For the detail of RSK/RSSK setting, refer to the user's manual of RSK/RSSK.



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3.2 Software

- 1) Setup e² studio
 - a) Start e² studio
 - b) If you start up e² studio at first, the following dialog is displayed. Specify the folder to store the project in this dialog.

e ² Eclipse Launcher		×	
Select a directory as workspace e2 studio uses the workspace directory to store its preferences and development artifacts.			
<u>W</u> orkspace:	~	<u>B</u> rowse	
Use this as the default and do not ask again	OK	Cancel	

- 2) Import the project to the workspace
- a) Select [File] > [Import]
- b) Select [General] => [Existing Projects into Workspace]

e ² Import		×
Select Create new projects from an archive file or directory.	Ľ	5
<u>S</u> elect an import wizard: type filter text		
 ✓ ➢ General 		^



RX Family Sample Program using USB Peripheral Communication Device Class Driver (PCDC) for USB Mini Firmware to communicate via USB with USB Host

c) Select the root directory of the project, that is, the folder containing the ".cproject" file.

e ² Import	—	
Import Projects Select a directory to search for existing Eclipse projects.		
Select root directory: Select archive file:	~	B <u>r</u> owse
Projects:		
		<u>S</u> elect All
		Deselect All
		R <u>e</u> fresh

3) Click "Finish".

You have now imported the project into the workspace. Note that you can import other projects into the same workspace.

- 4) Generate the binary target program by clicking the "Build" button.
- 5) Connect the target board to the debug tool and download the executable. The target is run by clicking the "Run" button.



4. Sample Application

4.1 Application Specifications

The main functions of the APL are as follows:

1) Echo mode (Loopback mode) (Note1)

Transmits data received from the USB host back to the USB host.

2) USB-serial converter mode (Note1) (Note2) (Note3) (Note4)

Transmits data received from the USB host back to a COM port and data received from the COM port to the USB host. Note that if a COM port error (parity error, framing error, or overrun error) occurs, the APL sends a "SerialState" class notification to the USB host.

3) Low-power functionality

This functionality transitions the MCU to low-power mode according to the status of the USB.

- a. The APL transitions the MCU to sleep mode when the USB is suspended.
- b. When the USB is detached (disconnected), the APL transitions the MCU to software standby mode.
- (Note1) The selection of Echo mode or USB-serial converter mode is made by setting the "OPERATION_MODE" macro definition in the file "*r_usb_pcdc_apl_config.h.*"
- (Note2) The COM port is connected to RL78G1C on the RSK/RSSK, so G1CUSB0 is used for USB-USB communication.
- (Note3) In USB-serial converter mode, the following setting is required to RSK/RSSK.
 - a. The setting to change the channel number from SCI1 to SCI2 is required when using RSKRX111. Please set the follows to RSK.
 - a) Removes the option link resistance (0 ohm) on R224 and R116.
 - b) Mounts the option link resistance (0 ohm) on R37 and R38.
 - b. The setting to change the channel number from SCI1 to SCI6 is required when using RSKRX113. Please set the follows to RSK.
 - a) Removes the option link resistance (0 ohm) on R100 and R121.
 - b) Mounts the option link resistance (0 ohm) on R99 and R106.
 - c. The change is not required to RSK when using RSKRX231.
 - d. Please short Pin1-2 in J1(jumper) when using RSSKRX23W.
- (Note4) To select USB-serial converter mode, the following FIT is required.
 - a) RX Family Serial Communication Interface Firmware Integration Technology
 - b) RX Family BYTEQ Module Firmware Integration Technology



4.2 Application Processing (for Non-OS)

The APL comprises two parts: initial settings and main loop. An overview of the processing in these two parts is provided below.

4.2.1 Initial Setting

Initial settings consist of MCU pin settings, USB driver settings, and initial settings to the USB controller.

4.2.2 Main Loop (Echo mode)

In Echo mode, loop-back processing in which data sent by the USB host is received and then transmitted unmodified back to the USB host takes place as part of the main routine. An overview of the processing of the main loop is presented below.

- 1. When the *R_USB_GetEvent* function is called after enumeration with the USB host completes, *USB_STS_CONFIGURED* is set as the return value. When the APL confirms *USB_STS_CONFIGURED*, it calls the *R_USB_Read* function to make a data receive request for data sent by the USB host.
- 2. When enumeration with the USB host completes, the USB host sends an CDC class request to the CDC device. After it receives the CDC class request, the CDC device calls the *R_USB_GetEvent* function and the return value is set to *USB_STS_REQUST*. When the APL confirms *USB_STS_REQUEST*, it analyzes the received class request and performs processing corresponding to it.
- 3. When the *R_USB_GetEvent* function is called after reception of data from the USB host has completed, *USB_STS_READ_COMPLETE* is set as the return value. When the APL confirms *USB_STS_READ_COMPLETE*, it calls the *R_USB_Write* function to make a data transmit request to transmit the received data to the USB host.
- 4. When the *R_USB_GetEvent* function is called after transmission of data to the USB host completes, *USB_STS_WRITE_COMPLETE* is set as the return value. When the APL confirms *USB_STS_WRITE_COMPLETE*, it calls the *R_USB_Read* function to make a data receive request for data sent by the USB host.
- 5. The processing in steps 3 and 4, above, is repeated.
- 6. When it confirms reception of a suspend signal from the USB host or DETACH, the APL performs processing to transition the CDC device (RSK/RSSK) to low-power mode. For information on low-power mode, refer to 4.4, MCU Low power consumption processing. Note that confirmation of reception of a suspend signal or DETACH is performed by referencing the return value (USB_STS_SUSPEND or USB_STS_DETACH) of the R_USB_GetEvent function.



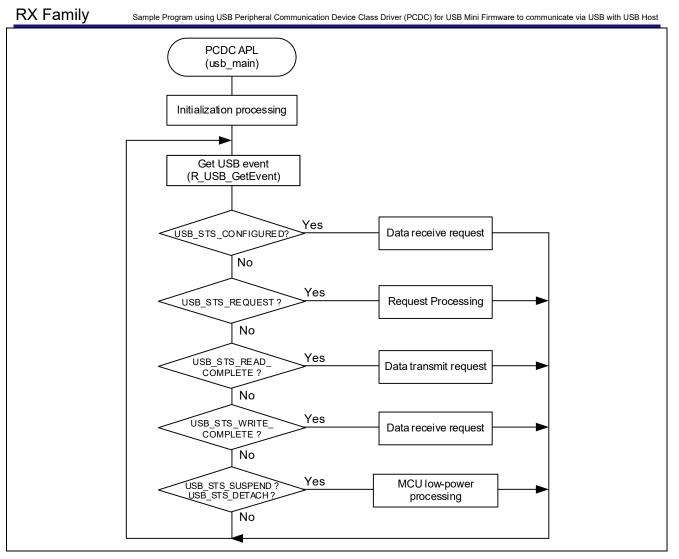


Figure 4-1 Main Loop processing (Loopback mode)



4.2.3 Main Loop (USB-serial converter mode)

The processing performed in USB serial convert mode is described below.

- a. Reception of data from the USB host, and transmission of the received data to the COM port.
- b. Transmission to the USB host of data received from the COM port.

An overview of the processing of the main loop is presented below.

- 1. When the *R_USB_GetEvent* function is called after enumeration with the USB host completes, *USB_STS_CONFIGURED* is set as the return value. When the APL confirms *USB_STS_CONFIGURED*, it calls the *R_USB_Read* function to make a data receive request for Bulk data sent by the USB host.
- 2. When enumeration with the USB host completes, the USB host sends an CDC class request to the CDC device. After it receives the CDC class request, the CDC device calls the *R_USB_GetEvent* function and the return value is set to *USB_STS_REQUST*. When the APL confirms *USB_STS_REQUEST*, it analyzes the received class request and performs processing corresponding to it.
- 3. When the *R_USB_GetEvent* function is called after processing of the class request mentioned in 2, above, completes, the return value is set to *USB_STS_REQUST_COMPLETE*. The APL performs processing to make request information settings, etc.
- 4. When the *R_USB_GetEvent* function is called after reception of Bulk data by the USB host has completed, *USB_STS_READ_COMPLETE* is set as the return value. When the APL confirms *USB_STS_READ_COMPLETE*, it sets the reception size of the bulk data to the global variable. This global variable is referenced in step 6, below.
- 5. When the *R_USB_GetEvent* function is called after sending of USB data (refer to step 6, below) to the USB host has completed, *USB_STS_WRITE_COMPLETE* is set as the return value. When the APL confirms *USB_STS_WRITE_COMPLETE*, it references member type in the *usb_ctrl_t* structure to identify the device class on which data transmission has completed. It then sets the completion flag corresponding to the device class on which data transmission has completed. This flag is referenced in step 6, below.
- 6. When the *R_USB_GetEvent* function is called when no USB event has occurred, *USB_STS_NONE* is set as the return value. The processing of *USB_STS_NONE* consists of the transmission processing described below. Note that before the transmission processing described below, the global variable and the flags set in steps 4 and 5 above are referenced to determine if transmission is possible.
 - (1). SCI transmission processing to send bulk data received from the USB host to the COM port
 - (2). If a SCI error (parity error, framing error, overrun error, etc.) has been detected, class notification (serial state) transmit request processing to notify the USB host
 - (3). Data transmit request processing to send the data received from the COM port to the USB host
- 7. If reception of a suspend signal from the USB host or DETACH is confirmed while steps 4 to 6 to, above, are repeating, the APL performs processing to transition the CDC device (RSK/RSSK) to low-power mode. For information on low-power mode, refer to **4.4**, **MCU Low power consumption processing**. Note that confirmation of reception of a suspend signal or DETACH is performed by referencing the return value (*USB STS SUSPEND* or *USB STS DETACH*) of the *R USB GetEvent* function.





Sample Program using USB Peripheral Communication Device Class Driver (PCDC) for USB Mini Firmware to communicate via USB with USB Host

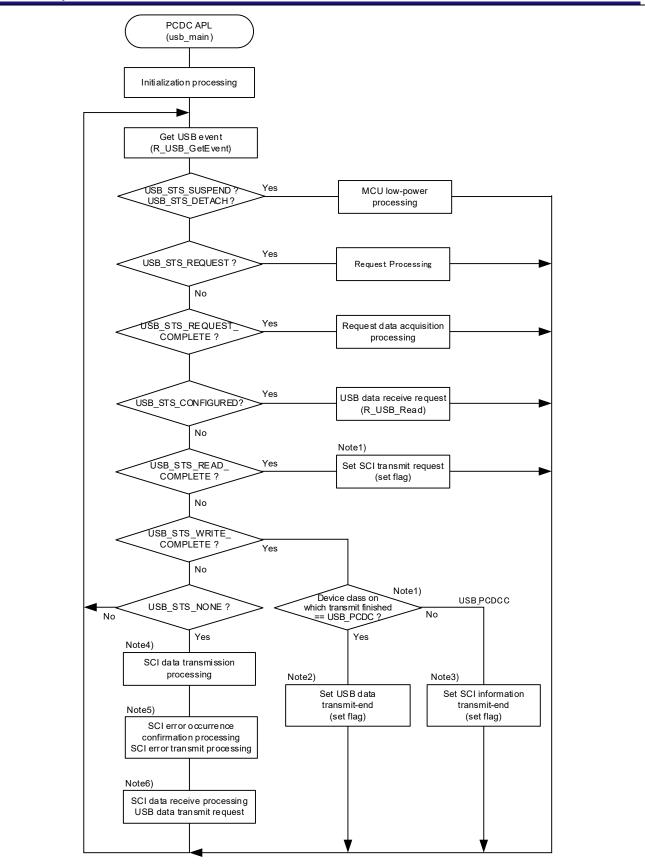


Figure 4-2 Main Loop processing (USB-serial converter mode mode)

(Note1) Sets the Bulk data reception size to the global variable. This global variable is referenced in the processing described in (Note4).

(Note2) Sets the USB data transmit-end flag. This flag is referenced in the processing described in (Note6).



RX Fami	Sample Program using USB Peripheral Communication Device Class Driver (PCDC) for USB Mini Firmware to communicate via USB with USB Host
(Note3)	Sets the SCI information (serial state) transmit-end flag. This flag is referenced in the processing described in (Note5).
(Note4)	References the global variable in (Note1) and performs SCI data transmit processing. If SCI data transmission completes normally, the SCI transmit request flag is cleared. If SCI data transmission fails, the SCI transmit request flag it not cleared so that SCI data transmit processing will be retried.

- (Note5) Performs processing to check the SCI status for the occurrence of an error. If an error has occurred, the R_USB_Write function is used to send error information to the USB host.
- (Note6) References the flag mentioned in (Note2) to confirm whether USB transmission is possible. If transmission is possible, the USB_Write function is used to send the received SCI data to the USB host. Note that confirmation of reception of data from the COM port is not performed until transmission of data to the USB host completes.

4.3 Application Processing (for RTOS)

The APL comprises two parts: initial settings and main loop. An overview of the processing in these two parts is provided below.

4.3.1 Initial Setting

Initial settings consist of MCU pin settings, USB driver settings, and initial settings to the USB controller.

4.3.2 Main Loop (Echo mode)

In Echo mode, loop-back processing in which data sent by the USB host is received and then transmitted unmodified back to the USB host takes place as part of the main routine. An overview of the processing performed by the loop is shown below.

- 1. When a USB-related event has completed, the USB driver calls the callback function (*usb_apl_callback*). In the callback function (*usb_apl_callback*), the application task (APL) is notified of the USB completion event using the real-time OS functionality.
- 2. In APL, information regarding the USB completion event was notified from the callback function is retrieved using the real-time OS functionality.
- 3. If the USB completion event (the *event* member of the *usb_ctrl_t* structure) retrieved in step 2 above is *USB_STS_CONFIGURED*, APL performs a data reception request to receive data transmitted from the USB Host by calling the *R_USB_Read* function.
- 4. If the USB completion event (the *event* member of the *usb_ctrl_t* structure) retrieved in step 2 above is *USB_STS_REQUEST*, APL performs processing in response to the received request.
- 5. If the USB completion event (the *event* member of the *usb_ctrl_t* structure) retrieved in step 2 above is *USB_STS_READ_COMPLETE*, APL performs a data transmission request to send USB Host the reception data by calling the *R_USB_Write* function.
- 6. If the USB completion event (the *event* member of the *usb_ctrl_t* structure) retrieved in step 2 above is *USB_STS_WRITE_COMPLETE*, APL performs a data reception request to receive the data sent from USB Host by calling the *R_USB_Read* function.
- If the USB completion event (the *event* member of the *usb_ctrl_t* structure) retrieved in step 2 above is USB_STS_SUSPEND or USB_STS_DETACH, APL performs processing to transition the CDC device (RSK) to low-power mode. For information on low-power mode, refer to 4.4, MCU Low power consumption processing.



An overview of the processing performed by the APL is shown below:

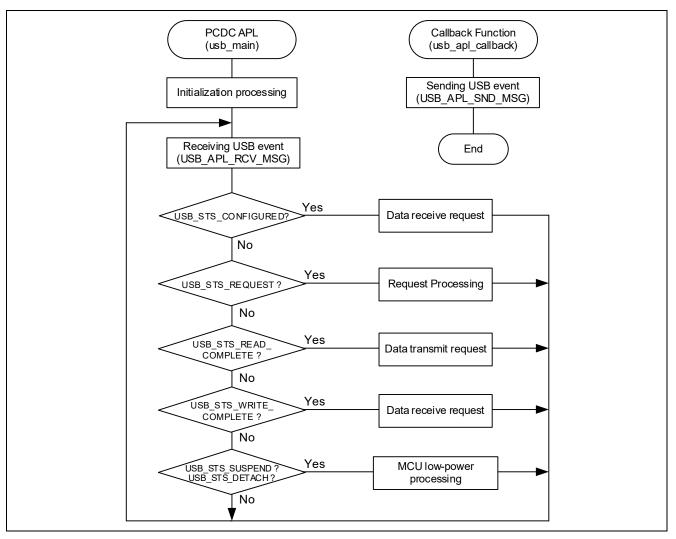


Figure 4-3 Main Loop processing (Echo mode)



4.3.3 Main Loop (USB-serial converter mode)

The processing performed in USB serial convert-back mode is described below.

- a. Reception of data from the USB host, and transmission of the received data to the COM port.
- b. Transmission to the USB host of data received from the COM port.

An overview of the processing of the main loop is presented below.

- 1. When a USB-related event has completed, the USB driver calls the callback function (*usb_apl_callback*). In the callback function (*usb_apl_callback*), the application task (APL) is notified of the USB completion event using the real-time OS functionality.
- 2. In APL, information regarding the USB completion event was notified from the callback function is retrieved using the real-time OS functionality.
- 3. If the USB completion event (the *event* member of the *usb_ctrl_t* structure) retrieved in step 2 above is *USB_STS_CONFIGURED*, APL performs a data reception request to receive data transmitted from the USB Host by calling the *R_USB_Read* function.
- 4. If the USB completion event (the *event* member of the *usb_ctrl_t* structure) retrieved in step 2 above is *USB_STS_REQUEST*, APL performs processing in response to the received request.
- 5. If the USB completion event (the *event* member of the *usb_ctrl_t* structure) retrieved in step 2 above is USB_STS_REQUEST_COMPLETE, APL performs processing to make request information settings, etc.
- 6. If the USB completion event (the *event* member of the *usb_ctrl_t* structure) retrieved in step 2 above is *USB STS READ COMPLETE*, APL sets the reception data size to a gloval variable.
- 7. If the USB completion event (the *event* member of the *usb_ctrl_t* structure) retrieved in step 2 above is *USB_STS_WRITE_COMPLETE*, APL references member *type* in the *usb_ctrl_t* structure to identify the device class on which data transmission has completed. It then sets the completion flag corresponding to the device class on which data transmission has completed. This flag is referenced in step 9, below.
- 8. If the USB completion event (the *event* member of the *usb_ctrl_t* structure) retrieved in step 2 above is *USB_STS_SUSPEND* or *USB_STS_DETACH*, APL performs processing to transition the CDC device (RSK) to low-power mode. For information on low-power mode, refer to **4.4**, **MCU Low power consumption processing**.
- 9. The following transmission processing is performed after completing the above processing. Note that before the transmission processing described below, the flags set in steps 5 and 7 above are referenced to determine if transmission is possible.
 - (1). SCI transmission processing to send bulk data received from the USB host to the COM port and the data reception processing from USB Host.
 - (2). If a SCI error (parity error, framing error, overrun error, etc.) has been detected, class notification (serial state) transmit request processing to notify the USB host
 - (3). Data transmit request processing to send the data received from the COM port to the USB host



An overview of the processing performed by the APL is shown below:

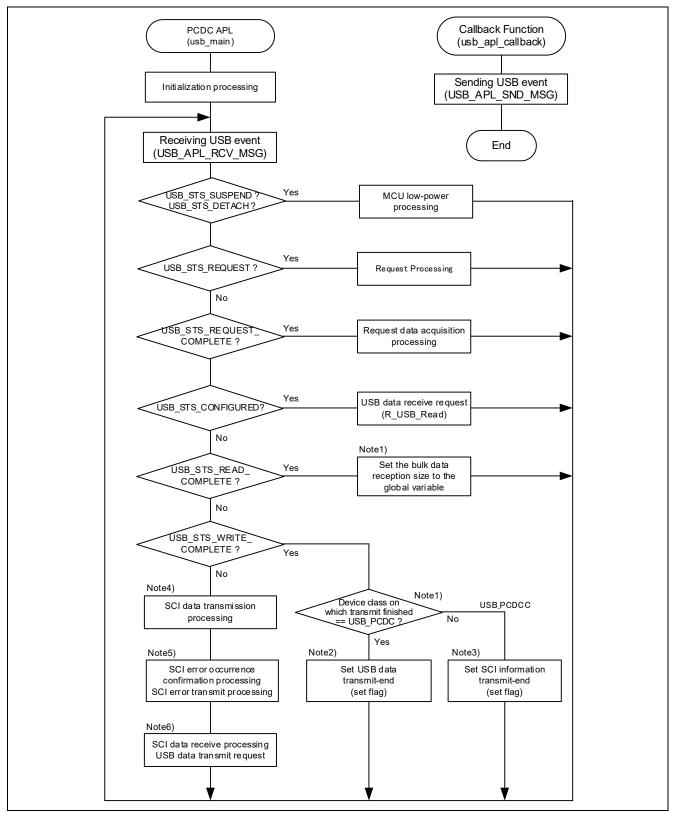


Figure 4-4 Main Loop processing (USB-serial converter mode mode)

(Note1) The reception data size is set to a global variable. This variable is refered in (Note4).

(Note2) Sets the USB data transmit-end flag. This flag is referenced in the processing described in (Note6).

RX Fami	Sample Program using USB Peripheral Communication Device Class Driver (PCDC) for USB Mini Firmware to communicate via USB with USB Host
(Note3)	Sets the SCI information (serial state) transmit-end flag. This flag is referenced in the processing described in (Note5).
(Note4)	References the global variable in (Note1) and performs SCI data transmit processing. If SCI data transmission completes normally, the SCI transmit request flag is cleared. If SCI data transmission fails, the SCI transmit request flag it not cleared so that SCI data transmit processing will be retried.
(Note5)	Performs processing to check the SCI status for the occurrence of an error. If an error has occurred, the R_USB_Write function is used to send error information to the USB host.

(Note6) References the flag mentioned in (Note2) to confirm whether USB transmission is possible. If transmission is possible, the USB_Write function is used to send the received SCI data to the USB host. Note that confirmation of reception of data from the COM port is not performed until transmission of data to the USB host completes.

4.4 MCU Low power consumption processing

MCU low-power processing occurs when the conditions in Table 4-1 or Table 4-2 are met, causing a transition to low-power mode.

1. Non-OS

Transition Condition		Transition Status
VBUS	USB State	
OFF	—	Software standby mode
ON	Suspend Configured	Sleep mode
ON	Other than Suspend Configured	Normal mode (program running)

- (1). When the CDC device (RSK/RSSK) detaches from the USB host (VBUS OFF), the APL performs processing to transition the MCU to software standby mode. Recovery from software standby mode occurs when the CDC device (RSK/RSSK) attaches to the USB host.
- (2). When a suspend signal sent by the USB host is received while the CDC device (RSK/RSSK) is connected to the USB host, the APL performs processing to transition the MCU to sleep mode. Note that recovery from sleep mode occurs when a resume signal is received from the USB host.

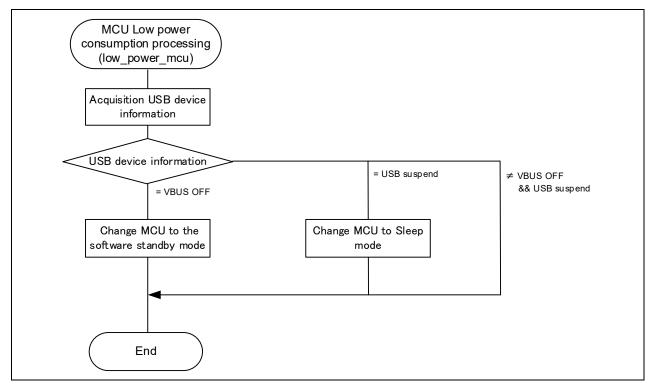


Figure 4-5 Flowchart of MCU Low Power Consumption Processsing

2. RTOS (FreeRTOS only)

Transition Condition		Transition Status
VBUS USB State		
OFF	1	Software standby mode
ON	Suspend Configured	Software standby mode
ON	Other than Suspend Configured	Normal mode (program running)

Table 4-2 Conditions for Transition to Low-Power Mode

- (1). When the CDC device (RSK) detaches from the USB host (VBUS OFF), the APL performs processing to transition the MCU to software standby mode. Recovery from software standby mode occurs when the CDC device (RSK) attaches to the USB host.
- (2). When a suspend signal sent by the USB host is received while the CDC device (RSK) is connected to the USB host, the APL performs processing to transition the MCU to software standby mode. Note that recovery from softwre standby mode occurs when a resume signal is received from the USB host.

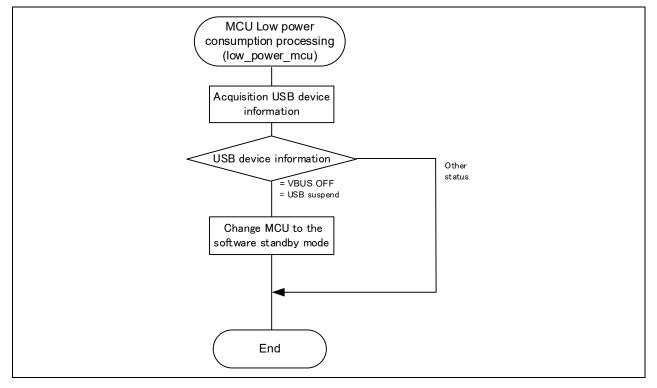


Figure 4-6 Flowchart of MCU Low Power Consumption Processsing



4.5 Configuration File for the application program (r_usb_pcdc_apl_config.h)

Make settings for the definitions listed below.

1. OPERATION_MODE Definition

Specify one of the following settings for the OPERATION MODE definition.

#define	OPERATION_MODE	USB_ECHO	// Echo mode
#define	OPERATION_MODE	USB_UART	<pre>// USB-serial conversion mode</pre>

2. Low-Power Function Definition

Specify whether or not the low-power function will be used. If the low-power function will be used, speify USB APL ENABLE to USB SUPPORT LPW definition.

#define	USE_SUPPORT_LPW	USB_APL_DISABLE	// No use the low-power function
#define	USB_SUPPORT_LPW	USB_APL_ENABLE	<pre>// Use the low-power function</pre>

3. USB_SUPPORT_RTOS Definition

Please specify USB APL ENABLE to USB SUPPORT RTOS definition when using the real-time OS.

#define	USB_SUPPORT_RTOS	USB_APL_DISABLE	// No use the real-time OS
#define	USB_SUPPORT_RTOS	USB_APL_ENABLE	// Use the real-time OS

4. Note

The above configuration settings apply to the application program. USB driver configuration settings (*r_usb_basic_mini_config.h*) are required in addition to the above settings. For information on USB driver configuration settings, refer to the application note USB Basic Mini Host and Peripheral Driver (USB Mini Firmware) using Firmware Integration Technology (Document number. R01AN2166).

4.6 Descriptor

The PCDC's descriptor information is contained in *r_usb_pcdc_descriptor.c*. Also, please be sure to use your vendor ID.



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5. CDC Driver Installation

If USB Host is PC, CDC driver must be installed in the PC. When you connect RSK which this PCDC sample program is written in to your PC, the wizard shown in Figure 5-1 will appear on your screen and prompt the CDC driver installation

- (1). Select **Update Driver Software** from the device manager.
- (2). Select "Browse my computer for driver software".

Note:

- (1). It is not necessary the following installation work for CDC driver when using Window® 10.
- (2). The catalog file with the digital signature is required when using Windows® 8.1. The customer needs to create this catalog

file.

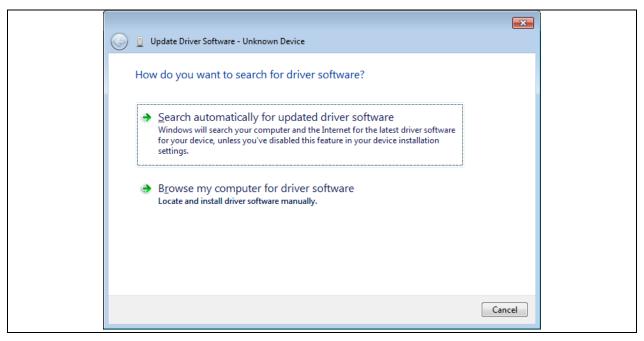


Figure 5-1 New Hardware Search Wizard



(3). Select "Browse for driver software on your computer"

Click Browse, specify the folder in which the CDC_Demo.inf is stored, then click "Next"

C International Content of Conten
Browse for driver software on your computer
Search for driver software in this location:
-tsushinou01\Desktop\an_r01an2179ej0103_usb\reference\cdc_inf 💌 Browse
☑ Include subfolders
Let me pick from a list of device drivers on my computer This list will show installed driver software compatible with the device, and all driver software in the same category as the device.
Next Cancel

Figure 5-2 Select Driver Location

Note:

The CDC_Demo.inf file is stored in "r_usb_pcdc¥utilities" folder in the package.

(4). If the following installation confirmation screen appears, click "Browse for driver software on your computer"

	Windows Security Windows can't verify the publisher of this driver software		
	Do <u>n</u> 't install this driver software You should check your manufacturer's website for updated driver software for your device.		
	Install this driver software anyway Only install driver software obtained from your manufacturer's website or disc. Unsigned software from other sources may harm your computer or steal information.		
See <u>d</u>	etails		

Figure 5-3 Installation Confirmation Screen



(5). When the following window appears, the CDC driver has been successfully installed. Click "Close."

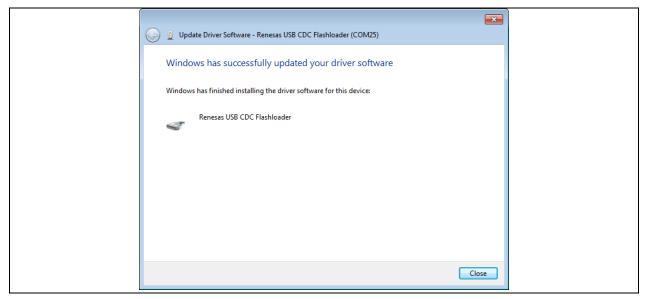


Figure 5-4 Installation Complete



6. Class Driver Overview

6.1 Class Request (Request from Host to Device)

Table 6-1 lists the class requests supported by the PCDC.

Table 0-1 Supported CDC class Requests		
Request	Coce	Description
SetLineCoding	0x21	Sets transmission line coding (transmission speed, data length, parity bit, stop bit length)
GetLineCoding	0x22	Notify transmission line coding state.
SetControlLineState	0x23	Sets control signals RTS and DTR for transmission line.

Table 6-1 Supported CDC Class Requests

6.2 Data Format

The CDC data class has no specified format. Data may be transferred in any format.



7. Using RI600V4 project with CS+

The RI600V4 project in the package does not support CS+. The user needs to create a project for CS+ according to the following procedure when using RI600V4 project on CS+.

7.1 New Project Creation

Select "Application(RI600V4, CC-RX) for the Kind of project.

Create Project		;	×
Microcon <u>t</u> roller:	RX	×	·
Using microcontroller:			
(Search microcontroller)		Update	
R5F52318BxFL(4 R5F52318BxFM(6 R5F52318BxFM(6 R5F52318BxLA(1 R5F52318BxND(6 R5F52318BxND(6 R5F52318BxNL(4 R5F52318BxNL(4 R5F52318BxNL(4 R523E-A RX23E-A RX23T	64pin) 00pin) 00pin) 64pin)	Product Name:R5F52318BxFP On-chip ROM size[KBytes]:512 On-chip RAM size[Bytes]:65536 Additional Information:Package=PLQP0100KB-A	
Kind of project:	Application(RI600	0V4.CC-RX)	Ĩ
Project <u>n</u> ame:	rx_usb		
P <u>l</u> ace:	D:\RX	✓ B <u>r</u> owse	

7.2 Launch Smart Configurator

1. Clock Setting (Select "Clocks" tab)

(1). Set the related clock so that "48MHz" is set to UCLK (USB clock).

The following is a setting example when using the oscillator(8MHz).

Frequency Division:				
x1/2 👻				
Frequency Multiplication:				
x12	Ŧ			

(2). Set the minimum value to the wait time for the main clock.

Main clo	ck		
Oscillation s	source:	Resonator	•
Frequency:	8		(MHz)
Wait time:	2	▼ ¹ 0.5	(us)

2. Component Setting (Select "Components" tab)

(1). Import the USB FIT module

Select the *r_usb_pcdc_mini* module and press the "Finish" button. The *r_usb_basic_mini* module is imported at the same time.

Note:

Select the r_dtc_rx / r_dmaca_rx module when using the DTC/DMA.

🔁 New Component — 🗆 🗙				
Software Component Selection Select component from those available in list				
Туре	All			~
Function	All			\sim
Filter				
Compo	nents	Туре	Version	^
🖶 r_bsp)	FIT	5.50	
🖶 r_cm	t_rx	FIT	4.30	
🖶 r_usb	_basic_mini	FIT	1.20	
🖶 r_usb	_hcdc_mini	FIT	1.20	
🖶 r_usb	_hhid_mini	FIT	1.20	
	_hmsc_mini	FIT	1.20	
🖶 r_usb	_pcdc_mini	FIT	1.20	
	_phid_mini	FIT	1.20	
🖶 r_usb	_pmsc_mini	FIT	1.20	<u> </u>

(2). Configuration Setting

a. r_bsp

Change the heap size when using DTC transfer. For the setting value, refer to the documentation for DTC FIT module.

b. r_usb_basic_mini

*rx_usb.scfg 🔀			- [
Software component configu	iration		6
Components 👌 📄 🕀 🕇 🔻	Configure		
te 😜	Property	Value	
type filter text	> 🏶 Configurations		
V 🗁 Startup	> 🔲 Resources		
V > Generic			
er_bsp			
V 🗁 Drivers			
✓ ➢ Communications	<		>
💣 r_usb_basic_mini			

(a). Configurations

Set each item according to the user system. For the detail of each item, refer to chapter "Configuration" in USB Basic Mini Host and Peripheral Driver Firmware Integration Technology application note (Document number: R01AN2166).

(b). Resources

Check the check box for USBx_VBUS pin.



Sample Program using USB Peripheral Communication Device Class Driver (PCDC) for USB Mini Firmware to communicate via USB with USB Host

Value	
Unused	
Unused	
Unused	
Used	
	Unused Unused Unused

c. r_usb_pcdc_mini

Refer to chapter "Configuration" in USB Peripheral Communications Devices Class Driver (PCDC) for USB Mini Firmware Firmware Integration Technology application note (Document number: R01AN2170).

3. Pin Setting (Select "Pins" tab)

Select the port for USB pin match the user system.

Hardware Resource $\exists \exists \Box \downarrow_z^a \Leftrightarrow$	Pin Function	1	3	🔛 📑 🕹	2
	type filter t	ext (* = any string,	? = any character)	All	\sim
* SD host interface * USB 2.0 host/function mo	Enabled	Function	Assignment	Pin Number	^
✓ Grand All Converter		USB0_EXICEN	Not assigned	Not assigned	
S12AD0		USB0_ID	Not assigned	Not assigned	
		USB0_OVRCURA	Not assigned	Not assigned	
12-bit D/A converter		USB0_OVRCURB	Not assigned	Not assigned	
V 💑 Comparator B		USB0_VBUS	P16/MTIOC3C/MTIOC3D/TMO2/TIOCB1/TCLKC/R	/ 30	
CMPB0		USB0_VBUSEN	Not assigned	Not assigned	
CMPB1		VCC_USB	VCC_USB	35	~

4. Generate Code

The Smart Configurator genrates source codes for USB FIT module and USB pin setting in "<*ProjectDir*>¥src¥smc gen" folder by by clicking on the [100 (Generate Code)] button.

∰ *usb_prj.scfg 🛛				
Software	component con	iguration	🖻 🕒	
Compon	.¦ª₂ 🕞 🕀 🛟 ▼	Configure		

Note:

Select "Yes" if the following dialog box is displayed.

2	Current section setting of project may not be compatible with Smart Configurator	
Do you want to change section setting?		
	Current section setting:	
	B_1,R_1,B_2,R_2,B,R,SU,SI/4,PResetPRG/FFC00000,C_1,C_2,C,C\$DSEC,C\$BSEC,C\$INIT,C	
	\$VTBL,C\$VECT,D_1,D_2,D,P,PIntPRG,W_1,W_2,W,L/FFC00100,EXCEPTVECT/FFFFF80, DESCEDUCET (SECTOR)	
	RESETVECT/FFFFFFC New section setting:	
	SI,SURI STACK,B*,R*/0x00000004,INTERRUPT VECTOR,P*,C*,D*,W*,L/0xFFC00000,FIX	
	INTERRUPT_VECTOR/0xFFFFF80	
	[Yes] to change section setting	
	[No] to keep current section setting	
	[Cancel] to cancel code generation	
Do	not show again	



7.3 Add the application program and the configuration file

- 1. Copy the *demo_src* folder in this package to the "<*ProjectDir*>¥*src*" folder.
- 2. Copy the RI600V4 configuration file (.cfg file) to "<*ProjectDir*>" folder.
- 3. Select "File" in the "Project Tree" and click the right button. Select [Add] → [Add New Category] and create the category to store the application program. Then select [Add File] and register the application program and the configuration file which are copied at the above 2.

Ξ.,	Project Tree 📮 🗙	Property	- ×
Smart Manual	2 🕜 🙎 🗷	File Property	₽ - +
E.		4 Build	
nu		Set as build-target Yes	
-	🖳 Smart Configurator (Design Tool)	File type C source	
		Set individual compile option	
	Program Analyzer (Analyze Tool)		
	Add	Add File	
	Open Folder with Explorer	Ndd New File	
	Windows Explorer Menu	Add New Category	
	B Romovo from Drojoct Shift	Dal	

Note:

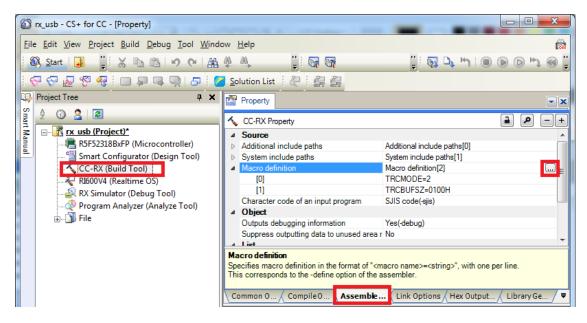
Remove the "task.c" file and "sample.cfg" created in "<ProjectDir>" folder by CS+.

7.4 Remote Macro Definition

Remove these macros since the following macros is defined in the new created project.

Select [CC-RX(Build Tool)] \rightarrow [Assemble Options] tab, remove the following macros.

- 1. TRCMODE = 2
- 2. TRCBUFSZ = 0100H



7.5 Build Execution

Excecute the build and generate the binary target program.



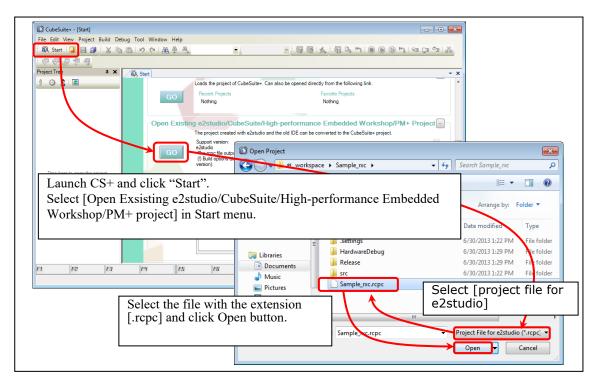
RX Family

8. Using the e² studio project with CS+

The PCDC contains a project only for e^2 studio. When you use the PCDC with CS+, import the project to CS+ by following procedures.

[Note]

- 1. Uncheck the checkbox Backup the project composition files after conversion in Project Convert Settings window.
- 2. The following method is not supported when using RI600V4. Refer to chapter **7**, Using RI600V4 project with CS+.



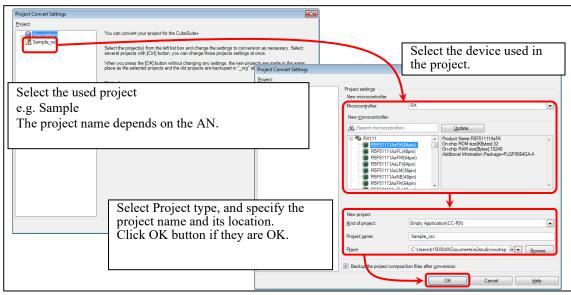


Figure 8-1 Using the e² studio project with CS+

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Revision Record

		Description	
Rev.	Date	Page	Summary
1.00	Dec 1, 2014	—	First Edition Issued.
1.01	Jun 1, 2015	—	RX231 is added in Target Device.
1.02	Dec 28, 2015	—	Checked the operation with Windows® 10.
1.10	Nov 30, 2018	—	1. The following chapter has beed added.
			(1). 3.1.2 RSK/RSSK Setting
			2. The following chapter has beed changed.
			(1). 4. Sample Application
1.12	Jun 30, 2019		RX23W is added in Target Device.
1.20	Jun 1, 2020	—	Supported the real-time OS.

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Handle unused pins in accordance with the directions given under Handling of Unused Pins in the manual.

— The input pins of CMOS products are generally in the high-impedance state. In operation with an unused pin in the open-circuit state, extra electromagnetic noise is induced in the vicinity of LSI, an associated shoot-through current flows internally, and malfunctions occur due to the false recognition of the pin state as an input signal become possible. Unused pins should be handled as described under Handling of Unused Pins in the manual.

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The state of the product is undefined at the moment when power is supplied.

- The states of internal circuits in the LSI are indeterminate and the states of register settings and pins are undefined at the moment when power is supplied.
 - In a finished product where the reset signal is applied to the external reset pin, the states of pins are not guaranteed from the moment when power is supplied until the reset process is completed.

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Access to reserved addresses is prohibited.

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