

Note on Using Peripheral Driver Generator V.2.09

When using the Peripheral Driver Generator, take note of the following problem:

- With using RX210 group MCUs
-

1. Product and Version Concerned

Peripheral Driver Generator V.2.09

2. Description

When the above product is used to generate code for an MCU of the RX210 group, the following device information is output regardless of the product type selected in the process of creating a project.

- Package: 144-pin LFQFP
- Chip version: Ver. B
- ROM size: 768 KB or 1 MB

Consequently, the packages, the chip versions, and ROM sizes in the generated code will usually differ from the actual device information, and processing for the judgment of branches may lead to unintended operation of the generated functions.

For example, in a project for a product of the RX210 group in an 80-pin LQFP, an error will occur inside the clock initialization function "R_PG_Clock_Set()", and the value returned by the function will always be "false".

3. Workaround

Overwrite the "r_pdl_configuration.c" file (see NOTE below) stored in the location where Peripheral Driver Generator is installed with a new file.

The new file can be downloaded and decompressed from the web page.
Click [here](#) to download.

This page will be available from September 22, 2014.

NOTE:

The file is installed in the following folder by default:

C:\renesas\PDG2\source\RX\RX210\i_src

4. Schedule for Fixing the Problem

This problem will be fixed in a later version of the product.

[Disclaimer]

The past news contents have been based on information at the time of publication. Now changed or invalid information may be included. The URLs in the Tool News also may be subject to change or become invalid without prior notice.

© 2010-2016 Renesas Electronics Corporation. All rights reserved.