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# **Note on Using Peripheral Driver Generator**

When using Peripheral Driver Generator, take note of the following problem:

With setting up the clock generation circuit when using the RX62T group of MCUs

#### 1. Product and Versions Concerned

Peripheral Driver Generator V.2.02 to V.2.07

### 2. Description

For the RX62T group of MCUs, values up to 100 MHz are specifiable as the system clock (ICLK) frequency. However, if you select any of the MCUs listed below during the process of creating a new project by using the Peripheral Driver Generator, values greater than 80 MHz cannot be specified for the system clock (ICLK) in the window for setting up the clock generation circuit.

R5F562TAADFM

R5F562T7ADFM

R5F562T6ADFM

R5F562TADDFM

R5F562T7DDFM

R5F562T6DDFM

#### 3. Workaround

When you create a new project, consult the list below and select the replacement MCU name that corresponds to the actual MCU you are using.

After that, values from 80 to 100 MHz will be specifiable for the system clock (ICLK).

Even though the replacement MCU is not identical to the actual MCU, the Peripheral Driver Generator will show the same windows and output the same code.

(Part name)	(Part name)	
R5F562TAADFM	->	R5F562TABDFM
R5F562T7ADFM	->	R5F562T7BDFM
R5F562T6ADFM	->	R5F562T6BDFM
R5F562TADDFM	->	R5F562TAEDFM
R5F562T7DDFM	->	R5F562T7EDFM
R5F562T6DDFM	->	R5F562T6EDFM

## 4. Schedule of Fixing Problem

This problem will be fixed in V.2.08 of the Peripheral Driver Generator.

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