

RENESAS TOOL NEWS on July 1, 2004: RSO-M3T-NC30WA\_2-040701D

## A Note on Using C Compiler Package M3T-NC30WA V.5.30 Release 1 --On Setting the Reset Vector in the Start-Up Program--

Please take note of the following problem in using the M3T-NC30WA V.5.30 Release 1 C-compiler package for the M16C/60, M16C/30, M16C/20, M16C/10, and R8C/Tiny series MCUs:

- On setting the Reset vector in the start-up program
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### 1. Versions Concerned

M3T-NC30WA V.5.30 Release 1

### 2. Description

The upper 8 bits of the Reset vector are set to 00.

Consequently, if any MCU belonging to the groups other than the R8C/10 and /11 groups is used in the R8C/Tiny series, the watchdog timer will start operating after reset.

#### 2.1 Conditions

This problem occurs if the following two conditions are satisfied:

- (1) Any MCU belonging to the groups other than the R8C/10 and /11 groups is used in the R8C/Tiny series.
- (2) Compile option "-R8C" is selected, or when a project is created using the High-performance Embedded Workshop, "Renesas M16C Standard" is selected out of the Toolchain list box, "5.3.0.0" is selected out of the Toolchain version list box, and "R8C/Tiny" is selected out of the CPU Series list box.

### 3. Workaround

This problem can be circumvented in either of the following ways:

- (1) Download the problem-fixed sect30.inc(19KB) start-up file and then replace the

current file with it.

- (2) Put a statement in the 736th line of the sect30.inc start-up file as follows (in the 735th line, the Reset vector is specified):

Original:

```
-----  
    .org 0fffch  
RESET:  
    .lword start    ; the 735th line  
-----
```

Modified:

```
-----  
    .org 0fffch  
RESET:  
    .lword start  
    .protect 0ffh    ; Put in the 736th line  
-----
```

#### 4. Schedule of Fixing the Problem

We plan to fix this problem in our next release of the product.

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