

RENESAS TECHNICAL UPDATE

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|--------------------|---|--|----------------------|------------------------|--|------|
| Product Category | MPU/MCU | | Document No. | TN-RX*-A174A/E | Rev. | 1.00 |
| Title | Errata to RX Family User's Manuals Regarding the Ethernet Controller (ETHERC) | | Information Category | Technical Notification | | |
| Applicable Product | RX64M Group, RX71M Group RX65N Group, RX651 Group | | Lot No. | Reference Document | User's Manual: Hardware for applicable products (see the table at the last page) | |
| | | | All | | | |

This document describes corrections to the section of the Ethernet Controller (ETHERC) in User's Manual: Hardware for the applicable products.

Page and section numbers are based on the RX64M Group. Refer to the table on the last page for the corresponding page and section numbers in the other groups.

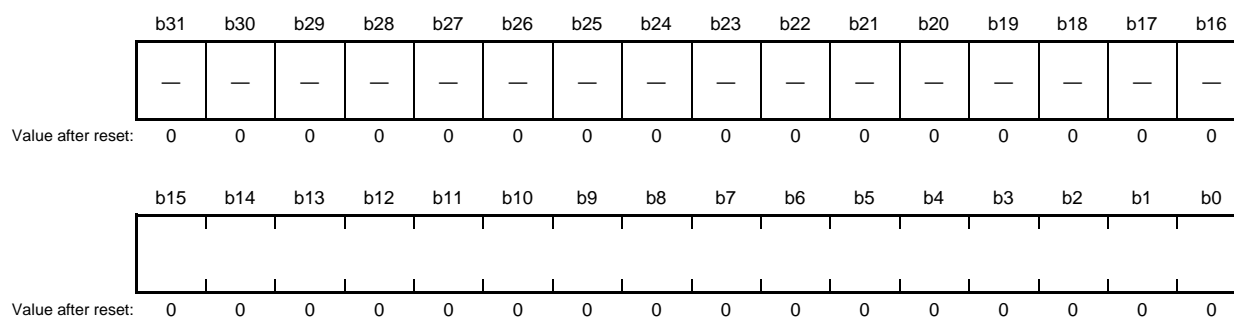
Errata

- Page 1533 of 2936

The bit symbol and the bit name of the lower 16 bits in the MALR register in section 35.2.16 are added as follows.

Before correction

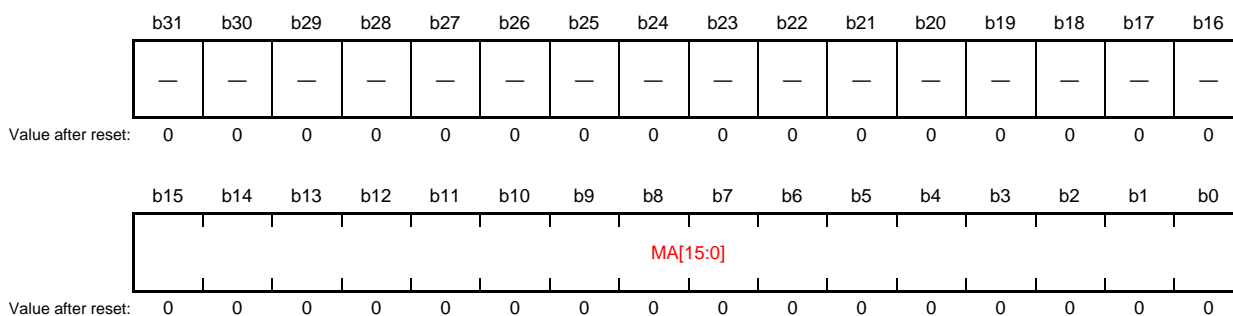
Address(es): ETHERC0.MALR 000C 01C8h, ETHERC1.MALR 000C 03C8h



| Bit | Symbol | Bit Name | Description | R/W |
|------------|--------|----------|--|-----|
| b15 to b0 | — | — | These bits set the lower 16 bits of the MAC address. | R/W |
| b31 to b16 | — | Reserved | The read value is 0. The write value should be 0. | R/W |

After correction

Address(es): ETHERC0.MALR 000C 01C8h, ETHERC1.MALR 000C 03C8h



| Bit | Symbol | Bit Name | Description | R/W |
|------------|----------|-----------------|--|-----|
| b15 to b0 | MA[15:0] | MAC Address Low | These bits set the lower 16 bits of the MAC address. | R/W |
| b31 to b16 | — | Reserved | The read value is 0. The write value should be 0. | R/W |

Reference Documents

| Applicable Product | Manual Title (Document Number) | Page Number | Section Number |
|--------------------------|---|-------------------|----------------|
| RX64M Group | RX64M Group User's Manual: Hardware Rev.1.10 (R01UH0377EJ0110) | Page 1533 of 2936 | 35.2.16 |
| RX71M Group | RX71M Group User's Manual: Hardware Rev.1.00 (R01UH0493EJ0100) | Page 1533 of 2923 | 35.2.16 |
| RX65N Group, RX651 Group | RX65N Group, RX651 Group User's Manual: Hardware Rev.1.00 (R01UH0590EJ0100) | Page 1539 of 2468 | 34.2.16 |

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