

# Microcomputer Technical Information

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<b>DF703218</b> Device File for V850 Series V850ES/KE1, V850ES/KF1, V850ES/KG1, V850ES/KJ1 Upgrade		Document No.	ZBG-CD-04-0081	1/4
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Related documents	Device File for V850ES/KE1, V850ES/KF1, V850ES/KG1, V850ES/KJ1 DF703218 (V2.00) User's Manual: ZUD-CD-04-0083	Document classification		Usage restriction
			√	Upgrade
				Document modification
				Other notification

## 1. Product types and version

Product	Outline	Target Version
μSxx13DF703210	Device file DF703210 for V850 Series V850ES/KF1	V1.10
μSxx13DF703214	Device file DF703214 for V850 Series V850ES/KG1	V1.10
μSxx13DF703217	Device file DF703217 for V850 Series V850ES/KJ1	V1.10
Product	Outline	Target Version
μSxx17SP850	V850 Series software package SP850	V3.10

**Note** xx indicates AB or BB.

## 2. Details of upgrade

The major revisions in DF703218 V2.00 are shown below. For details, refer to “Device File for V850ES/KE1, V850ES/KF1, V850ES/KG1, V850ES/KJ1 DF703218 (V2.00) User's Manual (ZUD-CD-04-0083)” supplied with the product.

(1) Collection of devices files for V850ES/KE1, V850ES/KF1, V850ES/KG1, V850ES/KJ1

(2) Addition of the following device files

μPD703207, μPD703207Y, μPD703211, μPD703211Y, μPD703215, μPD703215Y,  
 μPD70F3207H, μPD70F3207HY, μPD70F3210H, μPD70F3210HY, μPD70F3211H,  
 μPD70F3211HY, μPD70F3215H, μPD70F3215HY, μPD70F3217H, μPD70F3217HY,  
 μPD70F3218HY

(3) Deletion of the database files for simulator

(4) Addition of protect information<sup>Note</sup> of the following peripheral I/O register  
 RTBH0, RTPM0, PCC, ADTP0

**Note** “Protect information” is information that is used to check the accessible data patterns.

(5) Change of protect information<sup>Note</sup> of the following peripheral I/O register

TMC00, TMC01, WDTE, PSMR, CORAD0H, CORAD1H, CORAD2H, CORAD3H

**Note** “Protect information” is information that is used to check the accessible data patterns.

(6) Addition of the bit register names for the following peripheral I/O registers

TMC00, TMC01, WDTE, PSMR, CORAD0H, CORAD1H, CORAD2H, CORAD3H

Reserved Word	Peripheral I/O.location	Reserved Word	Peripheral I/O.location	Reserved Word	Peripheral I/O.location
PFCM	PFM.6	TOLEV0	TMHMD0.1	NRZ0	TMCYC0.0
TOLEV1	TMHMD1.1	NRZ1	TMCYC1.0	OSPE00	TOC01.5
RTPOE0	RTPC0.7	CK3	PCC.3	CLS	PCC.4
MCK	PCC.6	COREN0	CORCN.0	COREN1	CORCN.1
COREN2	CORCN.2	COREN3	CORCN.3	DIRA0	CSIMA0.1
RXEA0	CSIMA0.2	TXEA0	CSIMA0.3	--	--

(7) Deletion of the following peripheral I/O register

ADCRL (0x3FFF204)

(8) Addition of the bit register names for the following peripheral I/O registers

(Target devices:  $\mu$ PD703207,  $\mu$ PD703207Y,  $\mu$ PD703211,  $\mu$ PD703211Y,  $\mu$ PD703215,  $\mu$ PD703215Y,  $\mu$ PD70F3207H,  $\mu$ PD70F3207HY,  $\mu$ PD70F3210H,  $\mu$ PD70F3210HY,  $\mu$ PD70F3211H)

Reserved Word	Peripheral I/O.location	Reserved Word	Peripheral I/O.location	Reserved Word	Peripheral I/O.location
SPT0	IICC0.0	STT0	IICC0.1	ACKE0	IICC0.2
WTIM0	IICC0.3	SPIE0	IICC0.4	WREL0	IICC0.5
DAD0	IICCL0.4	CLD0	IICCL0.5	CLX0	IICX0.0
SPD0	IICS0.0	STD0	IICS0.1	ACKD0	IICS0.2
TRC0	IICS0.3	COI0	IICS0.4	EXC0	IICS0.5
EXC0	IICS0.5	ALD0	IICS0.6	MSTS0	IICS0.7
IICRSV0	IICF0.0	STCEN0	IICF0.1	IICBSY0	IICF0.6
STCF0	IICF0.7	--	--	--	--

(9) Change of the bit register names for the following peripheral I/O registers

Before Change		After Change	
Reserved Word	Peripheral I/O.location	Reserved Word	Peripheral I/O.location
OSPT0	TOC00.6	OSPT00	TOC00.6
OSPT1	TOC01.6	OSPT01	TOC01.6

(10) Addition of the bit register names for the following peripheral I/O registers

(Target devices:  $\mu$ PD703212,  $\mu$ PD703212Y,  $\mu$ PD703213,  $\mu$ PD703213Y,  $\mu$ PD703214,  $\mu$ PD703214Y,  $\mu$ PD703216,  $\mu$ PD703216Y,  $\mu$ PD703217,  $\mu$ PD703217Y,  $\mu$ PD70F3214,  $\mu$ PD70F3214Y,  $\mu$ PD70F3217,  $\mu$ PD70F3217Y)

Reserved Word	Peripheral I/O.location	Reserved Word	Peripheral I/O.location	Reserved Word	Peripheral I/O.location
DIRA1	CSIMA1.1	RXEA1	I CSIMA1.2	TXEA1	CSIMA1.3

(11) Change of the bit register names for the following peripheral I/O registers

(Target devices:  $\mu$ PD703216,  $\mu$ PD703216Y,  $\mu$ PD703217,  $\mu$ PD703217Y,  $\mu$ PD70F3217,  $\mu$ PD70F3217Y)

Before Change		After Change	
Reserved Word	Peripheral I/O.location	Reserved Word	Peripheral I/O.location
OSPT4	TOC04.6	OSPT04	TOC04.6
OSPT5	TOC05.6	OSPT05	TOC05.6

(12) Addition of the bit register names for the following peripheral I/O registers

(Target devices:  $\mu$ PD703216,  $\mu$ PD703217,  $\mu$ PD70F3217)

Reserved Word	Peripheral I/O.location	Reserved Word	Peripheral I/O.location	Reserved Word	Peripheral I/O.location
OSPE04	TOC04.5	OSPE05	TOC05.5	RTPOE1	RTPC1.7

(13) Addition of the bit register names for the following peripheral I/O registers

(Target devices:  $\mu$ PD703216Y,  $\mu$ PD703217Y,  $\mu$ PD70F3217Y)

Reserved Word	Peripheral I/O.location	Reserved Word	Peripheral I/O.location	Reserved Word	Peripheral I/O.location
OSPE04	TOC04.5	OSPE05	TOC05.5	RTPOE1	RTPC1.7
SPT1	IICC1.0	STT1	IICC1.1	ACKE1	IICC1.2
WTIM1	IICC1.3	SPIE1	IICC1.4	WREL1	IICC1.5
DAD1	IICCL1.4	CLD1	IICCL1.5	CLX1	IICX1.0
SPD1	IICS1.0	STD1	IICS1.1	ACKD1	IICS1.2
TRC1	IICS1.3	COI1	IICS1.4	EXC1	IICS1.5
ALD1	IICS1.6	MSTS1	IICS1.7	IICRSV1	IICF1.0
STCEN1	IICF1.1	IICBSY1	IICF1.2	STCF1	IICF1.3

### 3. How to upgrade

#### (1) $\mu$ Sxx13DF703210, $\mu$ Sxx13DF703214, $\mu$ Sxx13DF703217

- Free upgrade (without medium)

Download the software through ODS (online delivery service). When using this method, the supply medium will not be delivered but the upgrade is free of charge.

**Note** Access the following URL.

<http://www.necel.com/micro/ods/eng/>

( $\mu$ SBB13DF703210,  $\mu$ SBB13DF703214,  $\mu$ SBB13DF703217; English version)

<http://www.necel.com/micro/ods/jpn/>

( $\mu$ SAB13DF703210,  $\mu$ SAB13DF703214,  $\mu$ SAB13DF703217; Japanese version)

#### (2) SP850

- Free upgrade (without medium)

Download the upgraded software (DF703218) through ODS (online delivery service). When using this method, the medium will not be delivered but the upgrade is free of charge.

**Note** Access the following URL.

<http://www.necel.com/micro/ods/eng/> ( $\mu$ SBB17SP850; English version)

<http://www.necel.com/micro/ods/jpn/> ( $\mu$ SAB17SP850; Japanese version)

### 4. Upgrade start date

- Free upgrade (Windows version, without medium)

The upgraded product can be downloaded through ODS from October 4, 2004.