

Microcomputer Technical Information

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DF703107 Device File for V850E/MA1 Usage Restrictions		Document No.	ZBG-CD-04-0028	1/1
		Date issued	July 12, 2004	
		Issued by	Development Tool Group Multipurpose Microcomputer Systems Division 3rd Systems Operations Unit NEC Electronics Corporation	
Related documents	V850E/MA1 Device File DF703107 (V1.20) Operating Precautions: SBG-T-2207-E	Notification classification	√	Usage restriction
				Upgrade
				Document modification
				Other notification

1. Affected product

Part Number	Outline	Target Version
μSxx13DF703107	V850E/MA1 device file DF703107	V1.20
μSxx17SP850	V850 Series software package SP850	V2.00 or later

Note xx indicates AB or BB.

2. New restrictions

The following restrictions have been added. See the attachment for details.

- No.1 Writing to bit 7 of the BCP register (0xFFFFF48C) cannot be performed in the IOR window of the ID850 or SM850
- No.2 An error occurs when the TCO3 (bit 3 of DTOC), TCO2 (bit 2 of DTOC), TCO1 (bit 1 of DTOC), or TCO0 (bit 0 of DTOC) register is used by the CA850

3. Workarounds

The following workarounds are available for these restrictions. See the attachment for details.

- No.1 There is no workaround.
- No.2 Use the DTOC register as follows.

Example: DTOC.2 = 1;

4. Modification schedule

DF703107 V1.21 July 16, 2004

5. List of restrictions

A list of restrictions in the DF703107, including the revision history and detailed information, is described on the following pages.

6. Revision history

DF703107 Device File for V850E/MA1 Usage Restrictions

Document Number	Issued on	Description
ZBG-CD-04-0028	July 12, 2004	Newly created.

List of Restrictions in DF70107

1. Product History

No.	Bugs and Changes/Additions to Specifications	Version	
		V1.20	V1.21
1	Writing to bit 7 of the BCP register (0xFFFFF48C) cannot be performed in the IOR window of the ID850 or SM850	×	√
2	An error occurs when the TCO3, TCO2, TCO1, or TCO0 register is used by the CA850	×	√

×: Applicable, √: Corrected

2. Details of Bugs and Additions to Specifications

No.1 Writing to bit 7 of the BCP register (0xFFFFF48C) cannot be performed in the IOR window of the ID850 or SM850

[Description]

Writing to bit 7 of the BCP register (0xFFFFF48C) cannot be performed in the IOR window of the ID850 or SM850.

[Workaround]

There is no workaround.

[Action]

This restriction will be corrected in the next version (V1.21).

No.2 An error occurs when the TCO3, TCO2, TCO1, or TCO0 register is used by the CA850

[Description]

An error occurs when the TCO3, TCO2, TCO1, or TCO0 register is used by the CA850 because the following register bits are not defined

TCO3 (bit 3 of DTOC register), TCO2 (bit 2 of DTOC register),

TCO1 (bit 1 of DTOC register), TCO0 (bit 0 of DTOC register)

[Workaround]

Use the DTOC register as follows.

Example: DTOC.2 = 1;

[Action]

This restriction will be corrected in the next version (V1.21).