

Microcomputer Technical Information

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DF703089 Device File for V850/SC1, V850/SC2, V850/SC3 Usage Restriction		Document No.	SBG-DT-03-0172-E	1/2
		Date issued	July 7, 2003	
		Issued by	Microcomputer Group 2nd Solutions Division Solutions Operations Unit NEC Electronics Corporation	
Related documents	Device file for V850/SC1, V850/SC2, V850/SC3 DF703089 (V1.00) User's Manual: SUD-DT-03-0173-E	Notification classification	<input checked="" type="checkbox"/>	Usage restriction
			<input type="checkbox"/>	Upgrade
			<input type="checkbox"/>	Document modification
			<input type="checkbox"/>	Other notification

1. Affected products and version

Product	Outline	Target Version
μ Sxx13DF703089	DF703089 Device file for V850 Series V850/SC1, V850/SC2, V850/SC3	Ver.1.00
Product	Outline	Target Version
μ Sxx17SP850	SP850 V850 Series software package	Ver.3.00

Note xx indicates AB or BB.

2. New restriction

No.1 Restriction that DMA transfer cannot be performed on addresses 0xffe000 to 0xffefff in the RAM area

3. Workaround

[Workaround]

Set the ID850 as follows.

- (1) Activate the ID850 and select [Console] from the [Browse] menu; the Console window then opens.
- (2) Input "ie reg 0x110 0x24" after the prompt in the Console window and press the Enter key.
- (3) Close the Console window.

Remark Implement this workaround each time the debugger is activated.

4. Modification schedule

DF703089 V1.10 in which this bug is corrected is planned for release on July 18, 2003.

5. List of restrictions

A list of restrictions in the DF703089, including the revision history and detailed information, is described on the following pages.

6. Document revision history

DF703089 Device File for V850/SC1, V850/SC2, V850/SC3 Usage Restriction

Revision History

Document Number	Date Issued	Description
SBG-DT-03-0172-E	July 7, 2003	Newly created.

List of Usage Restrictions in DF703089

1. Product History

No.	Bugs and Changes/Additions to Specifications	Version	
		V1.00	V1.10
1	Restriction that DMA transfer cannot be performed on addresses 0xffe000 to 0xffff in the RAM area	×	√

×: Bug or restriction applicable √: Bug or restriction corrected

2. Details of Usage Restriction

No.1 Restriction that DMA transfer cannot be performed on addresses 0xffe000 to 0xffff in the RAM area
[Description]

DMA transfer cannot be performed on addresses 0xffe000 to 0xffff in the RAM area.

[Workaround]

Set the ID850 as follows.

- (1) Activate the ID850 and select [Console] from the [Browse] menu; the Console window then opens.
- (2) Input "ie reg 0x110 0x24" after the prompt in the Console window and press the Enter key.
- (3) Close the Console window.

Remark Implement this workaround each time the debugger is activated.

[Correction]

This restriction will be corrected in the next revision (V1.10).