

# RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU		Document No.	TN-RX*-A187A/E	Rev.	1.00
Title	Corrections to Descriptions for the P/E Suspend Command of Flash Memory in RX64M Group, RX71M Group Flash Memory User's Manual: Hardware Interface		Information Category	Technical Notification		
Applicable Product	RX64M Group, RX71M Group	Lot No.	Reference Document	RX64M Group, RX71M Group Flash Memory User's Manual: Hardware Interface Rev.1.10 (R01UH0435EJ0110)		
		All				

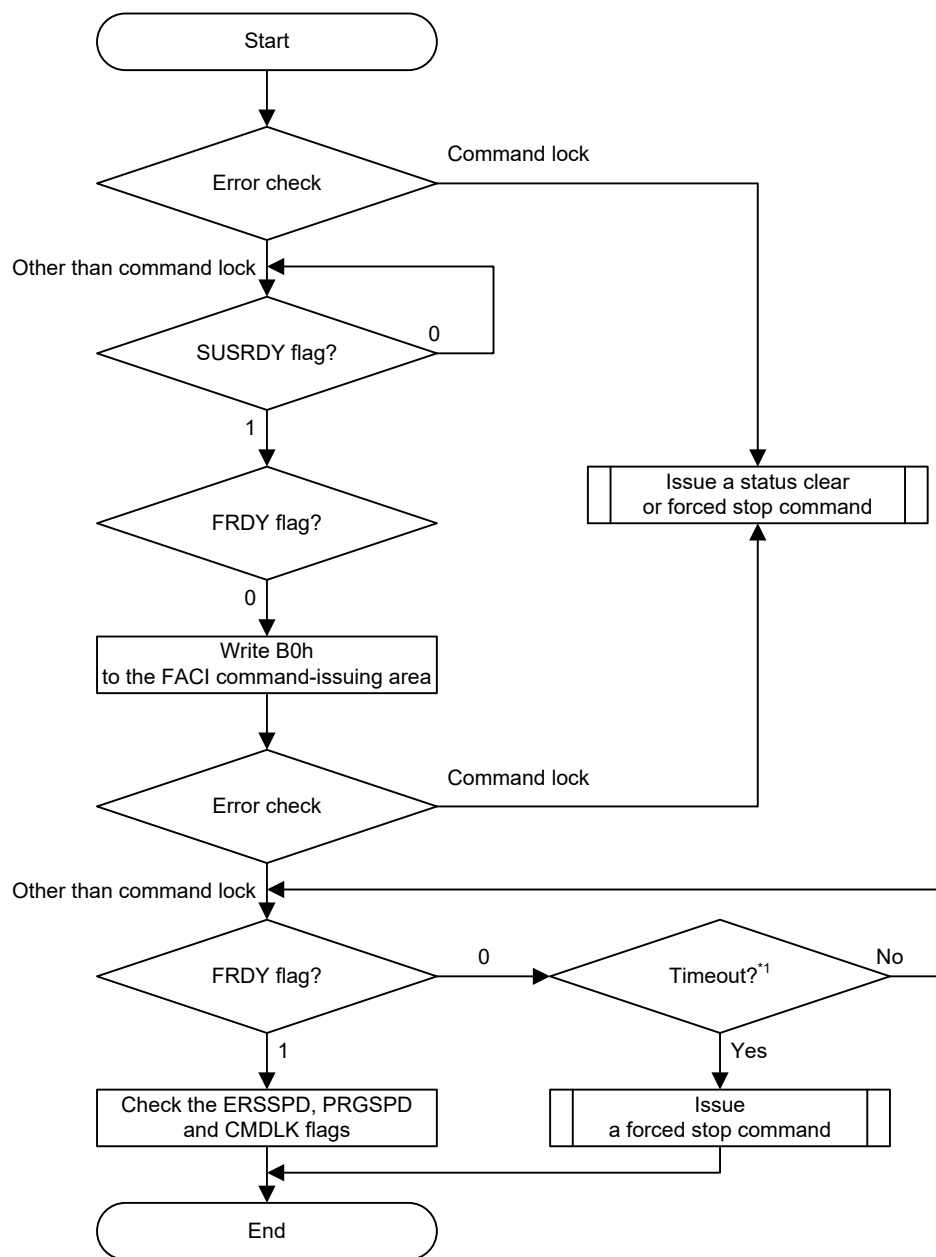
This document describes corrections to the flow chart of the P/E suspend command in RX64M Group, RX71M Group Flash Memory User's Manual: Hardware Interface Rev.1.10.

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In Figure 6.11, Usage of the P/E Suspend Command, a processing flow never escapes from the loop for a checking procedure if the program/erase processing completed before checking the SUSRDY flag.

Therefore, a timeout processing and a breaking processing when the FRDY flag is 1 are to be added.

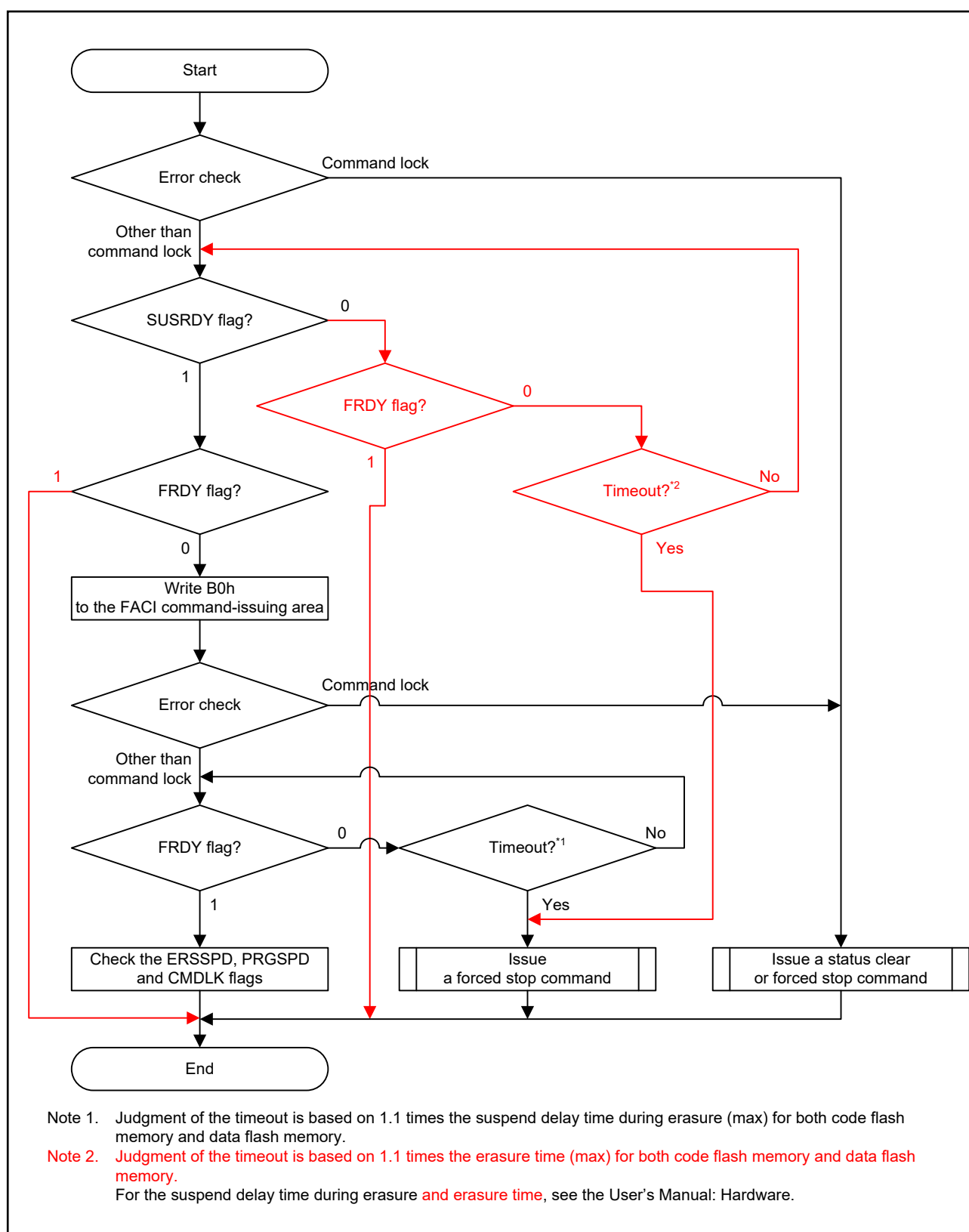
# Before correction



Note 1. Judgment of the timeout is based on 1.1 times the suspend delay time during erasure (max) for both code flash memory and data flash memory.  
For the suspend delay time during erasure, see the User's Manual: Hardware.

**Figure 6.11 Usage of the P/E Suspend Command**

# After correction



**Figure 6.11 Usage of the P/E Suspend Command**

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