

# RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU		Document No.	TN-RZ*-A031A/E	Rev.	1.00
Title	Addendum for DBSC3 Pin States		Information Category	Technical Notification		
Applicable Product	RZ/G Series, RZ/G1H, M, and N	Lot No.	Reference Document	RZ/G Series, each product User's Manual: Hardware (refer to table 1)		
		All lots				

There is a following addendum about the RZ/G series, each product user's manual: hardware.

#### [Summary]

DQS/DQS# pin states during power-on reset and default state

#### [Products]

RZ/G1H, M, and N

#### [Note]

There is no specification change (addendum only).

#### [Addendum]

Section 4, Pin Multiplexing: MnDQSx and MnDQSx# pins of DBSC3 (n=0, 1; 1 is available for RZ/G1H and M):

Pin states during power-on reset and default state

(gray parts (abcd) are newly added)

Current (from): Z

Addendum (to): Z\*

Note. \*: The drivers output states are both high-impedance (Z), and the internal circuit controls pin levels as low-level for the MnDQSx pin and high-level for the MnDQSx# pin respectively.

#### [Reference Document]

Table 1 Reference Document for each RZ/G Series Products

Products	Document Title	Revision	Date	Document No.
RZ/G1H	RZ/G1H User's Manual: Hardware	1.00	Sep. 30, 2016	R01UH0627EJ0100
RZ/G1M	RZ/G1M User's Manual: Hardware	1.00	Sep. 30, 2016	R01UH0626EJ0100
RZ/G1N	RZ/G1N User's Manual: Hardware	1.00	Sep. 30, 2016	R01UH0628EJ0100

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