User's Manual

RX630 Group

Peripheral Driver Generator
Reference Manual

All information contained in these materials, including products and product specifications, represents information on the product at the time of publication and is subject to change by Renesas Electronics Corp. without notice. Please review the latest information published by Renesas Electronics Corp. through various means, including the Renesas Electronics Corp. website (http://www.renesas.com).

Renesas Electronics
www.renesas.com

Rev.1.03 May 2014

Notice

1. Descriptions of circuits, software and other related information in this document are provided only to illustrate the operation of semiconductor products and application examples. You are fully responsible for the incorporation of these circuits, software, and information in the design of your equipment. Renesas Electronics assumes no responsibility for any losses incurred by you or third parties arising from the use of these circuits, software, or information.
2. Renesas Electronics has used reasonable care in preparing the information included in this document, but Renesas Electronics does not warrant that such information is error free. Renesas Electronics assumes no liability whatsoever for any damages incurred by you resulting from errors in or omissions from the information included herein.
3. Renesas Electronics does not assume any liability for infringement of patents, copyrights, or other intellectual property rights of third parties by or arising from the use of Renesas Electronics products or technical information described in this document. No license, express, implied or otherwise, is granted hereby under any patents, copyrights or other intellectual property rights of Renesas Electronics or others.
4. You should not alter, modify, copy, or otherwise misappropriate any Renesas Electronics product, whether in whole or in part. Renesas Electronics assumes no responsibility for any losses incurred by you or third parties arising from such alteration, modification, copy or otherwise misappropriation of Renesas Electronics product.
5. Renesas Electronics products are classified according to the following two quality grades: "Standard" and "High Quality". The recommended applications for each Renesas Electronics product depends on the product's quality grade, as indicated below.

   "Standard": Computers; office equipment; communications equipment; test and measurement equipment; audio and visual equipment; home electronic appliances; machine tools; personal electronic equipment; and industrial robots etc.

   "High Quality": Transportation equipment (automobiles, trains, ships, etc.); traffic control systems; anti-disaster systems; anti-crime systems; and safety equipment etc.

   Renesas Electronics products are neither intended nor authorized for use in products or systems that may pose a direct threat to human life or bodily injury (artificial life support devices or systems, surgical implantations etc.), or may cause serious property damages (nuclear reactor control systems, military equipment etc.). You must check the quality grade of each Renesas Electronics product before using it in a particular application. You may not use any Renesas Electronics product for any application for which it is not intended. Renesas Electronics shall not be in any way liable for any damages or losses incurred by you or third parties arising from the use of any Renesas Electronics product for which the product is not intended by Renesas Electronics.
6. You should use the Renesas Electronics products described in this document within the range specified by Renesas Electronics, especially with respect to the maximum rating, operating supply voltage range, movement power voltage range, heat radiation characteristics, installation and other product characteristics. Renesas Electronics shall have no liability for malfunctions or damages arising out of the use of Renesas Electronics products beyond such specified ranges.
7. Although Renesas Electronics endeavors to improve the quality and reliability of its products, semiconductor products have specific characteristics such as the occurrence of failure at a certain rate and malfunctions under certain use conditions. Further, Renesas Electronics products are not subject to radiation resistance design. Please be sure to implement safety measures to guard them against the possibility of physical injury, and injury or damage caused by fire in the event of the failure of a Renesas Electronics product, such as safety design for hardware and software including but not limited to redundancy, fire control and malfunction prevention, appropriate treatment for aging degradation or any other appropriate measures. Because the evaluation of microcomputer software alone is very difficult, please evaluate the safety of the final products or systems manufactured by you.
8. Please contact a Renesas Electronics sales office for details as to environmental matters such as the environmental compatibility of each Renesas Electronics product. Please use Renesas Electronics products in compliance with all applicable laws and regulations that regulate the inclusion or use of controlled substances, including without limitation, the EU RoHS Directive. Renesas Electronics assumes no liability for damages or losses occurring as a result of your noncompliance with applicable laws and regulations.
9. Renesas Electronics products and technology may not be used for or incorporated into any products or systems whose manufacture, use, or sale is prohibited under any applicable domestic or foreign laws or regulations. You should not use Renesas Electronics products or technology described in this document for any purpose relating to military applications or use by the military, including but not limited to the development of weapons of mass destruction. When exporting the Renesas Electronics products or technology described in this document, you should comply with the applicable export control laws and regulations and follow the procedures required by such laws and regulations.
10. It is the responsibility of the buyer or distributor of Renesas Electronics products, who distributes, disposes of, or otherwise places the product with a third party, to notify such third party in advance of the contents and conditions set forth in this document, Renesas Electronics assumes no responsibility for any losses incurred by you or third parties as a result of unauthorized use of Renesas Electronics products.
11. This document may not be reproduced or duplicated in any form, in whole or in part, without prior written consent of Renesas Electronics.
12. Please contact a Renesas Electronics sales office if you have any questions regarding the information contained in this document or Renesas Electronics products, or if you have any other inquiries.

(Note 1) "Renesas Electronics" as used in this document means Renesas Electronics Corporation and also includes its majority-owned subsidiaries.

(Note 2) "Renesas Electronics product(s)" means any product developed or manufactured by or for Renesas Electronics.

Introduction

This manual was written to explain how to make the peripheral I/O drivers on the Peripheral Driver Generator for RX630. For the basic information about the Peripheral Driver Generator, refer to the Peripheral Driver Generator user's manual.

Table of Contents

Introduction ... 3
Table of Contents ... 4
1. Overview ... 12
1.1 Supported peripheral modules ... 12
1.1.2 Tool requirements ... 13
2. Creating a new project ... 14
3. Setting Up the Peripheral Modules ... 15
3.1 Main Window ... 15
3.2 Pin Functions (Multifunction Pin Controller) ... 17
3.2.1 [Pin function] Sheet ... 17
3.2.2 [Peripheral pin usage] Sheet ... 20
3.2.3 [Pin layout] Sheet ... 22
3.2.4 Pin Settings Shared between Setting Windows ... 25
3.2.5 Error Messages and Warnings on Pin Settings ... 27
3.3 Endian ... 29
4. Tutorial ... 30
4.1 When the High-performance Embedded Workshop is in Use ... 30
4.1.1 An LED blinking on a 8-bit timer (TMR) interrupt ... 31
4.1.2 An LED blinking on the PWM output of the multi-function timer pulse unit 2 (MTU2a) ... 44
4.1.3 Continuously scanning on 12-Bit A/D converter (S12ADa) ... 51
4.1.4 Triggering DTCa by ICUb ... 58
4.2 When the CubeSuite+ is in Use ... 65
4.2.1 An LED blinking on Compare Match Timer (CMT) interrupt ... 66
4.3 When the e2 studio is in Use ... 75
4.3.1 Data transfer between SCIc channels 0 and 2 ... 76
5. Specification of Generated Functions ... 88
5.1 Clock-Generation Circuit ... 98
5.1.1 R_PG_Clock_Set ... 98
5.1.2 R_PG_Clock_WaitSet ... 99
5.1.3 R_PG_Clock_Start_MAIN ... 100
5.1.4 R_PG_Clock_Stop_MAIN ... 101
5.1.5 R_PG_Clock_Enable_MAIN_ForcedOscillation ... 102
5.1.6 R_PG_Clock_Disable_MAIN_ForcedOscillation ... 103
5.1.7 R_PG_Clock_Start_SUB ... 104
5.1.8 R_PG_Clock_Stop_SUB ... 105
5.1.9 R_PG_Clock_Start_LOCO ... 106
5.1.10 R_PG_Clock_Stop_LOCO ... 107
5.1.11 R_PG_Clock_Start_HOCO ... 108
5.1.12 R_PG_Clock_Stop_HOCO ... 109
5.1.13 R_PG_Clock_PowerON_HOCO ... 110

5.1.14 R_PG_Clock_PowerOFF_HOCO 111
5.1.15 R_PG_Clock_Start_PLL 112
5.1.16 R_PG_Clock_Stop_PLL 113
5.1.17 R_PG_Clock_Enable_BCLK_PinOutput 114
5.1.18 R_PG_Clock_Disable_BCLK_PinOutput 115
5.1.19 R_PG_Clock_Enable_MAIN_StopDetection 116
5.1.20 R_PG_Clock_Disable_MAIN_StopDetection 117
5.1.21 R_PG_Clock_GetFlag_MAIN_StopDetection 118
5.1.22 R_PG_Clock_ClearFlag_MAIN_StopDetection 119
5.1.23 R_PG_Clock_GetSelectedClockSource 120
5.1.24 R_PG_Clock_GetClocksStatus 121
5.1.25 R_PG_Clock_GetHOCOPowerStatus 122
5.2 Voltage Detection Circuit (LVDA) 123
5.2.1 R_PG_LVD_Set 123
5.2.2 R_PG_LVD_GetStatus 124
5.2.3 R_PG_LVD_ClearDetectionFlag_LVD<*Voltage Detection Circuit number*> 125
5.2.4 R_PG_LVD_Disable_LVD<*Voltage Detection Circuit number*> 126
5.3 Frequency Measurement Circuit (MCK) 127
5.3.1 R_PG_MCK_Set 127
5.3.2 R_PG_MCK_Change_ReferenceClock 129
5.3.3 R_PG_MCK_StopModule 130
5.4 Low Power Consumption 131
5.4.1 R_PG_LPC_Set 131
5.4.2 R_PG_LPC_Sleep 132
5.4.3 R_PG_LPC_AllModuleClockStop 133
5.4.4 R_PG_LPC_SoftwareStandby 134
5.4.5 R_PG_LPC_DeepSoftwareStandby 135
5.4.6 R_PG_LPC_IOPortRelease 136
5.4.7 R_PG_LPC_ChangeOperatingPowerControl 137
5.4.8 R_PG_LPC_ChangeSleepModeReturnClock 138
5.4.9 R_PG_LPC_GetPowerOnResetFlag 139
5.4.10 R_PG_LPC_GetLVDDetectionFlag 140
5.4.11 R_PG_LPC_GetDeepSoftwareStandbyResetFlag 141
5.4.12 R_PG_LPC_GetOperatingPowerControlFlag 142
5.4.13 R_PG_LPC_GetStatus 143
5.4.14 R_PG_LPC_WriteBackup 145
5.4.15 R_PG_LPC_ReadBackup 146
5.5 Register Write Protection Function 147
5.5.1 R_PG_RWP_RegisterWriteCgc 147
5.5.2 R_PG_RWP_RegisterWriteModeLpcReset 149
5.5.3 R_PG_RWP_RegisterWriteLvd 150
5.5.4 R_PG_RWP_RegisterWriteMpc 151
5.5.5 R_PG_RWP_GetStatusCgc 152
5.5.6 R_PG_RWP_GetStatusModeLpcReset 153
5.5.7 R_PG_RWP_GetStatusLvd 154

5.5.8 R_PG_RWP_GetStatusMpc 155
5.6 Interrupt Controller (ICUb) 156
5.6.1 R_PG_ExtInterrupt_Set_<*interrupt type*> 156
5.6.2 R_PG_ExtInterrupt_Disable_<*interrupt type*> 158
5.6.3 R_PG_ExtInterrupt_GetRequestFlag_<*interrupt type*> 159
5.6.4 R_PG_ExtInterrupt_ClearRequestFlag_<*interrupt type*> 160
5.6.5 R_PG_ExtInterrupt_EnableFilter_<*interrupt type*> 161
5.6.6 R_PG_ExtInterrupt_DisableFilter_<*interrupt type*> 162
5.6.7 R_PG_SoftwareInterrupt_Set 163
5.6.8 R_PG_SoftwareInterrupt_Generate 164
5.6.9 R_PG_FastInterrupt_Set 165
5.6.10 R_PG_Exception_Set 166
5.7 Buses 167
5.7.1 R_PG_ExtBus_PresetBus 167
5.7.2 R_PG_ExtBus_SetBus 168
5.7.3 R_PG_ExtBus_SetArea_CS<*CS area number*> 169
5.7.4 R_PG_ExtBus_SetEnable 170
5.7.5 R_PG_ExtBus_GetErrorStatus 171
5.7.6 R_PG_ExtBus_ClearErrorFlags 172
5.7.7 R_PG_ExtBus_DisableArea_CS<*CS area number*> 173
5.7.8 R_PG_ExtBus_SetDisable 174
5.8 DMA controller (DMACA) 175
5.8.1 R_PG_DMAC_Set_C<*channel number*> 175
5.8.2 R_PG_DMAC_Activate_C<*channel number*> 178
5.8.3 R_PG_DMAC_StartTransfer_C<*channel number*> 179
5.8.4 R_PG_DMAC_StartContinuousTransfer_C<*channel number*> 180
5.8.5 R_PG_DMAC_StopContinuousTransfer_C<*channel number*> 181
5.8.6 R_PG_DMAC_Suspend_C<*channel number*> 182
5.8.7 R_PG_DMAC_GetTransferCount_C<*channel number*> 183
5.8.8 R_PG_DMAC_SetTransferCount_C<*channel number*> 184
5.8.9 R_PG_DMAC_GetRepeatBlockSizeCount_C<*channel number*> 185
5.8.10 R_PG_DMAC_SetRepeatBlockSizeCount_C<*channel number*> 186
5.8.11 R_PG_DMAC_ClearInterruptFlag_C<*channel number*> 187
5.8.12 R_PG_DMAC_GetTransferEndFlag_C<*channel number*> 188
5.8.13 R_PG_DMAC_ClearTransferEndFlag_C<*channel number*> 189
5.8.14 R_PG_DMAC_GetTransferEscapeEndFlag_C<*channel number*> 190
5.8.15 R_PG_DMAC_ClearTransferEscapeEndFlag_C<*channel number*> 191
5.8.16 R_PG_DMAC_SetSrcAddress_C<*channel number*> 192
5.8.17 R_PG_DMAC_SetDestAddress_C<*channel number*> 193
5.8.18 R_PG_DMAC_SetAddressOffset_C<*channel number*> 194
5.8.19 R_PG_DMAC_SetExtendedRepeatSrc_C<*channel number*> 195
5.8.20 R_PG_DMAC_SetExtendedRepeatDest_C<*channel number*> 196
5.8.21 R_PG_DMAC_StopModule_C<*channel number*> 197
5.9 Data Transfer Controller (DTCa) 198
5.9.1 R_PG_DTC_Set 198

5.9.2 R_PG_DTC_Set_<*trigger source*> 199
5.9.3 R_PG_DTC_Activate 201
5.9.4 R_PG_DTC_SuspendTransfer 202
5.9.5 R_PG_DTC_GetTransmitStatus 203
5.9.6 R_PG_DTC_StopModule 204
5.10 I/O Ports 205
5.10.1 R_PG_IO_PORT_Set_P<*port number*> 205
5.10.2 R_PG_IO_PORT_Set_P<*port number*><*pin number*> 206
5.10.3 R_PG_IO_PORT_Read_P<*port number*> 207
5.10.4 R_PG_IO_PORT_Read_P<*port number*><*pin number*> 208
5.10.5 R_PG_IO_PORT_Write_P<*port number*> 209
5.10.6 R_PG_IO_PORT_Write_P<*port number*><*pin number*> 210
5.10.7 R_PG_IO_PORT_SetPortNotAvailable 211
5.11 Multi-Function Timer Pulse Unit 2 (MTU2a) 212
5.11.1 R_PG_Timer_Set_MTU_U<*unit number*>_<*channels*> 212
5.11.2 R_PG_Timer_StartCount_MTU_U<*unit number*>_C<*channel number*>(_<*phase*>) 214
5.11.3 R_PG_Timer_SynchronouslyStartCount_MTU_U<*unit number*> 215
5.11.4 R_PG_Timer_HaltCount_MTU_U<*unit number*>_C<*channel number*>(_<*phase*>) 216
5.11.5 R_PG_Timer_GetCounterValue_MTU_U<*unit number*>_C<*channel number*> 217
5.11.6 R_PG_Timer_SetCounterValue_MTU_U<*unit number*>_C<*channel number*>(_<*phase*>) 218
5.11.7 R_PG_Timer_GetRequestFlag_MTU_U<*unit number*>_C<*channel number*> 219
5.11.8 R_PG_Timer_StopModule_MTU_U<*unit number*> 221
5.11.9 R_PG_Timer_GetTGR_MTU_U<*unit number*>_C<*channel number*> 222
5.11.10 R_PG_Timer_SetTGR_<*general register*>_MTU_U<*unit number*>_C<*channel number*> 224
5.11.11 R_PG_Timer_SetBuffer_AD_MTU_U<*unit number*>_C<*channel number*> 225
5.11.12 R_PG_Timer_SetBuffer_CycleData_MTU_U<*unit number*>_<*channels*> 226
5.11.13 R_PG_Timer_SetOutputPhaseSwitch_MTU_U<*unit number*>_<*channels*> 227
5.11.14 R_PG_Timer_ControlOutputPin_MTU_U<*unit number*>_<*channels*> 228
5.11.15 R_PG_Timer_SetBuffer_PWMOutputLevel_MTU_U<*unit number*>_<*channels*> 229
5.11.16 R_PG_Timer_ControlBufferTransfer_MTU_U<*unit number*>_<*channels*> 230
5.12 Port Output Enable 2 (POE2a) 231
5.12.1 R_PG_POE_Set 231
5.12.2 R_PG_POE_SetHiZ_<*Timer channels*> 232
5.12.3 R_PG_POE_GetRequestFlagHiZ_<*Timer channels/flag*> 233
5.12.4 R_PG_POE_GetShortFlag_<*Timer channels*> 234
5.12.5 R_PG_POE_ClearFlag_<*Timer channels/flag*> 235
5.13 16-Bit Timer Pulse Unit (TPUa) 236
5.13.1 R_PG_Timer_Set_TPU_U<*unit number*> 236
5.13.2 R_PG_Timer_Start_TPU_U<*unit number*>_C<*channel number*> 237
5.13.3 R_PG_Timer_SynchronouslyStartCount_TPU_U<unit number> 238
5.13.4 R_PG_Timer_HaltCount_TPU<*unit number*>_C<*channel number*> 240
5.13.5 R_PG_Timer_ResumeCount_TPU_U<*unit number*>_C<*channel number*> 241
5.13.6 R_PG_Timer_GetCounterValue_TPU_U<*unit number*>_C<*channel number*> 242
5.13.7 R_PG_Timer_SetCounterValue_TPU_U<*unit number*>_C<*channel number*> 243
5.13.8 R_PG_Timer_GetTGR_TPU_U<*unit number*>_C<*channel number*> 244

5.13.9 R_PG_Timer_SetTGR_<*general register*>_TPU_U<*unit number*>_C<*channel number*> 245
5.13.10 R_PG_Timer_GetRequestFlag_TPU_U<*unit number*>_C<*channel number*> 246
5.13.11 R_PG_Timer_StopModule_TPU_U<*unit number*> 248
5.14 Programmable Pulse Generator (PPG) 249
5.14.1 R_PG_PPG_StartOutput_U<*unit number*>_G<*group number*> 249
5.14.2 R_PG_PPG_StopOutput_U<*unit number*>_G<*group number*> 250
5.14.3 R_PG_PPG_SetOutputValue_U<*unit number*>_G<*group number*> 251
5.14.4 R_PG_PPG_SetOutputValue_U<*unit number*>_G<*group number1*>_G<*group number2*> 252
5.15 8-Bit Timer (TMR) 253
5.15.1 R_PG_Timer_Start_TMR_U<*unit number*>(_C<*channel number*>) 253
5.15.2 R_PG_Timer_HaltCount_TMR_U<*unit number*>(_C<*channel number*>) 255
5.15.3 R_PG_Timer_ResumeCount_TMR_U<*unit number*>(_C<*channel number*>) 256
5.15.4 R_PG_Timer_GetCounterValue_TMR_U<*unit number*>(_C<*channel number*>) 257
5.15.5 R_PG_Timer_SetCounterValue_TMR_U<*unit number*>(_C<*channel number*>) 258
5.15.6 R_PG_Timer_GetRequestFlag_TMR_U<*unit number*>(_C<*channel number*>) 259
5.15.7 R_PG_Timer_StopModule_TMR_U<*unit number*> 260
5.16 Compare Match Timer (CMT) 261
5.16.1 R_PG_Timer_Set_CMT_U<*unit number*>_C<*channel number*> 261
5.16.2 R_PG_Timer_StartCount_CMT_U<*unit number*>_C<*channel number*> 262
5.16.3 R_PG_Timer_HaltCount_CMT_U<*unit number*>_C<*channel number*> 263
5.16.4 R_PG_Timer_GetCounterValue_CMT_U<*unit number*>_C<*channel number*> 264
5.16.5 R_PG_Timer_SetCounterValue_CMT_U<*unit number*>_C<*channel number*> 265
5.16.6 R_PG_Timer_SetConstantRegister_CMT_U<*unit number*>_C<*channel number*> 266
5.16.7 R_PG_Timer_StopModule_CMT_U<*unit number*> 267
5.17 Realtime Clock (RTCa) 268
5.17.1 R_PG_RTC_Start 268
5.17.2 R_PG_RTC_WarmStart 269
5.17.3 R_PG_RTC_Stop 270
5.17.4 R_PG_RTC_Restart 271
5.17.5 R_PG_RTC_SetCurrentTime 272
5.17.6 R_PG_RTC_GetStatus 274
5.17.7 R_PG_RTC_Adjust30sec 276
5.17.8 R_PG_RTC_ManualErrorAdjust 277
5.17.9 R_PG_RTC_Set24HourMode 278
5.17.10 R_PG_RTC_Set12HourMode 279
5.17.11 R_PG_RTC_AutoErrorAdjust_Enable 280
5.17.12 R_PG_RTC_AutoErrorAdjust_Disable 281
5.17.13 R_PG_RTC_AlarmControl 282
5.17.14 R_PG_RTC_SetAlarmTime 283
5.17.15 R_PG_RTC_SetPeriodicInterrupt 284
5.17.16 R_PG_RTC_ClockOut_Enable 285
5.17.17 R_PG_RTC_ClockOut_Disable 286
5.17.18 R_PG_RTC_TimeCapture<*number of the input pin for a time capture event*>_Enable 287
5.17.19 R_PG_RTC_TimeCapture<*number of the input pin for a time capture event*>_Disable 288
5.17.20 R_PG_RTC_GetCaptureTime<*number of the input pin for a time capture event*> 289

5.18 Watchdog Timer (WDTA) ... 290
5.18.1 R_PG_Timer_Start_WDT ... 290
5.18.2 R_PG_Timer_RefreshCounter_WDT ... 291
5.18.3 R_PG_Timer_GetStatus_WDT ... 292
5.19 Independent Watchdog Timer (IWDTa) ... 293
5.19.1 R_PG_Timer_Start_IWDT ... 293
5.19.2 R_PG_Timer_RefreshCounter_IWDT ... 294
5.19.3 R_PG_Timer_GetStatus_IWDT ... 295
5.20 Serial Communications Interface (SCIc, SCId) ... 296
5.20.1 R_PG_SCI_Set_C<*channel number*> ... 296
5.20.2 R_PG_SCI_SendTargetStationID_C<*channel number*> ... 297
5.20.3 R_PG_SCI_StartSending_C<*channel number*> ... 298
5.20.4 R_PG_SCI_SendAllData_C<*channel number*> ... 299
5.20.5 R_PG_SCI_I2CMode_Send_C<*channel number*> ... 300
5.20.6 R_PG_SCI_I2CMode_SendWithoutStop_C<*channel number*> ... 301
5.20.7 R_PG_SCI_I2CMode_GenerateStopCondition_C<*channel number*> ... 302
5.20.8 R_PG_SCI_I2CMode_Receive_C<*channel number*> ... 303
5.20.9 R_PG_SCI_I2CMode_RestartReceive_C<*channel number*> ... 304
5.20.10 R_PG_SCI_I2CMode_ReceiveLast_C<*channel number*> ... 306
5.20.11 R_PG_SCI_I2CMode_GetEvent_C<*channel number*> ... 308
5.20.12 R_PG_SCI_SPIMode_Transfer_C<*channel number*> ... 309
5.20.13 R_PG_SCI_SPIMode_GetErrorFlag_C<*channel number*> ... 310
5.20.14 R_PG_SCI_GetSentDataCount_C<*channel number*> ... 311
5.20.15 R_PG_SCI_ReceiveStationID_C<*channel number*> ... 312
5.20.16 R_PG_SCI_StartReceiving_C<*channel number*> ... 313
5.20.17 R_PG_SCI_ReceiveAllData_C<*channel number*> ... 314
5.20.18 R_PG_SCI_ControlClockOutput_C<*channel number*> ... 315
5.20.19 R_PG_SCI_StopCommunication_C<*channel number*> ... 316
5.20.20 R_PG_SCI_GetReceivedDataCount_C<*channel number*> ... 317
5.20.21 R_PG_SCI_GetReceptionErrorFlag_C<*channel number*> ... 318
5.20.22 R_PG_SCI_ClearReceptionErrorFlag_C<*channel number*> ... 319
5.20.23 R_PG_SCI_GetTransmitStatus_C<*channel number*> ... 320
5.20.24 R_PG_SCI_StopModule_C<*channel number*> ... 321
5.21 I^2C Bus Interface (RIIC) ... 322
5.21.1 R_PG_I2C_Set_C<*channel number*> ... 322
5.21.2 R_PG_I2C_MasterReceive_C<*channel number*> ... 323
5.21.3 R_PG_I2C_MasterReceiveLast_C<*channel number*> ... 325
5.21.4 R_PG_I2C_MasterSend_C<*channel number*> ... 327
5.21.5 R_PG_I2C_MasterSendWithoutStop_C<*channel number*> ... 329
5.21.6 R_PG_I2C_GenerateStopCondition_C<*channel number*> ... 331
5.21.7 R_PG_I2C_GetBusState_C<*channel number*> ... 332
5.21.8 R_PG_I2C_SlaveMonitor_C<*channel number*> ... 333
5.21.9 R_PG_I2C_SlaveSend_C<*channel number*> ... 335
5.21.10 R_PG_I2C_GetDetectedAddress_C<*channel number*> ... 336
5.21.11 R_PG_I2C_GetTR_C<*channel number*> ... 337

5.21.12 R_PG_I2C_GetEvent_C<*channel number*> 338
5.21.13 R_PG_I2C_GetReceivedDataCount_C<*channel number*> 339
5.21.14 R_PG_I2C_GetSentDataCount_C<*channel number*> 340
5.21.15 R_PG_I2C_Reset_C<*channel number*> 341
5.21.16 R_PG_I2C_StopModule_C<*channel number*> 342
5.22 Serial Peripheral Interface (RSPI) 343
5.22.1 R_PG_RSPI_Set_C<*channel number*> 343
5.22.2 R_PG_RSPI_SetCommand_C<*channel number*> 344
5.22.3 R_PG_RSPI_StartTransfer_C<*channel number*> 345
5.22.4 R_PG_RSPI_TransferAllData_C<*channel number*> 347
5.22.5 R_PG_RSPI_GetStatus_C<*channel number*> 349
5.22.6 R_PG_RSPI_GetError_C<*channel number*> 350
5.22.7 R_PG_RSPI_GetCommandStatus_C<*channel number*> 351
5.22.8 R_PG_RSPI_LoopBack<*loopback mode*>_C<*channel number*> 352
5.22.9 R_PG_RSPI_StopModule_C<*channel number*> 353
5.23 IEBus Controller (IEB) 354
5.23.1 R_PG_IEB_Set_C<*channel number*> 354
5.23.2 R_PG_IEB_MasterReceiveStatus_C<*channel number*> 355
5.23.3 R_PG_IEB_MasterReceiveLockAddress_C<*channel number*> 357
5.23.4 R_PG_IEB_MasterReceiveData_C<*channel number*> 359
5.23.5 R_PG_IEB_MasterSendCmd_C<*channel number*> 361
5.23.6 R_PG_IEB_MasterSendData_C<*channel number*> 362
5.23.7 R_PG_IEB_MasterSendCmdBroadcast_C<*channel number*> 363
5.23.8 R_PG_IEB_MasterSendDataBroadcast_C<*channel number*> 365
5.23.9 R_PG_IEB_SlaveMonitor_C<*channel number*> 367
5.23.10 R_PG_IEB_SlaveWrite_C<*channel number*> 368
5.23.11 R_PG_IEB_GetReceivedMasterAddress_C<*channel number*> 369
5.23.12 R_PG_IEB_GetReceivedCmd_C<*channel number*> 370
5.23.13 R_PG_IEB_GetReceivedDataCount_C<*channel number*> 371
5.23.14 R_PG_IEB_GetLockMasterAddress_C<*channel number*> 372
5.23.15 R_PG_IEB_GetGeneralFlag_C<*channel number*> 373
5.23.16 R_PG_IEB_GetTransmitStatus_C<*channel number*> 374
5.23.17 R_PG_IEB_GetReceiveStatus_C<*channel number*> 375
5.23.18 R_PG_IEB_Reset_C<*channel number*> 376
5.23.19 R_PG_IEB_SetSlaveStatus_C<*channel number*> 377
5.23.20 R_PG_IEB_CancelLock_C<*channel number*> 378
5.23.21 R_PG_IEB_StopCommunication_C<*channel number*> 379
5.23.22 R_PG_IEB_StopModule_C<*channel number*> 380
5.24 CRC Calculator (CRC) 381
5.24.1 R_PG_CRC_Set 381
5.24.2 R_PG_CRC_InputData 382
5.24.3 R_PG_CRC_GetResult 383
5.24.4 R_PG_CRC_StopModule 384
5.25 12-Bit A/D Converter (S12ADa) 385
5.25.1 R_PG_ADC_12_Set_S12AD0 385

5.25.2 R_PG_ADC_12_StartConversionSW_S12AD0 386
5.25.3 R_PG_ADC_12_StopConversion_S12AD0 387
5.25.4 R_PG_ADC_12_GetResult_S12AD0 388
5.25.5 R_PG_ADC_12_StopModule_S12AD0 389
5.26 10-Bit A/D Converter (ADb) 390
5.26.1 R_PG_ADC_10_Set_AD<*unit number*> 390
5.26.2 R_PG_ADC_10_SetSelfDiag_VREF_<*voltage*>_AD<*unit number*> 391
5.26.3 R_PG_ADC_10_StartConversionSW_AD<*unit number*> 392
5.26.4 R_PG_ADC_10_StartSelfDiag_AD<*unit number*> 393
5.26.5 R_PG_ADC_10_StopConversion_AD<*unit number*> 394
5.26.6 R_PG_ADC_10_GetResult_AD<*unit number*> 395
5.26.7 R_PG_ADC_10_StopModule_AD<*unit number*> 396
5.27 D/A Converter (DAa) 397
5.27.1 R_PG_DAC_Set_C<*channel number*> 397
5.27.2 R_PG_DAC_SetWithInitialValue_C<*channel number*> 398
5.27.3 R_PG_DAC_ControlOutput_C<*channel number*> 399
5.27.4 R_PG_DAC_StopOutput_C<*channel number*> 400
5.28 Temperature Sensor (TS) 401
5.28.1 R_PG_TS_Set 401
5.28.2 R_PG_TS_EnableOutput 402
5.28.3 R_PG_TS_DisableOutput 403
5.28.4 R_PG_TS_StopModule 404
5.29 Notes on Notification Functions 405
5.29.1 Interrupts and processor mode 405
5.29.2 Interrupts and DSP instructions 405
6. Registering Files with the IDE and Building Them 406
Appendix 1. Pin Functions for which the Allocation Can be Changed 407

1. Overview

1.1 Supported peripheral modules

The Peripheral Driver Generator supports the following products of RX630 group, peripheral modules and endian.

(1) Products

Part No.	Package	Part No.	Package
R5F56307xDFN	PLQP0080KB-A	R5F5630BxDFC	PLQP0176KB-A
R5F56307xDFP	PLQP0100KB-A	R5F5630BxDBG	PLBG0176GA-A
R5F56307xDLA	PTLG0100KA-A	R5F5630BxDLC	PTLG0177KA-A
R5F56308xDFN	PLQP0080KB-A	R5F5630DxDFP	PLQP0100KB-A
R5F56308xDFP	PLQP0100KB-A	R5F5630DxDFB	PLQP0144KA-A
R5F56308xDLA	PTLG0100KA-A	R5F5630DxDLK	PTLG0145KA-A
R5F5630AxDFP	PLQP0100KB-A	R5F5630DxDFC	PLQP0176KB-A
R5F5630AxDFB	PLQP0144KA-A	R5F5630DxDBG	PLBG0176GA-A
R5F5630AxDLK	PTLG0145KA-A	R5F5630DxDLC	PTLG0177KA-A
R5F5630AxDFC	PLQP0176KB-A	R5F5630ExDFP	PLQP0100KB-A
R5F5630AxDBG	PLBG0176GA-A	R5F5630ExDFB	PLQP0144KA-A
R5F5630AxDLC	PTLG0177KA-A	R5F5630ExDLK	PTLG0145KA-A
R5F5630BxDFP	PLQP0100KB-A	R5F5630ExDFC	PLQP0176KB-A
R5F5630BxDFB	PLQP0144KA-A	R5F5630ExDBG	PLBG0176GA-A
R5F5630BxDLK	PTLG0145KA-A	R5F5630ExDLC	PTLG0177KA-A

(2) Peripheral Modules

Voltage Detection Circuit (LVDA)	8-Bit Timer (TMR)
Clock Generation Circuit	Compare Match Timer (CMT)
Frequency Measurement Circuit (MCK)	Realtime Clock (RTCa)
Low Power Consumption	Watchdog Timer (WDTA)
Register Write Protection Function	Independent Watchdog Timer (IWDTa)
Exceptions, Interrupt Controller (ICUb)	Serial Communications Interface (SCIc,SCId)
Buses	I^2C Bus Interface (RIIC)
DMA Controller (DMACA)	Serial Peripheral Interface (RSPI)
Data Transfer Controller (DTCa)	IEBus™ Controller (IEB)
I/O Ports	CRC Calculator (CRC)
Multifunction Pin Controller (MPC)	12-Bit A/D Converter (S12ADa)
Multi-Function Timer Pulse Unit 2 (MTU2a)	10-Bit A/D Converter (ADb)
Port Output Enable 2 (POE2a)	D/A Converter (DAa)
16-Bit Timer Pulse Unit (TPUa)	Temperature Sensor
Programmable Pulse Generator (PPG)	

(3) Endian

Little
Big

1.1.2 Tool requirements

The following tools are required for this version of RX630 group Peripheral Driver Generator.

- RX Family C/C++ Compiler Package V.1.02 Release 00
- RX630 Group Renesas Peripheral Driver Library V.1.20 (Bundled in Peripheral Driver Generator)

2. Creating a new project

To create the new project file, select the menu [File] -> [New Project]. New project dialog box will open.

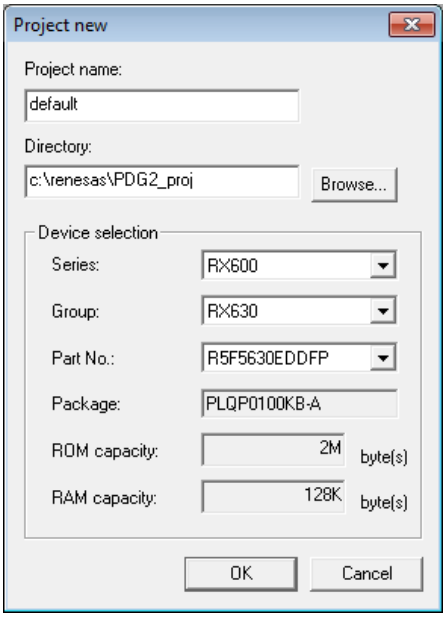


Fig 2.1 New project dialog box

For RX630 group, select [RX600] as a series and select [RX630] as a group. The package type, ROM capacity and RAM capacity of selected product are displayed.

By clicking [OK], new project is created and opened.

The EXTAL input clock frequency is not set after opening a new project. Therefore an error icon is displayed. For error display, refer to the user's manual.

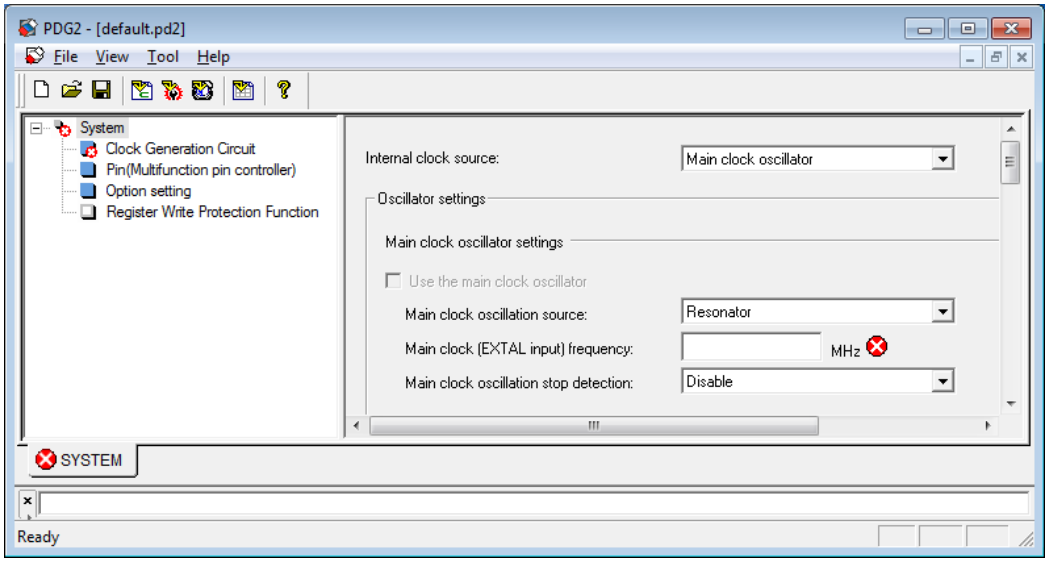


Fig 2.2 Error display of new project

Set the frequency of the clock to be used here.

> Each value (e.g. frequency) entered in the window will be rounded to its nearest valid value after division or multiplication. The final value is displayed as "Actual value" on the GUI.

3. Setting Up the Peripheral Modules

3.1 Main Window

Figure 3.1 shows the main window for setting up peripheral modules.

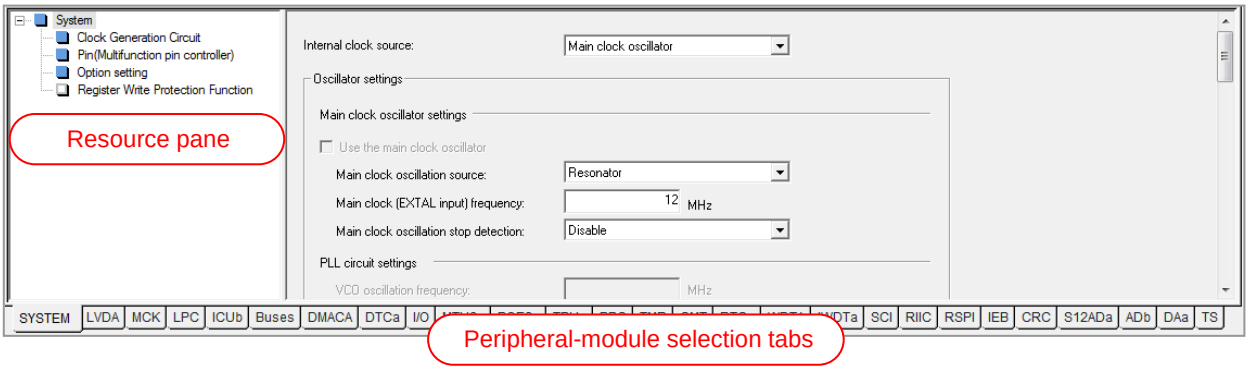


Figure 3.1 Display in the Main Window (Example)

Table 3.1 shows the correspondence between the peripheral-module selection tabs, items in the resource pane, and peripheral modules to be set up.

Table 3.1 Peripheral-Module Selection Tabs, Items in the Resource Pane, and Peripheral Modules

Tab	Resource pane	Corresponding Peripheral Module or Function
SYSTEM	Clock Generation Circuit	Clock Generation Circuit
	Pin(Multifunction pin controller)	Pinfunctions (Multifunction Pin Controller (MPC))
	Option setting	Endian setting
	Register Write Protection Function	Register Write Protection Function
LVDA	Voltage monitoring 0 to 2	Voltage monitoring 0 to 2
MCK	Frequency Measurement Circuit (MCK)	Frequency Measurement Circuit (MCK)
LPC	Low Power Consumption	Low Power Consumption
ICUb	Interrupts	Interrupt Control Unit (ICUb) (Fastinterrupt, Software Interrupt, External Interrupt (NMI, IRQ0 to IRQ15))
	Exceptions	Exceptions
Buses	CS0 to CS7	CS area (CS0 to CS7)
	Common settings	Bus Priority and Bus Error Monitoring
DMACA	DMAC0 to DMAC3	DMA Controller (DMACA) Channel 0 to 3
DTCa	Data transfer controller (DTCa)	Data Transfer Controller (DTCa)
I/O	Port 0 to H and J to L	I/O Port 0 to H and J to L
MTU2a	MTU0 to MTU5	Multi-Function Timer Pulse Unit 2 (MTU2a) Channlel 0 to 5
POE2a	Port Output Enable 2 (POE2a)	Port Output Enable 2 (POE2a)
TPUa	Unit0 (TPU0 to TPU5)	16-Bit Timer Pulse Unit (TPUa) Unit 0 (Channlel 0 to 5)
	Unit1 (TPU6 to TPU11)	16-Bit Timer Pulse Unit (TPUa) Unit 1 (Channlel 6 to 11)
PPG	Unit0 (Group 0 to 3)	Programmable Pulse Generator (PPG) Unit 0 (Group 0 to 3)
	Unit1 (Group 4 to 7)	Programmable Pulse Generator (PPG) Unit 1 (Group 4 to 7)
TMR	Unit0 (TMR0 and TMR1)	8-Bit Timer (TMR) Unit 0 (Channlel 0 and 1)

	Unit1 (TMR2 and TMR3)	8-Bit Timer (TMR) Unit 1 (Channlel 2 and 3)
CMT	Unit0 (CMT0 and CMT1)	Compare Match Timer (CMT) Unit 0 (Channlel 0 and 1)
	Unit1 (CMT2 and CMT3)	Compare Match Timer (CMT) Unit 1 (Channlel 2 and 3)
RTCa	Realtime Clock (RTCa)	Realtime Clock (RTCa)
WDTA	Watchdog Timer (WDTA)	Watchdog Timer (WDTA)
IWDTa	Independent Watchdog Timer (IWDTa)	Independent Watchdog Timer (IWDTa)
SCI	SCI0 to 12	Serial Communications Interface SCIc(SCI0 to 11) and SCId(SCI12)
RIIC	RIIC0 to 3	I^2C Bus Interface (RIIC) Channel 0 to 3
RSPI	RSPI0 to 2	Serial Peripheral Interface (RSPI) Channel 0 to 2
IEB	IEB0	IEBus™ Controller (IEB)
CRC	CRC Calculator (CRC)	CRC Calculator (CRC)
S12ADa	S12AD0	12-Bit A/D Converter (S12ADa)
ADb	AD0	10-Bit A/D Converter (ADb)
DAa	DA0 and DA1	D/A Converter (DAa) Channel 0 and 1
TS	Temperature Sensor	Temperature Sensor

For how to set up the peripheral modules, refer to the user's manual. For details on the setting of pin functions, refer to section 3.2, Pin Functions.

3.2 Pin Functions (Multifunction Pin Controller)

The multifunction pin controller (MPC) in RX630-group MCUs selects the functions to be assigned to individual pins. The Peripheral Driver Generator provides a pin-function pane through which settings for the MPC can be made.

Select the [SYSTEM] tab from the peripheral-module selection tabs and click on [Pin (Multi function pin controller)] in the resource pane to open the pin-function pane.

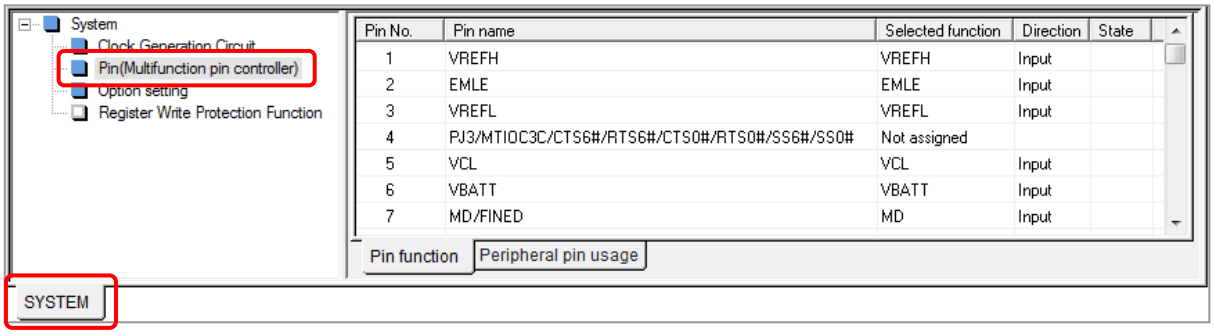


Figure 3.2 Opening the Pin-Function Pane

The pin-function pane has [Pin function] and [Peripheral pin usage] sheets. The two sheets are linked, so that settings can be made in either of them.

3.2.1 [Pin function] Sheet

(1) Configuration

The [Pin function] sheet shows all of the MCU pins in order and the functions that have been assigned to those pins. This sheet can be used to select functions for each of the pins with multiplexed functions.

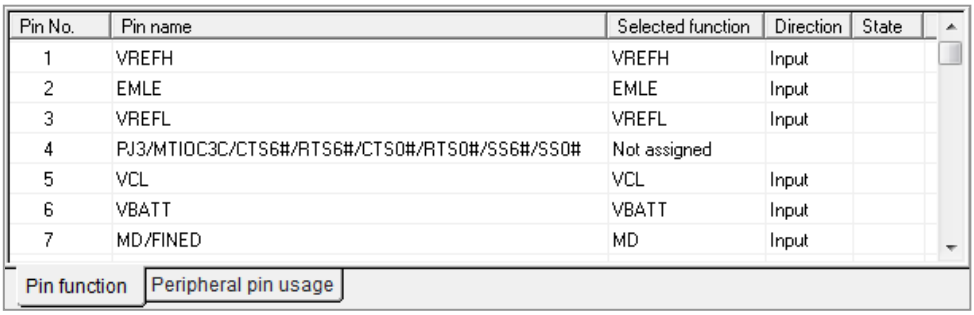


Figure 3.3 Pin-Function Pane ([Pin function] Sheet)

The contents of each column are shown in table 3.2.

Table 3.2 Columns on the [Pin function] Sheet

Column	Description
Pin No.	Pin number
Pin name	Name of the pin (which shows all of the functions assigned to that pin)
Selected function	Currently allocated pin function
Direction	Whether the pin function is an input or output
State	Warning or error message, if any

(2) Default State

By default (i.e. when no pins have been set up for use with peripheral modules), “Not assigned” is shown in the [Selected function] column for each port pin, indicating that no function has yet been selected (figure 3.4).

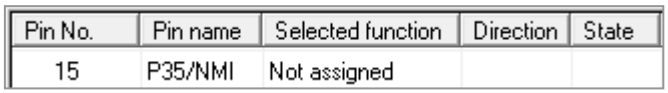
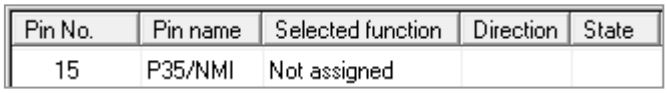


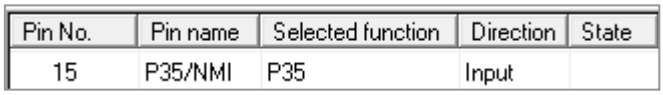
Figure 3.4 [Pin function] Sheet in the Default State (100-Pin Package)

Note:

Port pins of RX630-group MCUs are general-purpose input port pins by default. Even though “Not assigned” is shown in the [Selected function] column for each port pin by default (i.e. when no pins have been set up for use with peripheral modules), the pin will act as a general-purpose input port pin. When you designate a pin as a general-purpose input port pin in the [I/O] pane, the name of the general-purpose input port pin will appear in the [Selected function] column (figure 3.5(b)).



(a) Default State



(b) After Designating P35 as a General-Purpose Input Port Pin in the [I/O] Pane

Figure 3.5 Display for Pin No.15 (100-Pin Package)

(3) Selecting a Pin Function

When a pin has multiplexed functions, placing the mouse pointer on the [Selected function] column in the row for that pin brings up a drop-down button. Clicking on the button brings up a list of selectable pin functions (figure 3.6).

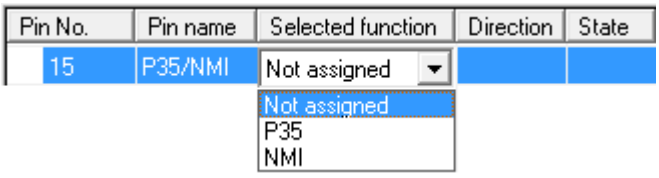


Figure 3.6 Selectable Pin Functions

In the default state (i.e. when no pins have been set up for use with peripheral modules), if [Selected function] is changed from “Not assigned” to another pin function, the warning [<Name of the pin function> has not been configured in the peripheral settings.] appears. For example, when [Selected function] for P35/NMI is changed from “Not assigned” to NMI despite the interrupt controller (ICUb) not being set up, a warning appears as shown in figure 3.7.

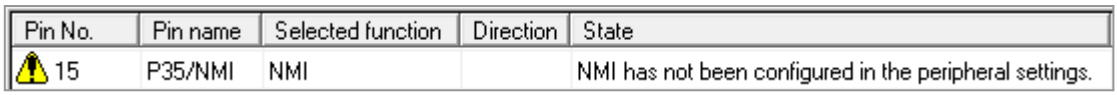


Figure 3.7 Warning on Changing [Selected function] in the Default State

When the NMI has been set up in the [ICUb] pane, the warning disappears and “NMI” appears in the [Selected function] column.

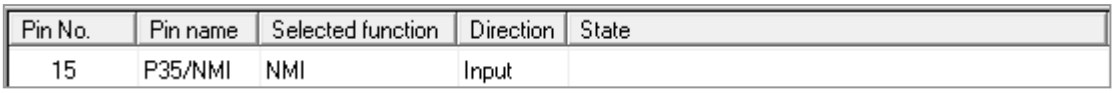


Figure 3.8 After Setting the NMI up

Note:
The generation of source files is still possible when the warning shown in figure 3.7 is being displayed, but the pin will not act as an NMI. For details, refer to section 3.2.4, Error Messages and Warnings on Pin Settings.

(4) Selecting a Pin Function before Setting up the Associated Peripheral Module
When a peripheral module is set up after selecting the pin functions on the [Pin function] sheet, the selected pin functions are automatically allocated to the pins.
IRQ5, for example, can be assigned to PA4, P15, PD5, or PE5 (PA4, P15, or PE5 for products in 80-pin packages). To assign IRQ5 to PE5, IRQ5 should be selected as the [Selected function] for PE5 on the [Pin function] sheet (figure 3.9).

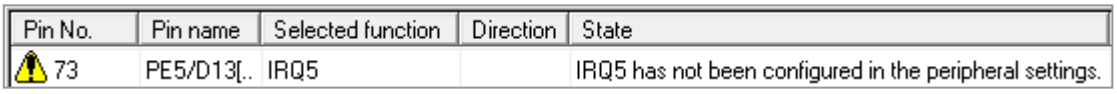


Figure 3.9 IRQ5 Selected for PE5 (with the ICUb Not Set up)

When IRQ5 is set up in the [ICUb] pane, IRQ5 is actually assigned to PE5 (figure 3.10).

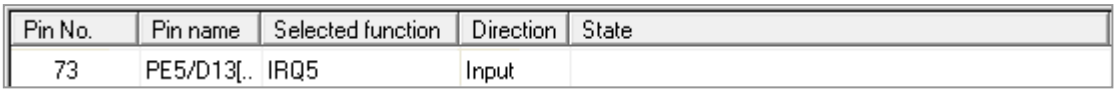


Figure 3.10 IRQ5 Selected for PE5 (after the ICUb Has been Set up)

3.2.2 [Peripheral pin usage] Sheet

The [Peripheral pin usage] sheet shows which pins are used by the corresponding peripheral module. The pin functions associated with the peripheral module selected in the left section and where those functions are assigned are listed in the right section. If multiple pins are selectable for a specific function, the allocation can be changed through this sheet.

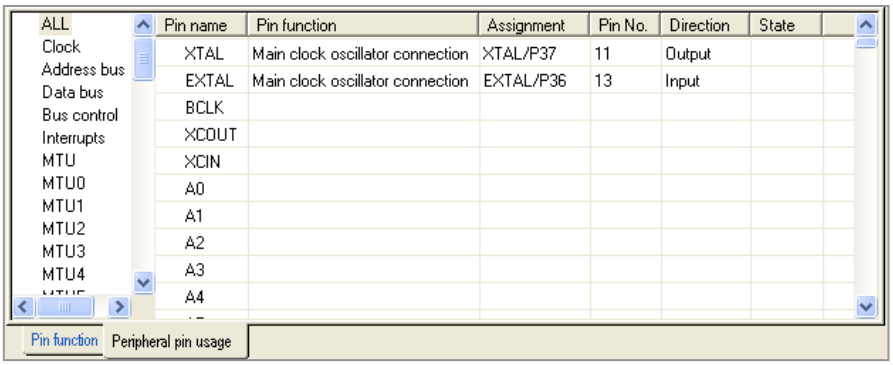


Figure 3.11 Pin-Function Pane ([Peripheral pin usage] Sheet)

Table 3.3 lists the columns on the [Peripheral pin usage] sheet.

Table 3.3 Columns on the [Peripheral pin usage] Sheet

Column	Contents
Pin Name	Names of pins used by the peripheral module selected in the left section
Pin Function	Pin function
Assignment	Full name of the MCU pin, showing all of the functions assigned to that pin
Pin No.	Pin number
Direction	Input or output
State	Warning or error message, if any

(1) Default State

By default (i.e. when no pins have been set up for use with peripheral modules), the [Pin Function] and [Assignment] columns are blank (figure 3.12).

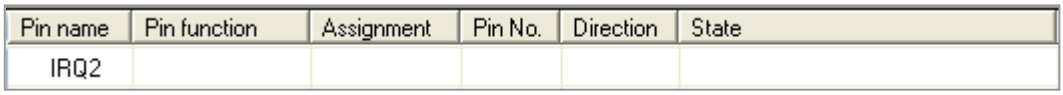


Figure 3.12 [Peripheral pin usage] Sheet in the Default State

(2) Assigning a Pin Function to a Port Pin

When a peripheral module associated with input to or output from pins has been set up, the pin functions to be used by that peripheral module are assigned to the corresponding port pins and the current settings are shown on the [Peripheral pin usage] sheet. If you have set up external interrupt IRQ2 in the detailed settings pane, for example, pin IRQ2 is assigned to P32 and the [Peripheral pin usage] sheet shows the setting of IRQ2 as follows.

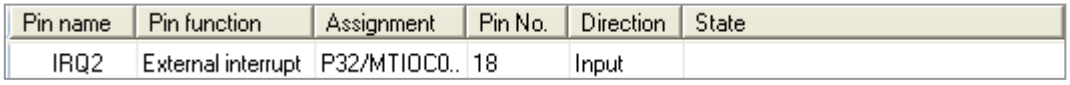
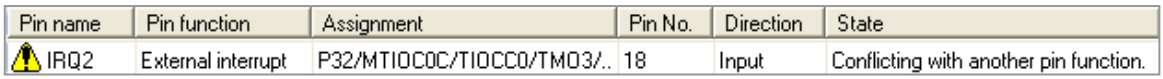


Figure 3.13 Display of a Pin Function Assigned to a Port Pin (Example)

Note:
When a peripheral module is set up in the default state (i.e. when no pin functions have been selected on the [Pin function] or [Peripheral pin usage] sheet), the pin functions for that peripheral module are assigned to the port pins listed in the “Allocation in the Default State” section of appendix 1, Pin Functions for which the Allocation Can be Changed. When the allocation of pin functions has been designated on the [Pin function] sheet before a peripheral module is set up, the pin functions are assigned to the selected port pins.

Subsequently setting up general-purpose I/O port pin P32, which uses the same pin as IRQ2, in the [I/O] pane will cause a conflict and a warning will be output as shown in figure 3.14.



Pin name	Pin function	Assignment	Pin No.	Direction	State
IRQ2	External interrupt	P32/MTIOC0C/TIOCC0/TMO3/..	18	Input	Conflicting with another pin function.

Figure 3.14 Warning of a Conflict between Pin Functions

Note:
Even if two or more pin functions are assigned to a single pin (as in figure 3.14), generating source files is still possible. You can switch between the functions, although more than one cannot be in use at the same time. For details, refer to section 3.2.4, Error Messages and Warnings on Pin Settings.

The allocation of IRQ2 can be changed. Other pins to which IRQ2 can be assigned are selectable from a drop-down list box. Placing the mouse pointer on the [Assignment] column brings up a drop-down button.

Pin name	Pin function	Assignment	Pin No.	Direction	State
IRQ2	External interrupt	P32/MTIOC0C/TIOCC0/TMO	18	Input	Conflicting with another pin function.

Figure 3.15 Drop-Down Button

Click on the drop-down button and select one of the options displayed in the list box.

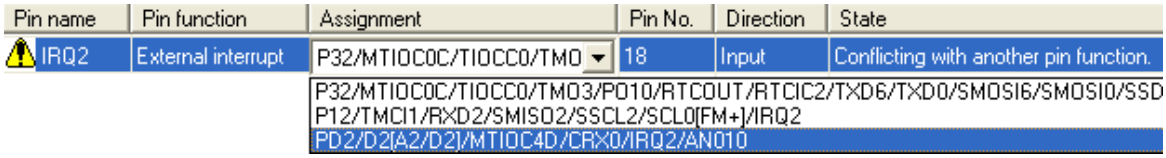
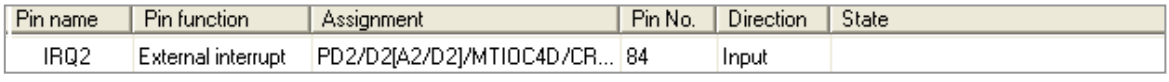


Figure 3.16 Changing the Allocation of a Pin Function

If IRQ2 is assigned to PD2 and that pin is not being used for any other peripheral module, the conflict between P32 and IRQ2 can be resolved.



Pin name	Pin function	Assignment	Pin No.	Direction	State
IRQ2	External interrupt	PD2/D2[A2/D2]/MTIOC4D/CR...	84	Input	

Figure 3.17 Display after Changing the Allocation

The pin functions for which you can select the assignment are listed in appendix 1, Pin Functions for which the Allocation Can be Changed.

Note:
When the peripheral module has not been set up (as in figure 3.12), the allocation of pin functions cannot be changed through this sheet.

3.2.3 [Pin layout] Sheet

(1) Configuration

The [Pin layout] sheet shows graphical pin layout view. This sheet can be used to select functions for each of the pins with multiplexed functions.

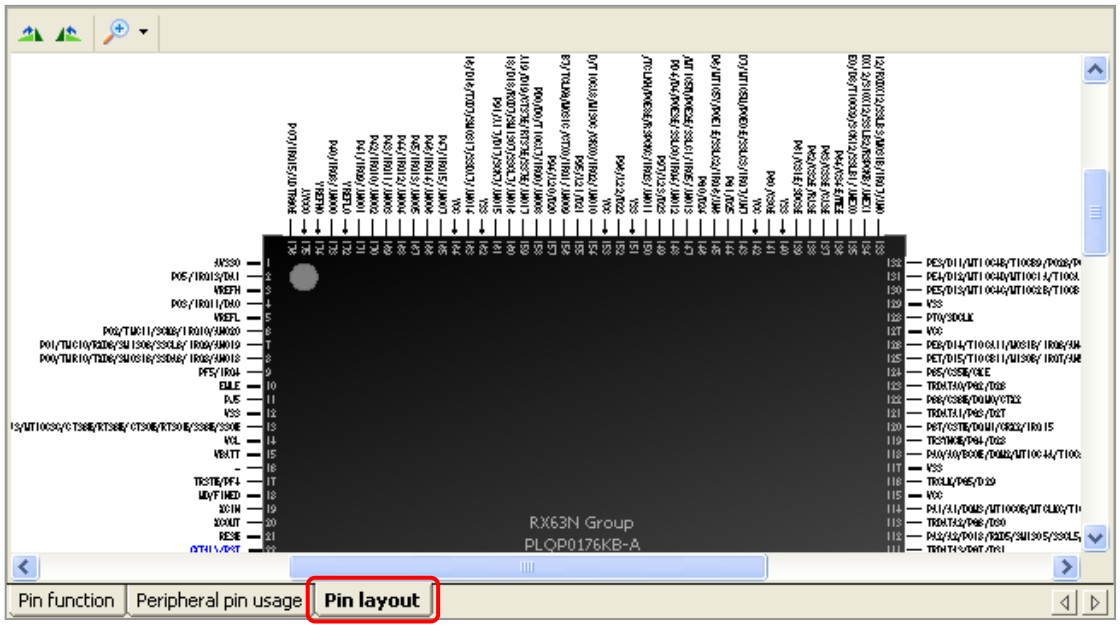


Figure 3.18 Pin-Function Pane ([Pin layout] Sheet)

Note:

When the product of TFLGA package or LFBGA package is selected, [Pin layout] sheet does not shows actual pin layout and LQFP package is displayed instead. In this case, a warning message is displayed as shown in figure 3.19.

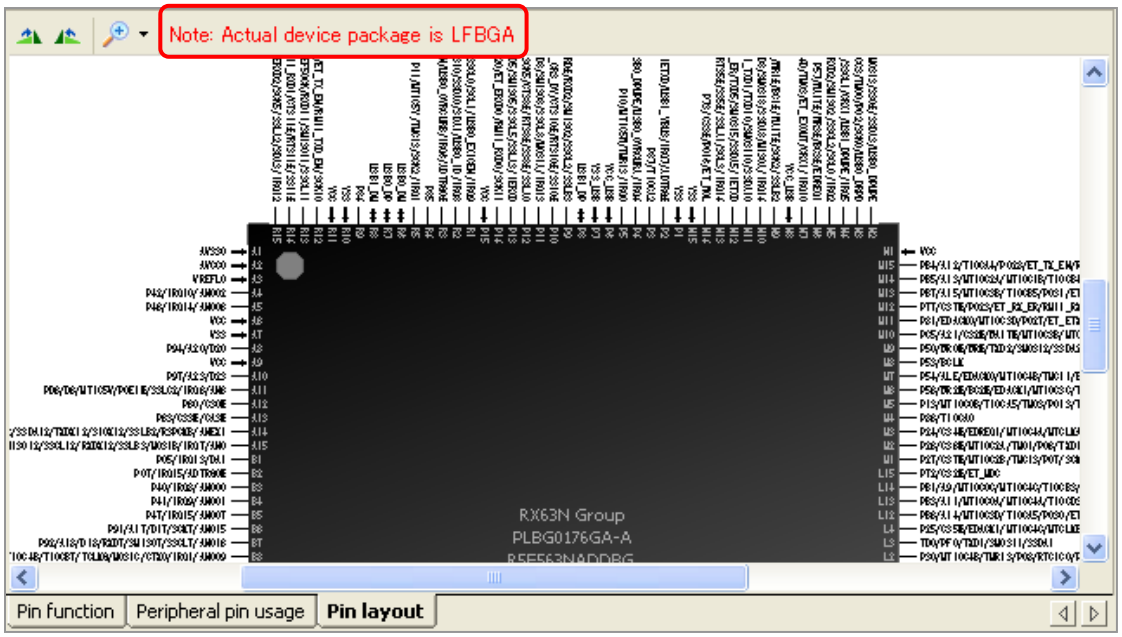


Figure 3.19 Warning message when selecting TFLGA or LFBGA

(2) Functions

The [Pin layout] sheet has the following functions.

- Rotate

  The [Rotate] buttons ( ) rotate the view by 90 degrees clockwise or counter clockwise.

- Zoom In/Zoom out

  The [Zoom in] button ( ) zooms the view by an additional 25%. It also has a drop-down list of zoom level.

(3) Selecting a Pin Function

Placing the mouse pointer on the pin which has multiplexed functions and clicking right button brings up a list of selectable pin functions. (Figure 3.20)

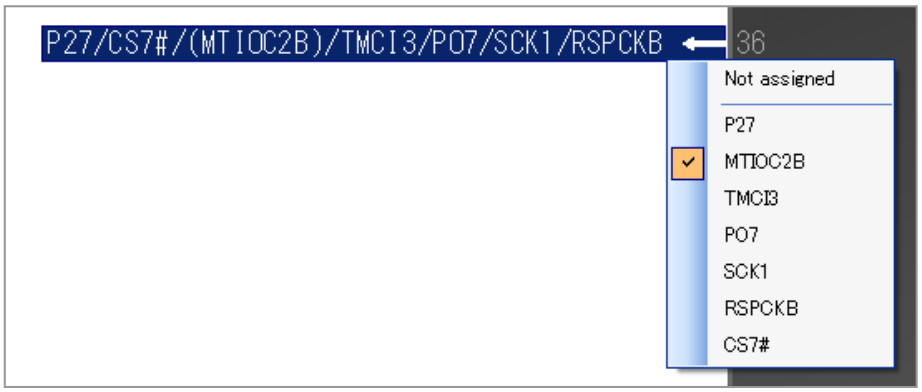


Figure 3.20 Pin function selection

The selection of pin function can be changed from this list. Setting changes on [Pin layout] sheet are reflected on the other sheets. For details, refer to 3.2.4 Peripheral-Module Setting Shared byxxx.

(4) Pin Status Display

The status of each pin are displayed as follows.

- Selected function

  If the pin function is assigned to the pin, the selected pin function is indicated by brackets as shown in Figure.3.21.

Figure 3.21 Indication of selected function (In the case when MTIOC2B is selected)

- Input/Output Direction

The signal direction of selected pin function is displayed as shown in Figure 3.22.

a. Pin function is not assigned b. Output c. Input d. Input/Output

Figure 3.22 Display of input/output direction

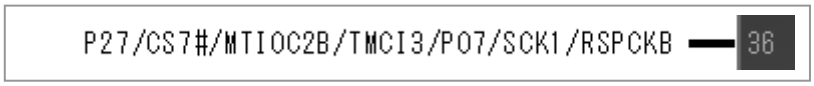
Note:

If two or more pin functions are assigned to one pin, signal direction is not shown.

- Error or Warning Status

The setting status of each pin is displayed as shown in Figure 3.23.

a. Pin function is not assigned (indicated by red characters)



b. Pin function is assigned and no error or warning is detected (indicated by blue characters)

c. Pin function is assigned and a warning is detected (indicated by brown characters)

d. Pin function is assigned and an error is detected (indicated by red characters)

図 3.23 Display of error or warning status

For the contents of error or warning, refer to the corresponding pin in [Pin function] sheet. For the details of error or warning in pin function window, refer to the section 3.2.5 Error Messages and Warnings on Pin Settings.

3.2.4 Pin Settings Shared between Setting Windows

A change to a setting on either the [Pin function] or [Peripheral pin usage] sheet is reflected on the other sheet. When the allocation of a pin function is changed on the [Pin function] sheet, that change also applies to the [Peripheral pin usage] sheet, and vice versa (figure 3.24).

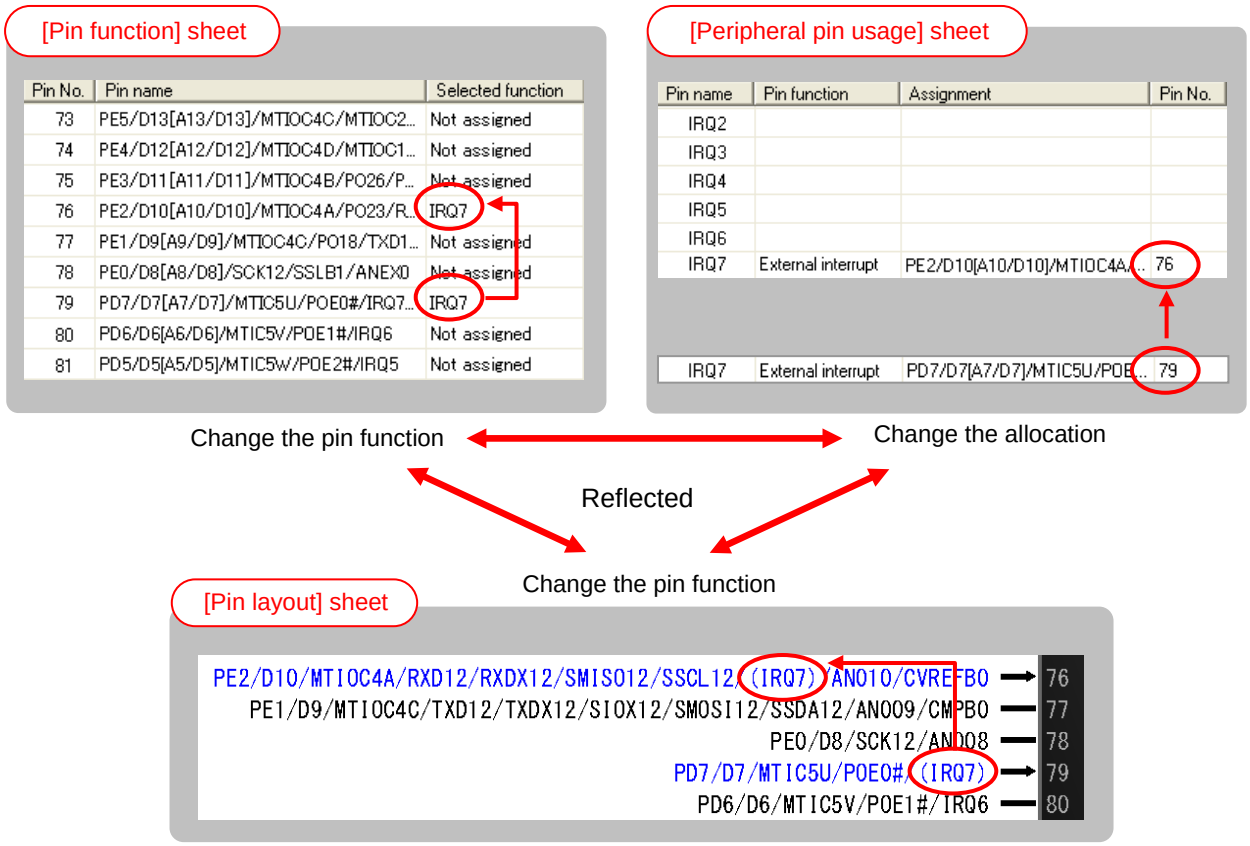


Figure 3.24 Linking of the [Pin function],[Pin layout] and [Peripheral pin usage] Sheets

The current settings for each peripheral module are reflected on the [Pin function] and [Peripheral pin usage] sheets. When IRQn is set up in the [ICUb] pane, for example, the [Peripheral pin usage] sheet shows that IRQn is in use and the allocation of IRQn is displayed on the [Pin function] and [Peripheral pin usage] sheets.

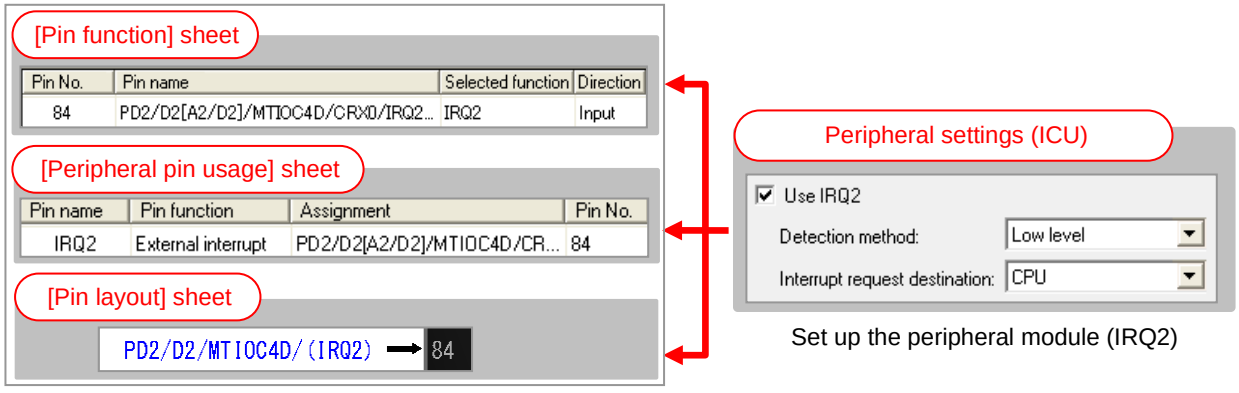


Figure 3.25 Setting up a Peripheral Module and Allocating Pin Functionss

When the setting for IRQn in the [ICUb] pane is canceled, the allocation of IRQn is canceled on the [Pin function] and [Peripheral pin usage] sheets.

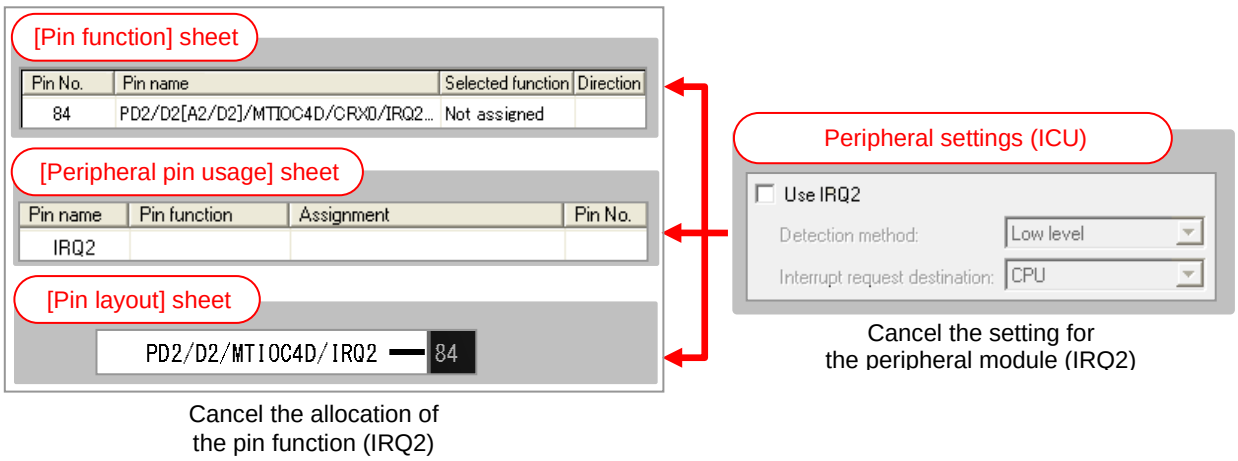


Figure 3.26 Deleting setting of Peripheral Module and Deallocating Pin Functions

On the other hand, a change made on the [Pin function] or [Peripheral pin usage] sheet is not reflected on the detailed-settings pane for the peripheral module. Even if [Selected function] for IRQn is changed to "Not assigned" on the [Pin function] sheet (or [Pin layout] sheet) after IRQn has been set up in the [ICUb] pane, for example, the setting of IRQn in the [ICUb] pane is not canceled. Since no pin is assigned to IRQn in this case, an error message appears. For details on the error messages, refer to section 3.2.5, Error Messages and Warnings on Pin Settings.

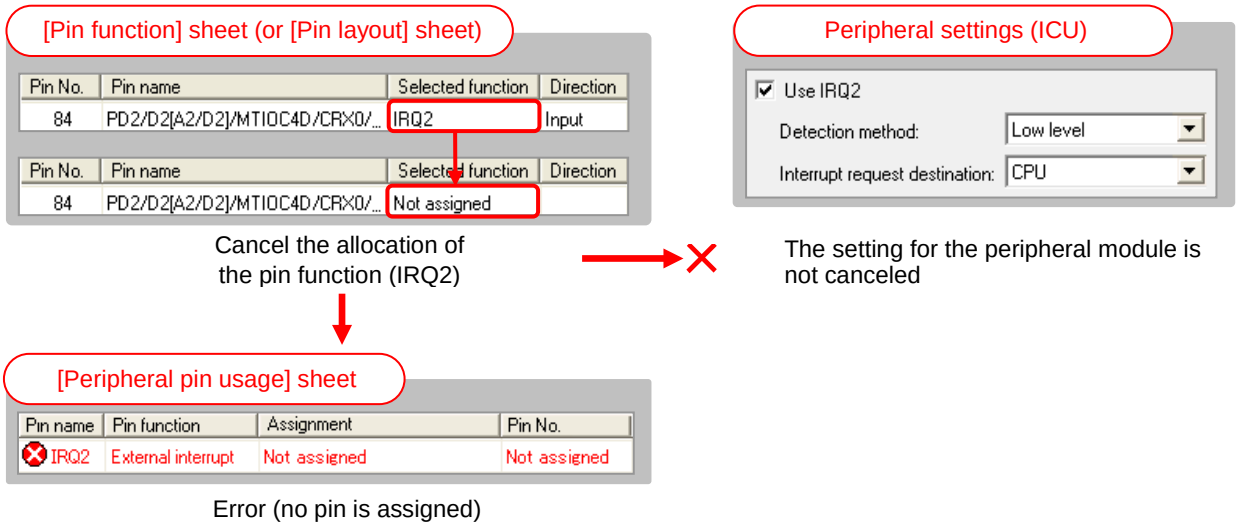


Figure 3.27 Canceling the Allocation of a Pin Function Leading to Display of an Error Message

3.2.5 Error Messages and Warnings on Pin Settings

When an incorrect setting is made, an error message or warning is displayed on the [Pin function] or [Peripheral pin usage] sheet. The errors and warnings are listed in table 3.4.

Table 3.4 Errors and Warnings

Cause	Type	Message
A single pin function has been selected for multiple pins.	Error	"The same function is assigned to <*pin numbers*>." ([Pin function] sheet) "Do not assign a single function to multiple pins." ([Peripheral pin usage] sheet)
The pin function has not been allocated.	Error	"Not assigned" ([Peripheral pin usage] sheet)
Multiple pin functions have been selected for a single pin.	Warning	"Conflicting between different functions." ([Pin function] sheet) "Conflicting with another pin function." ([Peripheral pin usage] sheet)
Conflict with use of a pin by a debugger	Warning	"Conflicting with an on-chip emulator pin." ([Pin function] sheet) "Conflicting between a peripheral module pin and an on-chip emulator pin." ([Peripheral pin usage] sheet)
The peripheral module has not been set up.	Warning	"<*pin function*> has not been configured in the peripheral settings." ([Pin function] sheet)

Details of the errors and warnings are given below.

(1) A single pin function has been selected for multiple pins.

Selecting a single pin function for multiple pins leads to an error that prevents the generation of source files. In this case, allocate another pin function to either of the pins, change the entry on the [Pin function] sheet to "Not assigned", or re-select the allocation of the pin function on the [Peripheral pin usage] sheet.

Pin No.	Pin name	Selected function	Direction	State
18	P32/MTIOC0C/TIOCC0/TMO3/PO10/RTCOUT/RTC..	IRQ2	Input	The same function is assigned to 18/84.
84	PD2/D2[A2/D2]/MTIOC4D/CRX0/IRQ2/AN010	IRQ2	Input	The same function is assigned to 18/84.

(a) [Pin function] Sheet

Pin name	Pin function	Assignment	Pin No.	Direction	State
IRQ2	External interrupt	Conflicted	18/84	Input	Do not assign a single function to multiple pins.

(b) [Peripheral pin usage] Sheet

Figure 3.28 Example of an Error (Selection of a Single Function for Multiple Pins)

(2) The pin function has not been allocated.

Failure to allocate a pin function required by a peripheral module leads to an error and prevents the generation of source files.Select the pin function for a corresponding pin on the [Pin function] sheet or designate the allocation of the pin function on the [Peripheral pin usage] sheet.

Pin name	Pin function	Assignment	Pin No.	Direction	State
IRQ2	External interrupt	Not assigned	Not assigned	Input	Not assigned.

[Peripheral pin usage] Sheet

Figure 3.29 Example of an Error (Pin Function not Allocated)

(3) Multiple pin functions have been selected for a single pin.

A warning appears when two or more pin functions have been assigned to a single pin (as in figure 3.30), but generating source files is still possible. You can switch between the functions, although they cannot be used at the same time.

To switch between pin functions, make the initial setting for the peripheral module using that pin function, since the individual pin functions are set by the initial-setting function for the given peripheral module. However, RTCOUT and RTCIC2 cannot be assigned to the same pin.

Pin No.	Pin name	Selected function	Direction	State
⚠ 18	P32/MTIOC0C/TIOCC0/TMO3/PO10/RTCOUT/RTC..	P32/IRQ2		Conflicting between different functions.

(a) [Pin function] Sheet

Pin name	Pin function	Assignment	Pin No.	Direction	State
⚠ IRQ2	External interrupt	P32/MTIOC0C/TIOCC0/TMO3/..	18	Input	Conflicting with another pin function.

Pin name	Pin function	Assignment	Pin No.	Direction	State
⚠ P32	General input port	P32/MTIOC0C/TIOCC0/TMO3...	18	Input	Conflicting with another pin function.

(b) [Peripheral pin usage] Sheet

Figure 3.30 Example of a Warning (Multiple Pin Functions Selected for a Single Pin)

(4) Conflict with use of a pin by a debugger

A warning appears when a pin function for a peripheral module has been allocated to a pin for use by an on-chip debugger. Generating source files is still possible. Note, however, that the other pin function allocated to the pin may not be usable while the on-chip debugger is in use.

Pin No.	Pin name	Selected function	Direction	State
⚠ 20	TDI/P30/MTIOC4B/TMRI3/PO8/RTCIC0/POE8#/R...	IRQ0		Conflicting with an on-chip emulator pin.

(a) [Pin function] Sheet

Pin name	Pin function	Assignment	Pin No.	Direction	State
⚠ IRQ0	External interrupt	TDI/P30/M...	20	Input	Conflicting between a peripheral module pin and an on-chip emulator pin.

(b) [Peripheral pin usage] Sheet

Figure 3.31 Example of a Warning (Conflict with Use of a Pin by a Debugger)

(5) The peripheral module has not been set up.

A warning appears when a pin function is selected on the [Pin function] sheet but the corresponding peripheral module has not been set up. Although generating source files is still possible, the selected pin function will not be usable. To enable the selected pin function, set up the peripheral module that is to use the function and call the initial-setting function, which sets the registers to change the pin function.

Pin No.	Pin name	Selected function	Direction	State
⚠ 73	PE5/D13[..	IRQ5		IRQ5 has not been configured in the peripheral settings.

[Pin function] Sheet

Figure 3.32 Example of a Warning (Peripheral Module Not Set up)

3.3 Endian

Select the [SYSTEM] tab from the peripheral-module selection tabs and click on [Option setting] in the resource pane to open the endian setting pane.

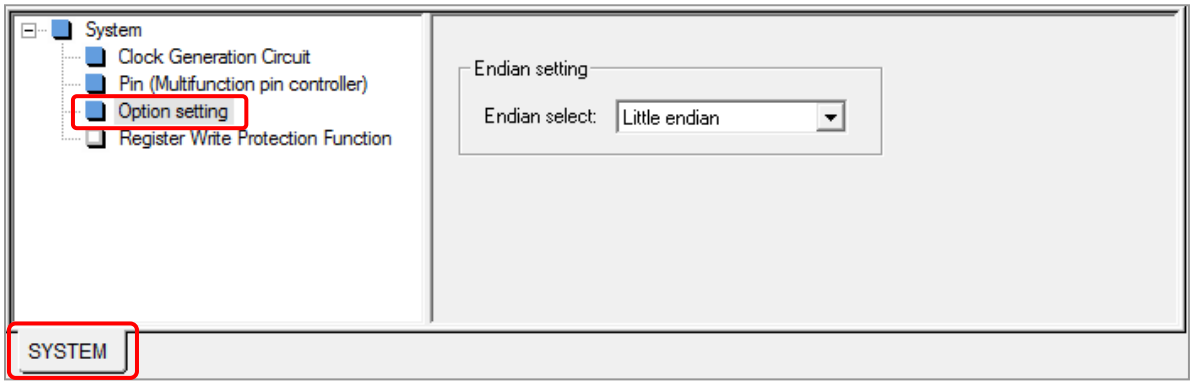


Figure 3.33 The setting method of endian

Select endian to be used here. This setting is only used for selecting Renesas Peripheral Driver Library files (xxx_little.lib or xxx_big.lib) to be linked and thus does not affect the output source code.

4. Tutorial

4.1 When the High-performance Embedded Workshop is in Use

This section introduces the usage of the Peripheral Driver Generator by giving instructions on how to use the Peripheral Driver Generator and High-performance Embedded Workshop to create a tutorial program that implements the following operations on the Renesas Starter Kit board for the RX630.

- An LED blinking on a 8-bit timer (TMR) interrupt
- An LED blinking on the PWM output of the multi-function timer pulse unit 2 (MTU2a)
- Continuously scanning on 12-Bit A/D converter (S12ADa)
- Triggering DTCa by ICUb
- Data transfer between SCIc channels 0 and 2

The labels given below respectively indicate operations to take place in the Peripheral Driver Generator and in the High-performance Embedded Workshop.

PDG : Operations in the Peripheral Driver Generator

HEW : Operations in the High-performance Embedded Workshop

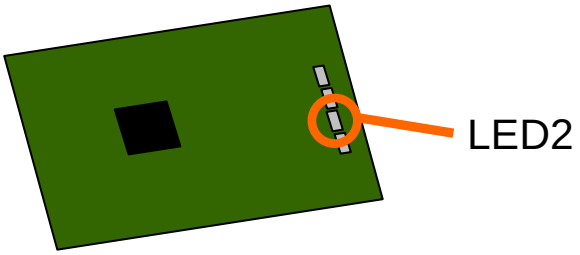
[Note on Using the High-performance Embedded Workshop]
Refer to the user's manual and check the HewTargetServer settings.

4.1.1 An LED blinking on a 8-bit timer (TMR) interrupt

The LED2 on RSK board is connected to PC2. In this tutorial, 8-bit Timer and I/O port will be set up to blink this LED as follows.

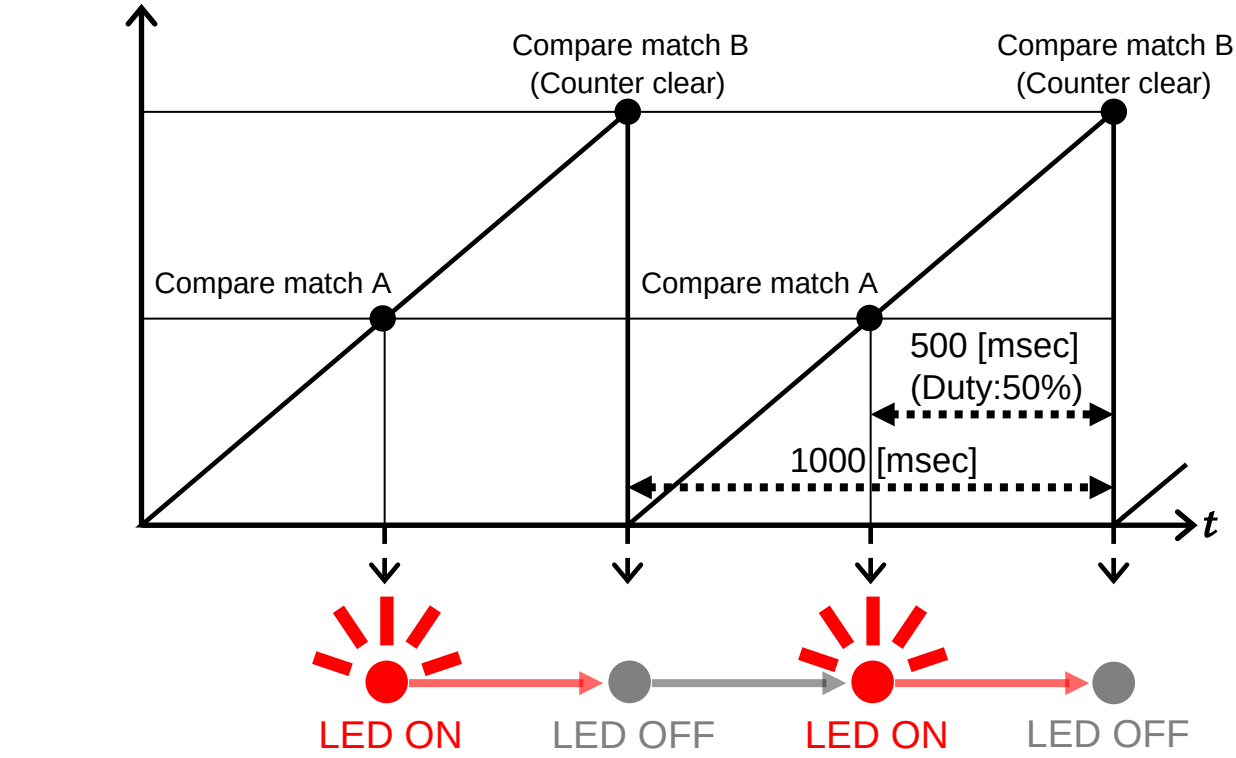
Note : If there is a switch that enables/disables PC2 on the RSK board, enable it.

The LED2 turns on when the output from PC2 is 0, and turns off when the output is 1.



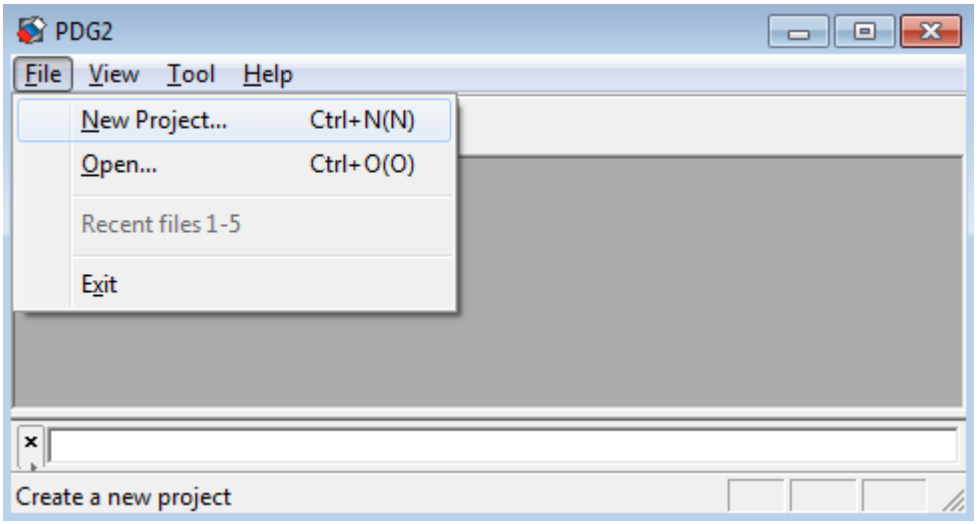
- Turn on the LED
  at compare match A
- Turn off the LED
  at compare match B
- Clear the counter
  at compare match B

TMR counter value



(1) Making the Peripheral Driver Generator project **PDG**

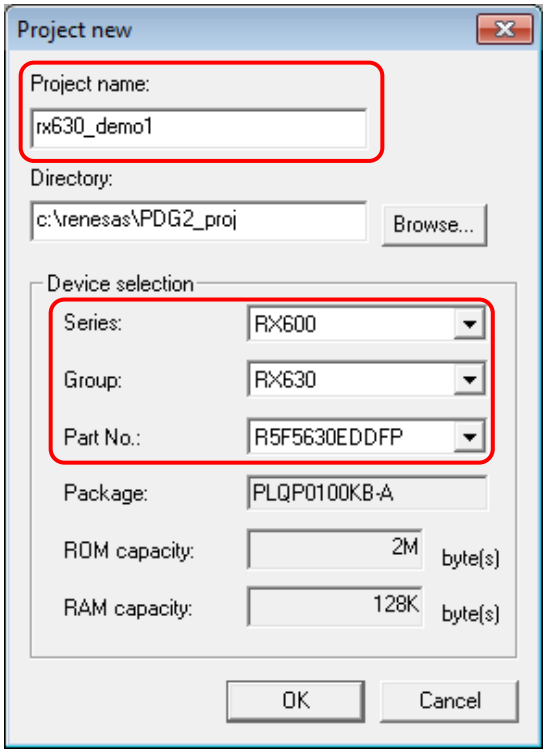
1. Start the Peripheral Driver Generator.
2. Select [File]->[New Project] menu.



3. Specify "rx630_demo1" as the project name.
   Set the CPU type as follows.

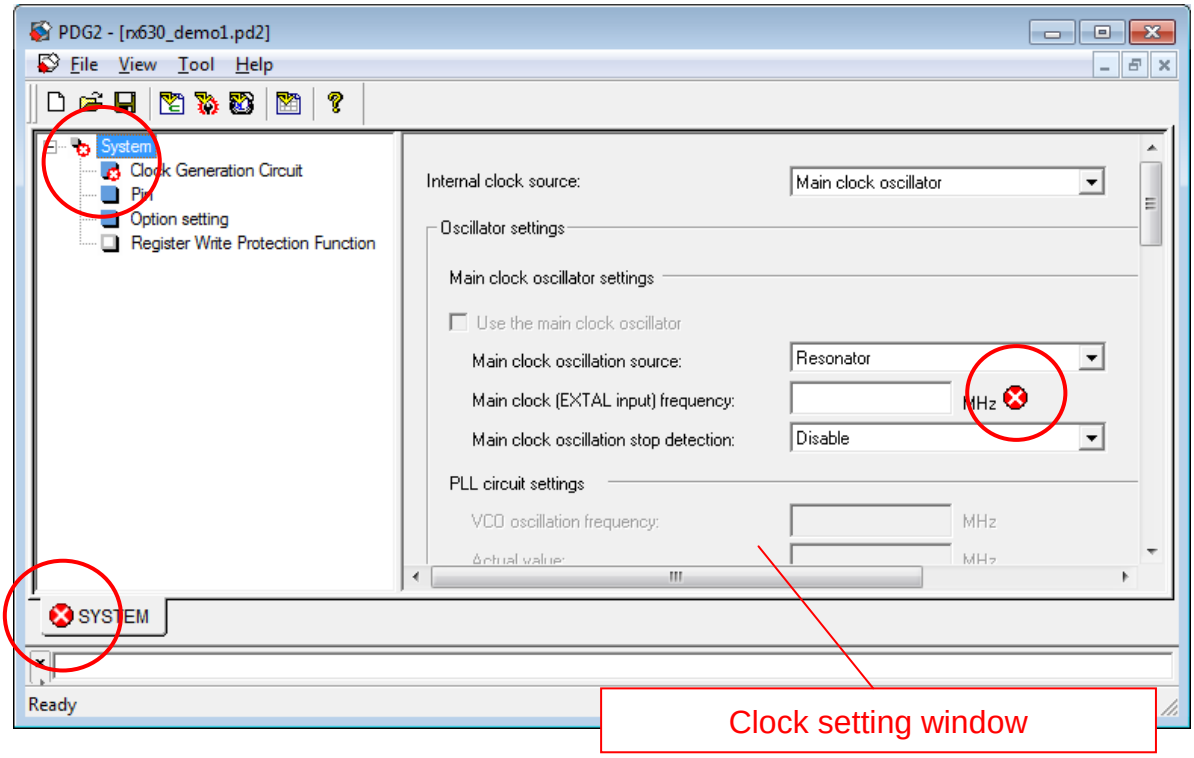
   Series : RX600
   Group : RX630
   Part No. : R5F5630EDDFP

   Note: If another type of chip is mounted on your RSK board, select corresponding CPU type.

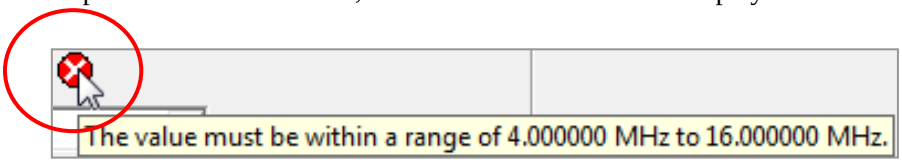


(2) Initial state **PDG**

-The clock setting window opens and the error icons are displayed in the initial state.



Place the mouse pointer on the error icon, then the contents of error is displayed.



There are 3 types of icons in Peripheral Driver Generator

Error
- The setting is not allowed.
- The source filese cannot be generated if there is an error setting.

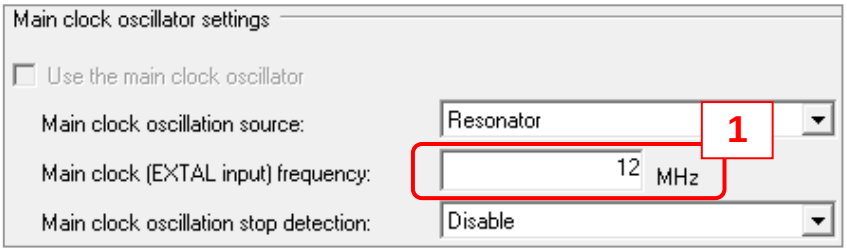
Warning
- The setting is possible but may be wrong.
- Source files can be generated.

Information
- Additional information for the complex setting.

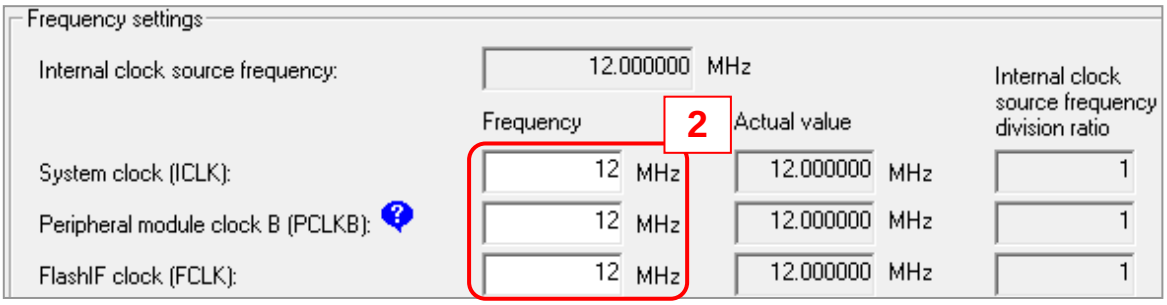
Only icons on the setting window can display the tooltip.

(3) Clock setting **PDG**

1. It is necessary to set the main (EXTAL) clock frequency first.
   External clock frequency of the RSK board is 12 MHz. Set 12 to the edit box.



2. ICLK, PCLKB and FCLK are used in 12 MHz.
   Set 12 to the edit box.



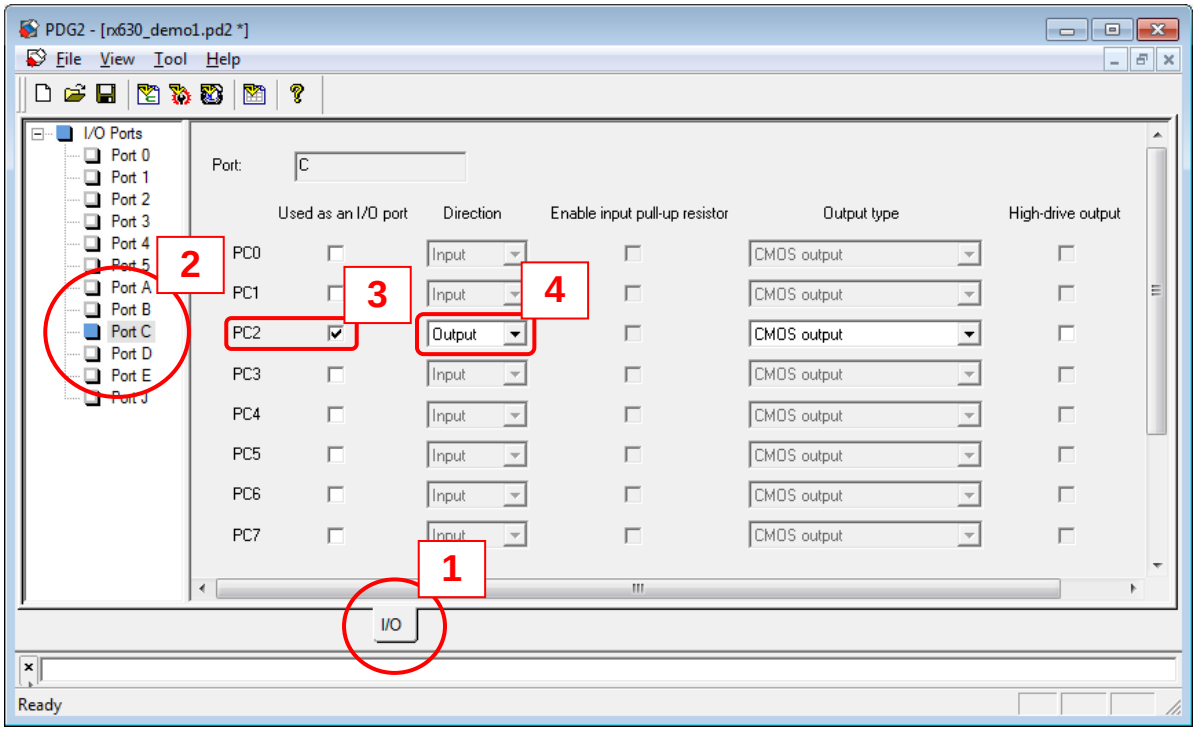
(4) Endian setting **PDG**

For the endian setting, refer to section 3.3, Endian.

(5) I/O Port setting **PDG**

The LED2 on RSK is connected to PC2 so set PC2 to output port.

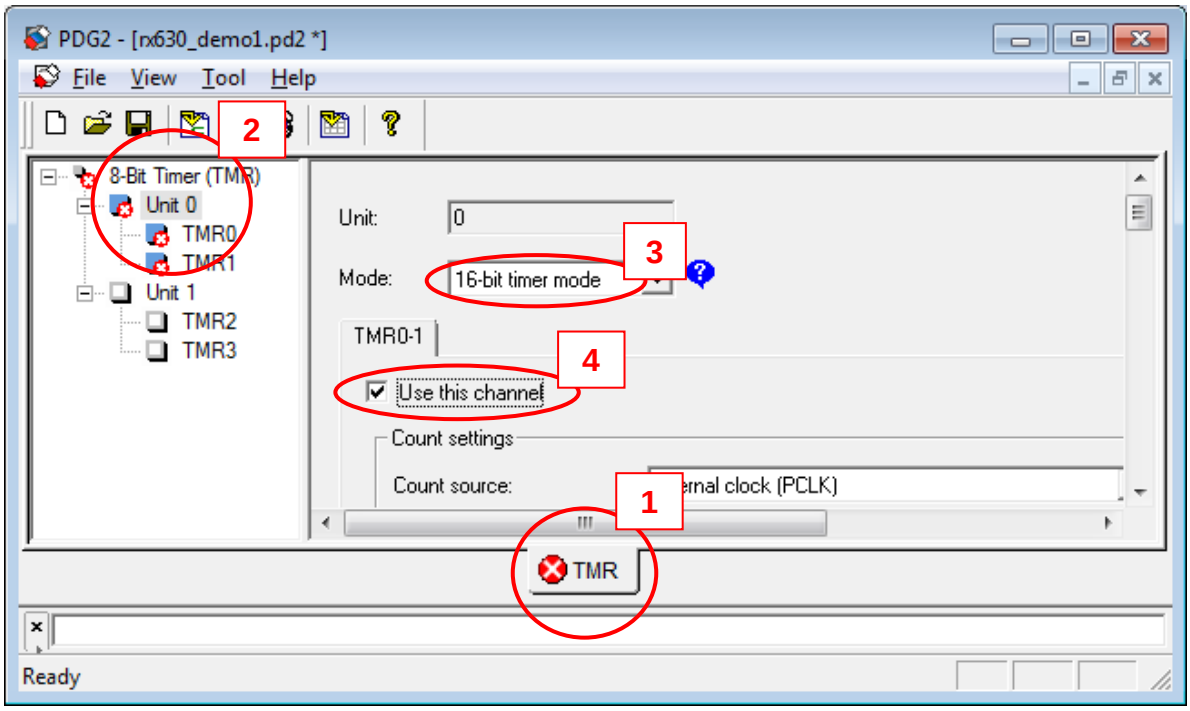
1. Select “I/O” tab
2. Select “Port C”
3. Check “PC2”
4. Select “Output”



(6) TMR setting-1 **PDG**

In this tutorial, TMR (8-bit timer) Unit0 is used in 16 bit mode (two 8-bit timers cascade connection)

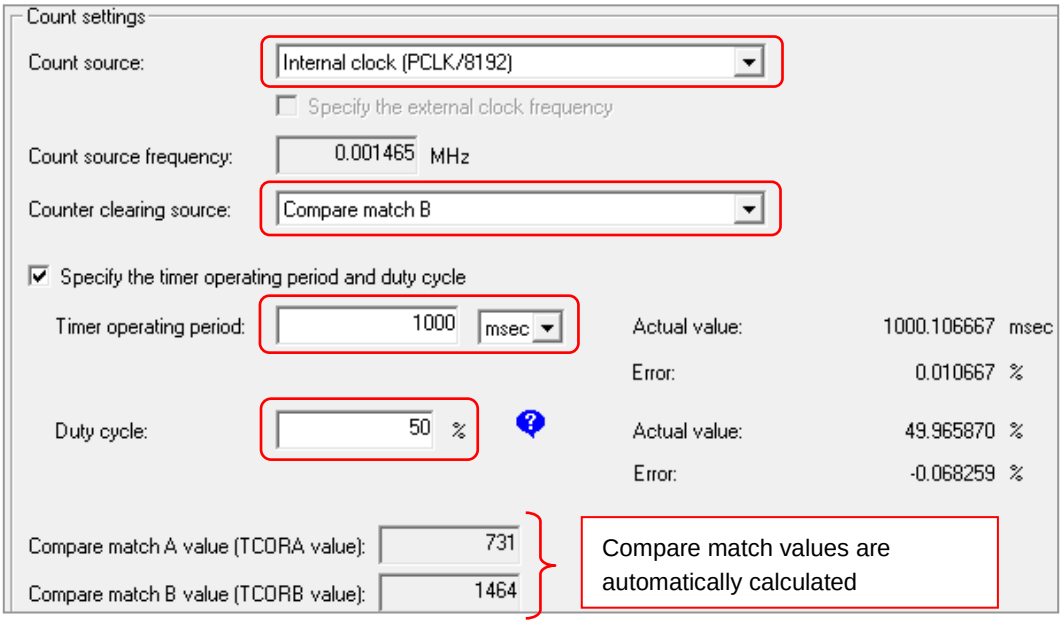
1. Select "TMR" tab
2. Select "Unit0"
3. Select "16 bit timer mode"
4. Check "Use this channel"



(7) TMR setting-2 **PDG**

Set the other items as follows.

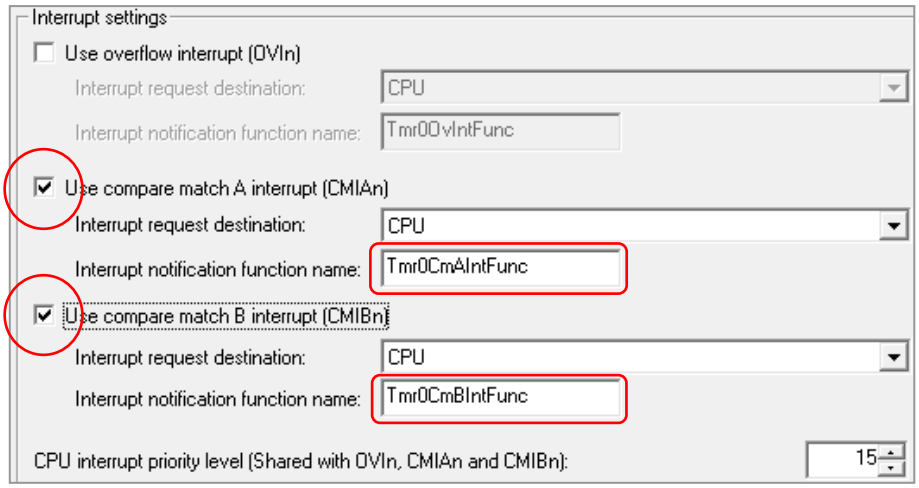
-Count source : Internal clock(PCLK/8192)
-Counter clearing source : Compare match B
-Interval : 1000 msec
-Duty cycle : 50%



(8) TMR setting-3 **PDG**

Set the interrupt notification functions.
These functions are called when the interrupt occurs.

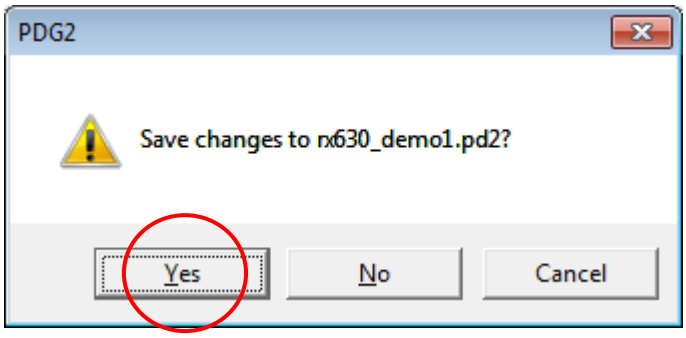
-Check compare match A interrupt
Notification function name is "Tmr0CmAIntFunc"
-Check compare match B interrupt
Notification function name is "Tmr0CmBIntFunc"



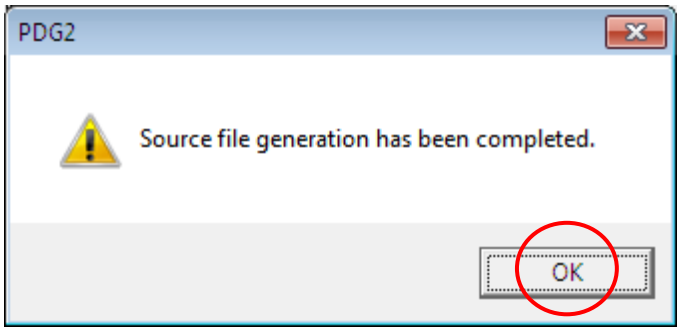
(9) Generating source files PDG

1. To generate source files, click  on the tool bar.

2. Save confirmation dialog box is displayed. Click [Yes].

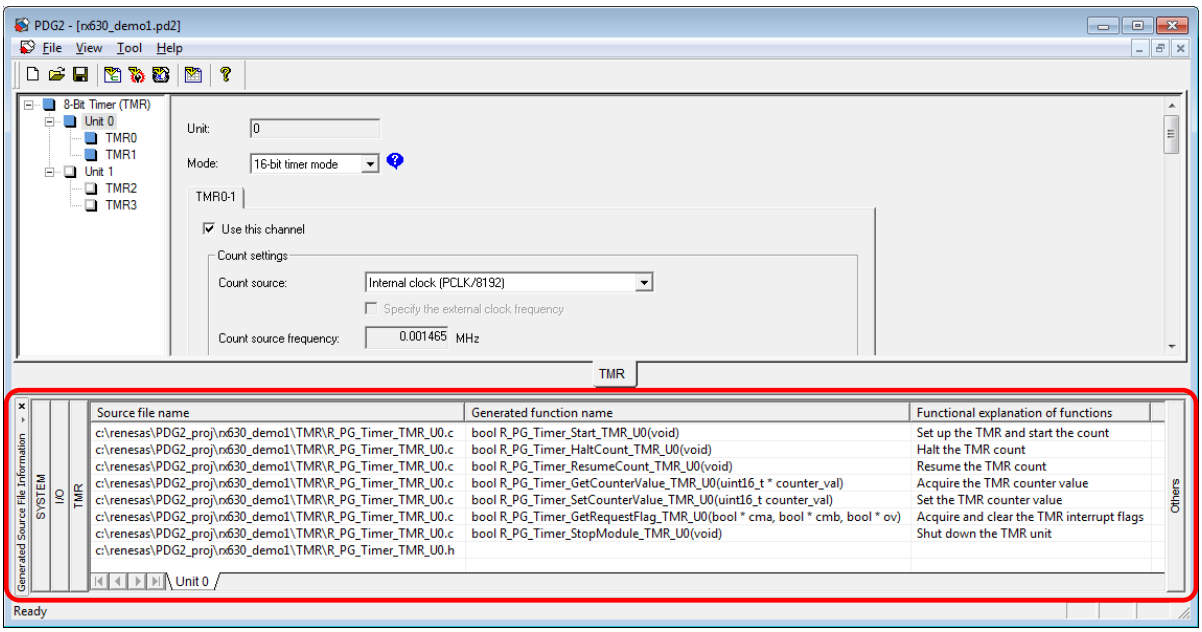


3. Click [OK] on the message box.



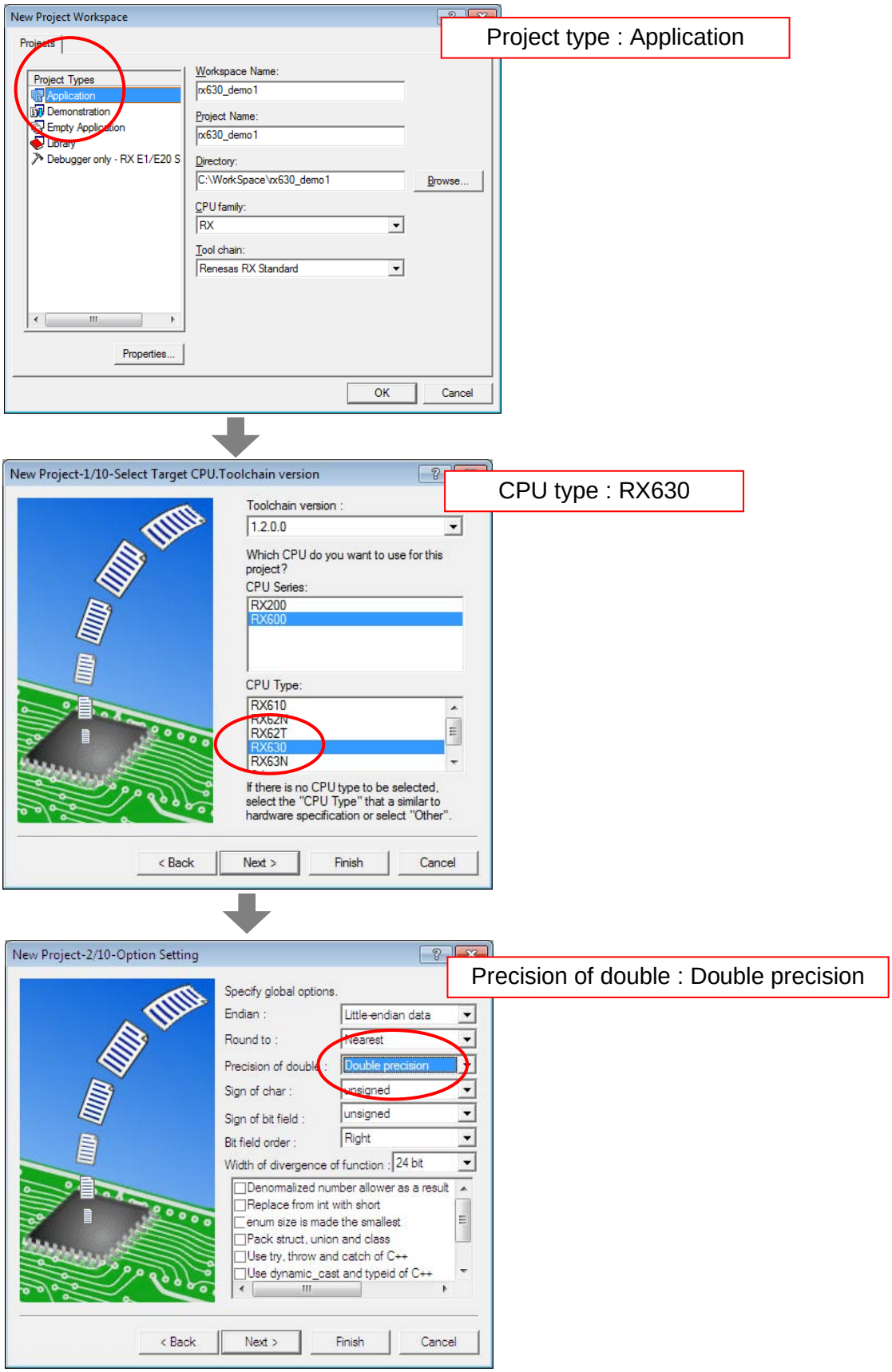
4. Generated functions are listed in lower pane.

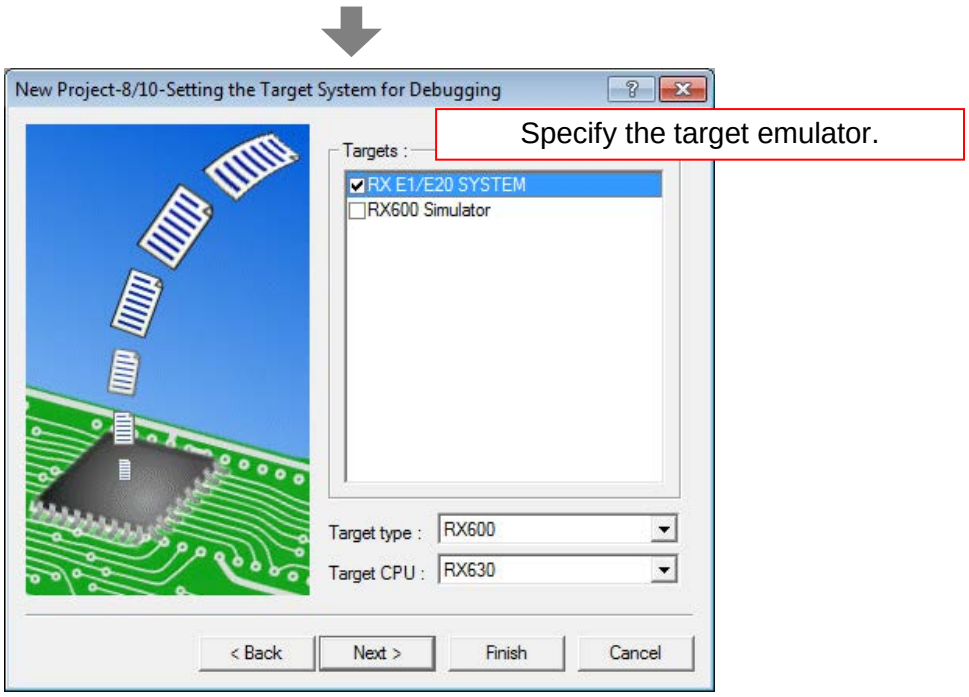
   By double clicking the line of function, source file can be opened.

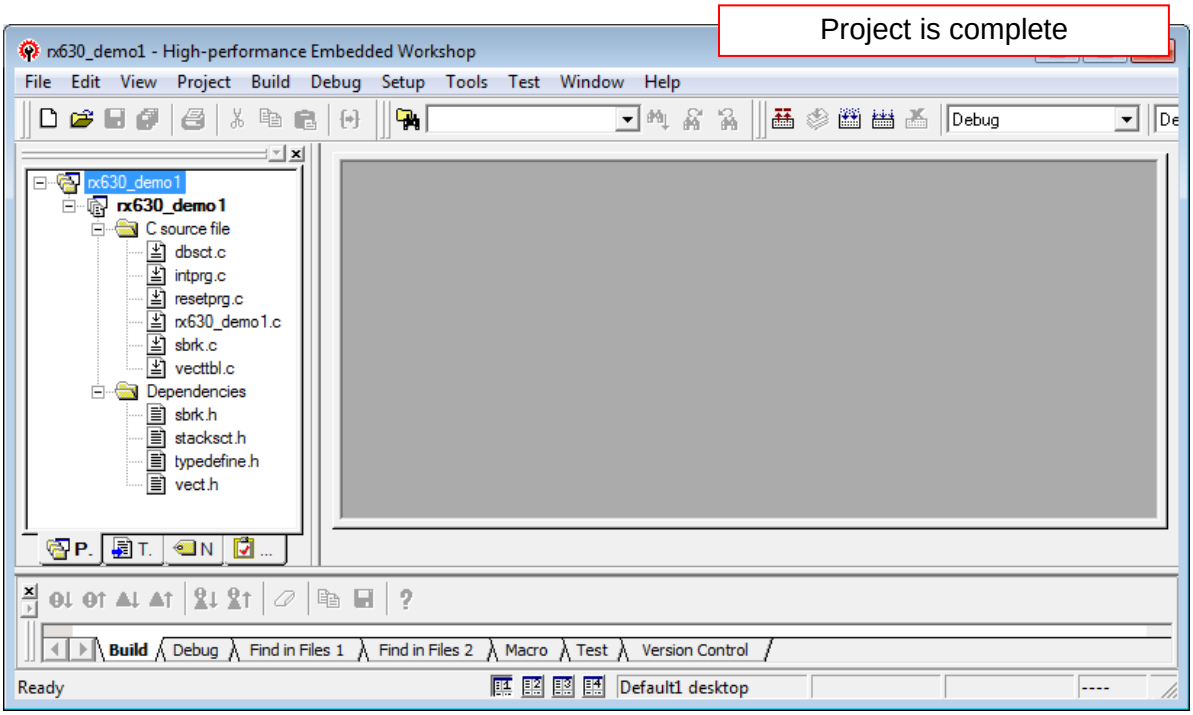


(10) Preparing the High-performance Embedded Workshop project **HEW**

Start the High-performance Embedded Workshop and make RX630 workspace.




New Project-8/10-Setting the Target System for Debugging
Specify the target emulator.
Targets :
RX E1/E20 SYSTEM
RX600 Simulator
Target type :
RX600
Target CPU :
RX630
< Back
Next >
Finish
Cancel

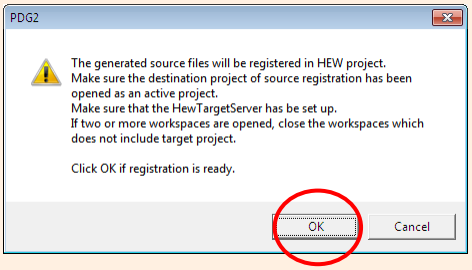

Project is complete
rx630_demo1 - High-performance Embedded Workshop
File Edit View Project Build Debug Setup Tools Test Window Help
Debug
rx630_demo1
rx630_demo1
C source file
dbsct.c
intprg.c
resetprg.c
rx630_demo1.c
sbrk.c
vecttbl.c
Dependencies
sbrk.h
stacksct.h
typedefine.h
vect.h
Build Debug Find in Files 1 Find in Files 2 Macro Test Version Control
Ready
Default1 desktop

(11) Adding the generated source files to the High-performance Embedded Workshop project

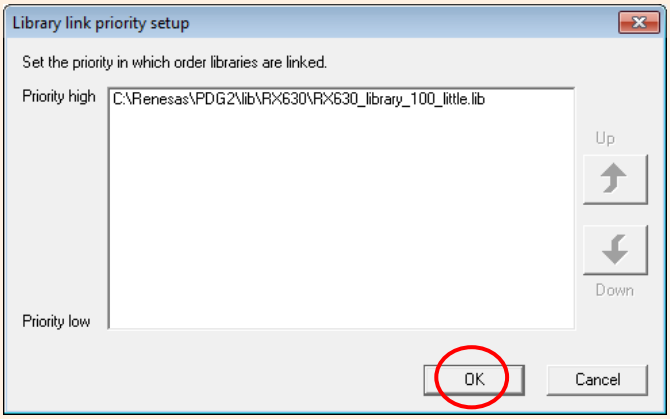
PDG

1. To add source files to High-performance Embedded Workshop, click  on the tool bar.

2. Click [OK] on the confirmation dialog box.

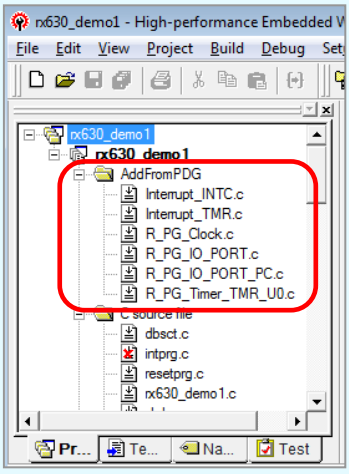


3. This is a linkage setting of Renesas Peripheral Dirver Library.
When using multiple lib files, linkage order can be set in this dialog box.



HEW

4. Source files are added to High-performance Embedded Workshop
Added source files are put in "AddFromPDG" folder.



Source files are registered via HEW Target Server.
Make sure that the HEW Target Server has been set up before executing registration.
For details, refer Peripheral Driver Generator user's manual.

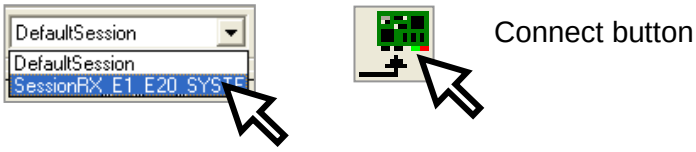
(12) Making the program on High-performance Embedded Workshop **HEW**

By changing the part of “main” function, make the following program on High-performance Embedded Workshop.

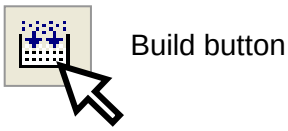
```
//Include "R_PG_<project name>.h"
#include "R_PG_rx630_demo1.h"

void main(void)
{
    //Configure I/O port pins that are not available
    R_PG_IO_PORT_SetPortNotAvailable();

    //Set up the clocks (wait cycle insertion)
    R_PG_Clock_WaitSet(0.01);

    //Set up port PC2
    R_PG_IO_PORT_Write_PC2(1);
    R_PG_IO_PORT_Set_PC();

    //Set up TMR Unit0 and start count
    R_PG_Timer_Start_TMR_U0();

    while(1);
}

// Compare match A interrupt notification function
void Tmr0CmAIntFunc(void)
{
    // Turn on the LED
    R_PG_IO_PORT_Write_PC2(0);
}

// Compare match B interrupt notification function
void Tmr0CmBIntFunc(void)
{
    // Turn off the LED
    R_PG_IO_PORT_Write_PC2(1);
}
```

(13) Connecting to the emulator, building the program and executing **HEW**

1. Connect to the emulator

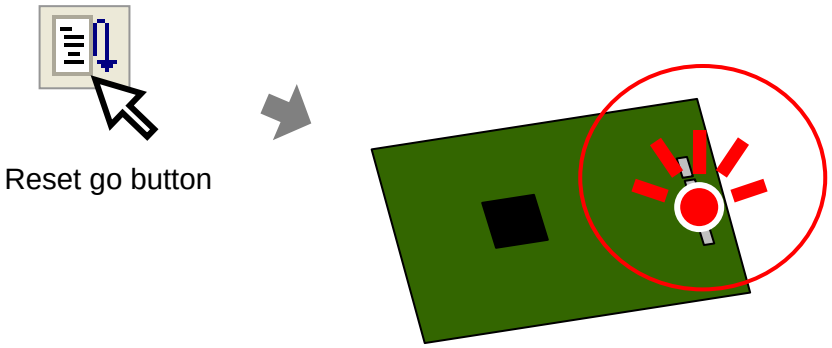


2. Just by clicking [Build] button, program can be built because Renesas Peripheral Driver Library and include directory are automatically registered in build setting.



3. Download the program.

4. Execute the program and see the LED on RSK board.

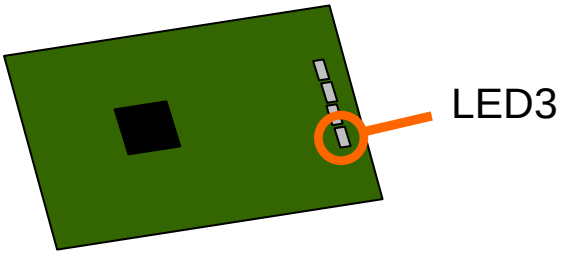


4.1.2 An LED blinking on the PWM output of the multi-function timer pulse unit 2 (MTU2a)

The LED3 on RSK board is connected to P17. This port can also be used as PWM output pin (MTIOC3A) of the multi-function timer pulse unit 2. In this tutorial, the multi-function timer pulse unit 2 will be set up to operate in PWM mode 1 and the PWM output will blink the LED3 as follows.

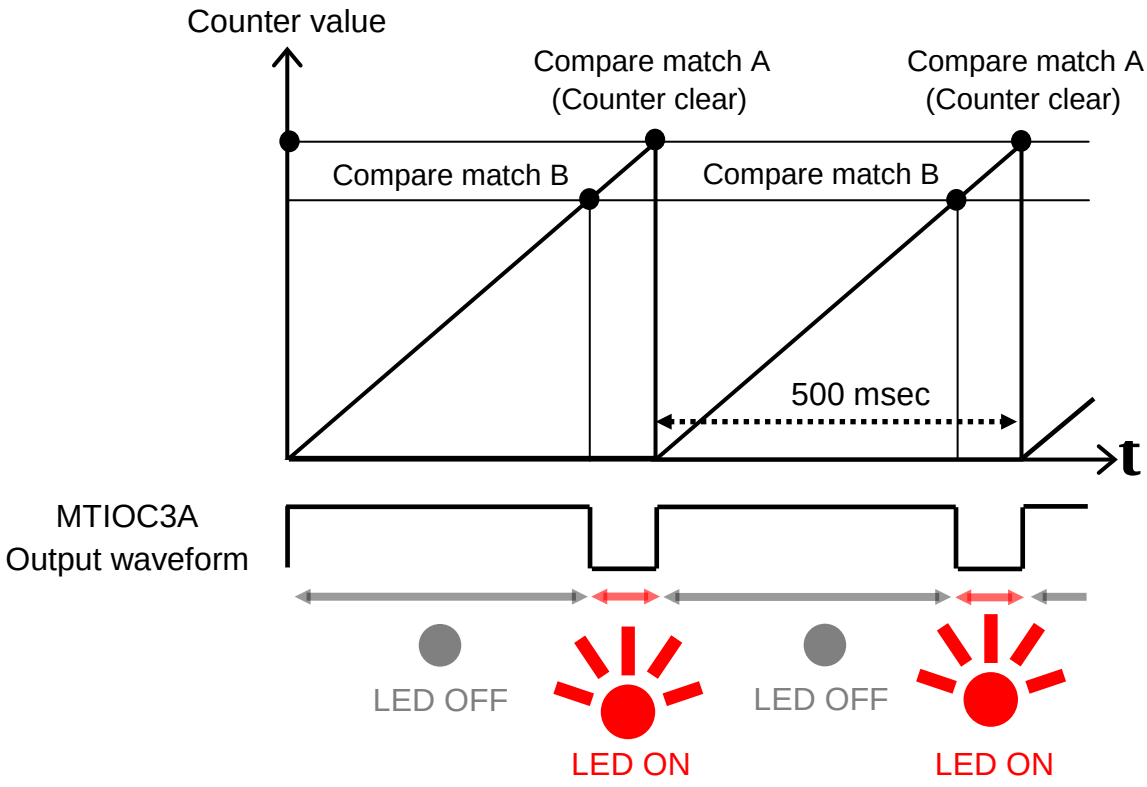
Note : If there is a switch that enables/disables P17(MTIOC3A) on the RSK board, enable it.

The LED3 turns on when the output from P17 is 0, and turns off when the output is 1.



The MTU2a channel 3 (MTU3) will be operated in PWM mode 1. In PWM mode 1, the output signal is controlled by compare match A and B.

Operation of the timer to be set
- Output 0 at compare match B -> LED turns on
- Output 1 at compare match A -> LED turns off
- Clear the counter at compare match A (Intervals of 500 msec)



(1) Making the Peripheral Driver Generator project

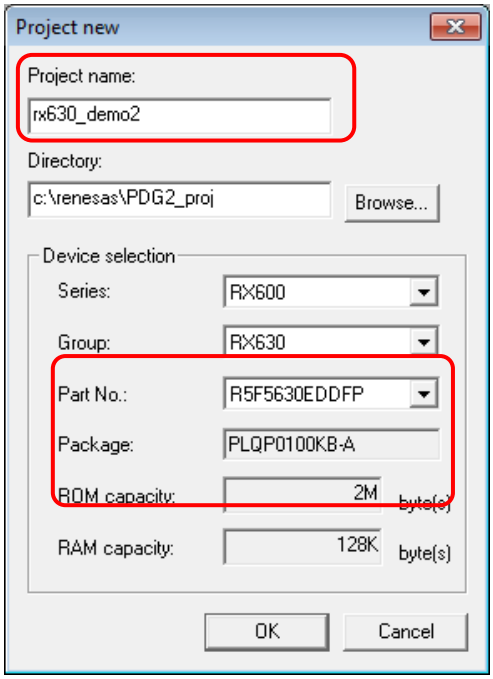
PDG

Make the new Peripheral Driver Generator project “rx630_demo2”. For details on how to make the new Peripheral Driver Generator project, refer to section 4.1.1 (1), Making the Peripheral Driver Generator project.

Set the CPU type as follows.

Series : RX600
Group : RX630
Part No. : R5F5630EDDFP

Note: If another type of chip is mounted on your RSK board, select corresponding CPU type.



(2) Clock setting

PDG

1. The clock setting window opens and the error icons are displayed in the initial state. For icons such as  and  displayed on window, refer to section 4.1.1 (2), Initial state.
2. For the clock setting, refer to section 4.1.1 (3), Clock setting.

(3) Endian setting

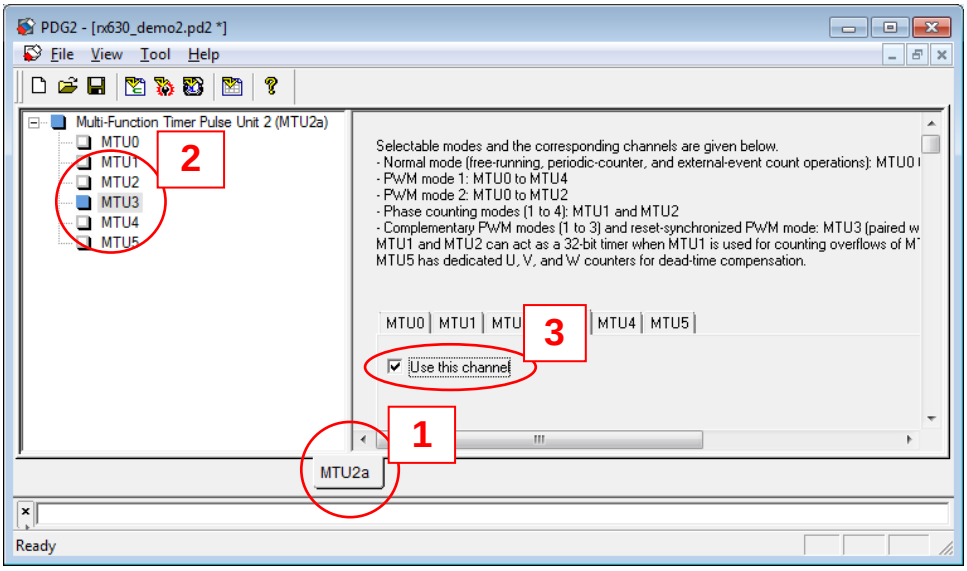
PDG

For the endian setting, refer to section 3.3, Endian.

(4) MTU2a setting-1 **PDG**

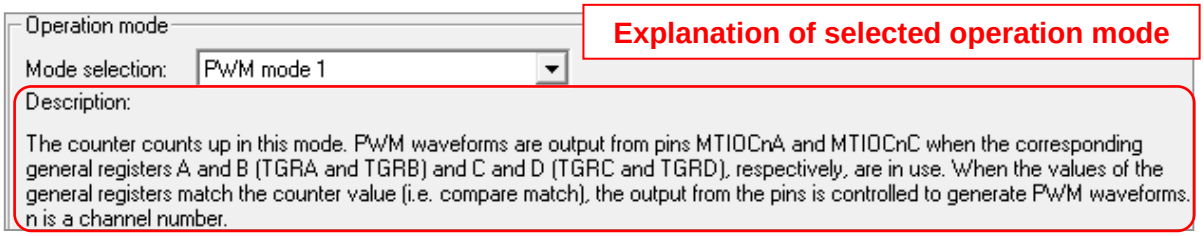
Opening MTU2a channel 3(MTU3) setting window

1. Select "MTU2a" tab.
2. Select "MTU3" on tree view.
3. Check "Use this channel".



(5) MTU2a setting-2 **PDG**

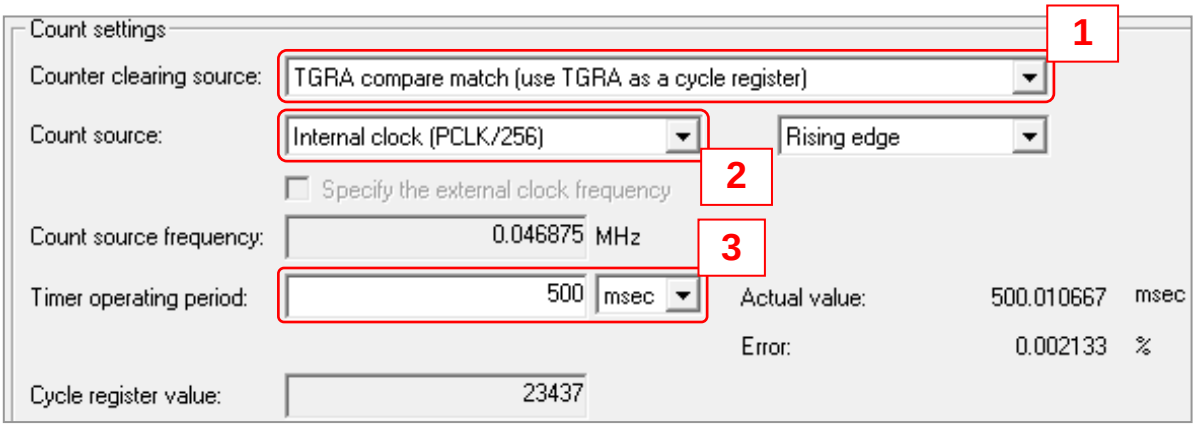
Select "PWM mode 1" for the operation mode.



(6) MTU2a setting-3 **PDG**

The counter setting is as follows.

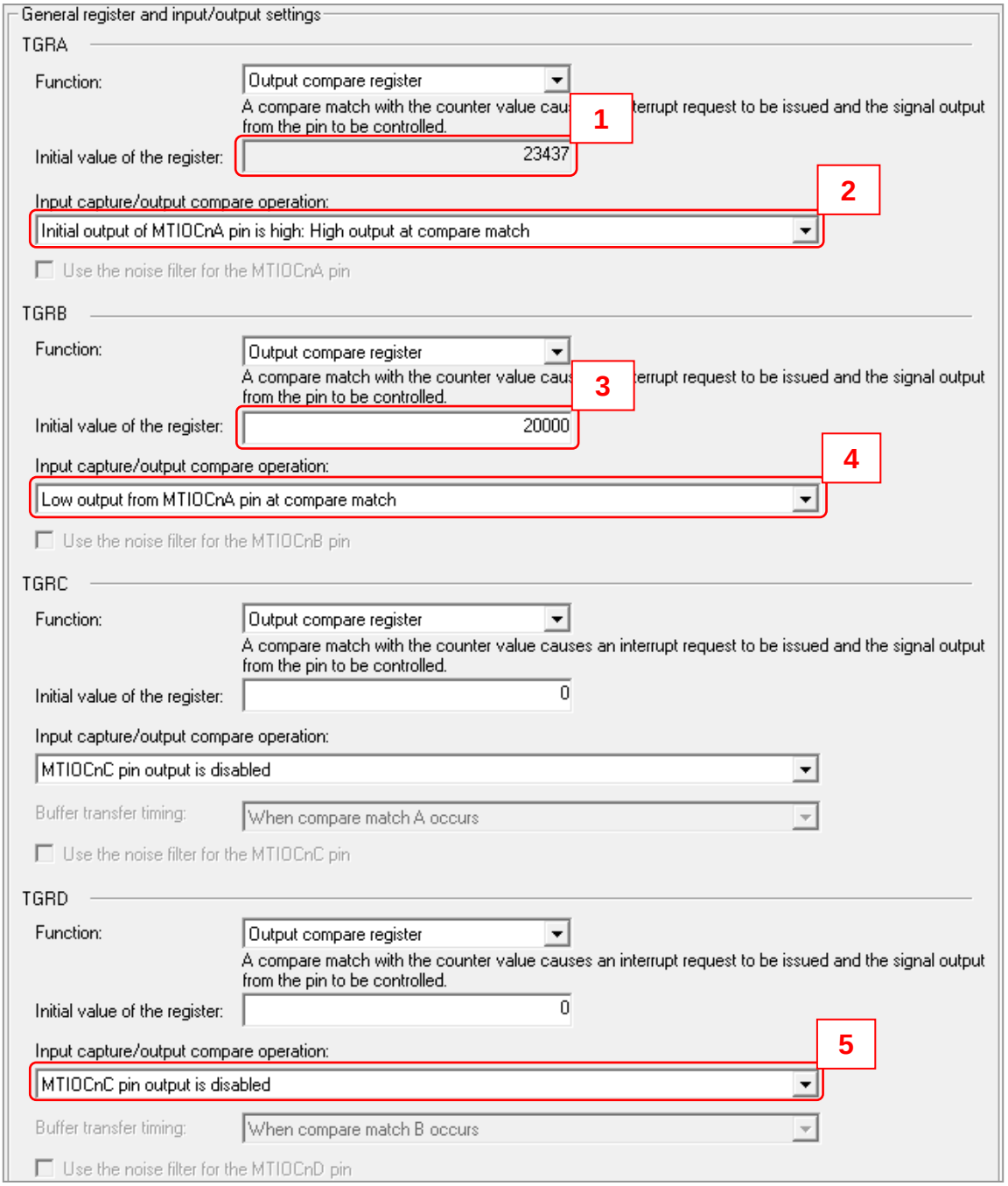
1. Select "TGRA compare match" for a counter clearing source.
2. Select "Internal clock (PCLK/256)" for a count source.
3. Set "500 msec" to timer operation period.



(7) MTU2a setting-4 **PDG**

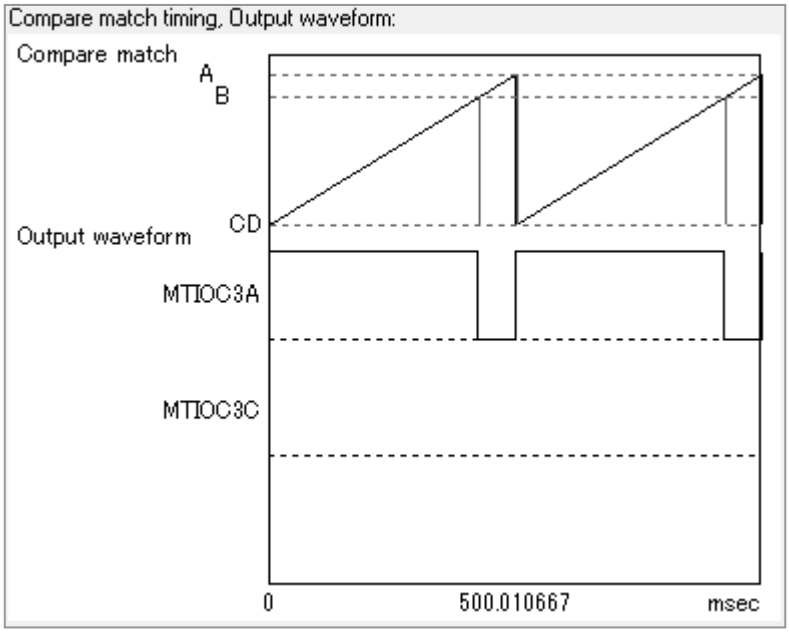
General register setting is as follows.

1. The TGRA is selected as a counter clearing source in the counter setting. Then the TGRA value is calculated from the count source frequency and the timer operating period.
2. Select "Initial output of MTIOCnA pin is high: High output at compare match" for TGRA output compare operation.
3. Set "20000" to TGRB initial value.
4. Select "Low output from MTIOCnA pin at compare match" for TGRB output compare operation.
5. The MTIOCnC output is not used in this tutorial. Select "MTIOCnC pin output is disabled" for TGRD output compare operation.



(8) MTU2a setting-5 PDG

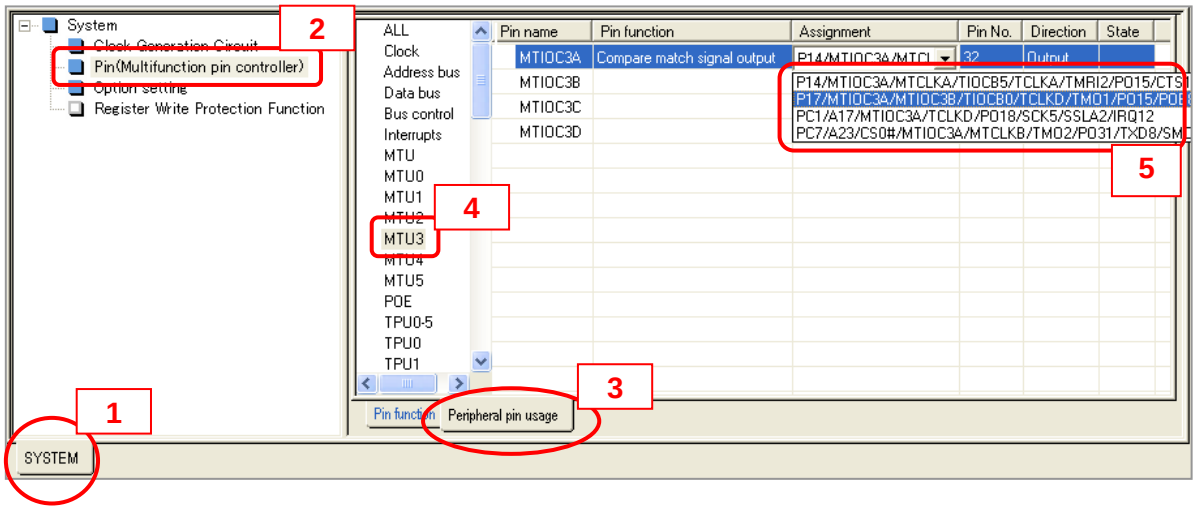
The compare match timing and the output waveform are displayed in a diagram.



(9) Pin setting PDG

MTIOC3A can be selected the pin function assignment. Select the pin function assignment as follows.

1. Select “SYSTEM” tab.
2. Select “Pin(Multifunction pin controller)” on tree view.
3. Select “Peripheral pin usage” tab.
4. Select “MTU3” from the peripheral module list.
5. When the mouse pointer is placed on “Assignment” column of MTIOC3A line, a dropdown button is displayed. Select “P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#” from the dropdown list.



(10) Generating source files **PDG**

To generate source files, click  on the tool bar. For details on generating source files, refer to section 4.1.1 (9), Generating source files.

(11) Preparing the High-performance Embedded Workshop project **HEW**

Start the High-performance Embedded Workshop and make RX630 workspace. For details on making High-performance Embedded Workshop project, refer to section 4.1.1 (10), Preparing the High-performance Embedded Workshop project.

(12) Adding the generated source files to the High-performance Embedded Workshop project **PDG**

To add the generated source files to High-performance Embedded Workshop, click  on the tool bar. For details on adding the source files to High-performance Embedded Workshop project, refer to section 4.1.1 (11), Adding the generated source files to the High-performance Embedded Workshop project.

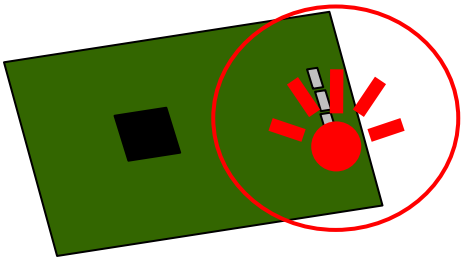
(13) Making the program on High-performance Embedded Workshop **HEW**

By changing the part of “main” function, make the following program on High-performance Embedded Workshop.

```
//Include "R_PG_<project name>.h"
#include "R_PG_rx630_demo2.h"

void main(void)
{
    //Configure I/O port pins that are not available
    R_PG_IO_PORT_SetPortNotAvailable();

    //Set up the clocks (wait cycle insertion)
    R_PG_Clock_WaitSet(0.01);

    //Set up MTU2a Channel 3
    R_PG_Timer_Set_MTU_U0_C3();

    //Start the count of MTU2a Channel 3
    R_PG_Timer_StartCount_MTU_U0_C3();

    while(1);
}
```

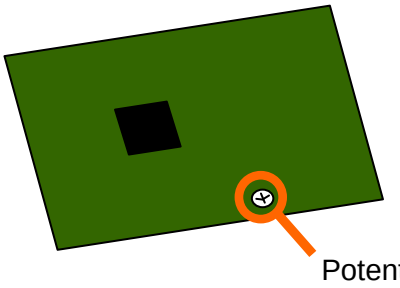
(14) Connecting to the emulator, building the program and executing **HEW**

Execute the program and see the LED blinking on RSK board. For details on connecting to the emulator, building the program, and executing the program, refer to section 4.1.1 (13), connecting to the emulator, building the program and executing.



4.1.3 Continuously scanning on 12-Bit A/D converter (S12ADa)

In RX630 RSK board, the potentiometer is connected to AN000 analog input. In this tutorial, the 12-Bit A/D converter (S12ADa) will be set up to execute A/D conversion continuously. And the result of A/D conversion will be monitored on High-performance Embedded Workshop.



Note : If there is a switch that enables/disables AN000 on the RSK board, enable it.

(1) Making the Peripheral Driver Generator project

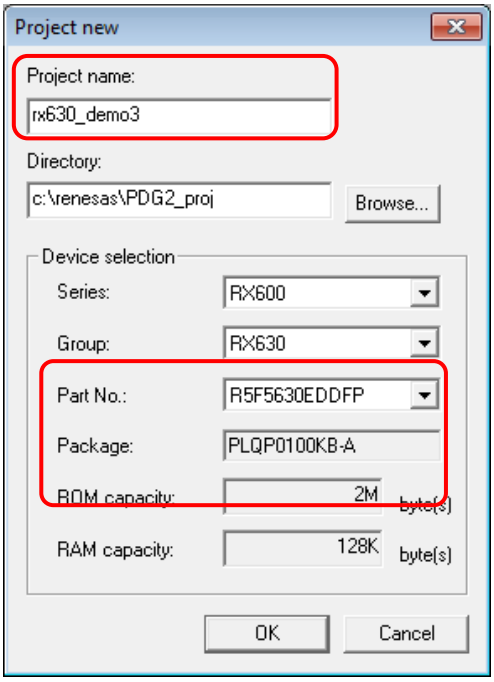
PDG

Make the new Peripheral Driver Generator project “rx630_demo3”. For details on how to make the new Peripheral Driver Generator project, refer to section 4.1.1 (1), Making the Peripheral Driver Generator project.

Set the CPU type as follows.

Series : RX600
Group : RX630
Part No. : R5F5630EDDFP

Note: If another type of chip is mounted on your RSK board, select corresponding CPU type.



(2) Clock setting **PDG**

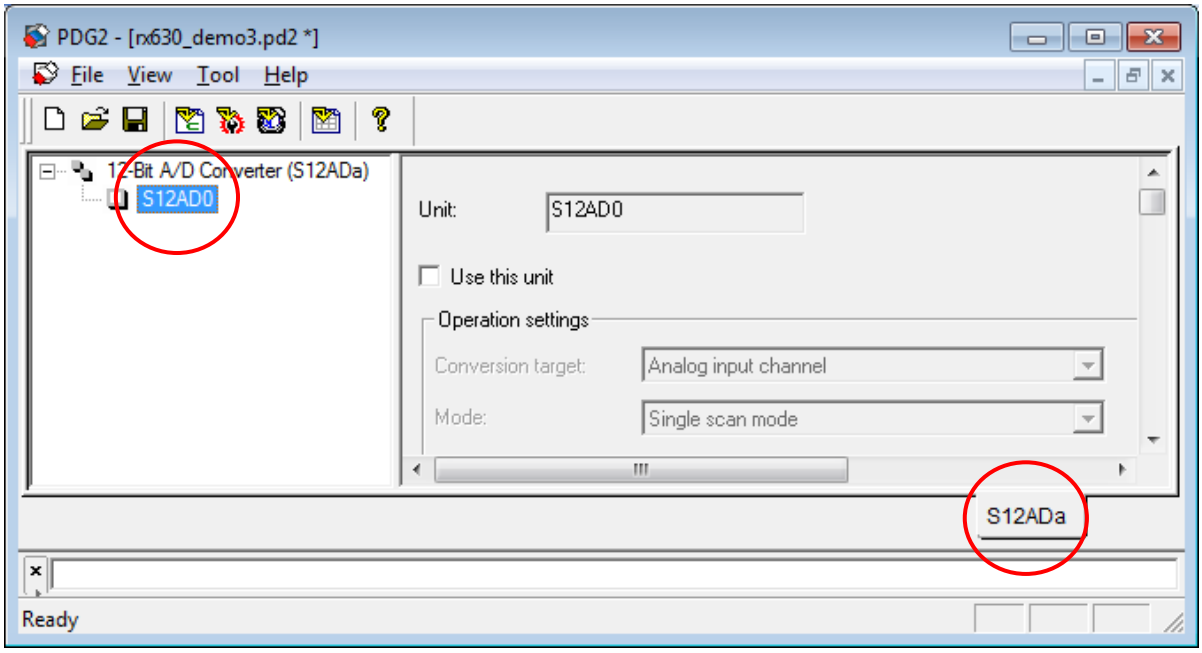
1. The clock setting window opens and the error icons are displayed in the initial state. For icons such as and displayed on window, refer to section 4.1.1 (2), Initial state.
2. For the clock setting, refer to section 4.1.1 (3), Clock setting.

(3) Endian setting **PDG**

For the endian setting, refer to section 3.3, Endian.

(4) A/D converter setting-1 **PDG**

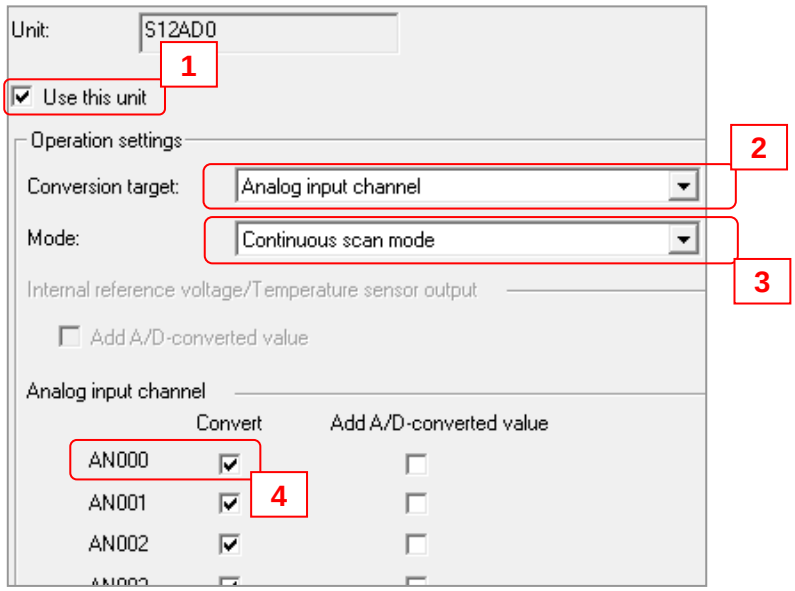
Select “S12ADa” tab and click S12AD0 on tree view.



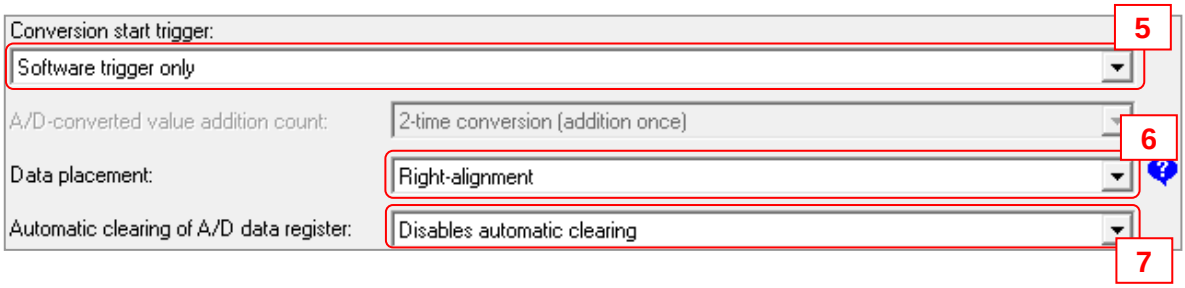
(5) A/D converter setting-2 PDG

Make the following setting for S12AD0.

1. Check "Use this unit".
2. Select "Analog input channel" for the conversion target.
3. Select "Continuous scan mode" for the operation mode.
4. Check "AN000" for the analog input channel.



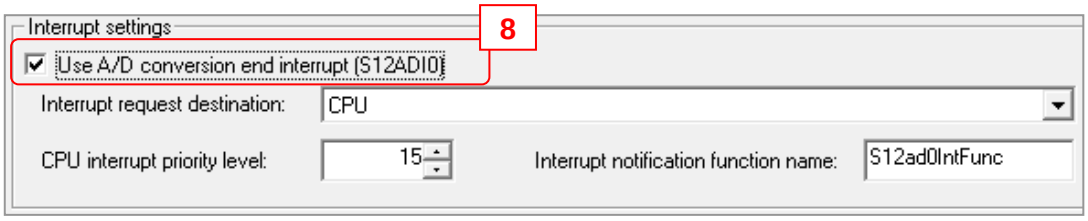
5. Select "Software trigger only" for the conversion start trigger.
6. Select "Right-alignment" for the data placement.
7. Select "Disables automatic clearing" for the automatic clearing of A/D data register.



(6) A/D converter setting-3 PDG

Make the following setting for S12AD0.

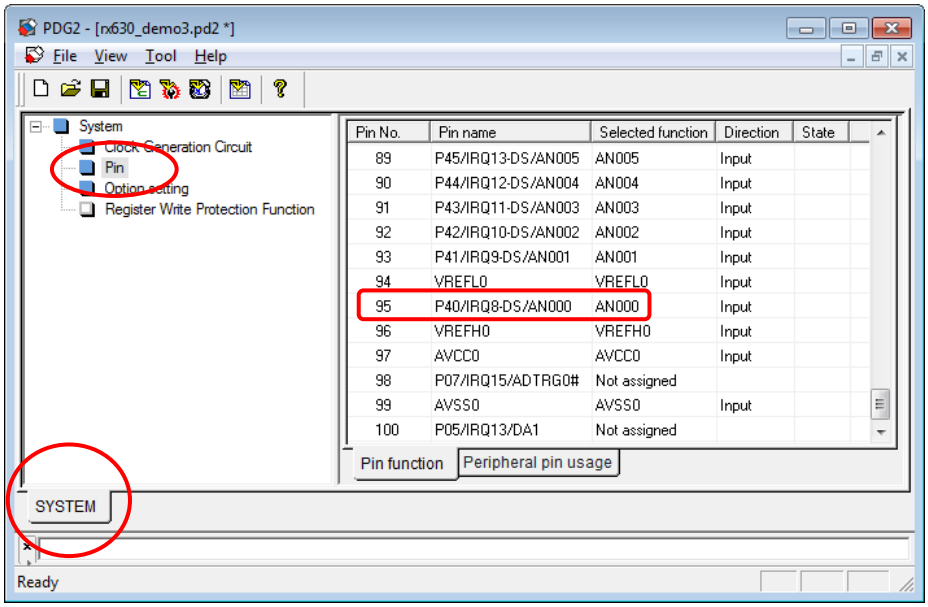
8. Check "Use A/D conversion end interrupt (S12ADI0)".



(7) Checking the pin usage **PDG**

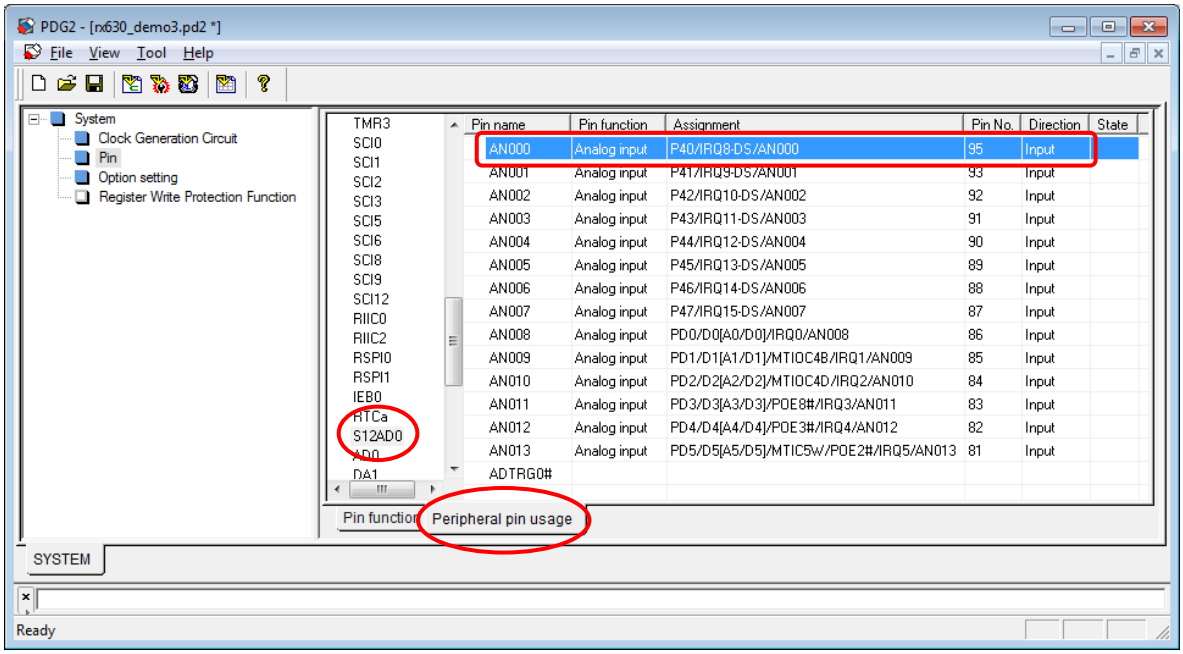
- It is possible to check the usage of pins on the pin function windows

1. After setting up the S12ADa, select “SYSTEM” tab and click “Pin” on the tree view.
2. On the Pin function window, you can see that No.95 pin is used as AN000.



- State of pin usage for each peripheral module is displayed in the peripheral pin usage window.

Select peripheral pin usage sheet and click S12AD0 to check the usage of AN000 pin.



(8) Generating source files **PDG**

To generate source files, click  on the tool bar. For details on generating source files, refer to section 4.1.1 (9), Generating source files.

(9) Preparing the High-performance Embedded Workshop project **HEW**

Start the High-performance Embedded Workshop and make RX630 workspace. For details on making High-performance Embedded Workshop project, refer to section 4.1.1 (10), Preparing the High-performance Embedded Workshop project.

PDG

(10) Adding the generated source files to the High-performance Embedded Workshop project

To add the generated source files to High-performance Embedded Workshop, click  on the tool bar. For details on adding the source files to High-performance Embedded Workshop project, refer to section 4.1.1 (11), Adding the generated source files to the High-performance Embedded Workshop project.

(11) Making the program onHigh-performance Embedded Workshop **HEW**

By changing the part of “main” function, make the following program on High-performance Embedded Workshop.

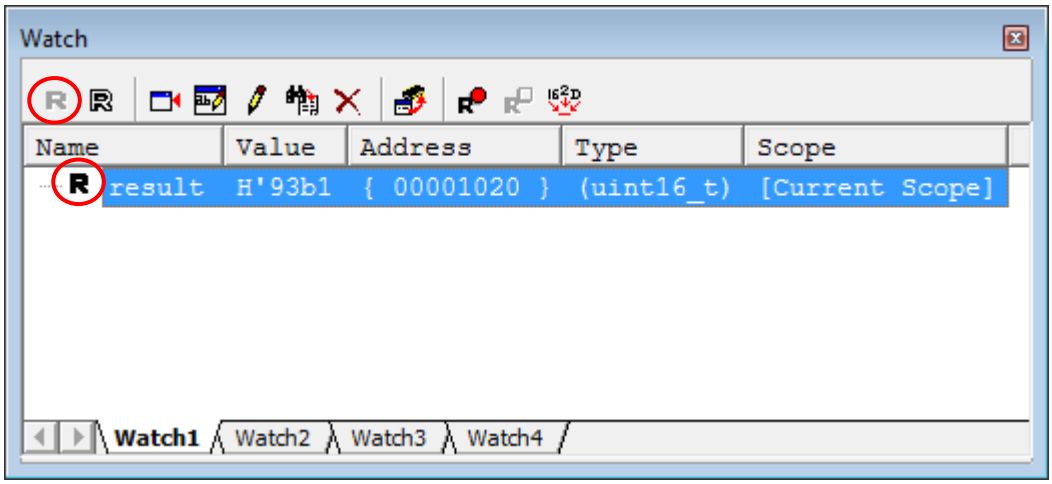
```
//Include "R_PG_<project name>.h"
#include "R_PG_rx630_demo3.h"

void main(void)
{
    //Configure I/O port pins that are not available
    R_PG_IO_PORT_SetPortNotAvailable();

    //Set up the clocks (wait cycle insertion)
    R_PG_Clock_WaitSet(0.01);

    //Set up A/D converter
    R_PG_ADC_12_Set_S12AD0();

    //Start A/D conversion
    R_PG_ADC_12_StartConversionSW_S12AD0();

    while(1);
}

//Variable to store the result
uint16_t result;

//A/D conversion end interrupt notification function
void S12ad0IntFunc(void)
{
    //Get the result of conversion
    R_PG_ADC_12_GetResult_S12AD0(&result);
}
```

(12) Connecting to the emulator, building the program and downloading **HEW**

Build the program and download it. For details on connecting to the emulator, building the program, and downloading refer to section 4.1.1 (13), connecting to the emulator, building the program and executing.

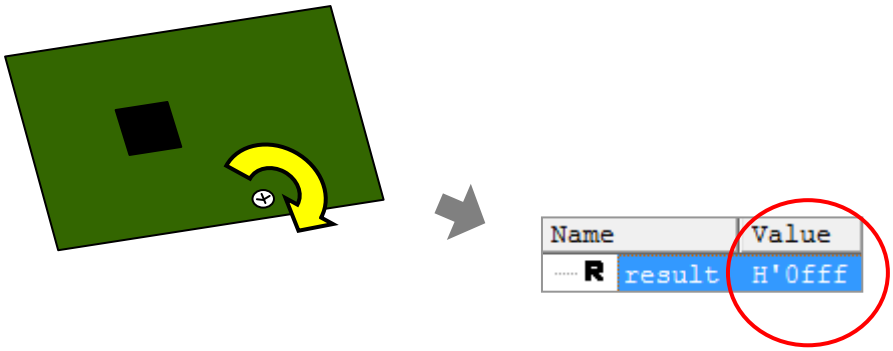
(13) Adding the variable of A/D conversion result to the watch window **HEW**

Open the Watch window and add the variable "result". Set "result" to the real time update to monitor the variable change during execution.



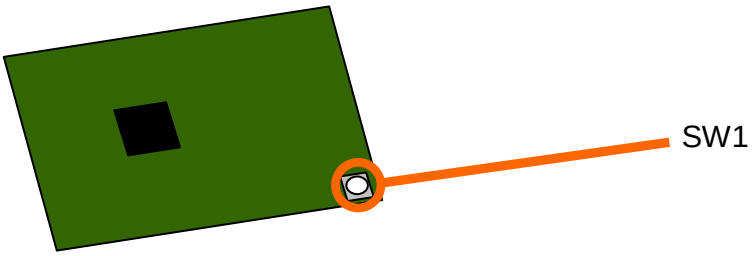
(14) Executing the program and monitoring the A/D conversion result **HEW**

Start the execution and screw the potentiometer to change the analog input voltage. The value of “result” on the watch window will change.



4.1.4 Triggering DTCa by ICUb

In RX630 RSK board, switch 1 (SW1) is connected to IRQ2. In this tutorial, the data transfer controller (DTCa) and ICUb will be set up and DTC transfer triggered by IRQ2 will be performed.



Note : If there is a switch that enables/disables IRQ2 on the RSK board, enable it.

(1) Making the Peripheral Driver Generator project

Make the new Peripheral Driver Generator project “rx630_demo4”. For details on how to make the new Peripheral Driver Generator project, refer to section 4.1.1 (1), Making the Peripheral Driver Generator project.

Set the CPU type as follows.

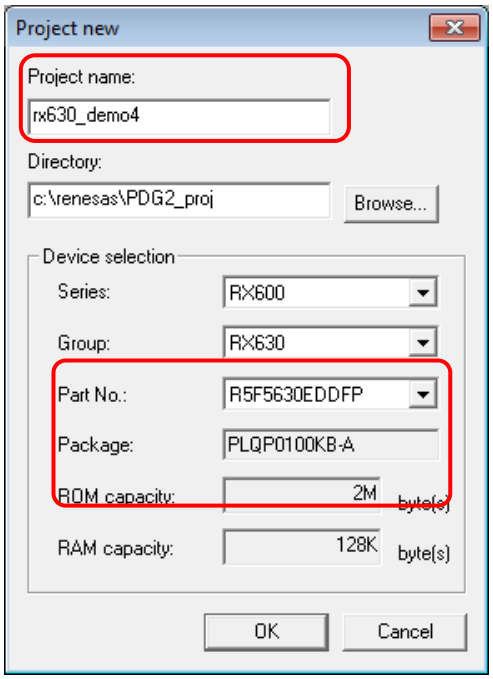
Series : RX600

Group : RX630

Part No. : R5F5630EDDFP

Note: If another type of chip is mounted on your RSK board, select corresponding CPU type.

PDG



(2) Clock setting **PDG**

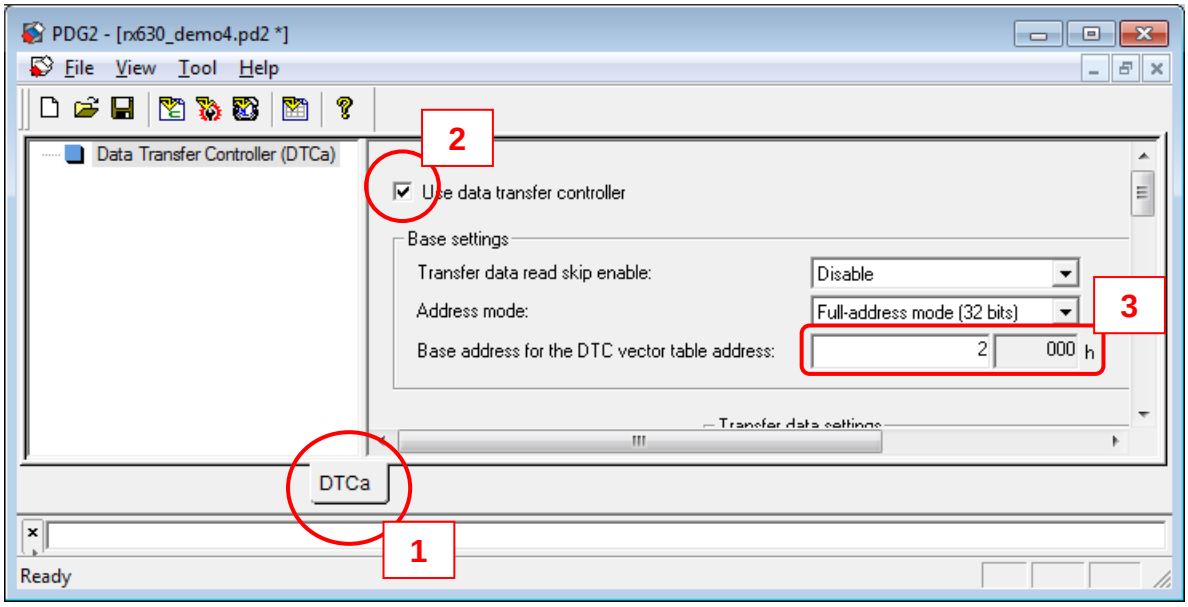
1. The clock setting window opens and the error icons are displayed in the initial state. For icons such as and displayed on window, refer to section 4.1.1 (2), Initial state.
2. For the clock setting, refer to section 4.1.1 (3), Clock setting.

(3) Endian setting **PDG**

For the endian setting, refer to section 3.3, Endian.

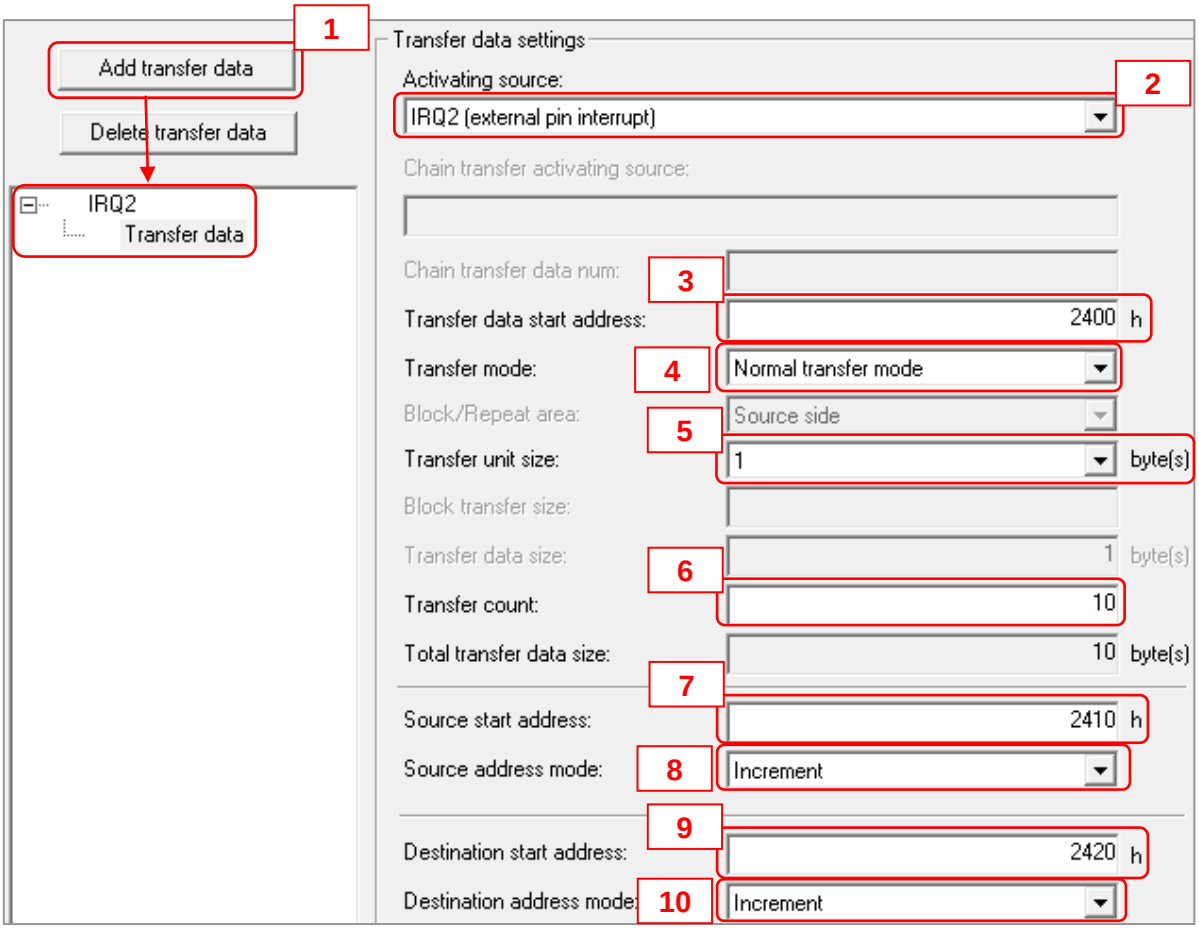
(4) DTCa setting-1 **PDG**

1. Select “DTCa” tab to open the DTCa setting window.
2. Check "Use data transfer controller".
3. The DTCa vector table will be allocated from 2000h. Set “2”.



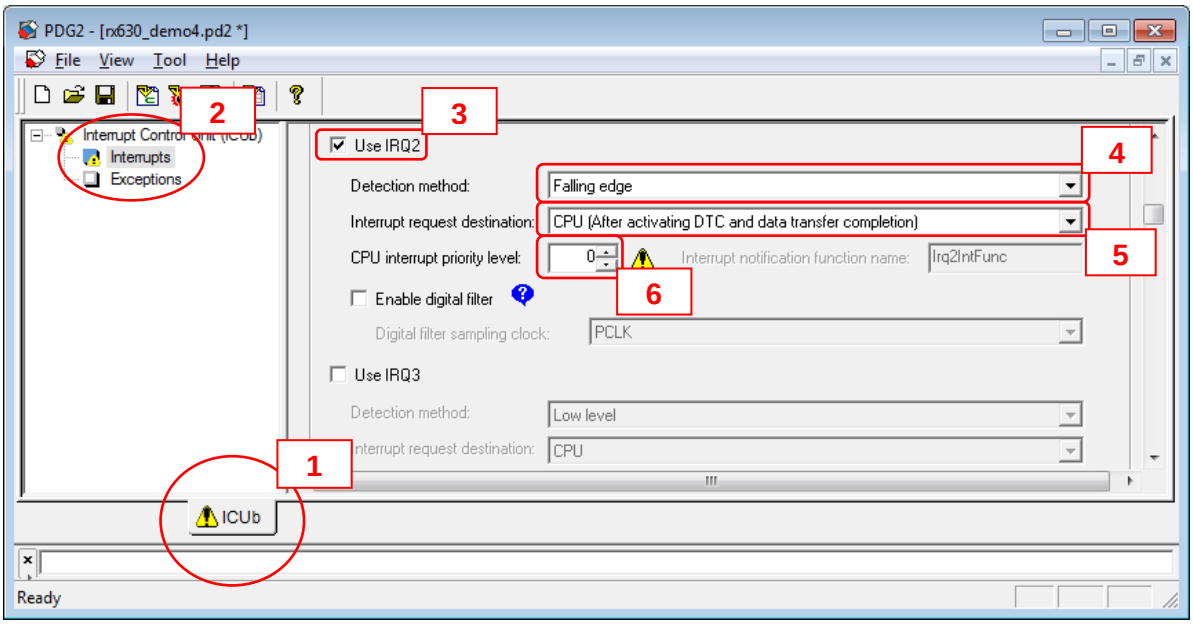
(5) DTCa setting-2 **PDG**

1. Click [Add transfer data] to add the transfer data.
2. Select “IRQ2 (external pin interrupt)” for the activating source.
3. Set “2400” to the transfer data start address.
4. Select “Normal transfer mode” for the transfer mode.
5. Set “1” to the transfer unit size.
6. Set “10” to the transfer count.
7. Set “2410” to the source start address.
8. Select “Increment” for the source address mode.
9. Set “2420” to the destination start address.
10. Select “Increment” for the destination address mode.



(6) ICUb setting **PDG**

1. Select “ICUb” tab to open the ICUb setting window.
2. Click “Interrupts” on the tree view.
3. Check "Use IRQ2".
4. Select “Falling edge” for the detection method of IRQ2.
5. Select “CPU (After activating DTC and data transfer completion)”.
6. CPU interrupt will not be used then set “0” to the CPU interrupt priority level.



(7) Generating source files **PDG**

To generate source files, click  on the tool bar. For details on generating source files, refer to section 4.1.1 (9), Generating source files.

(8) Preparing the High-performance Embedded Workshop project **HEW**

Start the High-performance Embedded Workshop and make RX630 workspace. For details on making High-performance Embedded Workshop project, refer to section 4.1.1 (10), Preparing the High-performance Embedded Workshop project.

PDG

(9) Adding the generated source files to the High-performance Embedded Workshop project

To add the generated source files to High-performance Embedded Workshop, click  on the tool bar. For details on adding the source files to High-performance Embedded Workshop project, refer to section 4.1.1 (11), Adding the generated source files to the High-performance Embedded Workshop project.

(10) Making the program on High-performance Embedded Workshop **HEW**

By changing the part of “main” function, make the following program on High-performance Embedded Workshop.

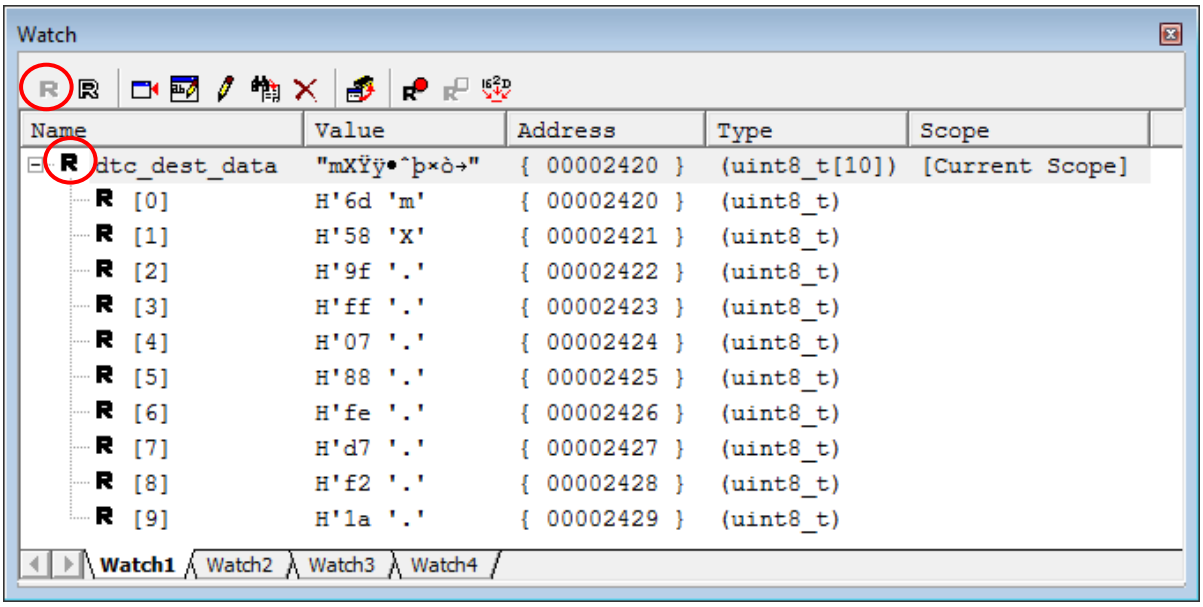
```
//Include "R_PG_<project name>.h"
#include "R_PG_rx630_demo4.h"

//DTC vector table
#pragma address dtc_vector_table = 0x00002000
uint32_t dtc_vector_table [256];

//DTC transfer data storage area (IRQ2)
#pragma address dtc_transfer_data_IRQ2 = 0x00002400
uint32_t dtc_transfer_data_IRQ2 [4];

//Transfer source
#pragma address dtc_src_data = 0x00002410
uint8_t dtc_src_data [10] = "ABCDEFGHIJ";

//Transfer destination
#pragma address dtc_dest_data = 0x00002420
uint8_t dtc_dest_data [10];

void main(void)
{
      //initialize transfer destination
      int i;
      for(i=0; i<10; i++ ){
            dtc_dest_data[i] = 0;
      }

      //Configure I/O port pins that are not available
      R_PG_IO_PORT_SetPortNotAvailable();

      R_PG_Clock_WaitSet(0.01);    //Set up the clocks (wait cycle insertion)

      // Set up the DTC (e.g. vector table address)
      R_PG_DTC_Set();

      // Set up the DTC (transfer data of IRQ2)
      R_PG_DTC_Set_IRQ2();

      R_PG_ExtInterrupt_Set_IRQ2();    // Set up IRQ2

      R_PG_DTC_Activate();    // Make the DTC be ready to the trigger
      while(1);
}
```

(11) Connecting to the emulator, building the program and downloading **HEW**

Build the program and download it. For details on connecting to the emulator, building the program, and downloading refer to section 4.1.1 (13), connecting to the emulator, building the program and executing.

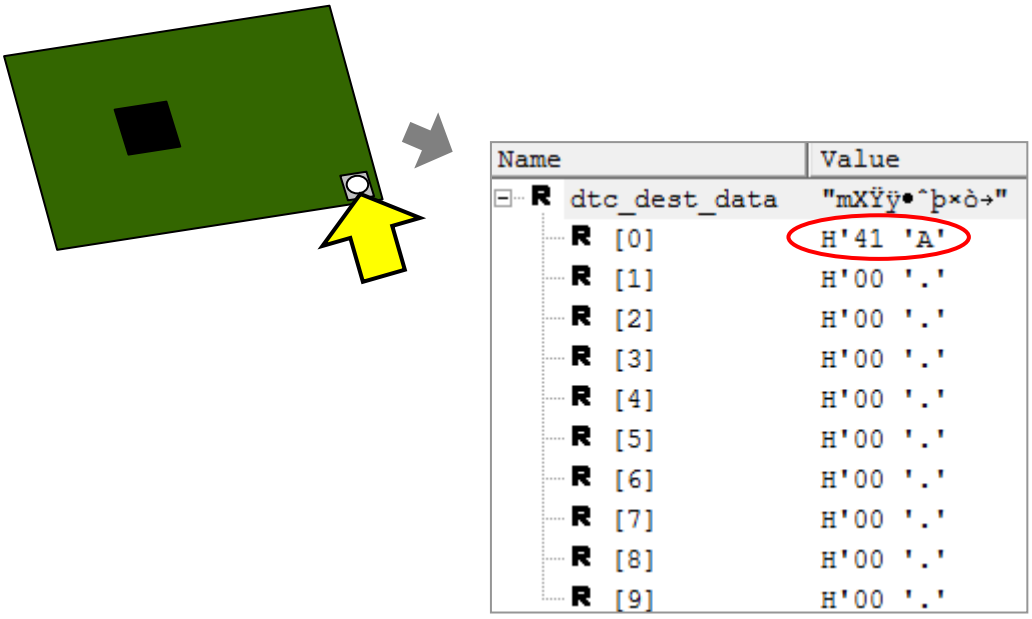
(12) Adding the variable of the transfer destination **HEW**

Open the Watch window and add the variable "dtc_dest_data". Expand the array and set it to the real time update to monitor the variable change during execution.



(13) Executing the program and monitoring the result of the transfer **HEW**

Start the execution and push the SW1. The value of “dtc_dest_data” on the watch window will change.

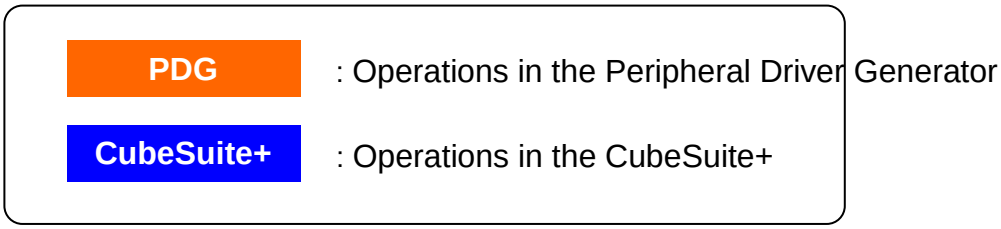


4.2 When the CubeSuite+ is in Use

This section introduces the usage of the Peripheral Driver Generator by giving instructions on how to use the Peripheral Driver Generator and CubeSuite+ to create a tutorial program that implements the following operations on the Renesas Starter Kit board for the RX630.

・ An LED blinking on Compare Match Timer (CMT) interrupt

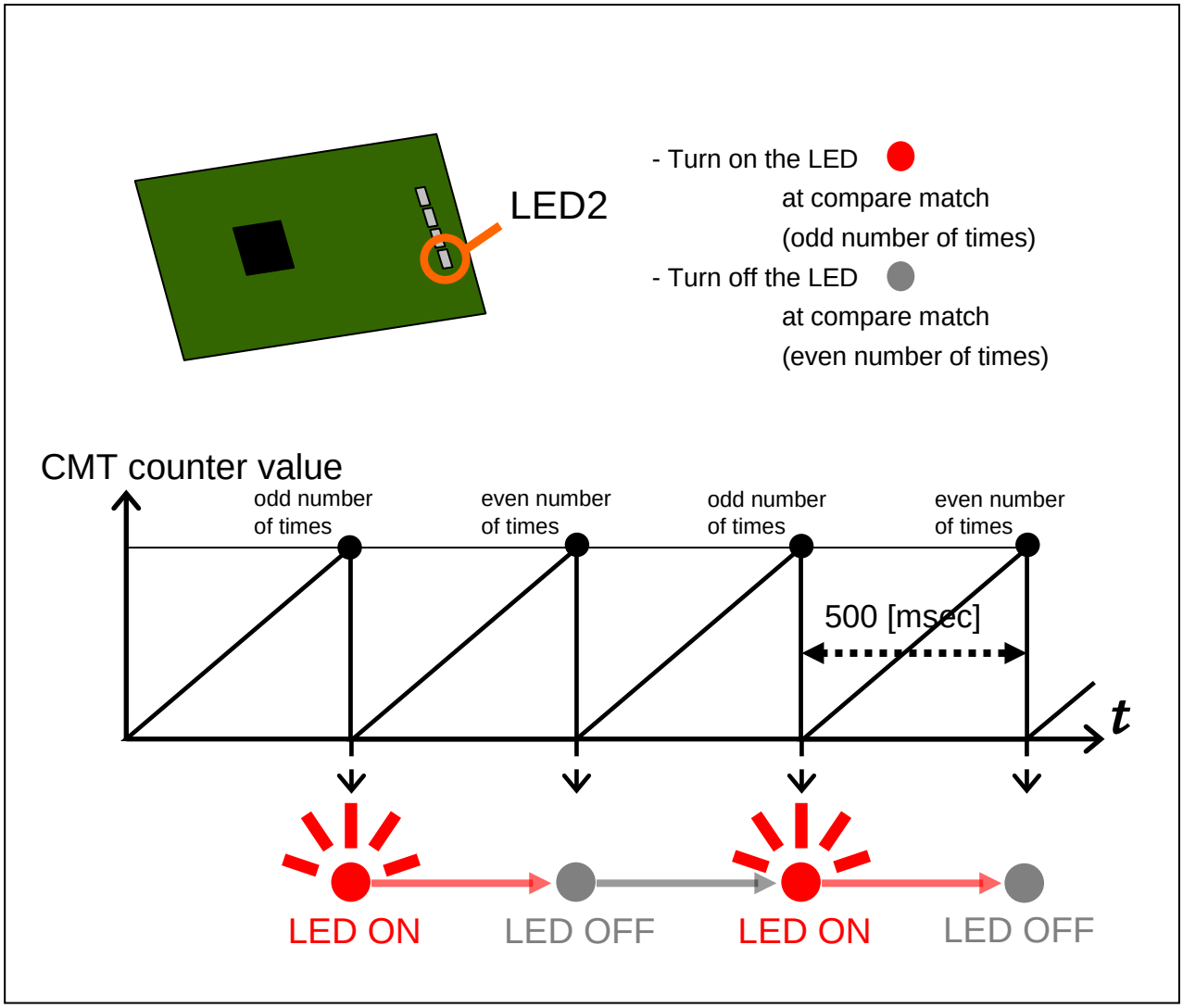
The labels given below respectively indicate operations to take place in the Peripheral Driver Generator and in the CubeSuite+.



4.2.1 An LED blinking on Compare Match Timer (CMT) interrupt

The LED2 on RSK board is connected to PC2. In this tutorial, Compare Match Timer and I/O port will be set up to blink this LED as follows.

Note : If there is a switch that enables/disables PC2 on the RSK board, enable it.



PDG

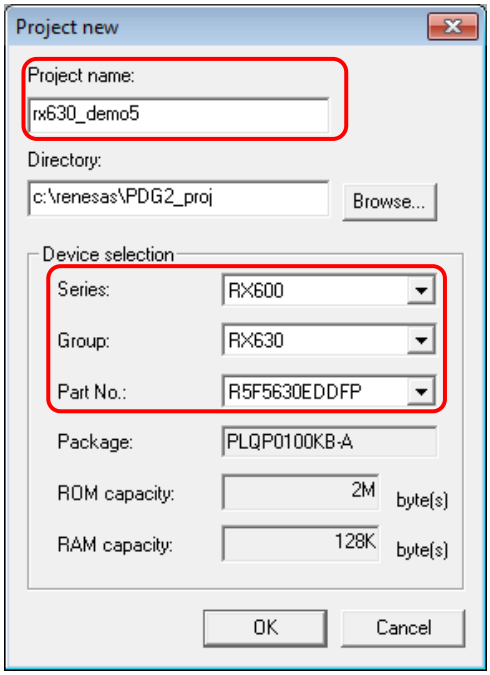
(1) Making the Peripheral Driver Generator project

Make the new Peripheral Driver Generator project “rx630_demo5”. For details on how to make the new Peripheral Driver Generator project, refer to section 4.1.1 (1), Making the Peripheral Driver Generator project.

Set the CPU type as follows.

Series : RX600
Group : RX630
Part No. : R5F5630EDDFP

Note: If another type of chip is mounted on your RSK board, select corresponding CPU type.



(2) Clock setting PDG

1. The clock setting window opens and the error icons are displayed in the initial state. For icons such as and displayed on window, refer to section 4.1.1 (2), Initial state.
2. For the clock setting, refer to section 4.1.1 (3), Clock setting.

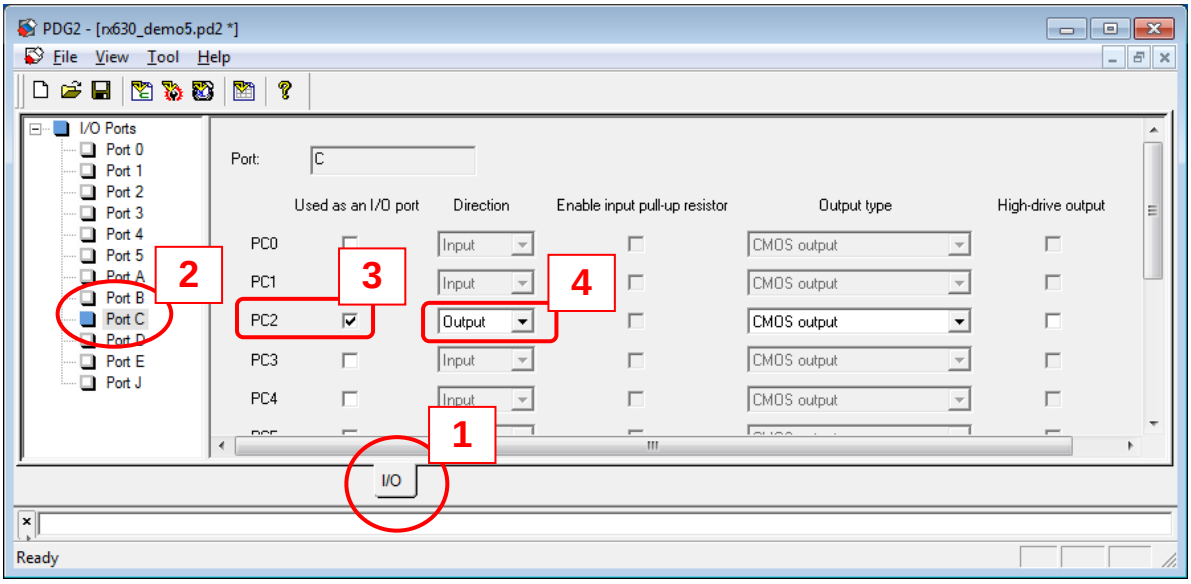
(3) Endian setting PDG

For the endian setting, refer to section 3.3, Endian.

(4) I/O Port setting PDG

The LED2 on RSK is connected to PC2 so set PC2 to output port.

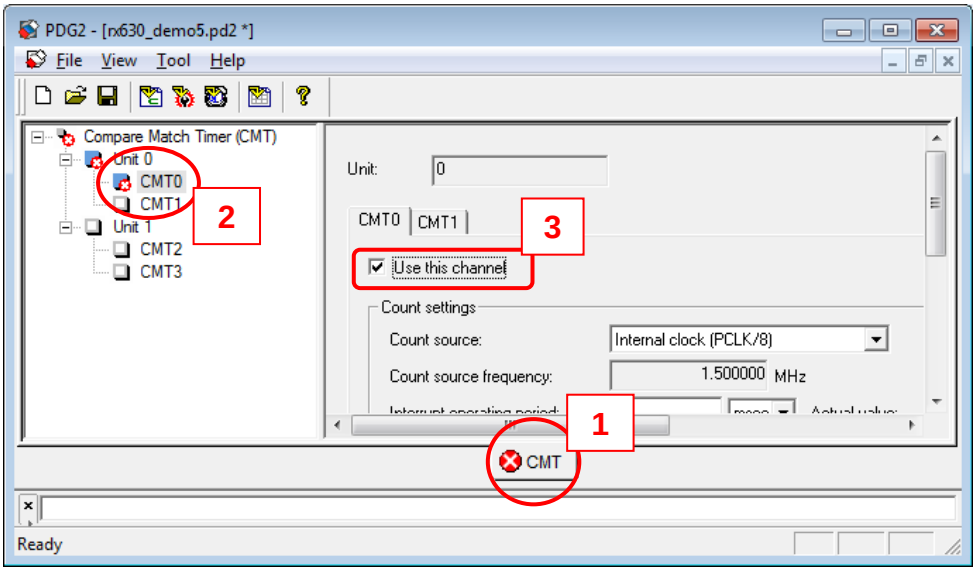
1. Select “I/O” tab
2. Select “Port C”
3. Check “PC2”
4. Select “Output”



(5) CMT setting-1 PDG

In this tutorial, CMT0 is used.

1. Select "CMT" tab
2. Select "CMT0"
3. Check "Use this channel"

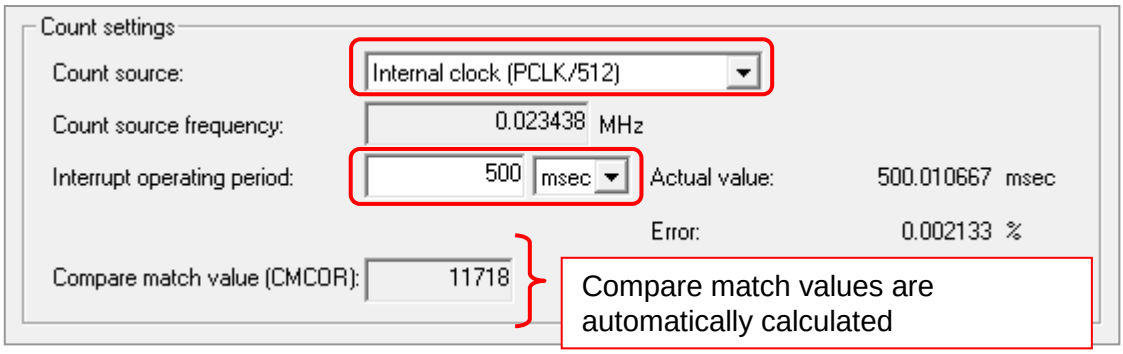


(6) CMT setting-2 PDG

Set the other items as follows.

-Count source: Internal clock(PCLK/512)

-Interrupt operating period: 500 msec



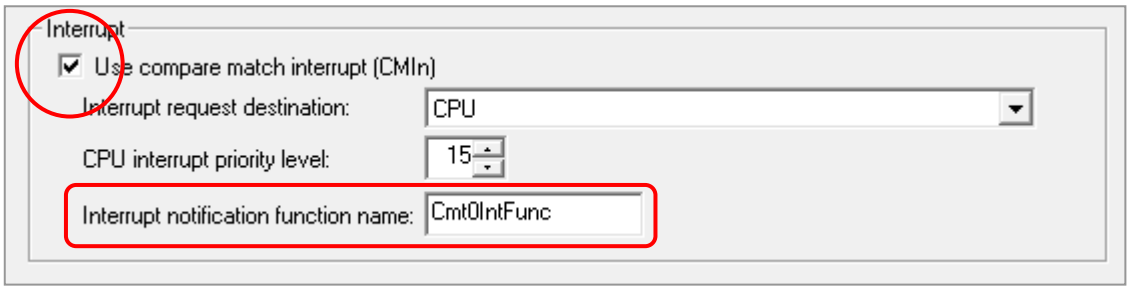
(7) CMT setting-3 PDG

Set the interrupt notification functions.

This functions are called when the interrupt occurs.

-Check “Use compare match interrupt (CMIn)”

Notification function name is "Cmt0IntFunc"



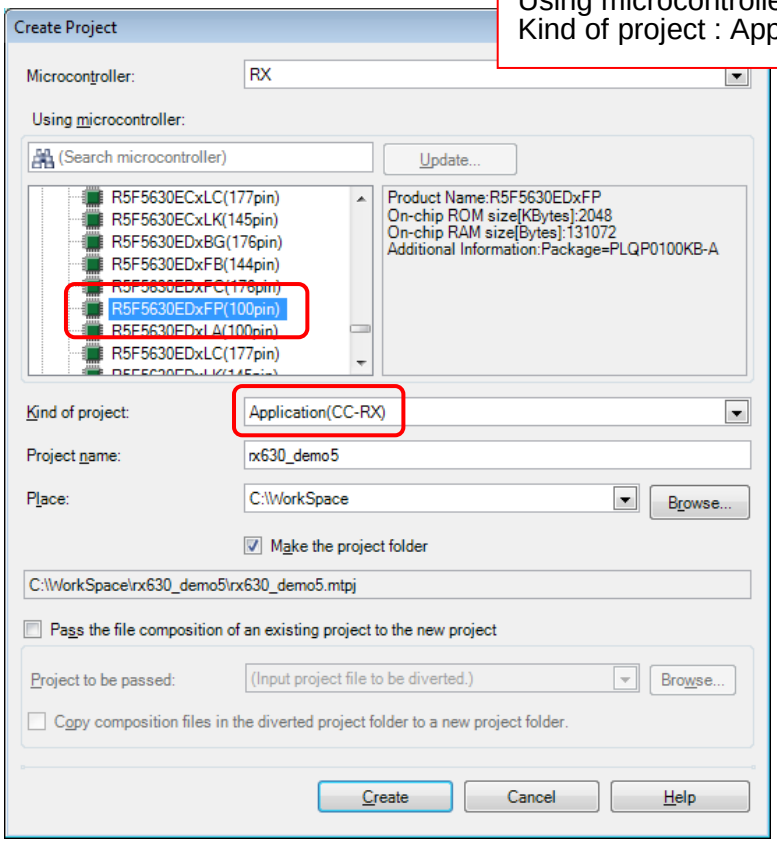
(8) Generating source files PDG

To generate source files, click  on the tool bar. For details on generating source files, refer to section 4.1.1 (9), Generating source files.

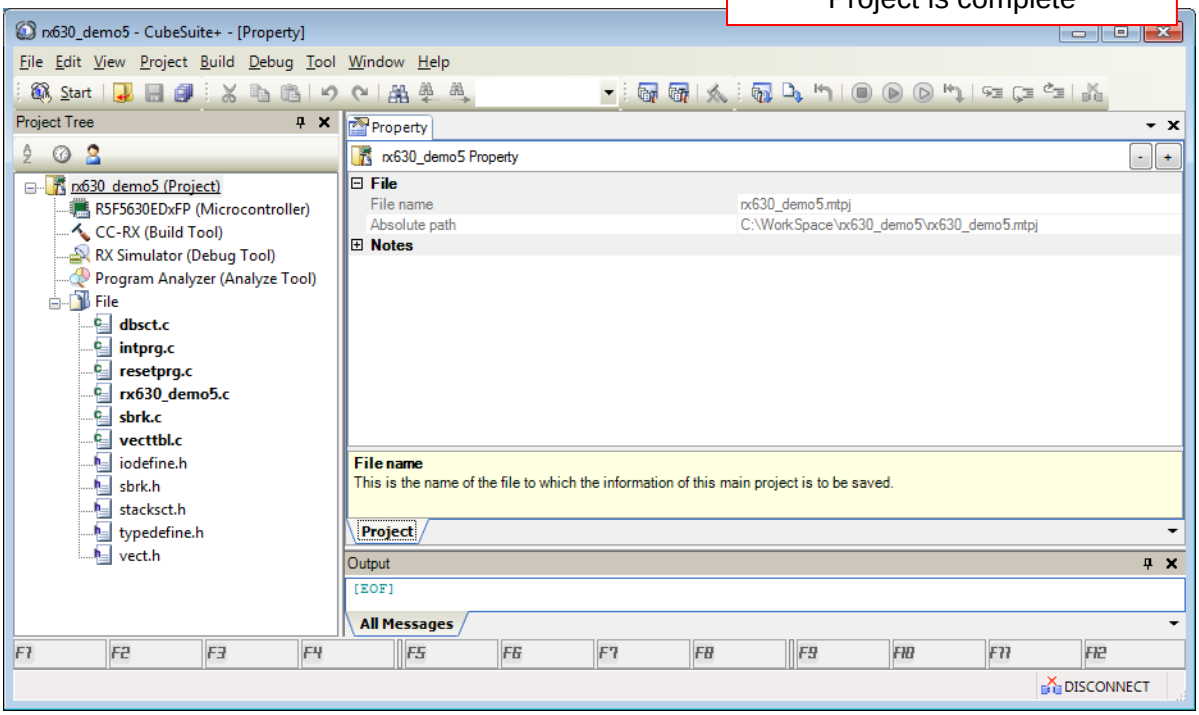
(9) Preparing the CubeSuite+ project **CubeSuite+**

Start the CubeSuite+ and make RX630 workspace.

Using microcontroller : R5F5630EdxFP(100pin)
Kind of project : Application(CC-RX)



Project is complete

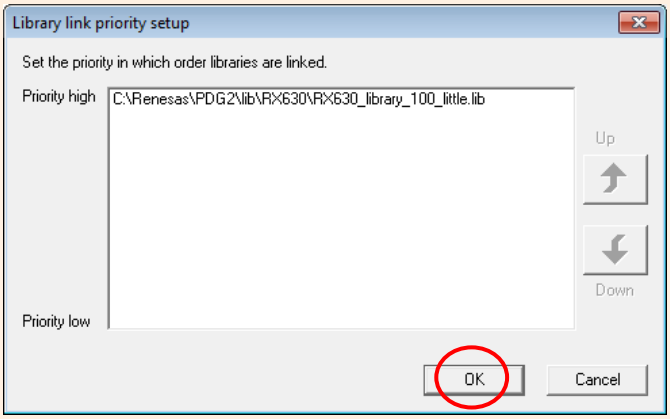


(10) Adding the generated source files to the CubeSuite+ project

PDG

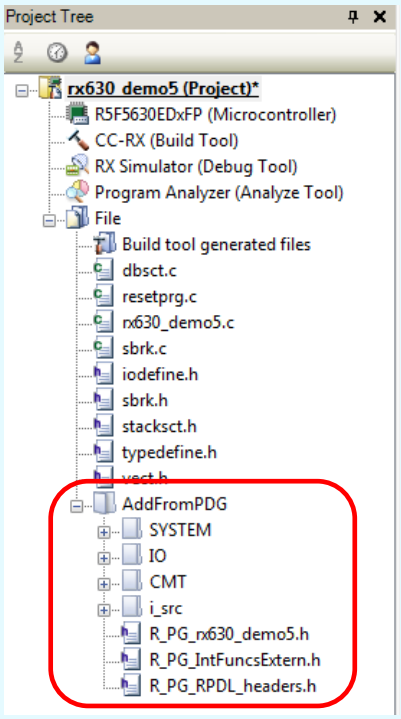
1. To add source files to CubeSuite+, click  on the tool bar.

2. This is a linkage setting of Renesas Peripheral Driver Library.
When using multiple lib files, linkage order can be set in this dialog box.



CubeSuite+

3. Source files are added to CubeSuite+.
Added source files are put in "AddFromPDG" category.



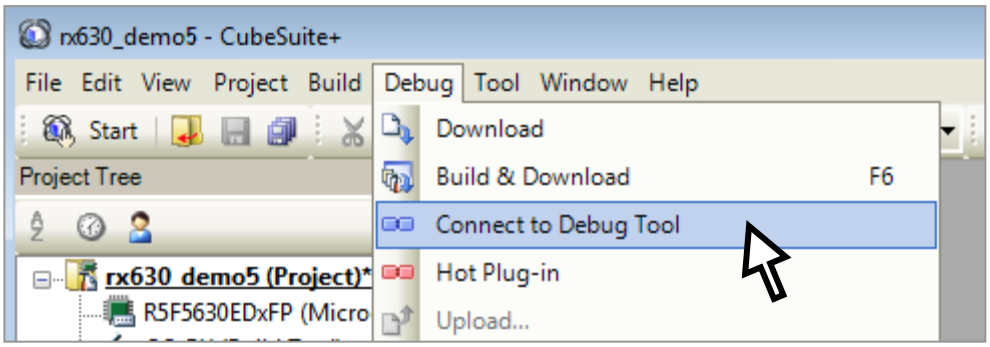
(11) Making the program on CubeSuite+ **CubeSuite+**

By changing the part of “main” function, make the following program on CubeSuite+.

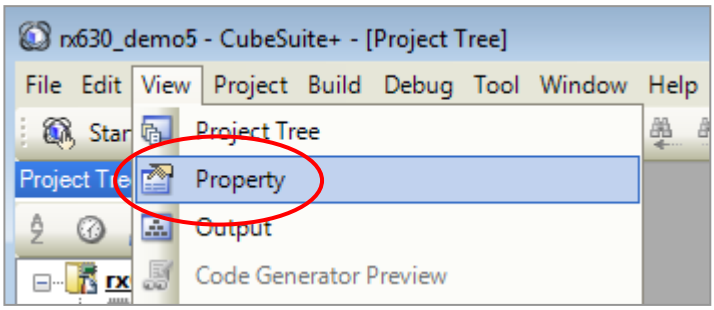
```
//Include "R_PG_<project name>.h"
#include "R_PG_rx630_demo5.h"

bool led=false;

void main(void)
{
	//Configure I/O port pins that are not available
	R_PG_IO_PORT_SetPortNotAvailable();

	//Set up the clocks (wait cycle insertion)
	R_PG_Clock_WaitSet(0.01);

	//Set up port PC2
	R_PG_IO_PORT_Write_PC2(1);	//Initial output value
	R_PG_IO_PORT_Set_PC2();

	//Set up the CMT
	R_PG_Timer_Set_CMT_U0_C0();

	//Start the CMT count
	R_PG_Timer_StartCount_CMT_U0_C0();

	while(1);
}

// Compare match interrupt notification function
void Cmt0IntFunc(void)
{
	if( led ){
		//Turn off the LED
		R_PG_IO_PORT_Write_PC2(1);
		led = false;
	}
	else{
		//Turn on the LED
		R_PG_IO_PORT_Write_PC2(0);
		led = true;
	}
}
```

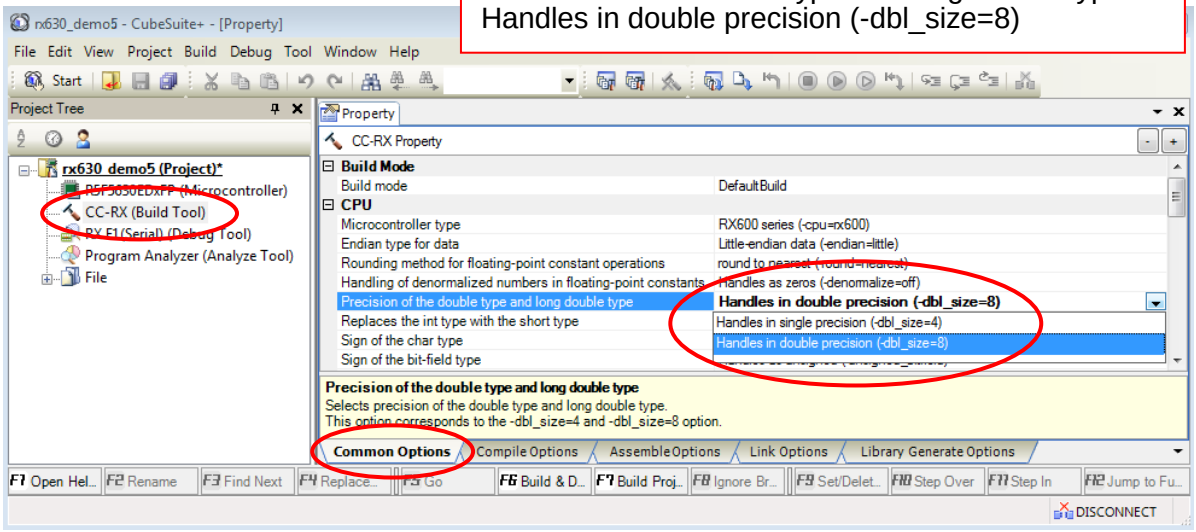
(12) Connecting to the emulator, building the program, downloading and executing **CubeSuite+**

1. Connect to the emulator.



2. Configure the option setting and build the program.





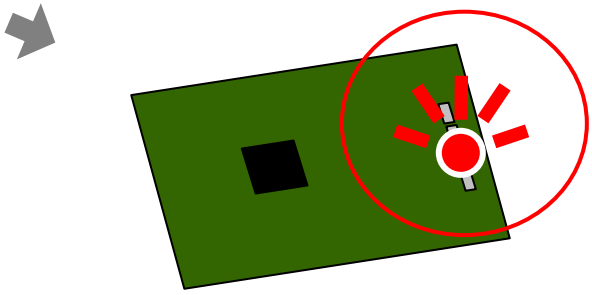
Build button

3. Download the program.

Download button

4. Execute the program and see the LED on RSK board.

Reset go button

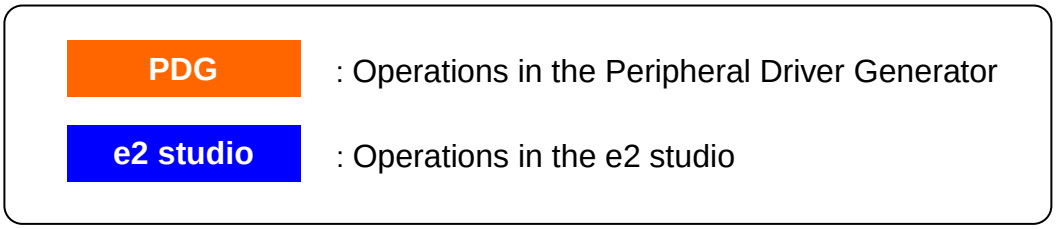


4.3 When the e2 studio is in Use

This section introduces the usage of the Peripheral Driver Generator by giving instructions on how to use the Peripheral Driver Generator and e2 studio to create a tutorial program that implements the following operations on the Renesas Starter Kit board for the RX630.

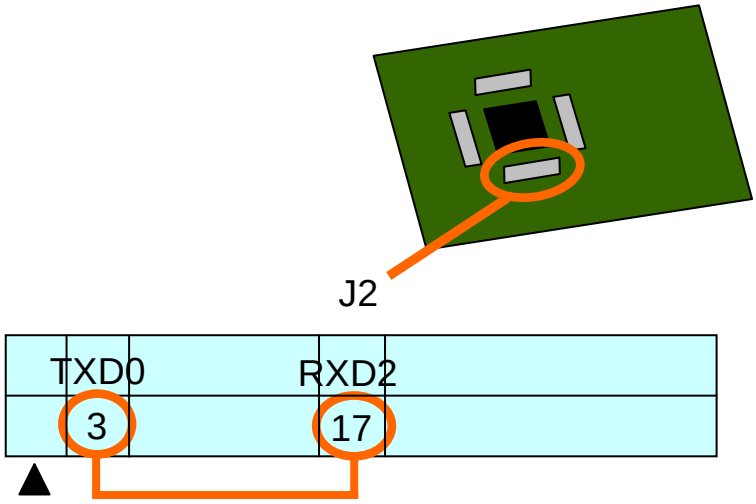
- Data transfer between SCIc channels 0 and 2

The labels given below respectively indicate operations to take place in the Peripheral Driver Generator and in the CubeSuite+.



4.3.1 Data transfer between SCIc channels 0 and 2

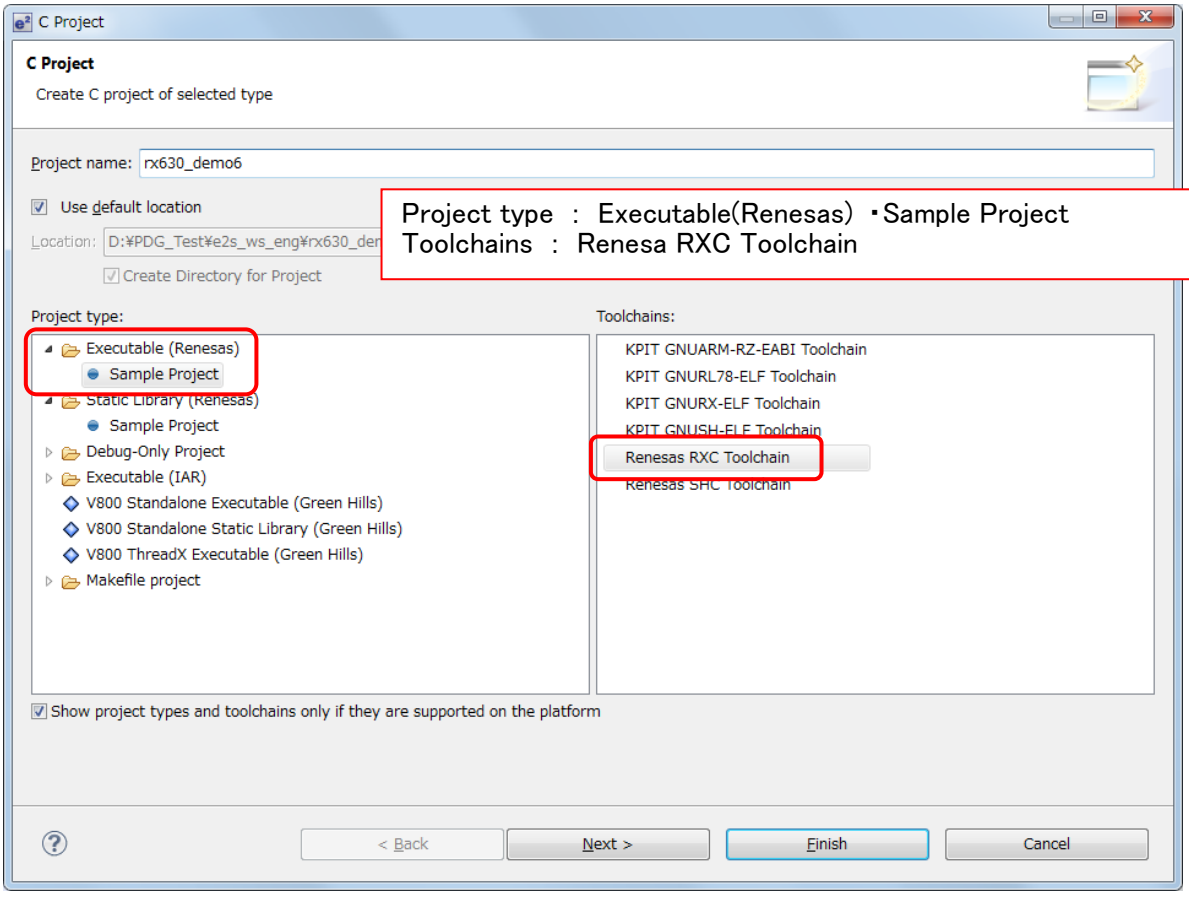
In this tutorial, SCI channel 0 and 2 will be set up to transfer data in asynchronous mode. Connect the transmission pin of channel 0 (TXD0) and the reception pin of channel 2 (RXD2) on the RSK board as follows.

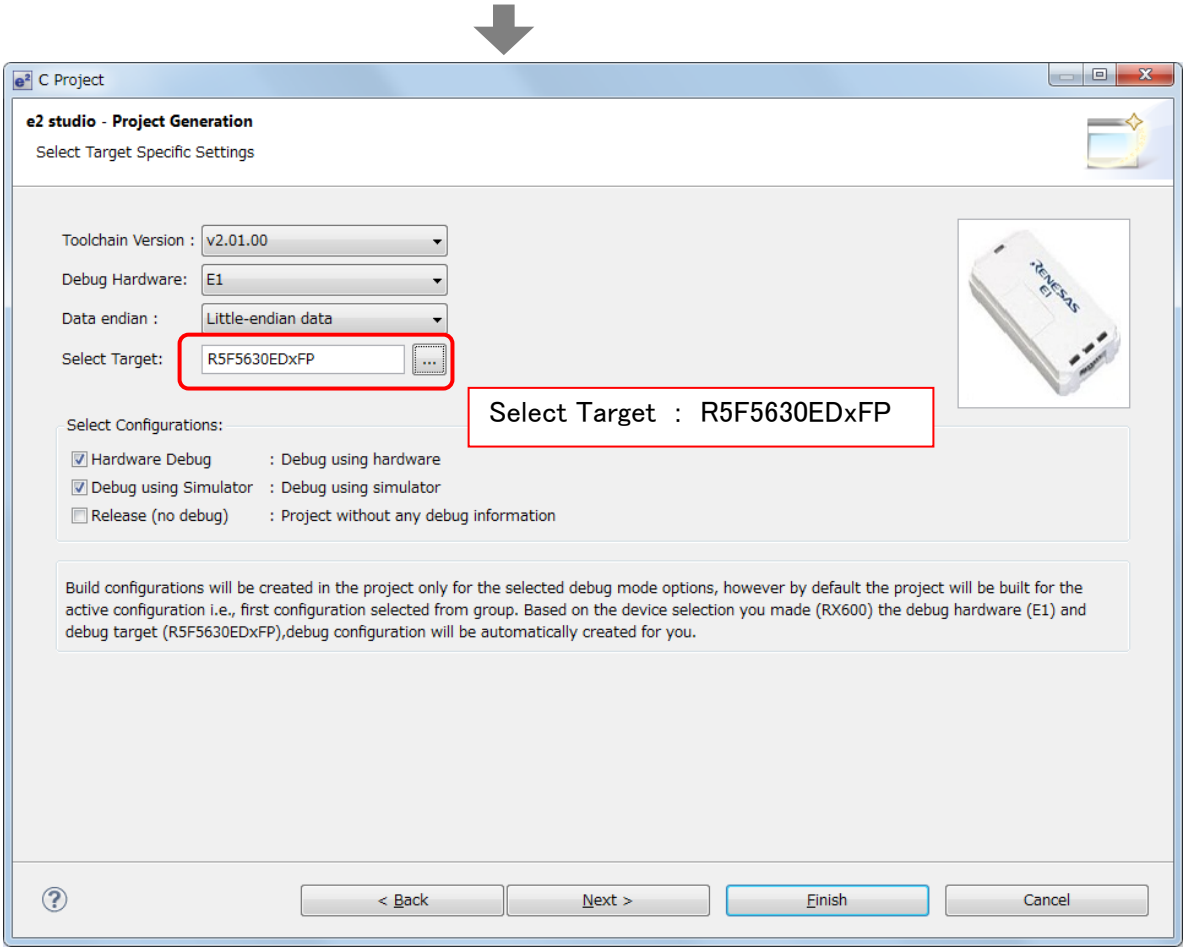


Note : If there are switches that enables/disables TXD0 and RXD2 on the RSK board, enable it.

(1) Preparing the e2 studio project **e2 studio**

Start the e2 studio and make RX630 workspace.




C Project
e2 studio - Project Generation
Select Target Specific Settings
Toolchain Version : v2.01.00
Debug Hardware: E1
Data endian : Little-endian data
Select Target: R5F5630EDxFP
Select Target : R5F5630EDxFP
Select Configurations:
Hardware Debug : Debug using hardware
Debug using Simulator : Debug using simulator
Release (no debug) : Project without any debug information
Build configurations will be created in the project only for the selected debug mode options, however by default the project will be built for the active configuration i.e., first configuration selected from group. Based on the device selection you made (RX600) the debug hardware (E1) and debug target (R5F5630EDxFP),debug configuration will be automatically created for you.
< Back
Next >
Finish
Cancel


C/C++ - e2 studio
File Edit Source Refactor Navigate Search Project Run Window Help
Welcome
Welcome
e2studio
Project is complete
RENESAS

(2) Making the Peripheral Driver Generator project

PDG

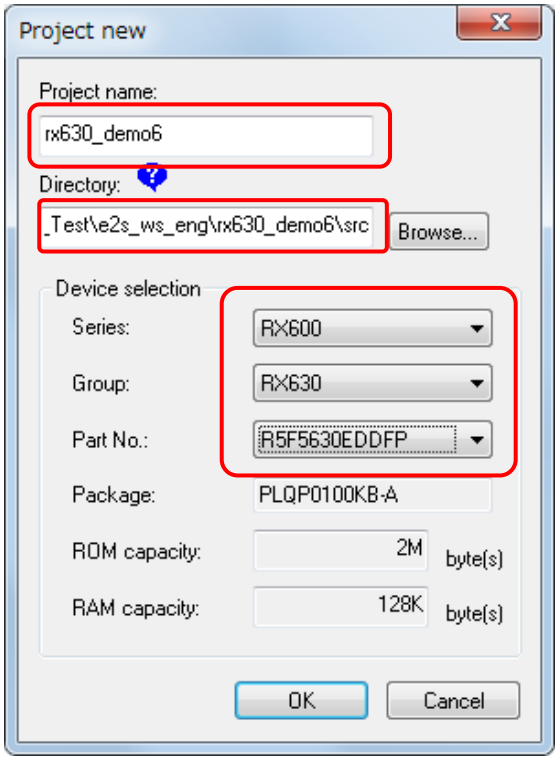
Make the new Peripheral Driver Generator project “rx630_demo6”. For details on how to make the new Peripheral Driver Generator project, refer to section 4.1.1 (1), Making the Peripheral Driver Generator project.

Set the CPU type as follows.

Series : RX600
Group : RX630
Part No. : R5F5630EDDFP

Note1: If another type of chip is mounted on your RSK board, select corresponding CPU type.

Note2: When making them cooperate with e2 studio, please choose the hierarchy below the src folder of a project of e2 studio by designation of a directory.



(3) Clock setting **PDG**

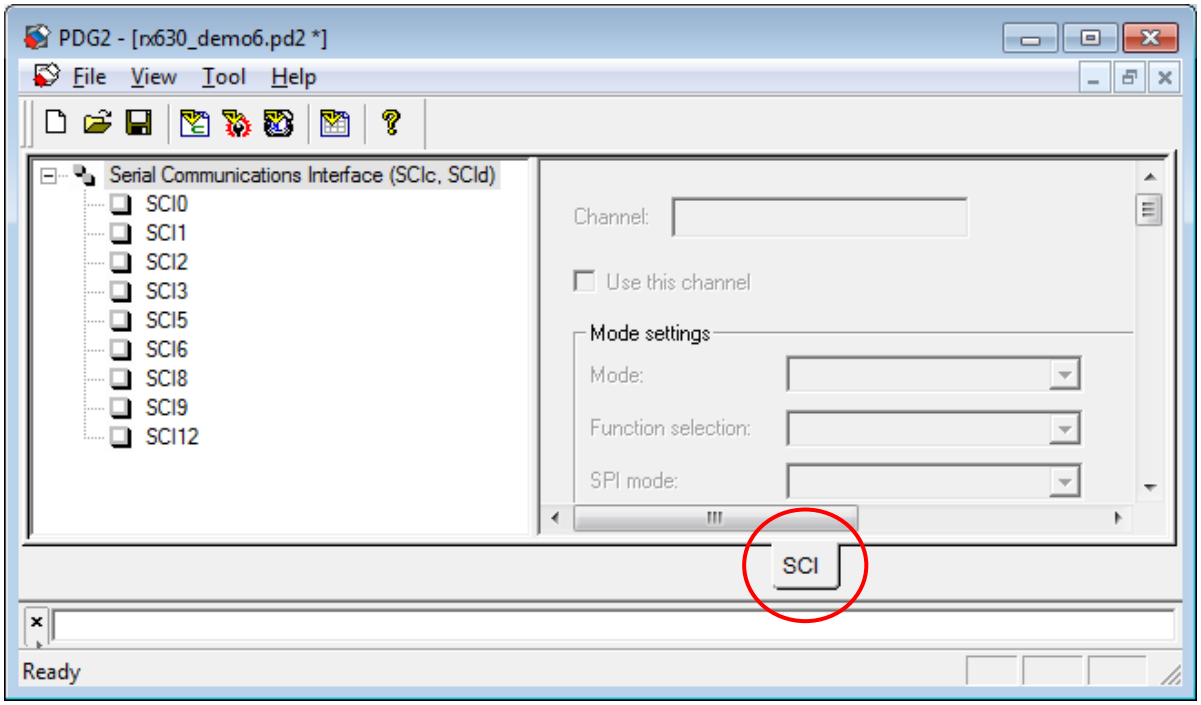
1. The clock setting window opens and the error icons are displayed in the initial state. For icons such as and displayed on window, refer to section 4.1.1 (2), Initial state.
2. For the clock setting, refer to section 4.1.1 (3), Clock setting.

(4) Endian setting **PDG**

For the endian setting, refer to section 3.3, Endian.

(5) SCIc setting **PDG**

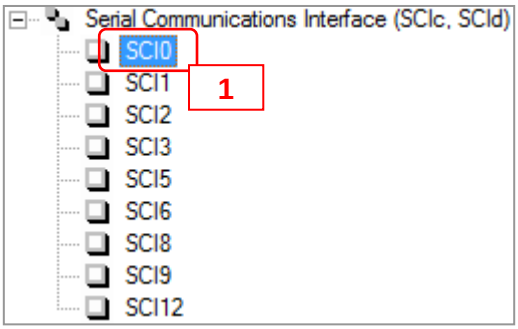
Select “SCI” tab to open the SCIc setting window.



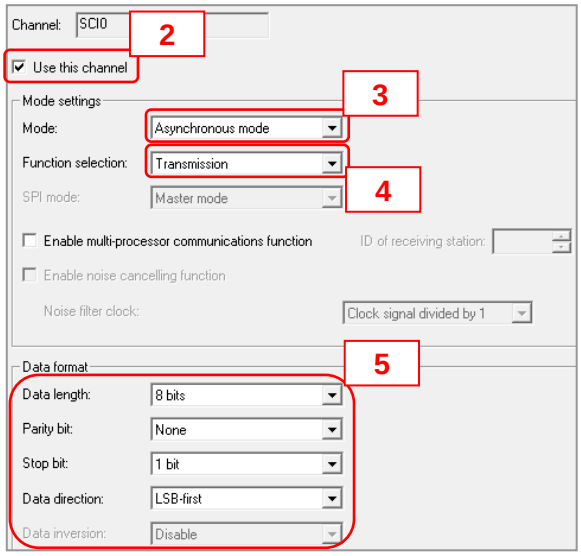
(6) SCI0 (transmitter) setting **PDG**

Make the setting for SCI0 as follows.

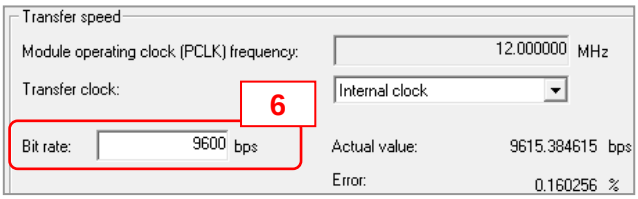
1. Select SCI0 on the tree view.



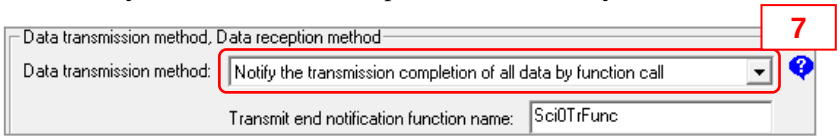
2. Check “Use this channel”.
3. Select “Asynchronous mode”.
4. Select “Transmission” for the function.
5. Leave the data format settings at the default.



6. Set the bit rate to “9600bps”.



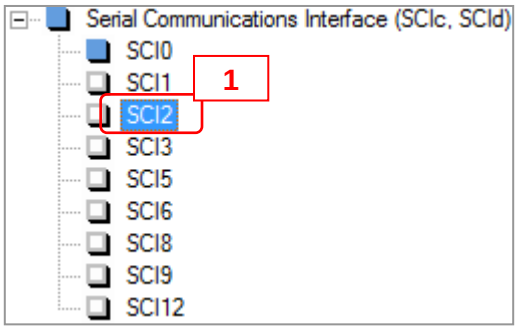
7. Select “Notify the transmission completion of all data by function call” for the data transmission method.



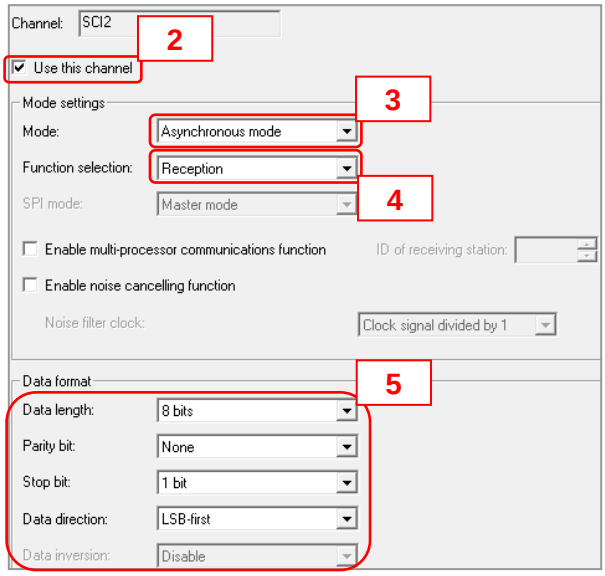
(7) SCI2 (receptor) setting **PDG**

Make the setting for SCI2 as follows.

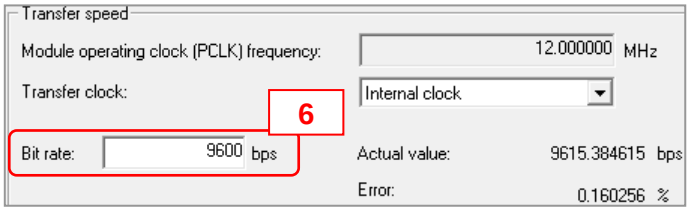
1. Select SCI2 on the tree view.



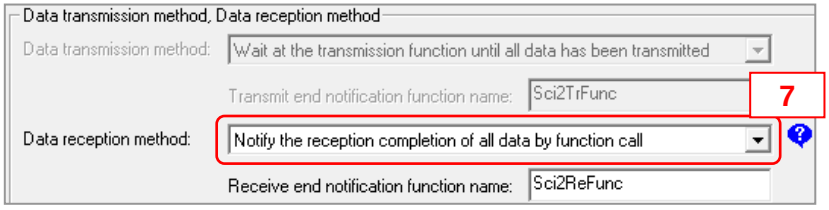
2. Check “Use this channel”.
3. Select “Asynchronous mode”.
4. Select “Reception” for the function.
5. Leave the data format settings at the default.



6. Set the bit rate to “9600bps”.



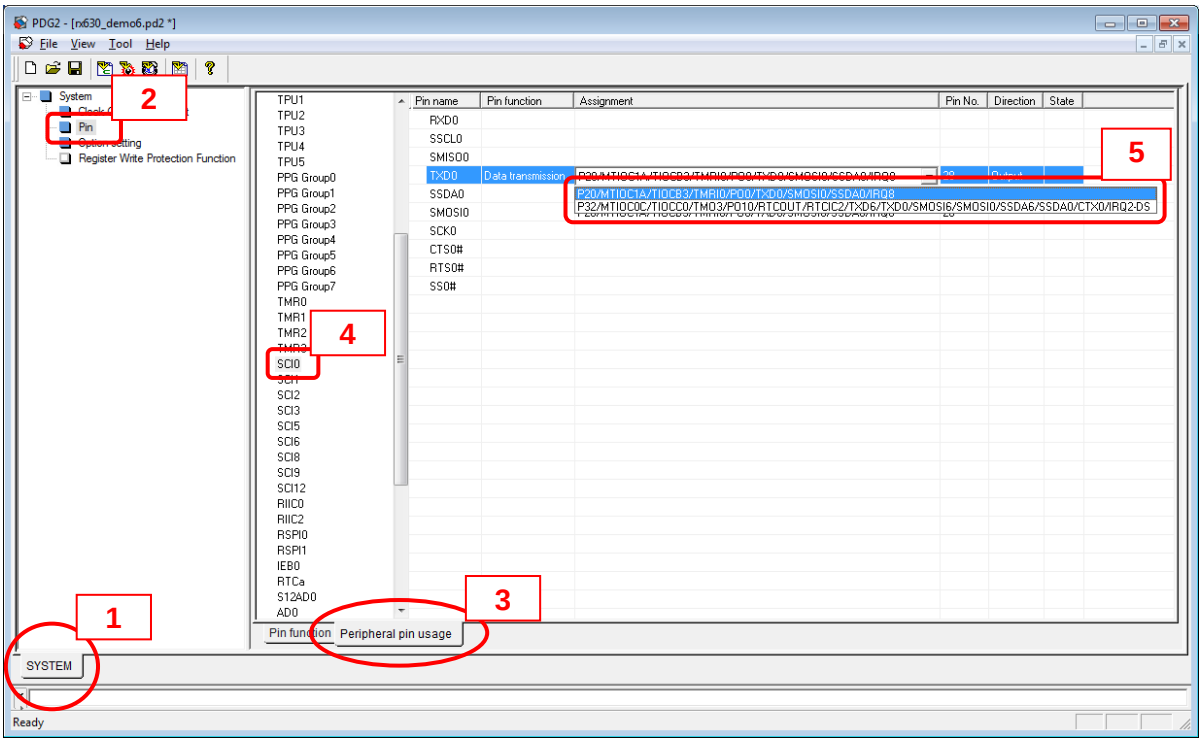
7. Select “Notify the reception completion of all data by function call” for the data reception method.



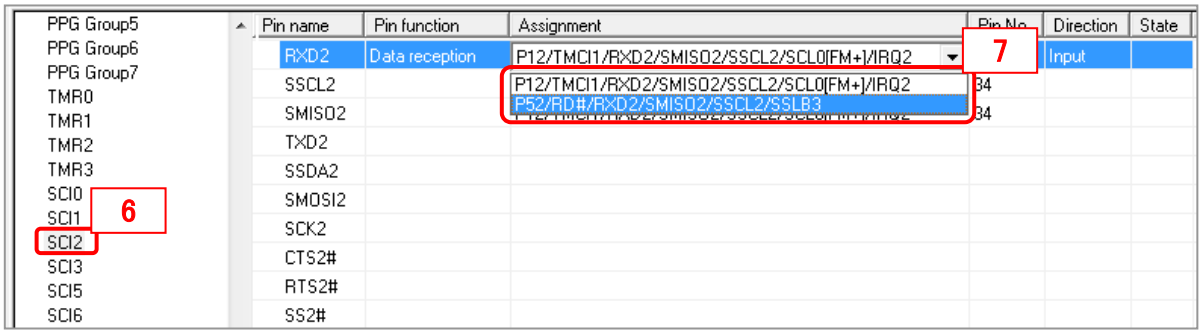
(8) Pin setting **PDG**

TXD0 and RXD2 can be selected the pin function assignment. Select the pin function assignment as follows.

1. Select “SYSTEM” tab.
2. Select “Pin” on tree view.
3. Select “Peripheral pin usage” tab.
4. Select “SCI0” from the peripheral module list.
5. When the mouse pointer is placed on “Assignment” column of TXD0 line, a dropdown button is displayed. Select “P20/MTIOC1A/TIOCB3/TMRI0/PO0/TXD0/SMOSI0/SSDA0/IRQ8” from the dropdown list.



6. Select “SCI2” from the peripheral module list.
7. When the mouse pointer is placed on “Assignment” column of RXD2 line, a dropdown button is displayed. Select “P52/RD#/RXD2/SMISO2/SSCL2/SSLB3” from the dropdown list.



(9) Generating source files **PDG**

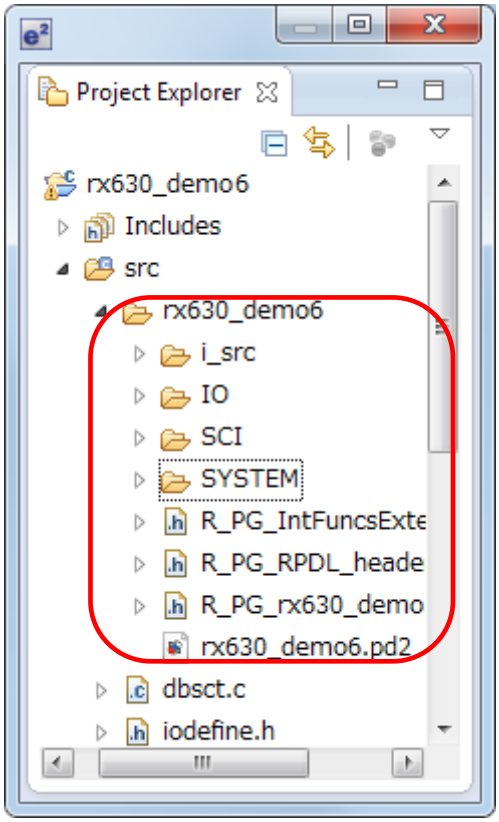
To generate source files, click  on the tool bar. For details on generating source files, refer to section 4.1.1 (9), Generating source files.

(10) Adding the generated source files to the e2 studio project **PDG**

To set a build property of e2 studio, click  on the tool bar. A project is established besides the registration of a file. Please refer to "6 About registration to IDE of a generation file" about setting of a project.

A file is added to the project of e2 studio.
An added file is registered by a folder image of a generation source of Peripheral Driver Generator.

e2 studio



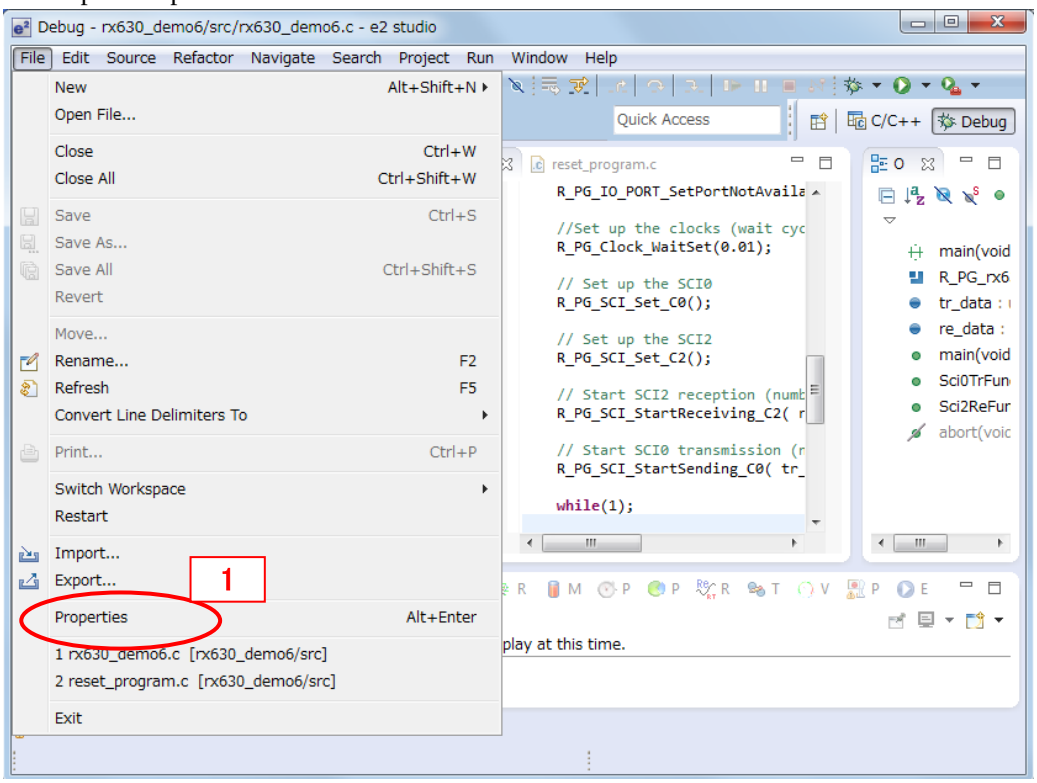
(11) Making the program on e2 studio **e2 studio**

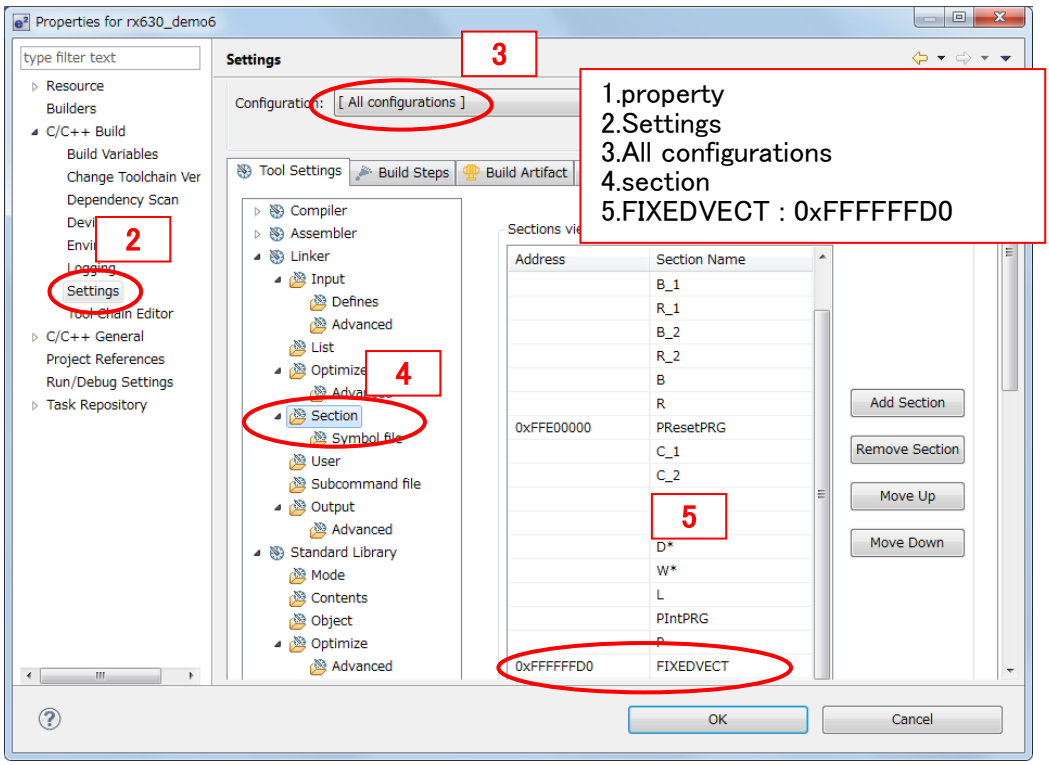
By changing the part of “main” function, make the following program on e2 studio.

```
//Include "R_PG_<project name>.h"
#include "R_PG_rx630_demo6.h"

//SCI0 transmission data
uint8_t tr_data[10] = "ABCDEFGHIJ";

//SCI2 reception data storage area
uint8_t re_data[10] = "----------";

void main(void)
{
      //Configure I/O port pins that are not available
      R_PG_IO_PORT_SetPortNotAvailable();

      //Set up the clocks (wait cycle insertion)
      R_PG_Clock_WaitSet(0.01);

      // Set up the SCI0
      R_PG_SCI_Set_C0();

      // Set up the SCI2
      R_PG_SCI_Set_C2();

      // Start SCI2 reception (number of data : 10)
      R_PG_SCI_StartReceiving_C2( re_data, 10 );

      // Start SCI0 transmission (number of data : 10)
      R_PG_SCI_StartSending_C0( tr_data, 10 );

      while(1);
}

//SCI0 transmission end notification function
void Sci0TrFunc(void)
{
      //Stop SCI0 communication
      R_PG_SCI_StopCommunication_C0();
}

//SCI2 reception end notification function
void Sci2ReFunc(void)
{
      //Stop SCI2 communication
      R_PG_SCI_StopCommunication_C2();
}
```

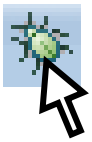
(12) Connecting to the emulator, building the program and downloading **e2 studio**

1.Set options options and execute a build.



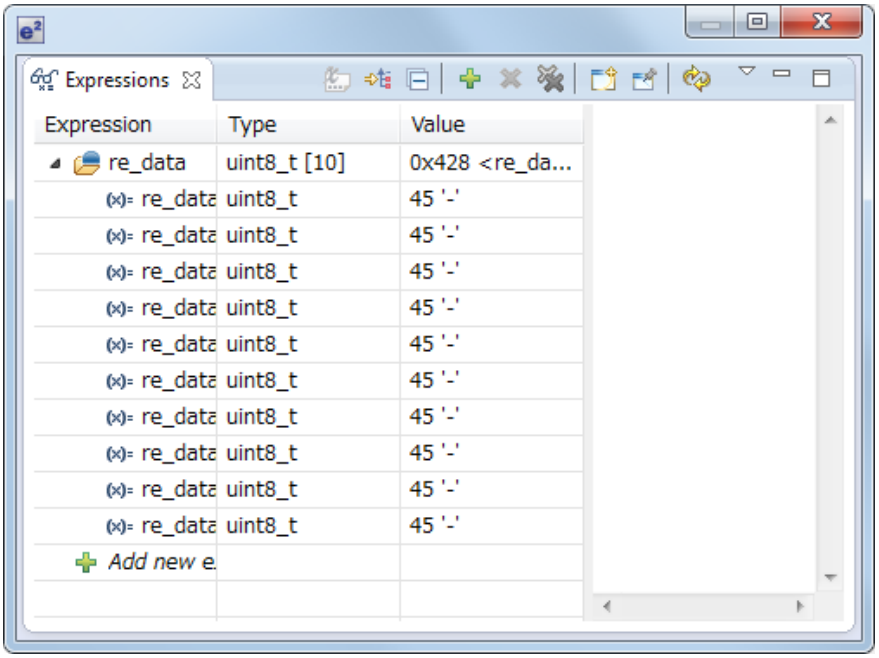


2.download a program.



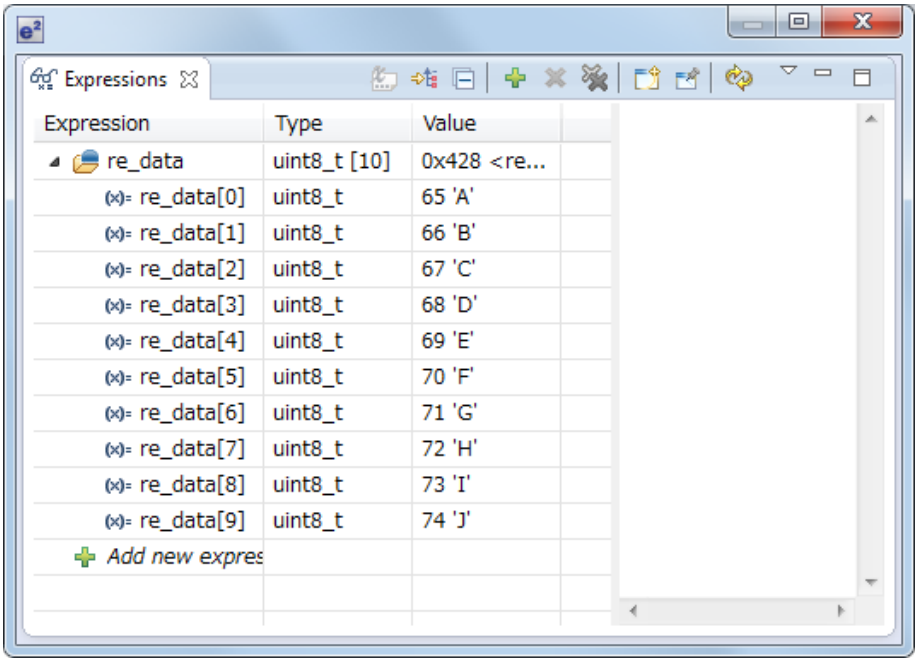
(13) Adding the variable of the reception data **e2 studio**

Open the Expressions window and add the variable "re_data".



(14) Executing the program and monitoring the result of the transfer **e2 studio**

Start the execution and check the value of “re_data” on the watch window.



5. Specification of Generated Functions

Table 5.1 shows generated functions for the RX630.

Table 5.1 Generated Functions for the RX630

Clock-generation circuit

Generated Function	Description
R_PG_Clock_Set	Set up the clocks
R_PG_Clock_WaitSet	Set up the clocks (wait cycle insertion)
R_PG_Clock_Start_MAIN	Start the main clock oscillator
R_PG_Clock_Stop_MAIN	Stop the main clock oscillator
R_PG_Clock_Enable_MAIN_ForcedOscillation	Enable the main clock forced oscillation
R_PG_Clock_Disable_MAIN_ForcedOscillation	Disable the main clock forced oscillation
R_PG_Clock_Start_SUB	Start the sub-clock oscillator
R_PG_Clock_Stop_SUB	Stop the sub-clock oscillator
R_PG_Clock_Start_LOCO	Start the low-speed on-chip oscillator (LOCO)
R_PG_Clock_Stop_LOCO	Stop the low-speed on-chip oscillator (LOCO)
R_PG_Clock_Start_HOCO	Start the high-speed on-chip oscillator (HOCO)
R_PG_Clock_Stop_HOCO	Stop the high-speed on-chip oscillator (HOCO)
R_PG_Clock_PowerON_HOCO	Turn on the high-speed on-chip oscillator (HOCO) power supply
R_PG_Clock_PowerON_HOCO	Turn off the high-speed on-chip oscillator (HOCO) power supply
R_PG_Clock_Start_PLL	Start the PLL circuit
R_PG_Clock_Stop_PLL	Stop the PLL circuit
R_PG_Clock_Enable_BCLK_PinOutput	Enable BCLK pin output
R_PG_Clock_Disable_BCLK_PinOutput	Disable BCLK pin output
R_PG_Clock_Enable_MAIN_StopDetection	Enable the main clock oscillation stop detection function
R_PG_Clock_Disable_MAIN_StopDetection	Disable the main clock oscillation stop detection function
R_PG_Clock_GetFlag_MAIN_StopDetection	Acquire the main clock oscillation stop detection flag
R_PG_Clock_ClearFlag_MAIN_StopDetection	Clear the main clock oscillation stop detection flag
R_PG_Clock_GetSelectedClockSource	Acquire the current internal clock source
R_PG_Clock_GetClocksStatus	Acquire the status of the clocks
R_PG_Clock_GetHOCOPowerStatus	Acquire the status of high-speed on-chip oscillator (HOCO) power supply

Voltage Detection Circuit (LVDA)

Generated Function	Description
R_PG_LVD_Set	Set up the voltage detection circuit

	(Voltage-monitoring 1 and 2)
R_PG_LVD_GetStatus	Get the status flag of Voltage Detection Circuit
R_PG_LVD_ClearDetectionFlag_LVD<*Voltage Detection Circuit number*>	Clear Voltage Monitoring n Voltage Change Detection Flag n: 1 or 2
R_PG_LVD_Disable_LVD<*Voltage Detection Circuit number*>	Disable Voltage Monitoring n n: 1 or 2

Frequency Measurement circuit (MCK)

Generated Function	Description
R_PG_MCK_Set	Set up the frequency measurement circuit
R_PG_MCK_Change_ReferenceClock	Change the reference clock
R_PG_MCK_StopModule	Shut down the frequency measurement circuit

Low Power Consumption

Generated Function	Description
R_PG_LPC_Set	Set up the low power consumption functions.
R_PG_LPC_Sleep	Enter sleep mode
R_PG_LPC_AllModuleClockStop	Enter all module clock stop mode
R_PG_LPC_SoftwareStandby	Enter software standby mode
R_PG_LPC_DeepSoftwareStandby	Enter deep software standby mode
R_PG_LPC_IOPortRelease	Release retained I/O port state
R_PG_LPC_ChangeOperatingPowerControl	Change the operating power control mode
R_PG_LPC_ChangeSleepModeReturnClock	Change the sleep mode return clock source
R_PG_LPC_GetPowerOnResetFlag	Acquire the value of the power-on reset flag
R_PG_LPC_GetLVDDetectionFlag	Acquire the value of the LVD detection flags
R_PG_LPC_GetDeepSoftwareStandbyResetFlag	Acquire the value of the deep software standby reset flag
R_PG_LPC_GetOperatingPowerControlFlag	Acquire the value of the operating power control mode transition flag
R_PG_LPC_GetStatus	Get the status of the low power consumption functions
R_PG_LPC_WriteBackup	Write data into the deep standby backup registers
R_PG_LPC_ReadBackup	Read data from the deep standby backup registers

Register Write Protection Function

Generated Function	Description
R_PG_RWP_RegisterWriteCgc	Enables or disables writing to registers associated with the clock generation circuit
R_PG_RWP_RegisterWriteModeLpcReset	Enables or disables writing to registers associated with the operating mode, low power comsumption, and software reset
R_PG_RWP_RegisterWriteLvd	Enables or disables writing to registers associated with LVD
R_PG_RWP_RegisterWriteMpc	Enables or disables writing to pin-function

	selection registers
R_PG_RWP_GetStatusCgc	Acquires a value indicating whether writing to registers associated with the clock generation circuit is enabled or disabled
R_PG_RWP_GetStatusModeLpcReset	Acquires a value indicating whether writing to registers associated with the operating mode, low power comsumption, and software reset is enabled or disabled
R_PG_RWP_GetStatusLvd	Acquires a value indicating whether writing to registers associated with LVD is enabled or disabled
R_PG_RWP_GetStatusMpc	Acquires a value indicating whether writing to pin-function selection registers is enabled or disabled

Interrupt controller (ICUb)

Generated Function	Description
R_PG_ExtInterrupt_Set_<*interrupt type*>	Set up an external interrupt
R_PG_ExtInterrupt_Disable_<*interrupt type*>	Disable the setting of an external interrupt
R_PG_ExtInterrupt_GetRequestFlag_<*interrupt type*>	Get an external interrupt request flag
R_PG_ExtInterrupt_ClearRequestFlag_<*interrupt type*>	Clear an external interrupt request flag
R_PG_ExtInterrupt_EnableFilter_<*interrupt type*>	Re-enable the digital filter
R_PG_ExtInterrupt_DisableFilter_<*interrupt type*>	Disable the digital filter
R_PG_SoftwareInterrupt_Set	Set up the software interrupt
R_PG_SoftwareInterrupt_Generate	Generate the software interrupt
R_PG_FastInterrupt_Set	Set an interrupt as the fast interrupt
R_PG_Exception_Set	Set exception handlers

Buses

Generated Function	Description
R_PG_ExtBus_PresetBus	Set the bus priority
R_PG_ExtBus_SetBus	Set the bus pins and the bus error monitoring
R_PG_ExtBus_SetArea_CS<*CS area number*>	Set up CS area
R_PG_ExtBus_SetEnable	Enable external bus
R_PG_ExtBus_GetErrorStatus	Acquire the status of bus error generation
R_PG_ExtBus_ClearErrorFlags	Clear the bus-error status registers
R_PG_ExtBus_DisableArea_CS<*CS area number*>	Disable CS area
R_PG_ExtBus_SetDisable	Disable the external bus

DMA controller (DMACA)

Generated Function	Description
R_PG_DMAC_Set_C<*channel number*>	Set up a DMAC channel
R_PG_DMAC_Activate_C<*channel number*>	Make the DMAC be ready for the start trigger
R_PG_DMAC_StartTransfer_C<*channel number*>	Start the one transfer of DMAC (Software trigger)
R_PG_DMAC_StartContinuousTransfer_C<*channel number*>	Start the continuous transfer of DMAC (Software trigger)
R_PG_DMAC_StopContinuousTransfer_C<*channel number*>	Stop the software-triggered continuous

	transfer of DMAC
R_PG_DMAC_Suspend_C<*channel number*>	Suspend the data transfer
R_PG_DMAC_GetTransferCount_C<*channel number*>	Get the transfer counter value
R_PG_DMAC_SetTransferCount_C<*channel number*>	Set the transfer counter
R_PG_DMAC_GetRepeatBlockSizeCount_C<*channel number*>	Get the repeat/block size counter value
R_PG_DMAC_SetRepeatBlockSizeCount_C<*channel number*>	Set the repeat/block size count
R_PG_DMAC_ClearInterruptFlag_C<*channel number*>	Get and clear the interrupt request flag
R_PG_DMAC_GetTransferEndFlag_C<*channel number*>	Get the transfer end flag
R_PG_DMAC_ClearTransferEndFlag_C<*channel number*>	Clear the transfer end flag
R_PG_DMAC_GetTransferEscapeEndFlag_C<*channel number*>	Get the transfer escape end flag
R_PG_DMAC_ClearTransferEscapeEndFlag_C<*channel number*>	Clear the escape transfer end flag
R_PG_DMAC_SetSrcAddress_C<*channel number*>	Set the source address
R_PG_DMAC_SetDestAddress_C<*channel number*>	Set the destination address
R_PG_DMAC_SetAddressOffset_C<*channel number*>	Set the address offset
R_PG_DMAC_SetExtendedRepeatSrc_C<*channel number*>	Set the source address extended repeat value
R_PG_DMAC_SetExtendedRepeatDest_C<*channel number*>	Set the destination address extended repeat value
R_PG_DMAC_StopModule_C<*channel number*>	Stop the DMAC channel

Data Transfer Controller (DTCa)

Generated Function	Description
R_PG_DTC_Set	Set up the DTC
R_PG_DTC_Set_<*trigger source*>	Set the DTC transfer data
R_PG_DTC_Activate	Make DTC be ready for the trigger
R_PG_DTC_SuspendTransfer	Stop transfer data
R_PG_DTC_GetTransmitStatus	Get transfer data status
R_PG_DTC_StopModule	Shut down the DTC

I/O port

Generated Function	Description
R_PG_IO_PORT_Set_P<*port number*>	Set the I/O ports
R_PG_IO_PORT_Set_P<*port number*><*pin number*>	Set an I/O port (one pin)
R_PG_IO_PORT_Read_P<*port number*>	Read data from Port Input Register
R_PG_IO_PORT_Read_P<*port number*><*pin number*>	Read 1-bit data from Port Input Register
R_PG_IO_PORT_Write_P<*port number*>	Write data to Port Output Data Register
R_PG_IO_PORT_Write_P <*port number*><*pin number*>	Write 1-bit data to Port Output Data Register
R_PG_IO_PORT_SetPortNotAvailable	Handle unavailable pins

Multi-Function Timer Pulse Unit 2 (MTU2a)

Generated Function	Description
R_PG_Timer_Set_MTU_U<*unit number*>_<*channels*>	Set up the MTU
R_PG_Timer_StartCount_MTU_U<*unit number*>_C<*channel number*>	Start the MTU count operation
R_PG_Timer_SynchronouslyStartCount_MTU_U<*unit number*>	Start the MTU count operation of two or more channels simultaneously
R_PG_Timer_HaltCount_MTU_U<*unit number*>_C<*channel number*>	Halt the MTU count operation

R_PG_Timer_GetCounterValue_MTU_U<*unit number*>_C<*channel number*>	Acquire the MTU counter value
R_PG_Timer_SetCounterValue_MTU_U<*unit number*>_C<*channel number*>(_<*phase*>)	Set the MTU counter value
R_PG_Timer_GetRequestFlag_MTU_U<*unit number*>_C<*channel number*>	Acquire and clear the MTU interrupt flags
R_PG_Timer_StopModule_MTU_U<*unit number*>	Shut down the MTU unit
R_PG_Timer_GetRequestFlag_MTU_U<*unit number*>_C<*channel number*>	Acquire the general register value
R_PG_Timer_SetTGR_<*general register*>_MTU_U<*unit number*>_C<*channel number*>	Set the general register value
R_PG_Timer_SetBuffer_AD_MTU_U<*unit number*>_C<*channel number*>	Set A/D converter start request cycle set buffer registers (TADCOBRA and TADCOBRB)
R_PG_Timer_SetBuffer_CycleData_MTU_U<*unit number*>_<*channels*>	Set the cycle buffer register
R_PG_Timer_SetOutputPhaseSwitch_MTU_U<*unit number*>_<*channels*>	Switch PWM output level
R_PG_Timer_ControlOutputPin_MTU_U<*unit number*>_<*channels*>	Enable or disable the PWM output
R_PG_Timer_SetBuffer_PWMOutputLevel_MTU_U<*unit number*>_<*channels*>	Set the PWM output level in the buffer register
R_PG_Timer_ControlBufferTransfer_MTU_U<*unit number*>_<*channels*>	Enable or disable buffer transfer from the buffer registers to the temporary registers

Port Output Enable 2 (POE2a)

Generated Function	Description
R_PG_POE_Set	Set up the POE
R_PG_POE_SetHiZ_<*Timer channels*>	Place the timer output pins in high-impedance state
R_PG_POE_GetRequestFlagHiZ_<*Timer channels/flag*>	Acquire the high-impedance request flags
R_PG_POE_GetShortFlag_<*Timer channels*>	Acquire the MTU output short flags
R_PG_POE_ClearFlag_<*Timer channels/flag*>	Clear the high-impedance request flags and the output short flags

16-Bit Timer Pulse Unit (TPUa)

Generated Function	Description
R_PG_Timer_Set_TPU_U<*unit number*>	Set up the TPU of two or more channels
R_PG_Timer_Start_TPU_U<*unit number*>_C<*channel number*>	Set up the TPU and start the count
R_PG_Timer_SynchronouslyStartCount_TPU_U<*unit number*>	Start the TPU count operation of two or more channels simultaneously
R_PG_Timer_HaltCount_TPU<*unit number*>_C<*channel number*>	Halt the TPU count
R_PG_Timer_ResumeCount_TPU_U<*unit number*>_C<*channel number*>	Resume the TPU count
R_PG_Timer_GetCounterValue_TPU_U<*unit number*>_C<*channel number*>	Acquire the TPU counter value
R_PG_Timer_SetCounterValue_TPU_U<*unit number*>_C<*channel number*>	Set the TPU counter value

R_PG_Timer_GetTGR_TPU_U<*unit number*>_C<*channel number*>	Acquire the TPU general register value
R_PG_Timer_SetTGR_<*general register*>_TPU_U<*unit number*>_C<*channel number*>	Set the TPU general register value
R_PG_Timer_GetRequestFlag_TPU_U<*unit number*>_C<*channel number*>	Acquire and clear the TPU interrupt flags
R_PG_Timer_StopModule_TPU_U<*unit number*>	Shut down the TPU unit

Programmable Pulse Generator (PPG)

Generated Function	Description
R_PG_PPG_StartOutput_U<*unit number*>_G<*group number*>	Set up the PPG and start outputting
R_PG_PPG_StopOutput_U<*unit number*>_G<*group number*>	Stop outputting
R_PG_PPG_SetOutputValue_ U<*unit number*>_G<*group number*>	Set the output value of single group
R_PG_PPG_SetOutputValue_ U<*unit number*>_G<*group number1*>_G<*group number2*>	Set the output value for a pair of groups

8-bit timer (TMR)

Generated Function	Description
R_PG_Timer_Start_TMR_U<*unit number*>(_C<*channel number*>)	Set a TMR and start it counting
R_PG_Timer_HaltCount_TMR_U<*unit number*>(_C<*channel number*>)	Halt counting by a TMR
R_PG_Timer_ResumeCount_TMR_U<*unit number*>(_C<*channel number*>)	Resume counting by a TMR
R_PG_Timer_GetCounterValue_TMR_U<*unit number*>(_C<*channel number*>)	Get the counter value of a TMR
R_PG_Timer_SetCounterValue_TMR_U<*unit number*>(_C<*channel number*>)	Set the counter value of a TMR
R_PG_Timer_GetRequestFlag_TMR_U<*unit number*>(_C<*channel number*>)	Acquire and clear the TMR interrupt flags
R_PG_Timer_StopModule _TMR_U<*unit number*>	Stop a TMR unit

Compare Match Timer (CMT)

Generated Function	Description
R_PG_Timer_Set_CMT_U<*unit number*>_C<*channel number*>	Set up the CMT
R_PG_Timer_StartCount_CMT_U<*unit number*>_C<*channel number*>	Start or resume the CMT count operation
R_PG_Timer_HaltCount_CMT_U<*unit number*>_C<*channel number*>	Halt the CMT count
R_PG_Timer_GetCounterValue_CMT_U<*unit number*>_C<*channel number*>	Acquire the CMT counter value
R_PG_Timer_SetCounterValue_CMT_U<*unit number*>_C<*channel number*>	Set the CMT counter value
R_PG_Timer_SetConstantRegister_CMT_U<*unit number*>_C<*channel number*>	Set the CMT constant register value
R_PG_Timer_StopModule _CMT_U<*unit number*>	Shut down the CMT unit

Realtime Clock (RTCa)

Generated Function	Description
R_PG_RTC_Start	Sets up the RTC and starts its counter
R_PG_RTC_WormStart	Sets up the RTC of warm start and starts its counter

R_PG_RTC_Stop	Suspends counting by the RTC
R_PG_RTC_Restart	Restarts counting by the RTC
R_PG_RTC_SetCurrentTime	Sets the current time
R_PG_RTC_GetStatus	Acquires information on the current state of the RTC
R_PG_RTC_Adjust30sec	Performs 30-second unit adjustment
R_PG_RTC_ManualErrorAdjust	Corrects an error of the timer
R_PG_RTC_Set24HourMode	Places the RTC in 24-hour mode
R_PG_RTC_Set12HourMode	Places the RTC in 12-hour mode
R_PG_RTC_AutoErrorAdjust_Enable	Enables automatic correction of errors of the timer
R_PG_RTC_AutoErrorAdjust_Disable	Disables automatic correction of errors of the timer
R_PG_RTC_AlarmControl	Enables or disables alarms
R_PG_RTC_SetAlarmTime	Sets the time for an alarm
R_PG_RTC_SetPeriodicInterrupt	Specifies the cycle for generating the cyclic interrupt
R_PG_RTC_ClockOut_Enable	Enables the clock output
R_PG_RTC_ClockOut_Disable	Disables the clock output
R_PG_RTC_TimeCapture<*number of the input pin for a time capture event*>_Enable	Enables time capturing
R_PG_RTC_TimeCapture<*number of the input pin for a time capture event*>_Disable	Disables time capturing
R_PG_RTC_GetCaptureTime<*number of the input pin for a time capture event*>	Acquires the captured time

Watchdog Timer (WDTA)

Generated Function	Description
R_PG_Timer_Start_WDT	Set up the WDT and start the count
R_PG_Timer_RefreshCounter_WDT	Refresh the counter of WDT
R_PG_Timer_GetStatus_WDT	Acquires the status flag and count value of WDT

Independent Watchdog Timer (IWDTa)

Generated Function	Description
R_PG_Timer_Start_IWDT	Sets up the IWDT and starts its timer
R_PG_Timer_RefreshCounter_IWDT	Refresh the counter
R_PG_Timer_GetStatus_IWDT	Acquires the status flag and count value of IWDT

Serial Communications Interface (SCIc, SCId)

Generated Function	Description
R_PG_SCI_Set_C<*channel number*>	Set a SCI channel
R_PG_SCI_SendTargetStationID_C<*channel number*>	Transmits the ID code of the receiving station
R_PG_SCI_StartSending_C<*channel number*>	Start the data transmission

R_PG_SCI_SendAllData_C<*channel number*>	Transmit all data
R_PG_SCI_I2CMode_Send_C<*channel number*>	Transmit data by simple I^2C bus interface
R_PG_SCI_I2CMode_SendWithoutStop_C<*channel number*>	Transmit data by simple I^2C bus interface (no stop condition)
R_PG_SCI_I2CMode_GenerateStopCondition_C<*channel number*>	Generate a stop condition
R_PG_SCI_I2CMode_Receive_C<*channel number*>	Receive data by simple I^2C bus interface
R_PG_SCI_I2CMode_RestartReceive_C<*channel number*>	Receive data by simple I^2C bus interface (RE-START condition)
R_PG_SCI_I2CMode_ReceiveLast_C<*channel number*>	Making reception complete in simple I^2C bus interface
R_PG_SCI_I2CMode_GetEvent_C<*channel number*>	Get the detected event in the simple I^2C mode
R_PG_SCI_SPIMode_Transfer_C<*channel number*>	Transmit data by simple SPI mode
R_PG_SCI_SPIMode_GetErrorFlag_C<*channel number*>	Get the serial reception error flag in the simple SPI mode
R_PG_SCI_GetSentDataCount_C<*channel number*>	Acquire the number of transmitted data
R_PG_SCI_ReceiveStationID_C<*channel number*>	Receives the ID code matches the ID of the receiving station itself
R_PG_SCI_StartReceiving_C<*channel number*>	Start the data reception
R_PG_SCI_ReceiveAllData_C<*channel number*>	Receive all data
R_PG_SCI_ControlClockOutput_C<*channel number*>	Control the output from the SCKn pin (n: 0, 1, 5, 6, 8, 9, or 12)
R_PG_SCI_StopCommunication_C<*channel number*>	Stop transmission and reception
R_PG_SCI_GetReceivedDataCount_C<*channel number*>	Acquire the number of received data
R_PG_SCI_GetReceptionErrorFlag_C<*channel number*>	Get the serial reception error flag
R_PG_SCI_ClearReceptionErrorFlag_C<*channel number*>	Clear the serial reception error flag
R_PG_SCI_GetTransmitStatus_C<*channel number*>	Get the state of transmission
R_PG_SCI_StopModule_C<*channel number*>	Shut down a SCI channel

I^2C Bus Interface (RIIC)

Generated Function	Description
R_PG_I2C_Set_C<*channel number*>	Set up the I^2C bus interface channel
R_PG_I2C_MasterReceive_C<*channel number*>	Master data reception
R_PG_I2C_MasterReceiveLast_C<*channel number*>	Complete a master reception process
R_PG_I2C_MasterSend_C<*channel number*>	Master data transmission
R_PG_I2C_MasterSendWithoutStop_C<*channel number*>	Master data transmission (No stop condition)
R_PG_I2C_GenerateStopCondition_C<*channel number*>	Generate the stop condition
R_PG_I2C_GetBusState_C<*channel number*>	Get the bus state
R_PG_I2C_SlaveMonitor_C<*channel number*>	Slave bus monitor
R_PG_I2C_SlaveSend_C<*channel number*>	Slave data transmission
R_PG_I2C_GetDetectedAddress_C<*channel number*>	Get the detected address
R_PG_I2C_GetTR_C<*channel number*>	Get the transmit/receive mode
R_PG_I2C_GetEvent_C<*channel number*>	Get the detected event
R_PG_I2C_GetReceivedDataCount_C<*channel number*>	Acquires the count of transmitted data
R_PG_I2C_GetSentDataCount_C<*channel number*>	Acquires the count of received data

R_PG_I2C_Reset_C<*channel number*>	Reset the bus
R_PG_I2C_StopModule_C<*channel number*>	Shut down the I^2C bus interface channel

Serial Peripheral Interface (RSPI)

Generated Function	Description
R_PG_RSPI_Set_C<*channel number*>	Set up a RSPI channel
R_PG_RSPI_SetCommand_C<*channel number*>	Set commands
R_PG_RSPI_StartTransfer_C<*channel number*>	Start the data transfer
R_PG_RSPI_TransferAllData_C<*channel number*>	Transfer all data
R_PG_RSPI_GetStatus_C<*channel number*>	Acquire the transfer status
R_PG_RSPI_GetError_C<*channel number*>	Acquire the error flags
R_PG_RSPI_GetCommandStatus_C<*channel number*>	Acquire the command status
R_PG_RSPI_LoopBack<*loopback mode*>_C<*channel number*>	Set loopback mode
R_PG_RSPI_StopModule_C<*channel number*>	Shut down a RSPI channel

IEBus Controller (IEB)

Generated Function	Description
R_PG_IEB_Set_C<*channel number*>	Set up the IEBus interface channel
R_PG_IEB_MasterReceiveStatus_C<*channel number*>	Read the slave status and unlock
R_PG_IEB_MasterReceiveLockAddress_C<*channel number*>	Read the locked address
R_PG_IEB_MasterReceiveData_C<*channel number*>	Master data reception
R_PG_IEB_MasterSendCmd_C<*channel number*>	Master command transmission
R_PG_IEB_MasterSendData_C<*channel number*>	Master data transmission
R_PG_IEB_MasterSendCmdBroadcast_C<*channel number*>	Master command transmission (Broadcast)
R_PG_IEB_MasterSendDataBroadcast_C<*channel number*>	Master data transmission (Broadcast)
R_PG_IEB_SlaveMonitor_C<*channel number*>	Slave bus monitor
R_PG_IEB_SlaveWrite_C<*channel number*>	Set the slave transmit data
R_PG_IEB_GetReceivedMasterAddress_C<*channel number*>	Get the master address
R_PG_IEB_GetReceivedCmd_C<*channel number*>	Get the receive command
R_PG_IEB_GetReceivedDataCount_C<*channel number*>	Get the message length of receive data
R_PG_IEB_GetLockMasterAddress_C<*channel number*>	Get the lock address
R_PG_IEB_GetGeneralFlag_C<*channel number*>	Get the general flags
R_PG_IEB_GetTransmitStatus_C<*channel number*>	Get the transmit status
R_PG_IEB_GetReceiveStatus_C<*channel number*>	Get the receive status
R_PG_IEB_Reset_C<*channel number*>	Reset the bus
R_PG_IEB_SetSlaveStatus_C<*channel number*>	Set the slave transmission status
R_PG_IEB_CancelLock_C<*channel number*>	Cancel the slave lock status
R_PG_IEB_StopCommunication_C<*channel number*>	Stop the communication
R_PG_IEB_StopModule_C<*channel number*>	Shut down the IEBus interface channel

CRC Calculator (CRC)

Generated Function	Description
R_PG_CRC_Set	Set up CRC calculator
R_PG_CRC_InputData	Input a data to CRC calculator
R_PG_CRC_GetResult	Get the the result of calculation
R_PG_CRC_StopModule	Shut down CRC Calculator

12-Bit A/D Converter (S12ADa)

Generated Function	Description
R_PG_ADC_12_Set_S12AD0	Sets up the 12-bit A/D converter
R_PG_ADC_12_StartConversionSW_S12AD0	Starts A/D conversion (by a software trigger)
R_PG_ADC_12_StopConversion_S12AD0	Stops A/D conversion
R_PG_ADC_12_GetResult_S12AD0	Gets the result of A/D conversion of an analog input, output from the temperature sensor, or internal reference voltage
R_PG_ADC_12_StopModule_S12AD0	Shuts down the 12-bit A/D converter

10-Bit A/D Converter (ADb)

Generated Function	Description
R_PG_ADC_10_Set_AD<*unit number*>	Set up the 10-Bit A/D Converter
R_PG_ADC_10_SetSelfDiag_VREF_<*voltage*>_AD<*unit number*>	Set up the A/D self-diagnostic function
R_PG_ADC_10_StartConversionSW_AD<*unit number*>	Start the A/D conversion (Software trigger)
R_PG_ADC_10_StartSelfDiag_AD<*unit number*>	Start the A/D conversion (Self-diagnostic function)
R_PG_ADC_10_StopConversion_AD<*unit number*>	Stop A/D conversion
R_PG_ADC_10_GetResult_AD<*unit number*>	Get the result of A/D conversion
R_PG_ADC_10_StopModule_AD<*unit number*>	Shut down the 10-Bit A/D Converter

D/A Converter (DAa)

Generated Function	Description
R_PG_DAC_Set_C<*channel number*>	Set up a D/A converter channel
R_PG_DAC_SetWithInitialValue_C<*channel number*>	Set up a D/A converter channel and input the data
R_PG_DAC_ControlOutput_C<*channel number*>	Input the data
R_PG_DAC_StopOutput_C<*channel number*>	Stop output

Temperature Sensor (TS)

Generated Function	Description
R_PG_TS_Set	Set up the temperature sensor
R_PG_TS_EnableOutput	Enable the temperature sensor output
R_PG_TS_DisableOutput	Disable the temperature sensor output
R_PG_TS_StopModule	Shut down the temperature sensor

5.1 Clock-Generation Circuit

5.1.1 R_PG_Clock_Set

Definition: bool R_PG_Clock_Set(void)

Description: Set up the clocks

Parameter:

None

Return value:

true	Setting was made correctly
false	Setting failed

File for output: R_PG_Clock.c

RPDL function: R_CGC_Set, R_CGC_Control

Details:

- Sets up each clock source and starts the oscillation.
- Switches the internal clock source to the clock which is specified on GUI.
- To insert wait cycles before switching the internal clock source, use R_PG_Clock_WaitSet.

Example:

```
//Include “R_PG_<project name>.h” to use this function.
#include “R_PG_default.h”

void func(void)
{
      //Set the clock-generation circuit.
      R_PG_Clock_Set();
}
```

5.1.2 R_PG_Clock_WaitSet

Definition: bool R_PG_Clock_WaitSet(double wait_time)

Description: Set up the clocks (wait cycle insertion)

Parameter

double wait_time	Oscillation stabilization waiting time (in seconds)

Return value

true	Setting was made correctly
false	Setting failed

File for output: R_PG_Clock.c

RPDL function: R_CGC_Set, R_CGC_Control

Details

- Sets up each clock source and starts the oscillation.
- Switches the internal clock source to the clock which is specified on GUI.
- This function inserts wait cycles before switching the internal clock source. If wait cycles are not required, use R_PG_Clock_Set.
- The actual waiting time may be different from the specified value.

Example

```
//Include “R_PG_<project name>.h” to use this function.
#include “R_PG_default.h”

void func(void)
{
      //Set the clock-generation circuit and switch the clock source after waiting 0.5 seconds.
      R_PG_Clock_WaitSet(0.5);
}
```

5.1.3 R_PG_Clock_Start_MAIN

Definition: bool R_PG_Clock_Start_MAIN(void)

Description: Start the main clock oscillator

Conditions for output: The main clock or PLL circuit is set to be used on GUI.

Parameter:

None

Return value:

true	Setting was made correctly
false	Setting failed

File for output: R_PG_Clock.c

RPDL function: R_CGC_Control

Details:

- Starts the main clock oscillator.
- If the main clock is set to be used on GUI, the main clock will start the oscillation in R_PG_Clock_Set.

Example:

```
//Include “R_PG_<project name>.h” to use this function.
#include “R_PG_default.h”

void func(void)
{
      //Start the main clock oscillator.
      R_PG_Clock_Start_MAIN();
}
```

5.1.4 R_PG_Clock_Stop_MAIN

Definition: bool R_PG_Clock_Stop_MAIN(void)

Description: Stop the main clock oscillator

Conditions for output: The main clock or PLL circuit is set to be used on GUI.

Parameter:

None

Return value:

true	Setting was made correctly
false	Setting failed

File for output: R_PG_Clock.c

RPDL function: R_CGC_Control

Details:

- Stops the main clock oscillator.
- The main clock oscillator cannot be stopped when the main clock or PLL circuit is used as the internal clock source.

Example:

```
//Include “R_PG_<project name>.h” to use this function.
#include “R_PG_default.h”

void func(void)
{
      //Stop the main clock oscillator.
      R_PG_Clock_Stop_MAIN();
}
```

5.1.5 R_PG_Clock_Enable_MAIN_ForcedOscillation

Definition: bool R_PG_Clock_Enable_MAIN_ForcedOscillation(void)

Description: Enable the main clock forced oscillation

Conditions for output: The main clock or PLL circuit is set to be used on GUI.

Parameter:

None

Return value:

true	Setting was made correctly
false	Setting failed

File for output: R_PG_Clock.c

RPDL function: R_CGC_Control

Details:

- Enables the main clock forced oscillation.

Example:

```
//Include “R_PG_<project name>.h” to use this function.
#include “R_PG_default.h”

void func(void)
{
      //Enable the main clock forced oscillation
      R_PG_Clock_Enable_MAIN_ForcedOscillation();
}
```

5.1.6 R_PG_Clock_Disable_MAIN_ForcedOscillation

Definition: bool R_PG_Clock_Disable_MAIN_ForcedOscillation(void)

Description: Disable the main clock forced oscillation

Conditions for output: The main clock or PLL circuit is set to be used on GUI.

Parameter:

None

Return value:

true	Setting was made correctly
false	Setting failed

File for output: R_PG_Clock.c

RPDL function: R_CGC_Control

Details:

- Disables the main clock forced oscillation

Example:

```
//Include “R_PG_<project name>.h” to use this function.
#include “R_PG_default.h”

void func(void)
{
      //Disable the main clock forced oscillation
      R_PG_Clock_Disable_MAIN_ForcedOscillation();
}
```

5.1.7 R_PG_Clock_Start_SUB

Definition bool R_PG_Clock_Start_SUB(void)

Description Start the sub-clock oscillator

Parameter

None

Return value

true	Setting was made correctly
false	Setting failed

File for output R_PG_Clock.c

RPDL function R_CGC_Control

Details

- Starts the sub-clock oscillator.

Example

```
//Include “R_PG_<project name>.h” to use this function.
#include “R_PG_default.h”

void func(void)
{
      //Start the sub-clock oscillator.
      R_PG_Clock_Start_SUB();
}
```

5.1.8 R_PG_Clock_Stop_SUB

Definition: bool R_PG_Clock_Stop_SUB(void)

Description: Stop the sub-clock oscillator

Parameter:

None

Return value:

true	Setting was made correctly
false	Setting failed

File for output: R_PG_Clock.c

RPDL function: R_CGC_Control

Details:

- Stops the sub-clock oscillator.
- The sub-clock oscillator cannot be stopped when the sub-clock is used as the internal clock source.

Example:

```
//Include “R_PG_<project name>.h” to use this function.
#include “R_PG_default.h”

void func(void)
{
      //Stop the sub-clock oscillator.
      R_PG_Clock_Stop_SUB();
}
```

5.1.9 R_PG_Clock_Start_LOCO

Definition: bool R_PG_Clock_Start_LOCO(void)

Description: Start the low-speed on-chip oscillator (LOCO)

Parameter:

None

Return value:

true	Setting was made correctly
false	Setting failed

File for output: R_PG_Clock.c

RPDL function: R_CGC_Control

Details:

- Starts the low-speed on-chip oscillator (LOCO).

Example:

```
//Include “R_PG_<project name>.h” to use this function.
#include “R_PG_default.h”

void func(void)
{
      //Start the low-speed on-chip oscillator (LOCO).
      R_PG_Clock_Start_LOCO();
}
```

5.1.10 R_PG_Clock_Stop_LOCO

Definition: bool R_PG_Clock_Stop_LOCO(void)

Description: Stop the low-speed on-chip oscillator (LOCO)

Parameter:

None

Return value:

true	Setting was made correctly
false	Setting failed

File for output: R_PG_Clock.c

RPDL function: R_CGC_Control

Details:

- Stops the low-speed on-chip oscillator (LOCO).
- The low-speed on-chip oscillator (LOCO) cannot be stopped when the LOCO is used as the internal clock source.

Example:

```
//Include “R_PG_<project name>.h” to use this function.
#include “R_PG_default.h”

void func(void)
{
      //Stop the low-speed on-chip oscillator (LOCO).
      R_PG_Clock_Stop_LOCO();
}
```

5.1.11 R_PG_Clock_Start_HOCO

Definition	bool R_PG_Clock_Start_HOCO(void)
Description	Start the high-speed on-chip oscillator (HOCO)
Conditions for output	The high-speed on-chip oscillator (HOCO) is set to be used on GUI.

Parameter

None

Return value

true	Setting was made correctly
false	Setting failed

File for output	R_PG_Clock.c
RPDL function	R_CGC_Control

Details

- Starts the high-speed on-chip oscillator (HOCO).

Example

```
//Include “R_PG_<project name>.h” to use this function.
#include “R_PG_default.h”

void func(void)
{
      //Start the high-speed on-chip oscillator (HOCO).
      R_PG_Clock_Start_HOCO();
}
```

5.1.12 R_PG_Clock_Stop_HOCO

Definition	bool R_PG_Clock_Stop_HOCO(void)
Description	Stop the high-speed on-chip oscillator (HOCO)
Conditions for output	The high-speed on-chip oscillator (HOCO) is set to be used on GUI.

Parameter

None

Return value

true	Setting was made correctly
false	Setting failed

File for output	R_PG_Clock.c
RPDL function	R_CGC_Control

Details

- Stops the high-speed on-chip oscillator (HOCO).
- The high-speed on-chip oscillator (HOCO) cannot be stopped when the HOCO is used as the internal clock source.

Example

```
//Include “R_PG_<project name>.h” to use this function.
#include “R_PG_default.h”

void func(void)
{
      //Stop the high-speed on-chip oscillator (HOCO).
      R_PG_Clock_Stop_HOCO();
}
```

5.1.13 R_PG_Clock_PowerON_HOCO

Definition	bool R_PG_Clock_PowerON_HOCO(void)
Description	Turn on the high-speed on-chip oscillator (HOCO) power supply
Conditions for output	The high-speed on-chip oscillator (HOCO) is set to be used on GUI.

Parameter

None

Return value

true	Setting was made correctly
false	Setting failed

File for output: R_PG_Clock.c

RPDL function: R_CGC_Control

Details

- Turns on the power supply of the high-speed on-chip oscillator (HOCO)

Example

```
//Include “R_PG_<project name>.h” to use this function.
#include “R_PG_default.h”

void func(void)
{
      //Turn on the HOCO power supply
      R_PG_Clock_PowerON_HOCO();
}
```

5.1.14 R_PG_Clock_PowerOFF_HOCO

Definition	bool R_PG_Clock_PowerON_HOCO(void)
Description	Turn off the high-speed on-chip oscillator (HOCO) power supply
Conditions for output	The high-speed on-chip oscillator (HOCO) is set to be used on GUI.

Parameter

None

Return value

true	Setting was made correctly
false	Setting failed

File for output R_PG_Clock.c

RPDL function R_CGC_Control

Details

- Turns off the power supply of the high-speed on-chip oscillator (HOCO)

Example

```
//Include “R_PG_<project name>.h” to use this function.
#include “R_PG_default.h”

void func(void)
{
      //Turn off the HOCO power supply
      R_PG_Clock_PowerOFF_HOCO();
}
```

5.1.15 R_PG_Clock_Start_PLL

Definition: bool R_PG_Clock_Start_PLL(void)

Description: Start the PLL circuit

Parameter:

None

Return value:

true	Setting was made correctly
false	Setting failed

File for output: R_PG_Clock.c

RPDL function: R_CGC_Control

Details:

- Starts the PLL circuit.

Example:

```
//Include “R_PG_<project name>.h” to use this function.
#include “R_PG_default.h”

void func(void)
{
      //Start the PLL circuit.
      R_PG_Clock_Start_PLL();
}
```

5.1.16 R_PG_Clock_Stop_PLL

Definition: bool R_PG_Clock_Stop_PLL(void)

Description: Stop the PLL circuit

Parameter:

None

Return value:

true	Setting was made correctly
false	Setting failed

File for output: R_PG_Clock.c

RPDL function: R_CGC_Control

Details:

- Stops the PLL circuit.
- The PLL circuit cannot be stopped when the PLL circuit is used as the internal clock source.

Example:

```
//Include “R_PG_<project name>.h” to use this function.
#include “R_PG_default.h”

void func(void)
{
      //Stop the PLL circuit.
      R_PG_Clock_Stop_PLL();
}
```

5.1.17 R_PG_Clock_Enable_BCLK_PinOutput

Definition: bool R_PG_Clock_Enable_BCLK_PinOutput(void)

Description: Enable BCLK pin output

Conditions for output: The BCLK pin output has been set on GUI.

Parameter:

None

Return value:

true	Setting was made correctly
false	Setting failed

File for output: R_PG_Clock.c

RPDL function: R_CGC_Control

Details:

- Enables clock output from BCLK pin.
- The BCLK clock is output when the external bus is enabled.
- If the BCLK pin output has been set on GUI, the BCLK pin output is enabled in R_PG_Clock_Set.

Example:

```
//Include “R_PG_<project name>.h” to use this function.
#include “R_PG_default.h”

void func(void)
{
      //Enable BCLK pin output
      R_PG_Clock_Enable_BCLK_PinOutput();
}
```

5.1.18 R_PG_Clock_Disable_BCLK_PinOutput

Definition	bool R_PG_Clock_Disable_BCLK_PinOutput(void)
Description	Disable BCLK pin output
Conditions for output	The BCLK pin output has been set on GUI.

Parameter

None

Return value

true	Setting was made correctly
false	Setting failed

File for output	R_PG_Clock.c
RPDL function	R_CGC_Control

Details

- Disables clock output from BCLK pin.

Example

```
//Include “R_PG_<project name>.h” to use this function.
#include “R_PG_default.h”

void func(void)
{
      //Disable BCLK pin output
      R_PG_Clock_Disable_BCLK_PinOutput();
}
```

5.1.19 R_PG_Clock_Enable_MAIN_StopDetection

Definition	bool R_PG_Clock_Enable_MAIN_StopDetection(void)
Description	Enable the main clock oscillation stop detection function
Conditions for output	The main clock oscillation stop detection function has been set on GUI.

Parameter

None

Return value

true	Setting was made correctly
false	Setting failed

File for output	R_PG_Clock.c
RPDL function	R_CGC_Control

Details

- Enables the main clock oscillation stop detection function.
- If the main clock oscillation stop detection function has been set on GUI, the function is set up and enabled in R_PG_Clock_Set.

Example

```
//Include “R_PG_<project name>.h” to use this function.
#include “R_PG_default.h”

void func(void)
{
      //Enable main clock oscillation stop detection function
      R_PG_Clock_Enable_MAIN_StopDetection();
}
```

5.1.20 R_PG_Clock_Disable_MAIN_StopDetection

Definition: bool R_PG_Clock_Disable_MAIN_StopDetection(void)

Description: Disable the main clock oscillation stop detection function

Conditions for output: The main clock oscillation stop detection function has been set on GUI.

Parameter:

None

Return value:

true	Setting was made correctly
false	Setting failed

File for output: R_PG_Clock.c

RPDL function: R_CGC_Control

Details:

- Disables the main clock oscillation stop detection function.

Example:

```
//Include “R_PG_<project name>.h” to use this function.
#include “R_PG_default.h”

void func(void)
{
      //Disable main clock oscillation stop detection function
      R_PG_Clock_Disable_MAIN_StopDetection();
}
```

5.1.21 R_PG_Clock_GetFlag_MAIN_StopDetection

Definition: bool R_PG_Clock_GetFlag_MAIN_StopDetection (bool* stop)

Description: Acquire the main clock oscillation stop detection flag

Conditions for output: The main clock oscillation stop detection function has been set on GUI.

Parameter

bool* stop	The address of storage area for the main clock oscillation stop detection flag

Return value

true	Acquisition of the flag succeeded
false	Acquisition of the flag failed

File for output: R_PG_Clock.c

RPDL function: R_CGC_GetStatus

Details

- Acquires the main clock oscillation stop detection flag.

Example

```
//Include “R_PG_<project name>.h” to use this function.
#include “R_PG_default.h”

bool stop;

void func(void)
{
      //Acquire the main clock oscillation stop detection flag
      R_PG_Clock_GetFlag_MAIN_StopDetection( &stop );
}
```

5.1.22 R_PG_Clock_ClearFlag_MAIN_StopDetection

Definition: bool R_PG_Clock_ClearFlag_MAIN_StopDetection (void)

Description: Clear the main clock oscillation stop detection flag

Conditions for output: The main clock oscillation stop detection function has been set on GUI.

Parameter:

None

Return value:

true	Clearing succeeded
false	Clearing failed

File for output: R_PG_Clock.c

RPDL function: R_CGC_Control

Details:

- Clears the main clock oscillation stop detection flag.

Example:

```
//Include “R_PG_<project name>.h” to use this function.
#include “R_PG_default.h”

void func(void)
{
      //Clear the main clock oscillation stop detection flag
      R_PG_Clock_ClearFlag_MAIN_StopDetection();
}
```

5.1.23 R_PG_Clock_GetSelectedClockSource

Definition bool R_PG_Clock_GetSelectedClockSource (uint8_t* clock)

Description Acquire the current internal clock source

Parameter

uint8_t* clock	The address of storage area for the value that corresponds to current internal clock source Correspondence between clock sources and stored values 0:Low-speed on-chip oscillator 1:High-speed on-chip oscillator 2:Main clock 3:Sub-clock 4:PLL circuit

Return value

true	Acquisition succeeded
false	Acquisition failed

File for output R_PG_Clock.c

RPDL function R_CGC_GetStatus

Details

- Acquires the current internal clock source

Example

```
//Include “R_PG_<project name>.h” to use this function.
#include “R_PG_default.h”

uint8_t clock;

void func(void)
{
      //Acquire the current internal clock source
      R_PG_Clock_GetSelectedClockSource( &clock );
}
```

5.1.24 R_PG_Clock_GetClocksStatus

Definition: bool R_PG_Clock_GetClocksStatus(bool* pll, bool* main, bool* sub, bool* loco, bool* iwdt, bool* hoco)

Description: Acquire the status of the clocks

Parameter

bool* pll	The address of storage area for the value of the PLL stop bit (0:Operating 1:Stopped)
bool* main	The address of storage area for the value of the main clock stop bit (0:Operating 1:Stopped)
bool* sub	The address of storage area for the value of the sub-clock stop bit (0:Operating 1:Stopped)
bool* loco	The address of storage area for the value of the low-speed on-chip oscillator stop bit (0:Operating 1:Stopped)
bool* iwdt	The address of storage area for the value of the IWDT-dedicated low-speed on-chip oscillator stop bit (0:Operating 1:Stopped)
bool* hoco	The address of storage area for the value of the high-speed on-chip oscillator stop bit (0:Operating 1:Stopped)

Return value

true	Acquisition succeeded
false	Acquisition failed

File for output: R_PG_Clock.c

RPDL function: R_CGC_GetStatus

Details

- Acquire the oscillation status of the clocks
- Specify the address of storage area for the item to be acquired. Specify 0 for a item that is not required.

Example

```
//Include “R_PG_<project name>.h” to use this function.
#include “R_PG_default.h”

bool loco;

void func(void)
{
      //Acquire the status of the the low-speed on-chip oscillator
      R_PG_Clock_GetClocksStatus ( 0,    0,    0,    &loco,    0,    0 );
}
```

5.1.25 R_PG_Clock_GetHOCOPowerStatus

Definition: bool R_PG_Clock_GetHOCOPowerStatus (bool* power)

Description: Acquire the status of high-speed on-chip oscillator (HOCO) power supply

Parameter

bool* power	The address of storage area for the value of the HOCO power supply bit (0:ON 1:OFF)

Return value

true	Acquisition of the flag succeeded
false	Acquisition of the flag failed

File for output: R_PG_Clock.c

RPDL function: R_CGC_GetStatus

Details

- Acquires the status of high-speed on-chip oscillator (HOCO) power supply.

Example

```
//Include “R_PG_<project name>.h” to use this function.
#include “R_PG_default.h”

bool power;

void func(void)
{
      //Acquire the status of    HOCO power supply
      R_PG_Clock_GetHOCOPowerStatus ( & power );
}
```

5.2 Voltage Detection Circuit (LVDA)

5.2.1 R_PG_LVD_Set

Definition: bool R_PG_LVD_Set (void)

Description: Set up the voltage detection circuit (Voltage-monitoring 1 and Voltage-monitoring 2)

Parameter: None

Return value:

true	Setting was made correctly.
false	Setting failed.

File for output: R_PG_LVD.c

RPDL function: R_LVD_Create

Details:

- This function sets the operation (internal reset or interrupt) when low voltage is detected.
- Both Voltage-monitoring 1 and Voltage-monitoring 2 can be set up in one function call.
- Function R_PG_Clock_Set must be called before any use of this function.

Example:

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

void func(void)
{
    R_PG_Clock_Set(); // The clock-generation circuit has to be set first.

    // Set up the voltage detection circuit(voltage-monitoring 1 and voltage-monitoring 2)
    R_PG_LVD_Set();
}
```

5.2.2 R_PG_LVD_GetStatus

Definition

bool R_PG_LVD_GetStatus
(bool * lvd1_detect, bool * lvd1_monitor, bool * lvd2_detect, bool * lvd2_monitor)

Description

Get the status flag of Voltage Detection Circuit

Parameter

bool * lvd1_detect	The address of storage area for Voltage Monitoring 1 Voltage Change Detection Flag
bool * lvd1_monitor	The address of storage area for Voltage Monitoring 1 Signal Monitor Flag
bool * lvd2_detect	The address of storage area for Voltage Monitoring 2 Voltage Change Detection Flag
bool * lvd2_monitor	The address of storage area for Voltage Monitoring 2 Signal Monitor Flag

Return value

true	Acquisition succeeded
false	Acquisition failed

File for output

R_PG_LVD.c

RPDL function

R_LVD_GetStatus

Details

- This function acquires the status flag of Voltage Detection Circuit.
- Specify 0 for a flag that is not required.

Example

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

bool lvd1_det, lvd2_det;
bool lvd1_mon, lvd2_mon;

void func(void)
{
    // Get the status flag of Voltage Detection Circuit.
    R_PG_LVD_GetStatus(&lvd1_detect, &lvd1_monitor, &lvd2_detect,
&lvd2_monitor);

    if( lvd1_det ){
        //Processing when Voltage Monitoring 1 Voltage Change is detected
    }
    if( lvd2_det ){
        //Processing when Voltage Monitoring 2 Voltage Change is detected
    }
}
```

5.2.3 R_PG_LVD_ClearDetectionFlag_LVD<*Voltage Detection Circuit number*>

Definition: bool R_PG_LVD_ClearDetectionFlag_LVD<*Voltage Detection Circuit number*> (void)
<*Voltage Detection Circuit number*>: 1 or 2

Description: Clear Voltage Monitoring n Voltage Change Detection Flag n: 1 or 2

Parameter: None

Return value:

true	Clearing succeeded
false	Clearing failed

File for output: R_PG_LVD.c

RPDL function: R_LVD_Control

Details:

- This function clears Voltage Monitoring n Voltage Change Detection Flag. n: 1 or 2

Example:

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

void func(void)
{
    // Clear Voltage Monitoring 1 Voltage Change Detection Flag.
    R_PG_LVD_ClearDetectionFlag_LVD1();
}
```

5.2.4 R_PG_LVD_Disable_LVD<*Voltage Detection Circuit number*>

Definition bool R_PG_LVD_Disable_LVD<*Voltage Detection Circuit number*> (void)
<*Voltage Detection Circuit number*>: 1 or 2

Description Disable Voltage Monitoring n n: 1 or 2

Parameter None

Return value

true	Setting was made correctly
false	Setting failed

File for output R_PG_LVD.c

RPDL function R_LVD_Control

Details

- This function disables Voltage Monitoring n. n: 1 or 2

Example

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

void func(void)
{
    // Disable Voltage Monitoring 1.
    R_PG_LVD_Disable_LVD1();
}
```

5.3 Frequency Measurement Circuit (MCK)

5.3.1 R_PG_MCK_Set

Definition: bool R_PG_MCK_Set(void)

Description: Set up the frequency measurement circuit

Parameter:

None

Return value:

true	Setting was made correctly
false	Setting failed

File for output: R_PG_MCK.c

RPDL function: R_MCK_Control

Details:

- Set up the frequency measurement circuit.
- Before calling this function, call R_PG_Clock_Set to set the clock.
- Call this function before configuring the MTU (system 1) or TPU (system 2) channels for frequency measurement operation.

Example:

```
//Include “R_PG_<project name>.h” to use this function.
#include “R_PG_default.h”

uint16_t tgr_a;

void func1(void)
{
      //Set the clock-generation circuit
      R_PG_Clock_Set();

      //Set up the frequency measurement circuit
      R_PG_MCK_Set();

      //Set up the MTU
      R_PG_Timer_Set_MTU_U0_C0();
      R_PG_Timer_Set_MTU_U0_C1();

      //Start the MTU count operation of two or more channels simultaneously
      R_PG_Timer_SynchronouslyStartCount_MTU_U0(1, 1, 0, 0, 0);
}

//Input capture A interrupt notification function
void Mtu1IcCmAIntFunc(void)
{
      //Acquire the general register value
      R_PG_Timer_GetTGR_MTU_U0_C1(&tgr_a, 0, 0, 0, 0, 0);

      //Is value of TGRA (channel 1) within permissible range?
}

void func2(void)
{
      //Change the reference clock
```

```
    R_PG_MCK_Change_ReferenceClock(1, 3);
}

void func3(void)
{
    //Shut down the frequency measurement circuit
    R_PG_MCK_StopModule();
}
```

5.3.2 R_PG_MCK_Change_ReferenceClock

Definition: bool R_PG_MCK_Change_ReferenceClock (uint8_t ref_clk1, uint8_t ref_clk2)

Description: Change the reference clock

Parameter

uint8_t ref_clk1	Reference clock of the counter-clock extension circuit 1
uint8_t ref_clk2	Reference clock of the counter-clock extension circuit 2

Return value

true	Setting was made correctly
false	Setting failed

File for output: R_PG_MCK.c

RPDL function: R_MCK_Control

Details

- Change the reference clock.

Example

Refer to the example of R_PG_MCK_Set.

5.3.3 R_PG_MCK_StopModule

Definition: bool R_PG_MCK_StopModule(void)

Description: Shut down the frequency measurement circuit

Parameter:

None

Return value:

true	Shutting down succeeded
false	Shutting down failed

File for output: R_PG_MCK.c

RPDL function: R_MCK_Control

Details:

- Shut down the frequency measurement circuit.

Example: Refer to the example of R_PG_MCK_Set.

5.4 Low Power Consumption

5.4.1 R_PG_LPC_Set

Definition: bool R_PG_LPC_Set (void)

Description: Set up the low power consumption functions.

Parameter: None

Return value:

true	Setting was made correctly
false	Setting failed

File for output: R_PG_LPC.c

RPDL function: R_LPC_Create

Details:

- This function configures the low power conditions.
- Call this function before starting the clock source for which you have set the oscillation settling time through the GUI.

Example:

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

void func(void)
{
    //Stop the sub-clock oscillator.
    R_PG_Clock_Stop_SUB();
    // Set up the low power consumption functions.
    R_PG_LPC_Set (void);
    //Start the sub-clock oscillator.
    R_PG_Clock_Start_SUB();
    //Set the clock-generation circuit and switch the clock source after waiting 2 seconds.
    R_PG_Clock_WaitSet(2);
}
```

5.4.2 R_PG_LPC_Sleep

Definition bool R_PG_LPC_Sleep (void)

Description Enter sleep mode.

Parameter None

Return value

true	Setting was made correctly
false	Setting failed

File for output R_PG_LPC.c

RPDL function R_LPC_Control

Details

- This function set the system to sleep mode.

Example

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

void func(void)
{
    // Enter sleep mode.
    R_PG_LPC_Sleep(void);
}
```

5.4.3 R_PG_LPC_AllModuleClockStop

Definition bool R_PG_LPC_AllModuleClockStop (void)

Description Enter all module clock stop mode.

Parameter None

Return value

true	Setting was made correctly
false	Setting failed

File for output R_PG_LPC.c

RPDL function R_LPC_Control

Details

- This function sets the system to all module clock stop mode.
- Before entering all module clock stop mode, this function sets TMR unit which is allowed to operate while all module clock stop mode.
- By default, TMR stops while the MCU is in all module clock stop mode. To prevent stopping TMR in all module clock stop mode, select the TMR unit that you wish to operate through the GUI.

Example

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

void func(void)
{
    // Enter all module clock stop mode.
    R_PG_LPC_AllModuleClockStop (void);
}
```

5.4.4 R_PG_LPC_SoftwareStandby

Definition bool R_PG_LPC_SoftwareStandby(void)

Description Enter software standby mode.

Parameter None

Return value

true	Setting was made correctly
false	Setting failed

File for output R_PG_LPC.c

RPDL function R_LPC_Control

Details
- This function set the system to software standby mode.
- Call R_PG_LPC_Set before calling this function to set the operation during software standby mode.

Example

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

void func(void)
{
    // Set up the low power consumption functions.
    R_PG_LPC_Set (void);

    // Enter software standby mode.
    R_PG_LPC_SoftwareStandby (void);
}
```

5.4.5 R_PG_LPC_DeepSoftwareStandby

Definition bool R_PG_LPC_DeepSoftwareStandby(void)

Description Enter deep software standby mode.

Parameter None

Return value

true	Setting was made correctly
false	Setting failed

File for output R_PG_LPC.c

RPDL function R_LPC_Control

Details

- This function set the system to deep software standby mode.
- Call R_PG_LPC_Set before calling this function to set the operation during deep software standby mode and release triggers.
- The deep software standby cancel flag is set to 1 when a cancel request is generated in any mode. In this function, the deep software standby cancel flag is not cleared before entering deep software standby mode. Clear the deep software standby cancel flag before calling this function by R_PD_LPC_GetDeepSoftwareStandbyCancelFlag.

Example

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

void func(void)
{
    // Set up the low power consumption functions.
    R_PG_LPC_Set (void);

    // Clear deep software standby cancel flag.
    R_PD_LPC_GetDeepSoftwareStandbyCancelFlag(0,0,0,0,0,0,0,0);

    // Enter deep software standby mode.
    R_PG_LPC_DeepSoftwareStandby (void);
}
```

5.4.6 R_PG_LPC_IOPortRelease

Definition: bool R_PG_LPC_IOPortRelease (void)

Description: Release retained I/O port state.

Conditions for output: On the GUI, [Release retained port state when 0 is written to the IOKEEP bit after release from deep software standby mode] is selected for the setting of [I/O port state retention].

Parameter: None

Return value:

true	Setting was made correctly
false	Setting failed

File for output: R_PG_LPC.c

RPDL function: R_LPC_Control

Details:

- This function releases I/O ports from the retention state after the system is releasd from deep software standby mode.

Example:

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"
void func(void)
{
    // Release I/O ports from the retention state
    R_PG_LPC_IOPortRelease(void);
}
```

5.4.7 R_PG_LPC_ChangeOperatingPowerControl

Definition bool R_PG_LPC_ChangeOperatingPowerControl(uint8_t mode)

Description Change the operating power control mode

Parameter

uint8_t mode	Operating power control mode 0 : High-speed operating mode 1 : Low-speed operating mode 1 2 : Low-speed operating mode 2

Return value

true	Setting was made correctly
false	Setting failed

File for output R_PG_LPC.c

RPDL function R_LPC_Control

Details

- Changes the operating power control mode.

Example

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"
void func(void)
{
    // Change the operating power control mode to middle-speed operating mode A
    R_PG_LPC_ChangeOperatingPowerControl( 1 );
}
```

5.4.8 R_PG_LPC_ChangeSleepModeReturnClock

Definition bool R_PG_LPC_ChangeSleepModeReturnClock(uint8_t return_clock)

Description Change the sleep mode return clock source

Parameter

uint8_t return_clock	Sleep mode return clock source 0:Switching is disabled 1:HOCO 2:Main clock oscillator)

Return value

true	Setting was made correctly
false	Setting failed

File for output R_PG_LPC.c

RPDL function R_LPC_Control

Details

- Changes the sleep mode return clock source.

Example

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"
void func(void)
{
    // Change the sleep mode return clock source to HOCO
    R_PG_LPC_ChangeSleepModeReturnClock( 1 );
}
```

5.4.9 R_PG_LPC_GetPowerOnResetFlag

Definition: bool R_PG_LPC_GetPowerOnResetFlag (bool *reset)

Description: Acquire the value of the power-on reset flag.

Parameter

bool *reset	The address of storage area for the power-on reset flag

Return value

true	Acquisition succeeded
false	Acquisition failed

File for output: R_PG_LPC.c

RPDL function: R_LPC_GetStatus

Details

- This function acquires the value of the power-on reset flag.
- The reset detection flags and the deep software standby cancel request flags are cleared by calling this function. Use R_PG_LPC_GetStatus instead of this function to get these flags simultaneously if needed.
- RSTSR.PORF(power-on reset flag) is only initialized by a pin reset.

Example

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

bool reset;

void func(void)
{
    // Acquire the power-on reset flags.
    R_PG_LPC_GetPowerOnResetFlag( &reset );

    if( reset ){
        // Processing when the power-on reset is detected
    }
}
```

5.4.10 R_PG_LPC_GetLVDDetectionFlag

Definition: bool R_PG_LPC_GetLVDDetectionFlag (bool * lvd0, bool * lvd1, bool * lvd2)

Description: Acquire the value of the LVD detection flags.

Parameter

bool * lvd0	The address of storage area for the LVD0 detection flag
bool * lvd1	The address of storage area for the LVD1 detection flag
bool * lvd2	The address of storage area for the LVD2 detection flag

Return value

true	Acquisition succeeded
false	Acquisition failed

File for output: R_PG_LPC.c

RPDL function: R_LPC_GetStatus

Details

- This function acquires the value of the LVD detection flags.
- Specify the address of storage area for the flags to be acquired.
- Specify 0 for a flag that is not required.
- The reset detection flags and the deep software standby cancel request flags are cleared by calling this function. Use R_PG_LPC_GetStatus instead of this function to get these flags simultaneously if needed.

Example

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

bool lvd1;
bool lvd2;

void func(void)
{
    // Acquire the LVD1 and LVD2 flags.
    R_PG_LPC_GetLVDDetectionFlag ( 0, &lvd1, &lvd2 );

    if( lvd1 ){
        //Processing when the LVD1 is detected
    }
    if( lvd2 ){
        //Processing when the LVD2 is detected
    }
}
```

5.4.11 R_PG_LPC_GetDeepSoftwareStandbyResetFlag

Definition: bool R_PG_LPC_GetDeepSoftwareStandbyResetFlag(bool *reset)

Description: Acquire the value of the deep software standby reset flag.

Parameter

bool *reset	The address of storage area for the deep software standby reset flag

Return value

true	Acquisition succeeded
false	Acquisition failed

File for output: R_PG_LPC.c

RPDL function: R_LPC_GetStatus

Details

- This function acquires the value of the deep software standby reset flag.
- The reset detection flags and the deep software standby cancel request flags are cleared by calling this function. Use R_PG_LPC_GetStatus instead of this function to get these flags simultaneously if needed.

Example

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

bool reset;

void func(void)
{
    // Acquire the deep software standby reset flag.
    R_PG_LPC_GetDeepSoftwareStandbyResetFlag ( &reset);

    if( reset ){
        //Processing when the deep software standby reset is detected
    }
}
```

5.4.12 R_PG_LPC_GetOperatingPowerControlFlag

Definition: bool R_PG_LPC_GetOperatingPowerControlFlag(bool * during_transition)

Description: Acquire the value of the operating power control mode transition flag

Parameter

bool * during_transition	The address of the storage area for the operating power control mode transition flag

Return value

true	Acquisition succeeded
false	Acquisition failed

File for output: R_PG_LPC.c

RPDL function: R_LPC_GetStatus

Details

- This function acquires the value of the operating power control mode transition flag.
- The reset detection flags and the deep software standby cancel request flags are cleared by calling this function. Use R_PG_LPC_GetStatus instead of this function to get these flags simultaneously if needed.

Example

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

bool during_transition;

void func(void)
{
    // Acquire the operating power control mode transition flag
    R_PD_LPC_GetDeepSoftwareStandbyCancelFlag ( &during_transition );
}
```

5.4.13 R_PG_LPC_GetStatus

Definition bool R_PG_LPC_GetStatus(uint32_t *data1, uint8_t * data2)

Description Get the status of the low power consumption functions.

Parameter

uint32_t *data1	The address of storage area for the status data 1
uint8_t *data2	The address of storage area for the status data 2

Return value

true	Acquisition succeeded
false	Acquisition failed

File for output R_PG_LPC.h

RPDL function R_LPC_GetStatus

Details

- This function acquires the reset status and deep software standby cancel request flags.
- When calling this function, the function of RPDL R_PG_LPC_GetStatus is called directly.
- The status flags shall be stored in the format below.

data1

b31-b26	b25	b24
0	CAN deep standby cancel flag	Operating Power Control Mode transition flag 0: Transition completed 1: During Transition

b23	b22-b20	b19	b18	b17	b16
Reset status (RSTSR) (0: not detected; 1: detected)					
Deep software reset	0	LVD2	LVD1	LVD0	Power-on reset

b15	b14	b13	b12	b11	b10	b9	b8
Deep software standby cancel request detection (DPSIFR) (0: not detected; 1: detected)							
0	IIC (SCL)	IIC (SDA)	NMI	RTC alarm	RTC interval	LVD2	LVD1

b7	b6	b5	b4	b3	b2	b1	b0
Deep software standby cancel request detection (DPSIFR) (0: not detected; 1: detected)							
IRQ7 -DS	IRQ6 -DS	IRQ5 -DS	IRQ4 -DS	IRQ3 -DS	IRQ2 -DS	IRQ1 -DS	IRQ0 -DS

data2

b7	b6	b5	b4	b3	b2	b1	b0
Deep software standby cancel request detection (DPSIFR) (0: not detected; 1: detected)							
IRQ15 -DS	IRQ14 -DS	IRQ13 -DS	IRQ12 -DS	IRQ11 -DS	IRQ10 -DS	IRQ9 -DS	IRQ8 -DS

- The RSTSR(LVD detection flags, deep software standby reset flag) and DPSIFR(deep software standby cancel request flags) are cleared by calling this function.
- RSTSR.PORF(power-on reset flag) is only initialized by a pin reset.

Example

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"
uint16_t data;
void func(void)
{
    // Acquire the LPC status
    R_PG_LPC_GetStatus( &data );

    //Has deep software standby reset been detected?
    if( (data >> 15) & 0x1 ){
        if( (data >> 7) &0x1){
            // Processing when the deep software standby is canceled by NMI
        }
        else if( data &0x1){
            // Processing when the deep software standby is canceled by IRQ0-A
        }
    }
}
```

5.4.14 R_PG_LPC_WriteBackup

Definition: bool R_PG_LPC_WriteBackup (uint8_t * data, uint8_t count)

Description: Write data into the deep standby backup registers.

Parameter

uint8_t * data	The start address of data to be written to the backup area.
uint8_t count	The number of bytes to be written to the backup area. Valid from 1 to 32.

Return value

true	Setting was made correctly
false	Setting failed

File for output: R_PG_LPC.h

RPDL function: R_LPC_WriteBackup

Details

- Writes data into the deep standby backup registers.
- When calling this function, the function of RPDL R_LPC_WriteBackup is called directly.

Example

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

uint8_t w_data[]=”ABCDEFG”;
uint8_t r_data[]=”-------”;

void func1(void)
{
    // Set up the low power consumption functions.
    R_PG_LPC_Set (void);

    // Write data into the deep standby backup registers
    R_PG_LPC_WriteBackup( w_data, 7 );

    // Enter deep software standby mode.
    R_PG_LPC_DeepSoftwareStandby (void);
}

void func2(void)
{
    // Read data from the deep standby backup registers
    R_PG_LPC_ReadBackup( r_data, 7 );
}
```

5.4.15 R_PG_LPC_ReadBackup

Definition: bool R_PG_LPC_ReadBackup (uint8_t * data, uint8_t count)

Description: Read data from the deep standby backup registers.

Parameter

uint8_t * data	The start address of storage area for the data read from the backup area.
uint8_t count	The number of bytes to be read from the backup area. Valid from 1 to 32.

Return value

true	Acquisition succeeded.
false	Acquisition failed.

File for output: R_PG_LPC.h

RPDL function: R_LPC_ReadBackup

Details

- Reads data from the deep standby backup registers.
- When calling this function, the function of RPDL R_LPC_ReadBackup is called directly.

Example

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

uint8_t w_data[]=”ABCDEFG”;
uint8_t r_data[]=”-------”;

void func1(void)
{
    // Set up the low power consumption functions.
    R_PG_LPC_Set (void);

    // Write data into the deep standby backup registers
    R_PG_LPC_WriteBackup( w_data, 7 );

    // Enter deep software standby mode.
    R_PG_LPC_DeepSoftwareStandby (void);
}

void func2(void)
{
    // Read data from the deep standby backup registers
    R_PG_LPC_ReadBackup( r_data, 7 );
}
```

5.5 Register Write Protection Function

5.5.1 R_PG_RWP_RegisterWriteCgc

Definition: bool R_PG_RWP_RegisterWriteCgc (bool enable)

Description: Enables or disables writing to registers associated with the clock generation circuit

Parameter

bool enable	Whether writing to registers is enabled or disabled (1: enabled, 0: disabled)

Return value

true	Setting was made correctly.
false	Setting failed.

File for output: R_PG_RWP.c

RPDL function: R_RWP_Control

Details

- Enables or disables writing to registers associated with the clock generation circuit.

Example

```
// Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

bool cgc;
bool mode_lpc_reset;
bool lvd;
bool b0wi,pfswe;

void func1(void)
{
    // Enable writing to registers associated with the clock generation circuit.
    R_PG_RWP_RegisterWriteCgc( 1 );

    // Enable writing to registers associated with the operating mode,
    // low power comsumption, and software reset.
    R_PG_RWP_RegisterWriteModeLpcReset( 1 );

    // Enable writing to registers associated with LVD.
    R_PG_RWP_RegisterWriteLvd( 1 );

    // Enable writing to pin-function selection registers.
    R_PG_RWP_RegisterWriteMpc( 1 );
}

void func2(void)
{
    // Disable writing to registers associated with the clock generation circuit.
    R_PG_RWP_RegisterWriteCgc( 0 );

    // Disable writing to registers associated with the operating mode,
    // low power comsumption, and software reset.
    R_PG_RWP_RegisterWriteModeLpcReset( 0 );

    // Disable writing to registers associated with LVD.
    R_PG_RWP_RegisterWriteLvd( 0 );

    // Disable writing to pin-function selection registers.
    R_PG_RWP_RegisterWriteMpc( 0 );
```

```
}

void func3(void)
{
    // Acquire the value indicating whether writing to registers associated with the clock
    // generation circuit is enabled or disabled.
    R_PG_RWP_GetStatusCgc(&cgc);

    // Acquire the value indicating whether writing to registers associated with
    // the operating mode, low power comsumption, and software reset is enabled or
    // disabled.
    R_PG_RWP_GetStatusModeLpcReset(&mode_lpc_reset);

    // Acquire the value indicating whether writing to registers associated with LVD is
    // enabled or disabled.
    R_PG_RWP_GetStatusLvd(&lvd);

    // Acquire the value indicating whether writing to pin-function selection registers is
    // enabled or disabled.
    R_PG_RWP_GetStatusMpc(&b0wi, &pfswe);
}
```

5.5.2 R_PG_RWP_RegisterWriteModeLpcReset

Definition: bool R_PG_RWP_RegisterWriteModeLpcReset (bool enable)

Description: Enables or disables writing to registers associated with the operating mode, low power comsumption, and software reset

Parameter

bool enable	Whether writing to registers is enabled or disabled (1: enabled, 0: disabled)

Return value

true	Setting was made correctly.
false	Setting failed.

File for output: R_PG_RWP.c

RPDL function: R_RWP_Control

Details

- Enables or disables writing to registers associated with the operating mode, low power comsumption, and software reset.

Example: Refer to the example of R_PG_RWP_RegisterWriteCgc.

5.5.3 R_PG_RWP_RegisterWriteLvd

Definition: bool R_PG_RWP_RegisterWriteLvd (bool enable)

Description: Enables or disables writing to registers associated with LVD

Parameter

bool enable	Whether writing to registers is enabled or disabled (1: enabled, 0: disabled)

Return value

true	Setting was made correctly.
false	Setting failed.

File for output: R_PG_RWP.c

RPDL function: R_RWP_Control

Details

- Enables or disables writing to registers associated with LVD.

Example: Refer to the example of R_PG_RWP_RegisterWriteCgc.

5.5.4 R_PG_RWP_RegisterWriteMpc

Definition: bool R_PG_RWP_RegisterWriteMpc (bool enable)

Description: Enables or disables writing to pin-function selection registers

Parameter

bool enable	Whether writing to registers is enabled or disabled (1: enabled, 0: disabled)

Return value

true	Setting was made correctly.
false	Setting failed.

File for output: R_PG_RWP.c

RPDL function: R_RWP_Control

Details

- Enables or disables writing to pin-function selection registers.

Example: Refer to the example of R_PG_RWP_RegisterWriteCgc.

5.5.5 R_PG_RWP_GetStatusCgc

Definition: bool R_PG_RWP_GetStatusCgc (bool * cgc)

Description: Acquires a value indicating whether writing to registers associated with the clock generation circuit is enabled or disabled

Parameter

bool * cgc	Whether writing to registers associated with the clock generation circuit is enabled or disabled (1: enabled, 0: disabled)

Return value

true	The value of the flag was successfully acquired.
false	Acquisition of the value of the flag failed.

File for output: R_PG_RWP.c

RPDL function: R_RWP_GetStatus

Details

- Acquires a value indicating whether writing to registers associated with the clock generation circuit is enabled or disabled.

Example: Refer to the example of R_PG_RWP_RegisterWriteCgc.

5.5.6 R_PG_RWP_GetStatusModeLpcReset

Definition: bool R_PG_RWP_GetStatusModeLpcReset (bool * mode_lpc_reset)

Description: Acquires a value indicating whether writing to registers associated with the operating mode, low power comsumption, and software reset is enabled or disabled

Parameter

bool * mode_lpc_reset	Whether writing to registers associated with the operating mode, low power consumption, and software reset is enabled or disabled (1: enabled, 0: disabled)

Return value

true	The value of the flag was successfully acquired.
false	Acquisition of the value of the flag failed.

File for output: R_PG_RWP.c

RPDL function: R_RWP_GetStatus

Details

- Acquires a value indicating whether writing to registers associated with the operating mode, low power comsumption, and software reset is enabled or disabled.

Example: Refer to the example of R_PG_RWP_RegisterWriteCgc.

5.5.7 R_PG_RWP_GetStatusLvd

Definition: bool R_PG_RWP_GetStatusLvd (bool * lvd)

Description: Acquires a value indicating whether writing to registers associated with LVD is enabled or disabled

Parameter

bool * lvd	Whether writing to registers associated with LVD is enabled or disabled (1: enabled, 0: disabled)

Return value

true	The value of the flag was successfully acquired.
false	Acquisition of the value of the flag failed.

File for output: R_PG_RWP.c

RPDL function: R_RWP_GetStatus

Details

- Acquires a value indicating whether writing to registers associated with LVD is enabled or disabled.

Example: Refer to the example of R_PG_RWP_RegisterWriteCgc.

5.5.8 R_PG_RWP_GetStatusMpc

Definition bool R_PG_RWP_GetStatusMpc (bool * b0wi, bool * pfswe)

Description Acquires a value indicating whether writing to pin-function selection registers is enabled or disabled

Parameter

bool * b0wi	Whether writing to the PFSWE bit in the PWPR register is enabled or disabled (1: enabled, 0: disabled)
bool * pfswe	Whether writing to the PFS register is enabled or disabled (1: enabled, 0: disabled)

Return value

true	The value of the flag was successfully acquired.
false	Acquisition of the value of the flag failed.

File for output R_PG_RWP.c

RPDL function R_RWP_GetStatus

Details

- Acquires a value indicating whether writing to pin-function selection registers is enabled or disabled.

Example Refer to the example of R_PG_RWP_RegisterWriteCgc.

5.6 Interrupt Controller (ICUb)

5.6.1 R_PG_ExtInterrupt_Set_<*interrupt type*>

Definition: bool R_PG_ExtInterrupt_Set_<*interrupt type*> (void)
<*interrupt type*>: IRQ0 to IRQ15 or NMI

Description: Set up an external interrupt

Parameter: None

Return value:

true	Setting was made correctly
false	Setting failed

File for output: R_PG_ExtInterrupt_<*interrupt type*>.c
<*interrupt type*>: IRQ0 to IRQ15 or NMI

RPDL function: R_INTC_SetExtInterrupt, R_INTC_CreateExtInterrupt

Details:

- The Multifunction Pin Control registers are modified to enable each selected IRQ pin and the I/O Port PMR and PDR registers are modified to set the pin as an input. For IRQn, the pin to be used is set according to the selection in the [Peripheral Pin Usage] window.
- When the name of the interrupt notification function has been specified in the GUI, if an interrupt occurs in the CPU, the function having the specified name will be called. Create the interrupt notification function as follows:
  void <*name of the interrupt notification function*> (void)
  For the interrupt notification function, note the contents of this chapter end, Notes on Notification Functions.
- If the interrupt propriety level is set to 0 in the GUI, an interrupt handler will not be called even when the external interrupt is input. The request flag can be acquired by calling R_PG_ExtInterrupt_GetRequestFlag_<*interrupt type*> and the flag can be cleared by R_PG_ExtInterrupt_ClearRequestFlag_<*interrupt type*>.
- If [Enable digital filter] is specified in the GUI, the digital filter is enabled when called this function.

Example1: A case where Irq0IntFunc has been specified as the name of an interrupt notification function:

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

void func(void)
{
      //Set IRQ0.
      R_PG_ExtInterrupt_Set_IRQ0();
}

//IRQ0 notification function
void Irq0IntFunc (void)
{
      func_irq0();      //Processing of IRQ0
}
```

Example2 A case where the interrupt propriety level is set to 0:

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

void func(void)
{
    bool flag;

    //Set IRQ0.
    R_PG_ExtInterrupt_Set_IRQ0();

    do{
        //Acquire the interrupt request flag for IRQ0.
        R_PG_ExtInterrupt_GetRequestFlag_IRQ0( &flag );
    }while( ! flag );

    func_irq0();        //Processing of IRQ0

    //Clear the interrupt request flag for IRQ0.
    R_PG_ExtInterrupt_ClearRequestFlag_IRQ0();
}
```

5.6.2 R_PG_ExtInterrupt_Disable_<*interrupt type*>

Definition bool R_PG_ExtInterrupt_Disable_<*interrupt type*> (void)
<*interrupt type*>: IRQ0 to IRQ15

Description Disable an external interrupt

Parameter None

Return value

true	Disabling was made correctly
false	Disabling failed

File for output R_PG_ExtInterrupt_<*interrupt type*>.c
<*interrupt type*>: IRQ0 to IRQ15

RPDL function R_INTC_ControlExtInterrupt

Details
- Disables an external interrupt (IRQ0 to IRQ15).
- Settings of MPC and I/O ports registers for the pin being used for the external interrupt signal are retained.
- When disabling an IRQn pin, the Interrupt Request flag will be cleared automatically.
- When the name of the interrupt notification function has been specified in the GUI, the function having the specified name may be called once more if a valid event occurs just before the interrupt pin is disabled.

Example A case where Irq0IntFunc has been specified as the name of an interrupt notification function:

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

void func(void)
{
    //Set IRQ0.
    R_PG_ExtInterrupt_Set_IRQ0();
}

//External interrupt (IRQ0) notification function
void Irq0IntFunc (void)
{
    //Disable IRQ0.
    R_PG_ExtInterrupt_Disable_IRQ0();

    func_irq0();    //Processing of IRQ0
}
```

5.6.3 R_PG_ExtInterrupt_GetRequestFlag_<*interrupt type*>

Definition: bool R_PG_ExtInterrupt_GetRequestFlag_<*interrupt type*> (bool * flag)
<*interrupt type*>: IRQ0 to IRQ15 or NMI

Description: Get an external interrupt request flag

Parameter

bool * flag	The address of storage area for the interrupt request flag

Return value

true	Acquisition succeeded
false	Acquisition failed

File for output: R_PG_ExtInterrupt_<*interrupt type*>.c
<*interrupt type*>: IRQ0 to IRQ15 or NMI

RPDL function: R_INTC_GetExtInterruptStatus

Details
- Acquires the interrupt request flag for an external interrupt (IRQ0 to IRQ15 or the NMI). When an interrupt is requested, ‘true’ is entered in the specified destination for storage of the flag’s value.

Example: Refer to the Example2 of R_PG_ExtInterrupt_Set_<*interrupt type*>

5.6.4 R_PG_ExtInterrupt_ClearRequestFlag_<*interrupt type*>

Definition bool R_PG_ExtInterrupt_ClearRequestFlag_<*interrupt type*> (void)
<*interrupt type*>: IRQ0 to IRQ15 or NMI

Description Clear an external interrupt request flag

Parameter None

Return value

true	Clearing flag succeeded
false	Clearing flag failed

File for output R_PG_ExtInterrupt_<*interrupt type*>.c
<*interrupt type*>: IRQ0 to IRQ15 or NMI

RPDL function R_INTC_ControlExtInterrupt

Details
- Clears the interrupt request flag for an external interrupt (IRQ0 to IRQ15 or NMI).
- If the level-sensitive interrupt is selected, the interrupt request flag is cleared when high-level is input to the interrupt pin. The request flag of level-sensitive interrupt cannot be cleared by this function.

Example Refer to the Example2 of R_PG_ExtInterrupt_Set_<*interrupt type*>

5.6.5 R_PG_ExtInterrupt_EnableFilter_<*interrupt type*>

Definition: bool R_PG_ExtInterrupt_EnableFilter_<*interrupt type*> (uint32_t div)
<*interrupt type*>: IRQ0 to IRQ15 or NMI

Description: Re-enable the digital filter

Conditions for output: When [Enable digital filter] is specified in the GUI.

Parameter

uint32_t div	Peripheral module clock division values 1: digital filter sampling clock = PCLK 8: digital filter sampling clock = PCLK/8 32: digital filter sampling clock = PCLK/32 64: digital filter sampling clock = PCLK/64

Return value

true	Setting was made correctly
false	Setting failed

File for output: R_PG_ExtInterrupt_<*interrupt type*>.c
<*interrupt type*>: IRQ0 to IRQ15 or NMI

RPDL function: R_INTC_ControlExtInterrupt

Details

- The digital filter disabled by R_PG_ExtInterrupt_DisableFilter_<*interrupt type*> is enabled, and digital filter sampling clock is set again.

Example: When [Use IRQ0] is specified in the GUI ([Enable digital filter] is specified)

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

void func1(void)
{
    R_PG_Clock_Set();    //The clock-generation circuit has to be set first.
    R_PG_ExtInterrupt_Set_IRQ0();    //Set IRQ0 (Enabling digital filter)
}

void func2(void)
{
    R_PG_ExtInterrupt_DisableFilter_IRQ0();    //Disabling digital filter

    R_PG_ExtInterrupt_EnableFilter_IRQ0( 1 );    //Re-enabling the digital filter
}
```

5.6.6 R_PG_ExtInterrupt_DisableFilter_<*interrupt type*>

Definition	bool R_PG_ExtInterrupt_DisableFilter_<*interrupt type*> (void) <*interrupt type*>: IRQ0 to IRQ15 or NMI
Description	Disable the digital filter
Conditions for output	When [Enable digital filter] is specified in the GUI.
Parameter	None

Return value

true	Disabling was made correctly
false	Disabling failed

File for output	R_PG_ExtInterrupt_<*interrupt type*>.c <*interrupt type*>: IRQ0 to IRQ15 or NMI
RPDL function	R_INTC_ControlExtInterrupt

Details

- The digital filter is disabled.
- Disable the digital filter before transition to Software Standby Mode. To use the digital filter again after return from software standby mode, call R_PG_ExtInterrupt_EnableFilter_<*interrupt type*>.

Example Refer to the example of R_PG_ExtInterrupt_EnableFilter_<*interrupt type*>

5.6.7 R_PG_SoftwareInterrupt_Set

Definition bool R_PG_SoftwareInterrupt_Set(void)

Description Set up the software interrupt

Parameter None

Return value

true	Setting was made correctly
false	Setting failed

File for output R_PG_SoftwareInterrupt.c

RPDL function R_INTC_CreateSoftwareInterrupt

Details
- Sets up the software interrupt.
- The software interrupt cannot be generated by calling this function. To generate the software interrupt, call R_PG_SoftwareInterrupt_Generate.

Example A case where SwIntFunc was specified as the name of the software interrupt notification function in the GUI.

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"
void SwIntFunc(void);

void func(void)
{
    //Set up the software interrupt
    R_PG_SoftwareInterrupt_Set();

    //Generate the software interrupt
    R_PG_SoftwareInterrupt_Generate();
}

void SwIntFunc(void)
{
    //Processing of software interrupt
}
```

5.6.8 R_PG_SoftwareInterrupt_Generate

Definition bool R_PG_SoftwareInterrupt_Generate(void)

Description Generate the software interrupt

Parameter None

Return value

true	Generating was made correctly
false	Generating failed

File for output R_PG_SoftwareInterrupt.c

RPDL function R_INTC_Write

Details
- Generates the software interrupt.
- Call R_PG_SoftwareInterrupt_Set before calling this function to set up the software interrupt.

Example Refer to the example of R_PG_SoftwareInterrupt_Set

5.6.9 R_PG_FastInterrupt_Set

Definition bool R_PG_FastInterrupt_Set (void)

Description Set up the fast interrupt

Parameter None

Return value

true	Setting was made correctly
false	Setting failed

File for output R_PG_FastInterrupt.c

RPDL function R_INTC_CreateFastInterrupt

Details

- Sets the interrupt source specified in the GUI as the fast interrupt. The specified interrupt source is not set or enabled. The interrupt source to be set as the fast interrupt must be set and enabled by the functions for the peripheral module.
- This function uses an unconditional trap instruction (BRK) to set the fast-interrupt vector register (FINTV). If interrupts are disabled (the interrupt enable bit (I) of the processor status word is 0), this function will be locked.
- The interrupt handler that is specified as a fast interrupt will be compiled as a fast interrupt handler by specifying fint in #pragma interrupt declaration.

Example A case where IRQ0 has been specified as the fast interrupt in the GUI:

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

void func(void)
{
    //Set IRQ0 as the fast interrupt.
    R_PG_FastInterrupt_Set ();

    //Set IRQ0.
    R_PG_ExtInterrupt_Set_IRQ0();
}
```

5.6.10 R_PG_Exception_Set

Definition bool R_PG_Exception_Set (void)

Description Set the exception handlers

Parameter None

Return value

true	Setting was made correctly
false	Setting failed

File for output R_PG_Exception.c

RPDL function R_INTC_CreateExceptionHandlers

Details

- Sets the exception notification functions. If an exception for which the name of the exception notification function was specified in the GUI occurs after this function is called, the function with the specified name will be called.
  Create the exception notification function as follows:
  void <*name of the exception notification function*> (void)
  For the exception notification function, note the contents of this chapter end, Notes on Notification Functions.

Example A case where the following exception notification functions have been set in the GUI:
Privileged instruction exception: PrivInstExcFunc
Undefined instruction exception: UndefInstExcFunc

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

void func(void)
{
      //Set the exception handlers.
      R_PG_Exception_Set();
}

void PrivInstExcFunc(){
      func_pi_excep();       //Processing in response to a privileged instruction exception
}

void UndefInstExcFunc (){
      func_ui_excep();       //Processing in response to an undefined instruction exception
}
```

5.7 Buses

5.7.1 R_PG_ExtBus_PresetBus

Definition	bool R_PG_ExtBus_PresetBus(void)
Description	Set the bus priority
Conditions for output	The bus priority has been set on GUI
Parameter	None

Return value

true	Setting was made correctly
false	Setting failed

File for output	R_PG_ExtBus.c
RPDL function	R_BSC_Set

Details

- Sets the bus priority.
- If required, call this function before calling R_PG_ExtBus_SetBus.

Example

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

void func(void)
{
      R_PG_ExtBus_PresetBus();       // Set the bus priority
      R_PG_ExtBus_SetBus();       //Set up the bus pins and bus error monitoring.
}
```

5.7.2 R_PG_ExtBus_SetBus

Definition: bool R_PG_ExtBus_SetBus(void)

Description: Set up the bus pins and the bus error monitoring

Parameter: None

Return value:

true	Setting was made correctly
false	Setting failed

File for output: R_PG_ExtBus.c

RPDL function: R_BSC_Create

Details:

- Sets up the bus pins and the bus error monitoring.
- The bus error interrupt is set by this function. If the bus error interrupt has been set to be enabled on GUI, the function having the specified name will be called when an interrupt occurs. Create the interrupt notification function as follows:
  void <*name of the interrupt notification function*> (void)
  For the interrupt notification function, note the contents of the section Notes on Notification Functions.
- The status of bus error generation can be acquired by calling R_PG_ExtBus_GetErrorStatus.
- The external bus clock (BCLK) can be set by R_PG_Clock_Set.
- If required, call R_PG_ExtBus_PresetBus before calling this function.

Example: A case where BusErrFunc has been specified as the name of the bus error interrupt notification function.

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

void func(void)
{
      R_PG_ExtBus_SetBus();       //Set up the bus pins and bus error monitoring.
}

//Bus error notification function
void BusErrFunc(void)
{
      bool addr_err;
      uint8_t master;
      uint16_t err_addr;

      //Aquire bus error status
      R_PG_ExtBus_GetErrorStatus(&addr_err, 0, &master, &err_addr);
      if( addr_err ){
            //Processing when illegal address access error occurs
     }

      //Clear the bus error status registers
      R_PG_ExtBus_ClearErrorFlags();
}
```

5.7.3 R_PG_ExtBus_SetArea_CS<*CS area number*>

Definition: bool R_PG_ExtBus_SetArea_CS<*CS area number*>(void)
<*CS area number*>: 0 to 7

Description: Set up CS area

Conditions for output: External area has been set on GUI

Parameter: None

Return value:

true	Setting was made correctly
false	Setting failed

File for output: R_PG_ExtBus_CS<area number>.c
<*CS area number*>: 0 to 7

RPDL function: R_BSC_CreateArea

Details:
- Sets up CS area.
- Call R_PG_ExtBus_SetBus before calling this function to set up the bus pins and the bus error monitoring.

Example: A case where CS1 and CS2 are set up.

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

void func(void)
{
    //Set up the bus pins and bus error monitoring.
    R_PG_ExtBus_SetBus();

    //Set up CS1
    R_PG_ExtBus_SetArea_CS1();

    //Set up CS2
    R_PG_ExtBus_SetArea_CS2();

    //Enable the external bus
    R_PG_ExtBus_SetEnable();
}
```

5.7.4 R_PG_ExtBus_SetEnable

Definition	bool R_PG_ExtBus_SetEnable(void)
Description	Enable external bus
Conditions for output	External area has been set on GUI
Parameter	None

Return value

true	Setting was made correctly
false	Setting failed

File for output	R_PG_ExtBus.c
RPDL function	R_BSC_Control

Details

- Enables the external bus.
- Call R_PG_ExtBus_SetBus and R_PG_ExtBus_SetArea_CS<*CS area number*> to set up the bus pins, the bus error monitoring and CS area before calling this function.

Example Refer to the example of R_PG_ExtBus_SetArea_CS<*CS area number*>

5.7.5 R_PG_ExtBus_GetErrorStatus

Definition

bool R_PG_ExtBus_GetErrorStatus
(bool * addr_err, bool * time_err, uint8_t * master, uint16_t * err_addr)

Description

Acquire the status of bus error generation

Conditions for output

The bus error monitoring has been set on GUI

Parameter

bool * addr_err	The address of storage area for the illegal address access error flag
bool * time_err	The address of storage area for the timeout error flag
uint8_t * master	The address of storage area for ID code of bus master that accessed a bus when a bus error occurred ID code of bus master: 0:CPU 3:DMAC/DTC
uint16_t * err_addr	The address of storage area for upper 13 bits of an address that was accessed when a bus error occurred

Return value

true	Acquisition succeeded.
false	Acquisition failed.

File for output

R_PG_ExtBus.c

RPDL function

R_BSC_GetStatus

Details

- Acquires the status of bus error generation from the bus error status registers.
- Specify the address of storage area for an item to be acquired. Specify 0 for an item that is not required.

Example

A case where BusErrFunc has been specified as the name of the bus error interrupt notification function.

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

void func(void)
{
    //Set up the bus pins and bus error monitoring.
    R_PG_ExtBus_SetBus();
}

//Bus error notification function
void BusErrFunc(void)
{
    bool addr_err;
    uint8_t master;
    uint16_t err_addr;

    //Aquire bus error status
    R_PG_ExtBus_GetErrorStatus(&addr_err, 0, &master, &err_addr);
    if( addr_err ){
        //Processing when illegal address access error occurs
    }

    //Clear the bus error status registers
    R_PG_ExtBus_ClearErrorFlags();
}
```

5.7.6 R_PG_ExtBus_ClearErrorFlags

Definition: bool R_PG_ExtBus_ClearErrorFlags(void)

Description: Clear the bus-error status registers

Conditions for output: The bus error monitoring has been set on GUI

Parameter: None

Return value:

true	Clearing succeeded
false	Clearing failed

File for output: R_PG_ExtBus.c

RPDL function: R_BSC_Control

Details:

- Clears the bus-error status registers (illegal address access error flag, timeout error flag, ID code of bus master and a value of accessed address).

Example: A case where BusErrFunc has been specified as the name of the bus error interrupt notification function.

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

void func(void)
{
    //Set up the bus pins and bus error monitoring.
    R_PG_ExtBus_SetBus();
}

//Bus error notification function
void BusErrFunc(void)
{
    bool addr_err;
    uint8_t master;
    uint16_t err_addr;

    //Aquire bus error status
    R_PG_ExtBus_GetErrorStatus(&addr_err, 0, &master, &err_addr);
    if( addr_err ){
        //Processing when illegal address access error occurs
    }

    //Clear the bus error status registers
    R_PG_ExtBus_ClearErrorFlags();
}
```

5.7.7 R_PG_ExtBus_DisableArea_CS<*CS area number*>

Definition: bool R_PG_ExtBus_DisableArea_CS<*CS area number*>(void)

<*CS area number*>: 0 to 7

Description: Disable CS area

Conditions for output: External area has been set on GUI

Parameter: None

Return value:

true	Setting was made correctly
false	Setting failed

File for output: R_PG_ExtBus_CS<*CS area number*>.c

<*CS area number*>: 0 to 7

RPDL function: R_BSC_Destroy

Details:

- Disables CS area

Example:

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

void func1(void)
{
    //Set up the bus pins and bus error monitoring.
    R_PG_ExtBus_SetBus();

    //Set up CS0
    R_PG_ExtBus_SetArea_CS0();

    //Set up CS6
    R_PG_ExtBus_SetArea_CS6();
}

void func2(void)
{
    //Disable CS0
    R_PG_ExtBus_DisableArea_CS0();

    //Disable CS6
    R_PG_ExtBus_DisableArea_CS6();
}
```

5.7.8 R_PG_ExtBus_SetDisable

Definition	bool R_PG_ExtBus_SetDisable(void)
Description	Disable the external bus
Conditions for output	External area has been set on GUI
Parameter	None

Return value

true	Setting was made correctly
false	Setting failed

File for output	R_PG_ExtBus.c
RPDL function	R_BSC_Control

Details

- Disables the external bus.

Example

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

void func(void)
{
    // Disable the external bus
    R_PG_ExtBus_SetDisable(void);
}
```

5.8 DMA controller (DMACA)

5.8.1 R_PG_DMAC_Set_C<*channel number*>

Definition: bool R_PG_DMAC_Set_C<*channel number*> (void)
<*channel number*>: 0 to 3

Description: Set up a DMAC channel

Parameter: None

Return value:

true	Setting was made correctly.
false	Setting failed.

File for output: R_PG_DMAC_C <*channel number*>.c
<*unit number*>: 0 to 3

RPDL function: R_DMAC_Create

Details:

- Releases the DMAC from the module-stop and makes initial settings.
- If an interrupt was selected as a transfer start trigger, the DMAC channel will be ready for the interrupt signal by calling R_PG_DMAC_Activate_C<*channel number*> after calling this function. If the software trigger was selected as a transfer start trigger, DMAC channel will start the data transfer when calling R_PG_DMAC_StartTransfer_C<*channel number*> or R_PG_DMAC_StartContinuousTransfer_C<*channel number*> after calling this function.
- The DMAC interrupt is set by this function. When the name of the interrupt notification function has been specified in the GUI, if a CPU interrupt occurs, the function having the specified name will be called. Create the interrupt notification function as follows:
  void <*name of the interrupt notification function*> (void)
  For the interrupt notification function, note the contents of this chapter end, Notes on Notification Functions.
- To transfer the SCI transmission data by DMAC, make the following settings.
  DMAC settings

Transfer request source	: TXI0 (SCI0 transmit data empty interrupt)
Operation when the transfer completes	: Clear the interrupt flag of the activation source
Destination start address *Destination start address can be set also from the program. Refer the usage example 2 and 3.	: Address of Transmit Data Register (TDR)
Destination address update mode	: Fixed
Length of a single data	: 1 byte

  SCIc setting

Data transmission method	: Transfer the transmitted serial data by DMAC

  For usage of function, refer to example 2.
- To transfer the SCI reception data by DMAC, make the following settings.
  DMAC settings

Transfer request source	: RXI0 (SCI0 receive data full interrupt)
Operation when the transfer completes	: Clear the interrupt flag of the activation source
Source start address *Source start address can be set also from the program. Refer the usage example 2 and 3.	: Address of Receive Data Register (RDR)
Source address update mode	: Fixed
Length of a single data	: 1 byte

SCIc setting

Data transmission method	: Transfer the received serial data by DMAC

For usage of function, refer to example 3.

Example 1 A case where IRQ0 activates DMA transfer

- IRQ0 interrupt was selected as a transfer start trigger of DMAC0 in GUI.
- Dmac0IntFunc was specified as the DMA interrupt notification function name in the GUI.
- DMAC was selected as an interrupt request destination for IRQ0.

```
#include "R_PG_default.h" //Include "R_PG_<project name>.h" to use this function.

void func(void)
{
    R_PG_DMAC_Set_C0();    //Set up DMAC0

    R_PG_ExtInterrupt_Set_IRQ0();    //Set up IRQ0

    R_PG_DMAC_Activate_C0();    //Make DMAC0 be ready for the transfer start trigger
}

//DMA interrupt notification function
void Dmac0IntFunc (void)
{
    R_PG_DMAC_StopModule_C0();    //Stop DMAC
}
```

Example 2 A case where the SCI transmission data is transferred by DMAC

- Dmac0IntFunc was specified as the DMA interrupt notification function name in the GUI.
- The SCI0 transmit data empty interrupt is selected as a DMA transfer trigger.

```
#include "R_PG_default.h" //Include "R_PG_<project name>.h" to use this function.

volatile bool sci_dma_transfer_complete;    //DMA transfer end flag
uint8_t tr[]=”ABCDEFG”;    //Data source

void func(void)
{
    //Initialize DMA transfer end flag
    sci_dma_transfer_complete = false;

    R_PG_Clock_Set();    //The clock-generation circuit has to be set first.

    R_PG_SCI_Set_C0();    //Set up SCI0

    R_PG_DMAC_Set_C0();    //Set up DMAC0

   //Set source address, destination address and transfer counter
   R_PG_DMAC_SetSrcAddress_C0( tr );
   R_PG_DMAC_SetDestAddress_C0((void*)&(SCI0.TDR));
   R_PG_DMAC_SetTransferCount_C0( 8 );

    //Make DMAC0 be ready for the transfer start trigger
    R_PG_DMAC_Activate_C0();

    //Enable the SCI0 transmission (TXI interrupt occurs and DMA transfer starts)
    R_PG_SCI_SendAllData_C0(
        PDL_NO_PTR,
        PDL_NO_DATA
    );
    // Wait for the DMAC to complete the transfer
    while (sci_dma_transfer_complete == false);
}

//DMA interrupt notification function
```

```
void Dmac0IntFunc (void)
{
	//SCI transmit end flag
	bool sci_transfer_complete;
	sci_transfer_complete = false;

	// Wait for the SCI to complete the transmission
	do{
		R_PG_SCI_GetTransmitStatus_C0( &sci_transfer_complete );
	} while( ! sci_transfer_complete );

	//Stop the SCI
	R_PG_SCI_StopCommunication_C0();

	//Stop the DMAC
	R_PG_DMAC_StopModule_C0();

	sci_dma_transfer_complete = true;
}
```

Example 3 A case where the SCI reception data is transferred by DMAC

- Dmac0IntFunc was specified as the DMA interrupt notification function name in the GUI.
- The SCI0 receive data empty interrupt is seleclted as a DMA transfer trigger.

```
#include "R_PG_default.h" //Include "R_PG_<project name>.h" to use this function.

volatile bool sci_dma_transfer_complete;    //DMA transfer end flag
uint8_t re[]="--------";    //Data destination

void func(void)
{
	//Initialize DMA transfer end flag
	sci_dma_transfer_complete = false;

	R_PG_SCI_Set_C0();    //Set up SCI0

	R_PG_DMAC_Set_C0();    //Set up DMAC0

	//Set source address, destination address and transfer counter
	R_PG_DMAC_SetSrcAddress_C0((void*)&(SCI0.RDR) );
	R_PG_DMAC_SetDestAddress_C0( re );
	R_PG_DMAC_SetTransferCount_C0( 8 );

	//Make DMAC0 be ready for the transfer start trigger
	R_PG_DMAC_Activate_C0();

	//Enable the SCI0 reception
	R_PG_SCI_ReceiveAllData_C0(
		PDL_NO_PTR,
		PDL_NO_DATA
	);
}

//DMA interrupt notification function
void Dmac0IntFunc (void)
{
	//Stop the SCI reception
	R_PG_SCI_StopCommunication_C0();

	//Stop the DMAC
	R_PG_DMAC_StopModule_C0();
}
```

5.8.2 R_PG_DMAC_Activate_C<*channel number*>

Definition: bool R_PG_DMAC_Activate_C<*channel number*> (void)
< *channel number* > : 0 to 3

Description: Make the DMAC be ready for the start trigger

Conditions for output: An interrupt is selected as a transfer start trigger

Parameter: None

Return value:

true	Setting was made correctly.
false	Setting failed.

File for output: R_PG_DMAC_C <*channel number*>.c
<*channel number*>: 0 to 3

RPDL function: R_DMAC_Control

Details:
- This function makes the DMAC channel be ready for the transfer start trigger.
- This function is genetarted when an interrupt is selected as a transfer start trigger.
- Call R_PG_DMAC_Set_C<*channel number*> to set up a DMAC channel before calling this function.

Example: A case where the setting is made as follows.
- IRQ0 was selected as a transfer start trigger of DMAC0 in normal transfer mode
- Dmac0IntFunc was specified as the DMA0 interrupt notification function name

```
#include "R_PG_default.h" //Include "R_PG_<project name>.h" to use this function.
void func(void)
{
    //Set up DMAC0
    R_PG_DMAC_Set_C0();
    //Set IRQ0
    R_PG_ExtInterrupt_Set_IRQ0();
    //Make DMAC0 be ready for the transfer start trigger
    R_PG_DMAC_Activate_C0();
}
//DMA interrupt notification function
void Dmac0IntFunc (void)
{
    //Stop the DMAC
    R_PG_DMAC_StopModule_C0();
}
```

5.8.3 R_PG_DMAC_StartTransfer_C<*channel number*>

Definition: bool R_PG_DMAC_StartTransfer_C<*channel number*> (void)
< *channel number* > : 0 to 3

Description: Start the one transfer of DMAC (Software trigger)

Conditions for output: The software trigger is selected as a transfer start trigger

Parameter: None

Return value:

true	Setting was made correctly.
false	Setting failed.

File for output: R_PG_DMAC_C <*channel number*>.c
<*channel number*>: 0 to 3

RPDL function: R_DMAC_Control

Details:
- This function starts DMA transfer of the channel specified the software trigger as a transfer start trigger.
- A DMA transfer request is cleared automatically when data transfer is started.

Example: A case where the setting is made as follows.
- The software trigger was selected as a transfer start trigger of DMAC0 in normal transfer mode
- Dmac0IntFunc was specified as the DMA interrupt notification function name

```
#include "R_PG_default.h" //Include "R_PG_<project name>.h" to use this function.
volatile bool transferred;
void func(void)
{
    transferred = false;
    //Set up DMAC0
    R_PG_DMAC_Set_C0();
    while( transferred == false ){
        //Start the DMA transfer of DMAC0
        R_PG_DMAC_StartTransfer_C0();
    }
    //Stop the DMAC
    R_PG_DMAC_StopModule_C0();
}
//DMA interrupt notification function
void Dmac0IntFunc (void)
{
    transferred = true;
}
```

5.8.4 R_PG_DMAC_StartContinuousTransfer_C<*channel number*>

Definition: bool R_PG_DMAC_StartContinuousTransfer_C<*channel number*> (void)
< *channel number* > : 0 to 3

Description: Start the continuous transfer of DMAC (Software trigger)

Conditions for output: The software trigger is selected as a transfer start trigger

Parameter: None

Return value:

true	Setting was made correctly.
false	Setting failed.

File for output: R_PG_DMAC_C <*channel number*>.c
<*channel number*>: 0 to 3

RPDL function: R_DMAC_Control

Details:
- This function starts DMA transfer of the channel specified the software trigger as a transfer start trigger.
- This function enables continuous DMA transfer because a DMA transfer request is generated again after completion of a transfer.

Example: A case where the setting is made as follows.
- The software trigger was selected as a transfer start trigger of DMAC0 in normal transfer mode
- Dmac0IntFunc was specified as the DMA interrupt notification function name

```
#include "R_PG_default.h" //Include "R_PG_<project name>.h" to use this function.
void func(void)
{
    //Set up DMAC0
    R_PG_DMAC_Set_C0();
    //Start the DMA transfer of DMAC0
    R_PG_DMAC_StartContinuousTransfer_C0();
}
//DMA interrupt notification function
void Dmac0IntFunc (void)
{
    //Stop the DMAC
    R_PG_DMAC_StopModule_C0();
}
```

5.8.5 R_PG_DMAC_StopContinuousTransfer_C<*channel number*>

Definition: bool R_PG_DMAC_StopContinuousTransfer_C<*channel number*> (void)
< *channel number* > : 0 to 3

Description: Stop the software-triggered continuous transfer of DMAC

Conditions for output: The software trigger is selected as a transfer start trigger

Parameter: None

Return value:

true	Setting was made correctly.
false	Setting failed.

File for output: R_PG_DMAC_C <*channel number*>.c
<*channel number*>: 0 to 3

RPDL function: R_DMAC_Control

Details:
- This function clears DMA transfer request of the channel specified the software trigger as a transfer start trigger.

Example: A case where the setting is made as follows.
- The software trigger was selected as a transfer start trigger of DMAC0 in normal transfer mode

```
#include "R_PG_default.h" //Include "R_PG_<project name>.h" to use this function.

void func1(void)
{
    //Set up DMAC0
    R_PG_DMAC_Set_C0();

    //Start the DMA transfer of DMAC0
    R_PG_DMAC_StartContinuousTransfer_C0();
}

void func2(void)
{
    //Clear DMA transfer request by software
    R_PG_DMAC_StopContinuousTransfer_C0();
}
```

5.8.6 R_PG_DMAC_Suspend_C<*channel number*>

Definition bool R_PG_DMAC_Suspend_C<*channel number*> (void)
< *channel number* > : 0 to 3

Description Suspend the data transfer

Parameter None

Return value

true	Suspending succeeded.
false	Suspending failed.

File for output R_PG_DMAC_C <*channel number*>.c
<*channel number*>: 0 to 3

RPDL function R_DMAC_Control

Details
- This function suspends(disables) the DMA transfer.
- This function can suspend the DMA transfer triggered by hardware.
- To resume the transfer, when interrupt is selected as a transfer start trigger, clear the interrupt request flag of trigger source and call R_PG_DMAC_Activate_C<*channel number*> to make the DMAC channel be ready for the transfer start trigger.

Example A case where the setting is made as follows.
- IRQ0 interrupt was selected as a transfer start trigger of DMAC0 in normal transfer mode
- Dmac0IntFunc was specified as the DMA interrupt notification function name
- Irq1IntFunc was specified as the IRQ1 interrupt notification function name
- Irq2IntFunc was specified as the IRQ2 interrupt notification function name

```
#include "R_PG_default.h" //Include "R_PG_<project name>.h" to use this function.

void func(void)
{
    R_PG_DMAC_Set_C0();     //Set up DMAC0
    R_PG_ExtInterrupt_Set_IRQ0();     //Set IRQ0
    R_PG_ExtInterrupt_Set_IRQ1();     //Set IRQ1
    R_PG_ExtInterrupt_Set_IRQ2();     //Set IRQ2
    R_PG_DMAC_Activate_C0();      // Make DMAC0 be ready for the transfer start trigger
}

//DMA interrupt notification function
void Dmac0IntFunc (void)
{
    R_PG_DMAC_StopModule_C0();      //Stop the DMAC
}

//DMA transfer is suspended by IRQ1 input
void Irq1IntFunc (void)
{
    R_PG_DMAC_Suspend_C0();       //Suspend the DMA transfer
}

//DMA transfer is re-activated by IRQ2 input
void Irq2IntFunc (void)
{
    R_PG_ExtInterrupt_ClearRequestFlag_IRQ0();       //Clear the request flag of trigger
    R_PG_DMAC_Activate_C0();       // Make DMAC0 be ready for the transfer start trigger
}
```

5.8.7 R_PG_DMAC_GetTransferCount_C<*channel number*>

Definition

bool R_PG_DMAC_GetTransferCount_C<*channel number*> (uint16_t * count)

< *channel number* > : 0 to 3

Description

Get the transfer counter value

Parameter

uint16_t * count	The address of storage area for the counter value

Return value

true	Acquisition succeeded
false	Acquisition failed.

File for output

R_PG_DMAC_C <*channel number*>.c

<*channel number*>: 0 to 3

RPDL function

R_DMAC_GetStatus

Details

- This function gets the current transfer counter value.
- The DMA interrupt request flag (IR flag) is cleared in this function. Call R_PG_DMAC_ClearInterruptFlag_C<*channel number*> to get the DMA interrupt request flag before calling this function if needed.

Example

A case where the setting is made as follows.

- The transfer start trigger of DMAC0 is interrupt

```
#include "R_PG_default.h" //Include "R_PG_<project name>.h" to use this function.
void func(void)
{
    uint16_t count;

    //Set up DMAC0
    R_PG_DMAC_Set_C0();

    //Make DMAC0 be ready for the transfer start trigger
    R_PG_DMAC_Activate_C0();

    //Wait for the transfer counter to become lower than 10
    do{
        R_PG_DMAC_GetTransferCount_C0( & count );
    } while( count >= 10 );

    //Suspend the DMA transfer
    R_PG_DMAC_Suspend_C0();
}
```

5.8.8 R_PG_DMAC_SetTransferCount_C<*channel number*>

Definition

bool R_PG_DMAC_SetTransferCount_C<*channel number*>(uint16_t count)

< *channel number* > : 0 to 3

Description

Set the transfer counter

Parameter

uint16_t count	Value to be set to the transfer counter

Return value

true	Setting was made correctly
false	Setting failed

File for output

R_PG_DMAC_C<*channel number*>.c

<*channel number*>: 0 to 3

RPDL function

R_DMAC_Control

Details

- This function sets the transfer counter.
- The valid range of the counter value is from 0 to 65535 (0 : free running mode) in normal
- transfer mode, 0 to 1023 (0 = 1024 units) in repeat transfer mode and block transfer mode.

Example

A case where the setting is made as follows.

- IRQ0 interrupt was selected as a transfer start trigger of DMAC0
- Dmac0IntFunc was specified as the DMA interrupt notification function name

```
#include "R_PG_default.h" //Include "R_PG_<project name>.h" to use this function.
void func(void)
{
      //Set up DMAC0
      R_PG_DMAC_Set_C0();

      //Set IRQ0
      R_PG_ExtInterrupt_Set_IRQ0();

      // Make DMAC0 be ready for the transfer start trigger
      R_PG_DMAC_Activate_C0();
}
//DMA interrupt notification function
void Dmac0IntFunc (void)
{
     //Suspend the DMA transfer
     R_PG_DMAC_Suspend_C0();

      //Change the DMAC0 settings
      R_PG_DMAC_SetSrcAddress_C0( src_address );    //Source address
      R_PG_DMAC_SetDestAddress_C0( dest_address );    //Destination address
      R_PG_DMAC_SetTransferCount_C0( tr_count );    //Transfer counter

      // Make DMAC0 be ready for the transfer start trigger
      R_PG_DMAC_Activate_C0();
}
```

5.8.9 R_PG_DMAC_GetRepeatBlockSizeCount_C<*channel number*>

Definition

bool R_PG_DMAC_GetRepeatBlockSizeCount_C<*channel number*> (uint16_t * count)

< *channel number* > : 0 to 3

Description

Get the repeat/block size counter value

Conditions for output

Repeat transfer mode or block transfer mode is selected for the transfer mode.

Parameter

uint16_t * count	The address of storage area for the counter value

Return value

true	Acquisition succeeded
false	Acquisition failed.

File for output

R_PG_DMAC_C <*channel number*>.c

<*channel number*>: 0 to 3

RPDL function

R_DMAC_GetStatus

Details

- This function gets the current repeat/block size counter value.
- The DMA interrupt request flag (IR flag) is cleared in this function. Call R_PG_DMAC_ClearInterruptFlag_C<*channel number*> to get the DMA interrupt request flag before calling this function if needed.

Example

A case where the setting is made as follows.

- DMAC0 is set to repeat transfer mode
- The transfer start trigger is interrupt

```
#include "R_PG_default.h" //Include "R_PG_<project name>.h" to use this function.
void func(void)
{
    uint16_t count;

    //Set up DMAC0
    R_PG_DMAC_Set_C0();

    //Make DMAC0 be ready for the transfer start trigger
    R_PG_DMAC_Activate_C0();

    //Wait for the repeat size counter to become lower than 10
    do{
        R_PG_DMAC_GetRepeatBlockSizeCount_C0( & count );
    } while( count >= 10 );

    //Suspend the DMA transfer
    R_PG_DMAC_Suspend_C0();
}
```

5.8.10 R_PG_DMAC_SetRepeatBlockSizeCount_C<*channel number*>

Definition

bool R_PG_DMAC_SetRepeatBlockSizeCount_C<*channel number*> (uint16_t count)

< *channel number* > : 0 to 3

Description

Set the repeat/block size counter value

Conditions for output

Repeat transfer mode or block transfer mode is selected for the transfer mode.

Parameter

uint16_t count	Value to be set to the repeat/block size counter

Return value

true	Setting was made correctly
false	Setting failed

File for output

R_PG_DMAC_C <*channel number*>.c

<*channel number*>: 0 to 3

RPDL function

R_DMAC_GetStatus

Details

- This function sets the repeat/block size counter.
  The valid range of the counter value is from 0 to 1023 (0 = 1024 units) in repeat transfer mode, 1 to 1023 in block transfer mode.

Example

A case where the setting is made as follows.

- DMAC0 is set to repeat transfer mode
- IRQ0 interrupt was selected as a transfer start trigger
- Dmac0IntFunc was specified as the DMA interrupt notification function name

```
#include "R_PG_default.h" //Include "R_PG_<project name>.h" to use this function.
void func(void)
{
      //Set up DMAC0
      R_PG_DMAC_Set_C0();

      //Set IRQ0
      R_PG_ExtInterrupt_Set_IRQ0();

      // Make DMAC0 be ready for the transfer start trigger
      R_PG_DMAC_Activate_C0();
}
//DMA interrupt notification function
void Dmac0IntFunc (void)
{
     //Suspend the DMA transfer
     R_PG_DMAC_Suspend_C0();

      //Change the DMAC0 settings
      R_PG_DMAC_SetTransferCount_C0( tr_count );    //Transfer counter
      R_PG_DMAC_SetRepeatBlockSizeCount_C0( repeat_count );    //Repeat size counter

      // Make DMAC0 be ready for the transfer start trigger
      R_PG_DMAC_Activate_C0();
}
```

5.8.11 R_PG_DMAC_ClearInterruptFlag_C<*channel number*>

Definition

bool R_PG_DMAC_ClearInterruptFlag_C<*channel number*> (bool * int_request)

< *channel number* > : 0 to 3

Description

Get and clear the interrupt request flag

Conditions for output

DMA interrupt is enabled

Parameter

bool * int_request	The address of storage area for the interrupt request flag

Return value

true	Acquisition and clearing succeeded
false	Acquisition and clearing failed

File for output

R_PG_DMAC_C <*channel number*>.c

<*channel number*>: 0 to 3

RPDL function

R_DMAC_GetStatus

Details

- This function gets and clears the DMA interrupt request flag (IR flag).

Example

A case where the setting is made as follows.

- DMAC0 is set to mormal transfer mode
- The transfer start trigger is interrupt
- The DMA interrupt is enabled
- The DMA interrupt priority level is 0

```
#include "R_PG_default.h" //Include "R_PG_<project name>.h" to use this function.
void func(void)
{
    bool int_request;

    //Set up DMAC0
    R_PG_DMAC_Set_C0();

    //Make DMAC0 be ready for the transfer start trigger
    R_PG_DMAC_Activate_C0();

    //Wait for the IR flag to become 1
    do{
        R_PG_DMAC_ClearInterruptFlag_C0(& int_request );
    } while( int_request == false );
}
```

5.8.12 R_PG_DMAC_GetTransferEndFlag_C<*channel number*>

Definition

bool R_PG_DMAC_GetTransferEndFlag_C<*channel number*> (bool* end)

< *channel number* > : 0 to 3

Description

Get the transfer end flag

Parameter

bool* end	The address of storage area for the transfer end flag

Return value

true	Acquisition succeeded
false	Acquisition failed.

File for output

R_PG_DMAC_C <*channel number*>.c

<*channel number*>: 0 to 3

RPDL function

R_DMAC_GetStatus

Details

- This function gets the transfer end flag.
- The DMA interrupt request flag (IR flag) is cleared in this function. Call R_PG_DMAC_ClearInterruptFlag_C<*channel number*> to get the DMA interrupt request flag before calling this function if needed.
- The transfer end flag is not cleared in this function. Call R_PG_DMAC_ClearTransferEndFlag_C<*channel number*> to clear the transfer end flag if needed.

Example

A case where the setting is made as follows.

- DMAC0 is set to normal transfer mode
- The transfer start trigger is interrupt
- The DMA interrupt is not enabled

```
#include "R_PG_default.h" //Include "R_PG_<project name>.h" to use this function.
void func(void)
{
    bool end;
    //Set up DMAC0
    R_PG_DMAC_Set_C0();
    //Make DMAC0 be ready for the transfer start trigger
    R_PG_DMAC_Activate_C0();
    //Wait for the transfer end flag to become 1
    do{
        R_PG_DMAC_GetTransferEndFlag_C0( & end );
    } while( end == false );
    //Clear the DMA transfer end flag
    R_PG_DMAC_ClearTransferEndFlag_C0();
}
```

5.8.13 R_PG_DMAC_ClearTransferEndFlag_C<*channel number*>

Definition

bool R_PG_DMAC_ClearTransferEndFlag_C<*channel number*> (void)

< *channel number* > : 0 to 3

Description

Clear the transfer end flag

Parameter

None

Return value

true	Clearing succeeded
false	Clearing failed

File for output

R_PG_DMAC_C <*channel number*>.c

<*channel number*>: 0 to 3

RPDL function

R_DMAC_Control

Details

- This function clears the transfer end flag.
- To get the transfer end flag, call R_PG_DMAC_GetTransferEndFlag_C<*channel number*>.

Example

A case where the setting is made as follows.

- DMAC0 is set to mormal transfer mode
- The transfer start trigger is interrupt
- The DMA interrupt is not enabled

```
#include "R_PG_default.h" //Include "R_PG_<project name>.h" to use this function.

void func(void)
{
    bool end;

    //Set up DMAC0
    R_PG_DMAC_Set_C0();

    //Make DMAC0 be ready for the transfer start trigger
    R_PG_DMAC_Activate_C0();

    //Wait for the transfer end flag to become 1
    do{
        R_PG_DMAC_GetTransferEndFlag_C0( & end );
    } while( end == false );

    //Clear the DMA transfer end flag
    R_PG_DMAC_ClearTransferEndFlag_C0();
}
```

5.8.14 R_PG_DMAC_GetTransferEscapeEndFlag_C<*channel number*>

Definition

bool R_PG_DMAC_GetTransferEscapeEndFlag_C<*channel number*> (bool* end)

< *channel number* > : 0 to 3

Description

Get the transfer escape end flag

Conditions for output

[Completion of a 1-block/repeat size transfer], [Source address extended repeat area overflow] or [Destination address extended repeat area overflow] is selected as the interrupt output source

Parameter

bool* end	The address of storage area for the transfer escape end flag

Return value

true	Acquisition succeeded
false	Acquisition failed.

File for output

R_PG_DMAC_C <*channel number*>.c

<*channel number*>: 0 to 3

RPDL function

R_DMAC_GetStatus

Details

- This function gets the DMA transfer escape end flag (EDMSTS.ESIF).
- The DMA interrupt request flag (IR flag) is cleared in this function. Call R_PG_DMAC_ClearInterruptFlag_C<*channel number*> to get the DMA interrupt request flag before calling this function if needed.
- The transfer escape end flag is not cleared in this function. Call R_PG_DMAC_ClearTransferEscapeEndFlag_C<*channel number*> to clear the transfer escape end flag if needed.

Example

A case where the setting is made as follows.

- DMAC0 is set to repeat transfer mode
- The transfer start trigger is interrupt
- [Completion of a 1-block/repeat size transfer] is selected for the interrupt output source
- The DMA interrupt priority level is 0

```
#include "R_PG_default.h" //Include "R_PG_<project name>.h" to use this function.
void func(void)
{
    bool end;

    //Set up DMAC0
    R_PG_DMAC_Set_C0();

    //Make DMAC0 be ready for the transfer start trigger
    R_PG_DMAC_Activate_C0();

    //Wait for the transfer escape end flag to become 1
    do{
        R_PG_DMAC_GetTransferEscapeEndFlag_C0( & end );
    } while( end == false );

    //Clear the DMA transfer escape end flag
    R_PG_DMAC_ClearTransferEscapeEndFlag_C0();
}
```

5.8.15 R_PG_DMAC_ClearTransferEscapeEndFlag_C<*channel number*>

Definition: bool R_PG_DMAC_ClearTransferEscapeEndFlag_C<*channel number*> (void)
< *channel number* > : 0 to 3

Description: Clear the transfer escape end flag

Conditions for output: [Completion of a 1-block/repeat size transfer], [Source address extended repeat area overflow] or [Destination address extended repeat area overflow] is selected as the interrupt output source

Parameter: None

Return value:

true	Clearing succeeded
false	Clearing failed

File for output: R_PG_DMAC_C <*channel number*>.c
<*channel number*>: 0 to 3

RPDL function: R_DMAC_Control

Details:
- This function clears the transfer escape end flag.
- To get the transfer escape end flag, call R_PG_DMAC_GetTransferEscapeEndFlag_C<*channel number*>.

Example: A case where the setting is made as follows.
- DMAC0 is set to repeat transfer mode
- The transfer start trigger is interrupt
- [Completion of a 1-block/repeat size transfer] is selected for the interrupt output source
- The DMA interrupt priority level is 0

```
#include "R_PG_default.h" //Include "R_PG_<project name>.h" to use this function.

void func(void)
{
    bool end;

    //Set up DMAC0
    R_PG_DMAC_Set_C0();

    //Make DMAC0 be ready for the transfer start trigger
    R_PG_DMAC_Activate_C0();

    //Wait for the transfer escape end flag to become 1
    do{
        R_PG_DMAC_GetTransferEscapeEndFlag_C0( & end );
    } while( end == false );

    //Clear the DMA transfer escape end flag
    R_PG_DMAC_ClearTransferEscapeEndFlag_C0();
}
```

5.8.16 R_PG_DMAC_SetSrcAddress_C<*channel number*>

Definition

bool R_PG_DMAC_SetSrcAddress_C<*channel number*>(void * src_addr)

< *channel number* > : 0 to 3

Description

Set the source address

Parameter

void * src_addr	The source address to be set

Return value

true	Setting was made correctly
false	Setting failed.

File for output

R_PG_DMAC_C<*channel number*>.c

<*channel number*>: 0 to 3

RPDL function

R_DMAC_Control

Details

- This function sets the source address.

Example

A case where the setting is made as follows.

- IRQ0 interrupt was selected as a transfer start trigger of DMAC0
- Dmac0IntFunc was specified as the DMA interrupt notification function name

```
#include "R_PG_default.h" //Include "R_PG_<project name>.h" to use this function.
void func(void)
{
      //Set up DMAC0
      R_PG_DMAC_Set_C0();

      //Set IRQ0
      R_PG_ExtInterrupt_Set_IRQ0();

      // Make DMAC0 be ready for the transfer start trigger
      R_PG_DMAC_Activate_C0();
}
//DMA interrupt notification function
void Dmac0IntFunc (void)
{
     //Suspend the DMA transfer
     R_PG_DMAC_Suspend_C0();

      //Change the DMAC0 settings
      R_PG_DMAC_SetSrcAddress_C0( src_address );    //Source address
      R_PG_DMAC_SetDestAddress_C0( dest_address );    //Destination address
      R_PG_DMAC_SetTransferCount_C0( tr_count );    //Transfer counter

      // Make DMAC0 be ready for the transfer start trigger
      R_PG_DMAC_Activate_C0();
}
```

5.8.17 R_PG_DMAC_SetDestAddress_C<*channel number*>

Definition

bool R_PG_DMAC_SetDestAddress_C<*channel number*>(void * dest_addr)

< *channel number* > : 0 to 3

Description

Set the source address

Parameter

void * dest_addr	The destination address to be set

Return value

true	Setting was made correctly
false	Setting failed.

File for output

R_PG_DMAC_C<*channel number*>.c

<*channel number*>: 0 to 3

RPDL function

R_DMAC_Control

Details

- This function sets the destination address.

Example

A case where the setting is made as follows.

- IRQ0 interrupt was selected as a transfer start trigger of DMAC0
- Dmac0IntFunc was specified as the DMA interrupt notification function name

```
#include "R_PG_default.h" //Include "R_PG_<project name>.h" to use this function.
void func(void)
{
      //Set up DMAC0
      R_PG_DMAC_Set_C0();

      //Set IRQ0
      R_PG_ExtInterrupt_Set_IRQ0();

      // Make DMAC0 be ready for the transfer start trigger
      R_PG_DMAC_Activate_C0();
}
//DMA interrupt notification function
void Dmac0IntFunc (void)
{
       //Suspend the DMA transfer
       R_PG_DMAC_Suspend_C0();

       //Set up the DMAC and continue
       R_PG_DMAC_SetSrcAddress_C0( src_address );    //Source address
       R_PG_DMAC_SetDestAddress_C0( dest_address );    //Destination address
       R_PG_DMAC_SetTransferCount_C0( tr_count );    //Transfer counter

      // Make DMAC0 be ready for the transfer start trigger
      R_PG_DMAC_Activate_C0();
}
```

5.8.18 R_PG_DMAC_SetAddressOffset_C<*channel number*>

Definition: bool R_PG_DMAC_SetAddressOffset_C<*channel number*>(int32_t offset)
< *channel number* > : 0 to 3

Description: Set the address offset

Conditions for output: [Offset addition] is selected for [Source address update mode] or [Destination address update mode].

Parameter

int32_t offset	The offset value to be set

Return value

true	Setting was made correctly
false	Setting failed

File for output: R_PG_DMAC_C<*channel number*>.c
<*channel number*>: 0 to 3

RPDL function: R_DMAC_Control

Details
- This function sets the address offset.
- The range of the address offset value is from +FFFFFFh to -1000000h.

Example: A case where the setting is made as follows.
- IRQ0 interrupt was selected as a transfer start trigger of DMAC0
- Dmac0IntFunc was specified as the DMA interrupt notification function name
- [Offset addition] is selected.

```
#include "R_PG_default.h" //Include "R_PG_<project name>.h" to use this function.
void func(void)
{
      //Set up DMAC0
      R_PG_DMAC_Set_C0();

      //Set IRQ0
      R_PG_ExtInterrupt_Set_IRQ0();

      // Make DMAC0 be ready for the transfer start trigger
      R_PG_DMAC_Activate_C0();
}
//DMA interrupt notification function
void Dmac0IntFunc (void)
{
      //Suspend the DMA transfer
      R_PG_DMAC_Suspend_C0();

      //Set up the DMAC and continue
      R_PG_DMAC_SetSrcAddress_C0( src_address );    //Source address
      R_PG_DMAC_SetDestAddress_C0( dest_address );    //Destination address
      R_PG_DMAC_SetTransferCount_C0( tr_count );    //Transfer counter
      R_PG_DMAC_SetAddressOffset_C0( offset );    //Address offset

      // Make DMAC0 be ready for the transfer start trigger
      R_PG_DMAC_Activate_C0();
}
```

5.8.19 R_PG_DMAC_SetExtendedRepeatSrc_C<*channel number*>

Definition: bool R_PG_DMAC_SetExtendedRepeatSrc_C<*channel number*>(uint32_t area)
< *channel number* > : 0 to 3

Description: Set the source address extended repeat value

Conditions for output: An extended repeat area is specified for the transfer source.

Parameter

uint32_t area	The source address extended repeat value to be set

Return value

true	Setting was made correctly
false	Setting failed

File for output: R_PG_DMAC_C<*channel number*>.c
<*channel number*>: 0 to 3

RPDL function: R_DMAC_Control

Details
- This function sets the source address extended repeat value.
- The value can be any power of 2, from 2^1 to 2^{27}.

Example: A case where the setting is made as follows.
- IRQ0 interrupt was selected as a transfer start trigger of DMAC0
- Dmac0IntFunc was specified as the DMA interrupt notification function name
- An extended repeat area is specified for the transfer source and destination.

```
#include "R_PG_default.h" //Include "R_PG_<project name>.h" to use this function.
void func(void)
{
    //Set up DMAC0
    R_PG_DMAC_Set_C0();

    //Set IRQ0
    R_PG_ExtInterrupt_Set_IRQ0();

    // Make DMAC0 be ready for the transfer start trigger
    R_PG_DMAC_Activate_C0();
}
//DMA interrupt notification function
void Dmac0IntFunc (void)
{
    //Suspend the DMA transfer
    R_PG_DMAC_Suspend_C0();

    //Change the DMAC0 settings
    R_PG_DMAC_SetSrcAddress_C0( src_address );    //Source address
    R_PG_DMAC_SetDestAddress_C0( dest_address );    //Destination address
    R_PG_DMAC_SetTransferCount_C0( tr_count );    //Transfer counter
    R_PG_DMAC_SetExtendedRepeatSrc_C0( src_repeat );    //Source extended repeat size
    R_PG_DMAC_SetExtendedRepeatDest_C0( dest_repeat ); //Destination extended repeat size

    // Make DMAC0 be ready for the transfer start trigger
    R_PG_DMAC_Activate_C0();
}
```

5.8.20 R_PG_DMAC_SetExtendedRepeatDest_C<*channel number*>

Definition: bool R_PG_DMAC_SetExtendedRepeatDest_C<*channel number*>(uint32_t area)
< *channel number* > : 0 to 3

Description: Set the destination address extended repeat value

Conditions for output: An extended repeat area is specified for the transfer destination.

Parameter

uint32_t area	The destination address extended repeat value to be set

Return value

true	Setting was made correctly
false	Setting failed

File for output: R_PG_DMAC_C<*channel number*>.c
<*channel number*>: 0 to 3

RPDL function: R_DMAC_Control

Details
- This function sets the destination address extended repeat value.
- The value can be any power of 2, from 2^1 to 2^{27}.

Example: A case where the setting is made as follows.
- IRQ0 interrupt was selected as a transfer start trigger of DMAC0
- Dmac0IntFunc was specified as the DMA interrupt notification function name
- An extended repeat area is specified for the transfer source and destination.

```
#include "R_PG_default.h" //Include "R_PG_<project name>.h" to use this function.
void func(void)
{
    //Set up DMAC0
    R_PG_DMAC_Set_C0();

    //Set IRQ0
    R_PG_ExtInterrupt_Set_IRQ0();

    // Make DMAC0 be ready for the transfer start trigger
    R_PG_DMAC_Activate_C0();
}
//DMA interrupt notification function
void Dmac0IntFunc (void)
{
    //Suspend the DMA transfer
    R_PG_DMAC_Suspend_C0();

    //Change the DMAC0 settings
    R_PG_DMAC_SetSrcAddress_C0( src_address );    //Source address
    R_PG_DMAC_SetDestAddress_C0( dest_address );    //Destination address
    R_PG_DMAC_SetTransferCount_C0( tr_count );    //Transfer counter
    R_PG_DMAC_SetExtendedRepeatSrc_C0( src_repeat );    //Source extended repeat size
    R_PG_DMAC_SetExtendedRepeatDest_C0( dest_repeat );    //Destination extended repeat size

    // Make DMAC0 be ready for the transfer start trigger
    R_PG_DMAC_Activate_C0();
}
```

5.8.21 R_PG_DMAC_StopModule_C<*channel number*>

Definition bool R_PG_DMAC_StopModule_C<*channel number*> (void)
< *channel number* > : 0 to 3

Description Stop the DMAC channel

Parameter None

Return value

true	Stopping succeeded.
false	Stopping failed.

File for output R_PG_DMAC_C<*channel number*>.c
<*channel number*>: 0 to 3

RPDL function R_DMAC_Destroy

Details
- Stops the DMAC channel.
- If all DMAC channels and DTC are stopped, DMAC and DTC shall be module-stop state.
- If another peripheral is being used to trigger a DMA transfer, stop the trigger sources before calling this function.

Example A case where the setting is made as follows.
- The software trigger was selected as a transfer start trigger of DMAC0
- Dmac0IntFunc was specified as the DMA interrupt notification function name

```
#include "R_PG_default.h" //Include "R_PG_<project name>.h" to use this function.
void func(void)
{
    //Set up DMAC0
    R_PG_DMAC_Set_C0();
    //Start the DMA transfer of DMAC0
    R_PG_DMAC_StartTransfer_C0();
}
//DMA interrupt notification function
void Dmac0IntFunc (void)
{
    //Stop the DMAC0
    R_PG_DMAC_StopModule_C0();
}
```

5.9 Data Transfer Controller (DTCa)

5.9.1 R_PG_DTC_Set

Definition: bool R_PG_DTC_Set (void)

Description: Set the common options for DTC

Parameter: None

Return value:

true	Setting was made correctly
false	Setting failed

File for output: R_PG_DTC.c

RPDL function: R_DTC_Set

Details:

- Releases DTC and DMAC from the module-stop state.
- Before calling other functions of DTC, call this function.
- This function configures the read skip control, address mode and the DTC vector table base address.

Example: A case where the setting is made as follows.

- The DTC vector table address has been set to 2000h.
- The transfer setting of which the transfer start trigger is IRQ0 has been made.

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

//DTC vector table
#pragma address dtc_vector_table = 0x00002000
uint32_t dtc_vector_table [256];

//Set up the DTC
void func(void)
{
    // Set the common options for DTC
    R_PG_DTC_Set();

    //Make the transfer setting of which the transfer start trigger is IRQ0
    R_PG_DTC_Set_IRQ0();

    //Make DTC be ready for the transfer start trigger
    R_PG_DTC_Activate();

    //Set up IRQ0
    R_PG_ExtInterrupt_Set_IRQ0();
}
```

5.9.2 R_PG_DTC_Set_<*trigger source*>

Definition bool R_PG_DTC_Set_<*trigger source*> (void)

< *trigger source* >

SWINT	Software interrupt
CMI0 to 3	CMT0 to 3 compare match interrupt
DnFIFO0	USB0 DMA transfer request n n: 0 or 1
SPRI0 to 2	RSPI0 to 2 receive interrupt
SPTI0 to 2	RSPI0 to 2 transmit interrupt
IRQ0 to 15	External interrupts
ADI0	AD0 A/D conversion end interrupt
S12ADI0	S12AD scan end interrupt
TGI0A to D	TPU0 input capture/compare match A to D interrupt
TGI1A or B	TPU1 input capture/compare match A or B interrupt
TGI2A or B	TPU2 input capture/compare match A or B interrupt
TGI3A to D	TPU3 input capture/compare match A to D interrupt
TGI4A or B	TPU4 input capture/compare match A or B interrupt
TGI5A or B	TPU5 input capture/compare match A or B interrupt
TGI6n/TGIn0	TPU6/MTU0 input capture/compare match n interrupt n: A to D
TGI7n/TGIn1	TPU7/MTU1 input capture/compare match n interrupt n: A or B
TGI8n/TGIn2	TPU8/MTU2 input capture/compare match n interrupt n: A or B
TGI9n/TGIn3	TPU9/MTU3 input capture/compare match n interrupt n: A to D
TGI10n/TGIn4	TPU10/MTU4 input capture/compare match n interrupt n: A or B
TGIA0 to D0	MTU0 input capture/compare match A to D interrupt
TGIA1 or B1	MTU1 input capture/compare match A or B interrupt
TGIA2 or B2	MTU2 input capture/compare match A or B interrupt
TGIA3 to D3	MTU3 input capture/compare match A to D interrupt
TGIA4 to D4	MTU4 input capture/compare match A to D interrupt
TCIV4	MTU4 overflow/underflow interrupt
TGIU5 to W5	MTU5 input capture/compare match U to W interrupt
TGI11n	TPU11 input capture/compare match n interrupt n: A or B
CMIA0 or B0	TMR0 compare match A or B interrupt
CMIA1 or B1	TMR1 compare match A or B interrupt
CMIA2 or B2	TMR2 compare match A or B interrupt
CMIA3 or B3	TMR3 compare match A or B interrupt
ICRXI0 to 3	RIIC0 to 3 receive data full interrupt
ICTXI0 to 3	RIIC0 to 3 transmit data empty interrupt
DMAC0I to 3I	DMACA0 to 3 interrupt
RXI0 to 12	SCI0 to 12 receive data full interrupt
TXI0 to 12	SCI0 to 12 transmit data empty interrupt

Description Set the DTC transfer data

Parameter None

Return value

true	Setting was made correctly
false	Setting failed

File for output R_PG_DTC.c

RPDL function R_DTC_Create

Details

- Store the transfer data that will be triggered by transfer start trigger in specified address.
- The transfer data of the chain transfer will also be stored.
- If other transfer data has already been stored in the specified address, new data will be overwritten.
- This function does not set any interrupts used for transfer start triggers. Set up interrupts by each peripheral function.
- Select DTC as the request destination of interrupts used for the transfer start trigger.
- Call this function before configuring the peripherals that will be involved in the data transfer.

Example

A case where the setting is made as follows.

- The DTC vector table address has been set to 2000h.
- The transfer setting of which the transfer start trigger is IRQ0 has been made.
- The transfer setting of which the transfer start trigger is IRQ1 has been made.

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

#pragma address dtc_vector_table = 0x00002000
uint32_t dtc_vector_table [256];    //DTC vector table

//Set up the DTC
void func(void)
{
    // Set the common options for DTC
    R_PG_DTC_Set();

    //Make the transfer setting of which the transfer start trigger is IRQ
    R_PG_DTC_Set_IRQ0();
    R_PG_DTC_Set_IRQ1();

    //Make DTC be ready for the transfer start trigger
    R_PG_DTC_Activate();

    //Set up IRQ0 and IRQ1
    R_PG_ExtInterrupt_Set_IRQ0();
    R_PG_ExtInterrupt_Set_IRQ1();
}
```

5.9.3 R_PG_DTC_Activate

Definition bool R_PG_DTC_Activate (void)

Description Make the DTC be ready for the transfer start trigger

Parameter None

Return value

true	Setting was made correctly
false	Setting failed

File for output R_PG_DTC.c

RPDL function R_DTC_Control

Details

- Makes the DTC be ready for the transfer start trigger.
- Call R_PG_DTC_Set_<*trigger source*> to store the transfer data before calling this function.

Example A case where the setting is made as follows.

- The DTC vector table address has been set to 2000h.
- The transfer setting of which the transfer start trigger is IRQ0 has been made.
- “Request is transferred to CPU when specified transfer is completed” has been selected in the interrupt setting.
- The chain transfer has been disabled.
- Irq0IntFunc has been specified as an IRQ0 interrupt notification function name.

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

//DTC vector table
#pragma address dtc_vector_table = 0x00002000
uint32_t dtc_vector_table [256];

//Set up the DTC
void func(void)
{
    // Set the common options for DTC
    R_PG_DTC_Set();

    //Make the transfer setting of which the transfer start trigger is IRQ0
    R_PG_DTC_Set_IRQ0();

    //Make DTC be ready for the transfer start trigger
    R_PG_DTC_Activate();

}

void Irq0IntFunc(void)
{
    //Disable the IRQ0
    //(After specified number of transfer completes, transfer will be executed
    // when the trigger is input. To stop the data transfer, disable the interrupt.)
    R_PG_ExtInterrupt_Disable_IRQ0();
}
```

5.9.4 R_PG_DTC_SuspendTransfer

Definition bool R_PG_DTC_SuspendTransfer (void)

Description Stop the data transfer

Parameter None

Return value

true	Stopping succeeded
false	Stopping failed

File for output R_PG_DTC.c

RPDL function R_DTC_Control

Details
- Stops the data transfer.
- If transfer is stopped during data transfer, the accepted start request is active until the processing is completed.
- Call R_DTC_Activate to enable the transfer.

Example A case where the setting is made as follows.
- The DTC vector table address has been set to 2000h.
- The transfer setting of which the transfer start trigger is IRQ0 has been made.

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

//DTC vector table
#pragma address dtc_vector_table = 0x00002000
uint32_t dtc_vector_table [256];

//Set up the DTC
void func(void)
{
      // Set the common options for DTC
      R_PG_DTC_Set();

      //Make the transfer setting of which the transfer start trigger is IRQ0
      R_PG_DTC_Set_IRQ0();

      //Make DTC be ready for the transfer start trigger
      R_PG_DTC_Activate();

      //Set up IRQ0
      R_PG_ExtInterrupt_Set_IRQ0();
}

//Suspend the DTC transfer
void func2(void)
{
      R_PG_DTC_SuspendTransfer();
}

//Resume the DTC transfer
void func3(void)
{
      R_PG_DTC_Activate();
}
```

5.9.5 R_PG_DTC_GetTransmitStatus

Definition bool R_PG_DTC_GetTransmitStatus (uint8_t * vector, bool * active)

Description Get transfer status

Parameter

uint8_t * vector	The address of storage area for the vector number of current data transfer (Valid when "* active" is 1)
bool * active	The address of storage area for the progress flag. If this value is 1, the data transfer is processed.

Return value

true	Acquisition succeeded
false	Acquisition failed

File for output R_PG_DTC.c

RPDL function R_DTC_GetStatus

Details

- This function acquires the active flag and the vector number of the current data transfer.

Example

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

uint8_t vector;
bool active;

void func(void)
{
    //Get the DTC transfer status
    R_PG_DTC_GetTransmitStatus ( &vector, &active);

    if(active){
        switch( vector ){
            case 64:
                //Processing when the transfer of vector 64 is in progress
                break;
            case 65:
                //Processing when the transfer of vector 65 is in progress
                break;
            default:
        }
    }
}
```

5.9.6 R_PG_DTC_StopModule

Definition bool R_PG_DTC_StopModule (void)

Description Shut down the DTC

Parameter None

Return value

true	Shutting down succeeded
false	Shutting down failed

File for output R_PG_DTC.c

RPDL function R_DTC_Destroy

Details
- This function shuts down the DTC and places it in the module-stop state.
- Disable the interrupt used for transfer start trigger before calling this function.
- This function will also shut down the DMAC.

Example A case where the setting is made as follows.
- The DTC vector table address has been set to 2000h.
- The transfer setting of which the transfer start trigger is IRQ0 has been made.
- The transfer setting of which the transfer start trigger is IRQ1 has been made.

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

//DTC vector table
#pragma address dtc_vector_table = 0x00002000
uint32_t dtc_vector_table [256];

void func(void)
{
    // Set the common options for DTC
    R_PG_DTC_Set();

    //Make the transfer setting of which the transfer start trigger is IRQ0
    R_PG_DTC_Set_IRQ0();

    //Make the transfer setting of which the transfer start trigger is IRQ1
    R_PG_DTC_Set_IRQ1();

    //Make DTC be ready for the transfer start trigger
    R_PG_DTC_Activate();

    //Set up IRQ0 and IRQ1
    R_PG_ExtInterrupt_Set_IRQ0();
    R_PG_ExtInterrupt_Set_IRQ1();
}

void func2(void)
{
    //Disable IRQ0 and IRQ1
    R_PG_ExtInterrupt_Disable_IRQ0();
    R_PG_ExtInterrupt_Disable_IRQ1();
    //Shut down the DTC
    R_PG_DTC_StopModule();
}
```

5.10 I/O Ports

5.10.1 R_PG_IO_PORT_Set_P<*port number*>

Definition: bool R_PG_IO_PORT_Set_P<*port number*> (void)
<*port number*>: 0 to 9, A to H and J to L

Description: Set up the I/O port

Conditions for output: When [Used as an I/O port] of one or more pins are specified in the port in the GUI. However, when only P35 is specified in the PORT3, R_PG_IO_PORT_Set_P3 is not generated.

Parameter: None

Return value:

true	Setting was made correctly.
false	Setting failed.

File for output: R_PG_IO_PORT_P<*port number*>.c
<*port number*>: 0 to 9, A to H and J to L

RPDL function: R_IO_PORT_Set

Details:

- Selects the direction (input or output), input pull-up resistor, output type, and drive capacity for pins for which [Used as an I/O port] was specified in the GUI.
- This function is used to set all pins for which [Used as I/O port] has been selected in a port.

Example:

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

void func(void)
{
    //Configure I/O port pins that are not available.
    R_PG_IO_PORT_SetPortNotAvailable();

    //Set P0.
    R_PG_IO_PORT_Set_P0();
}
```

5.10.2 R_PG_IO_PORT_Set_P<*port number*><*pin number*>

Definition: bool R_PG_IO_PORT_Set_P<*port number*><*pin number*> (void)
<*port number*>: 0 to 9, A to H and J to L
<*pin number*>: 0 to 7

Description: Set up the I/O port pin

Conditions for output: When [Used as an I/O port] is specified in the GUI.
However, R_PG_IO_PORT_Set_P35 is not generated.

Parameter: None

Return value:

true	Setting was made correctly.
false	Setting failed.

File for output: R_PG_IO_PORT_P<*port number*>.c
<*port number*>: 0 to 9, A to H and J to L

RPDL function: R_IO_PORT_Set

Details:
- Selects the direction (input or output), input pull-up resistor, output type, and drive capacity for pins for which [Used as an I/O port] was specified in the GUI.
- The setting only applies to one pin.

Example:

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

void func(void)
{
    //Set P05.
    R_PG_IO_PORT_Set_P05();

    //Set P07.
    R_PG_IO_PORT_Set_P07();
}
```

5.10.3 R_PG_IO_PORT_Read_P<*port number*>

Definition: bool R_PG_IO_PORT_Read_P<*port number*> (uint8_t * data)
<*port number*>: 0 to 9, A to H and J to L

Description: Read data from Port Input Register

Conditions for output: When [Used as an I/O port] of one or more pins are specified in the port in the GUI.

Parameter

uint8_t * data	Destination for storage of the read pin state

Return value

true	Reading proceeded correctly.
false	Reading failed.

File for output: R_PG_IO_PORT_P<*port number*>.c
<*port number*>: 0 to 9, A to H and J to L

RPDL function: R_IO_PORT_Read

Details
- Reads Port Input Register to acquire the states of the pins. (Unit: Port)

Example

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

uint8_t data;

void func(void)
{
    //Acquire the states of P0 pins.
    R_PG_IO_PORT_Read_P0( &data );
}
```

5.10.4 R_PG_IO_PORT_Read_P<*port number*><*pin number*>

Definition: bool R_PG_IO_PORT_Read_P<*port number*><*pin number*> (uint8_t * data)
<*port number*>: 0 to 9, A to H and J to L
<*pin number*>: 0 to 7

Description: Read 1-bit data from Port Input Register

Conditions for output: When [Used as an I/O port] of one or more pins are specified in the port in the GUI, the function of all existing pins in the port is generated.

Parameter

uint8_t * data	Destination for storage of the read pin state

Return value

true	Reading proceeded correctly.
false	Reading failed.

File for output: R_PG_IO_PORT_P<*port number*>.c
(<*port number*>: 0 to 9, A to H and J to L)

RPDL function: R_IO_PORT_Read

Details
- Reads Port Input Register to acquire the state of one pin.
- The value is stored in the lowest-order bit of *data.

Example

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

uint8_t data_p05, data_p07;

void func(void)
{
    //Acquire the state of pin P05.
    R_PG_IO_PORT_Read_P05( & data_p05);

    //Acquire the state of pin P07.
    R_PG_IO_PORT_Read_P07( & data_p07);
}
```

5.10.5 R_PG_IO_PORT_Write_P<*port number*>

Definition: bool R_PG_IO_PORT_Write_P<*port number*> (uint8_t data)
<*port number*>: 0 to 9, A to H and J to L

Description: Write data to Port Output Data Register

Conditions for output: When [Used as an I/O port] of one or more pins are specified in the port in the GUI. However, when only P35 is specified in the PORT3, R_PG_IO_PORT_Write_P3 is not generated.

Parameter

uint8_t data	Value to be written

Return value

true	Writing proceeded correctly.
false	Writing failed.

File for output: R_PG_IO_PORT_P<*port number*>.c
<*port number*>: 0 to 9, A to H and J to L

RPDL function: R_IO_PORT_Write

Details
- Writes a value to Port Output Data Register. A value written to the register is output from the output port.
- Specify “0” for b5 of the argument when calling R_PG_IO_PORT_Write_P3.

Example

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

void func(void)
{
    //Set P0.
    R_PG_IO_PORT_Set_P0();

    //Output 0xa0 from P0.
    R_PG_IO_PORT_Write_P0( 0xa0 );
}
```

5.10.6 R_PG_IO_PORT_Write_P<*port number*><*pin number*>

Definition: bool R_PG_IO_PORT_Write_P<*port number*><*pin number*> (uint8_t data)
<*port number*>: 0 to 9, A to H and J to L
<*pin number*>: 0 to 7

Description: Write 1-bit data to Port Output Data Register

Conditions for output: When [Used as an I/O port] is specified in the GUI.
However, R_PG_IO_PORT_Write_P35 is not generated.

Parameter

uint8_t data	Value to be written

Return value

true	Writing proceeded correctly.
false	Writing failed.

File for output: R_PG_IO_PORT_P<*port number*>.c
<*port number*>: 0 to 9, A to H and J to L

RPDL function: R_IO_PORT_Write

Details
- Writes a value to Port Output Data Register. A value written to an output port is output. Store the value in the lowest-order bit of data.

Example

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

void func(void)
{
    //Set P05.
    R_PG_IO_PORT_Set_P05();

    //Set P07.
    R_PG_IO_PORT_Set_P07();

    //Output low level from P05.
    R_PG_IO_PORT_Write_P05( 0x00 );

    //Output high level from P07.
    R_PG_IO_PORT_Write_P07( 0x01 );
}
```

5.10.7 R_PG_IO_PORT_SetPortNotAvailable

Definition	bool R_PG_IO_PORT_SetPortNotAvailable (void)
Description	Configure I/O port pins that are not available.
Parameter	None

Return value

true	Setting was made correctly

File for output	R_PG_IO_PORT.c
RPDL function	R_IO_PORT_NotAvailable

Details

- All ports that are not available on smaller packages will be configured for CMOS-type low-level output.
- When using packages less than 176-pin, call this function first.

Example Refer to the example of R_PG_IO_PORT_Set_P<*port number*>.

5.11 Multi-Function Timer Pulse Unit 2 (MTU2a)

5.11.1 R_PG_Timer_Set_MTU_U<*unit number*>_<*channels*>

Definition bool R_PG_Timer_Set_MTU_U<*unit number*>_<*channels*> (void)
<*unit number*>: 0
<*channels*>: C0 to C5
C3_C4 (Complementary PWM mode or reset-synchronized PWM mode)

Description Set up the MTU

Parameter None

Return value

true	Setting was made correctly
false	Setting failed

File for output R_PG_Timer_MTU_U<*unit number*>_C<*channel number*>.c
<*unit number*>: 0
<*channel number*>: 0 to 5

RPDL function R_MTU2_Set, R_MTU2_Create

Details

- Releases the MTU from the module-stop and makes initial settings.
- Interrupts of the MTU are set by this function. When the name of the interrupt notification function has been specified in the GUI, if an interrupt occurs in the CPU, the function having the specified name will be called. Create the interrupt notification function as follows:
  void <*name of the interrupt notification function*> (void)
  For the interrupt notification function, note the contents of section Notes on Notification Functions.
- If the interrupt propriety level is set to 0 in the GUI, a CPU interrupt does not occur. The state of a request flag can be acquired by calling R_PG_Timer_GetRequestFlag_MTU_U<*unit number*>_C<*channel number*>.
- When counting driven by an externally input clock, the external reset signal, input capture, or pulse output is in use, the pin to be used is set in this function.
- To start the count operation, call R_PG_Timer_StartCount_MTU_U<*unit number*>_C<*channel number*>(_<*phase*>) or R_PG_Timer_SynchronouslyStartCount_MTU_U<*unit number*> after calling this function.
- In complementary PWM mode or reset-synchronized PWM mode, paired channels are set up in the same time. Channels 3 and 4 are set up by R_PG_Timer_Set_MTU_U0_C3_C4.
- In complementary PWM mode or reset-synchronized PWM mode, PWM output is disabled in the initial state. To enable the pin output, call R_PG_Timer_ControlOutputPin_MTU_U<*unit number*>_<*channels*> before starting the count operation.

Example 1 A case where the setting is made as follows.

- MTU channel 1 was set up in normal mode
- Mtu1IcCmAIntFunc was specified as a compare match A interrupt notification function name

```
#include "R_PG_default.h" //Include "R_PG_<project name>.h" to use this function.

void func(void)
{
    R_PG_Timer_Set_MTU_U0_C1();       //Set up the MTU1
    R_PG_Timer_StartCount_MTU_U0_C1();       // Start the count operation
}

void Mtu1IcCmAIntFunc(void)
{
    //Processing in response to a compare match A interrupt
}
```

Example 2 A case where the setting is made as follows.

- MTU channel 3 and 4 were set up in complementary PWM mode

```
#include "R_PG_default.h" //Include "R_PG_<project name>.h" to use this function.

void func(void)
{
    //Set up the MTU3 and MTU4 in complementary PWM mode
    R_PG_Timer_Set_MTU_U0_C3_C4 ();

    //Enable PWM output pin 1 positive and negative phase
    R_PG_Timer_ControlOutputPin_MTU_U0_C3_C4(
        1, //p1 : enable
        1, //n1 : enable
        0, //p2 : disable
        0, //n2 : disable
        0, //p3 : disable
        0 //n3 : disable
    );

    // Start the MTU3 and 4 count operation
    R_PG_Timer_SynchronouslyStartCount_MTU_U0(
        0, //ch0
        0, //ch1
        0, //ch2
        1, //ch3
        1 //ch4
    );
}
```

5.11.2 R_PG_Timer_StartCount_MTU_U<*unit number*>_C<*channel number*>(_<*phase*>)

Definition

bool R_PG_Timer_StartCount_MTU_U<*unit number*>_C<*channel number*> (void)
<*unit number*>: 0
<*channel number*>: 0 to 5

bool R_PG_Timer_StartCount_MTU_U<*unit number*>_C<*channel number*>_<*phase*> (void)
<*unit number*>: 0
<*channel number*>: 5
<*phase*>: U, V or W

Description

Start the MTU count operation

Parameter

None

Return value

true	Setting was made correctly
false	Setting failed

File for output

R_PG_Timer_MTU_U<*unit number*>_C<*channel number*>.c
<*unit number*>: 0
<*channel number*>: 0 to 5

RPDL function

R_MTU2_ControlChannel

Details

- Starts the MTU count operation.
- Call R_PG_Timer_Set_MTU_U<*unit number*>_<*channels*> to make the initial settings before calling this function.
- In complementary PWM mode or reset-synchronized PWM mode, start the count operation of paired 2 channels simultaneously by R_PG_Timer_SynchronouslyStartCount_MTU_U<*unit number*>.
- R_PG_Timer_StartCount_MTU_U0_C5 can start the count of U, V, and W phase simultaneously.

Example

A case where the setting is made as follows.

- MTU channel 1 was set up
- Mtu1IcCmAIntFunc was specified as the compare match A interrupt notification function name

```
#include "R_PG_default.h" //Include "R_PG_<project name>.h" to use this function.

void func(void)
{
    R_PG_Timer_Set_MTU_U0_C1();       //Set up the MTU1
    R_PG_Timer_StartCount_MTU_U0_C1();       // Start the count operation
}

void Mtu1IcCmAIntFunc(void)
{
    R_PG_Timer_HaltCount_MTU_U0_C1();       //Halt the count operation
    func_cmA();       //Processing in response to a compare match A interrupt
    R_PG_Timer_StartCount_MTU_U0_C1();       //Resume the count operation
}
```

5.11.3 R_PG_Timer_SynchronouslyStartCount_MTU_U<*unit number*>

Definition

bool R_PG_Timer_SynchronouslyStartCount_MTU_U<*unit number*>
(bool ch0, bool ch1, bool ch2, bool ch3, bool ch4)
<*unit number*>: 0

Description

Start the MTU count operation of two or more channels simultaneously

Parameter

bool ch0	Count operation of channel 0 (0:Do not start count 1:Start count)
bool ch1	Count operation of channel 1 (0:Do not start count 1:Start count)
bool ch2	Count operation of channel 2 (0:Do not start count 1:Start count)
bool ch3	Count operation of channel 3 (0:Do not start count 1:Start count)
bool ch4	Count operation of channel 4 (0:Do not start count 1:Start count)

Return value

true	Setting was made correctly
false	Setting failed

File for output

R_PG_Timer_MTU_U<*unit number*>.c
<*unit number*>: 0

RPDL function

R_MTU2_ControlUnit

Details

- Starts the MTU count operation of two or more channels simultaneously.
- Call R_PG_Timer_Set_MTU_U<*unit number*>_<*channels*> to make the initial settings before calling this function.
- In complementary PWM mode or reset-synchronized PWM mode, start the count operation of paired 2 channels simultaneously by this function.

Example

Refer to the example 2 of R_PG_Timer_Set_MTU_U<*unit number*>_<*channels*>

5.11.4 R_PG_Timer_HaltCount_MTU_U<*unit number*>_C<*channel number*>(_<*phase*>)

Definition

bool R_PG_Timer_HaltCount_MTU_U<*unit number*>_C<*channel number*> (void)
<*unit number*>: 0
<*channel number*>: 0 to 5

bool R_PG_Timer_HaltCount_MTU_U<*unit number*>_C<*channel number*>_<*phase*> (void)
<*unit number*>: 0
<*channel number*>: 5
<*phase*>: U, V or W

Description

Halt the MTU count operation

Parameter

None

Return value

true	Halting succeeded.
false	Halting failed.

File for output

R_PG_Timer_MTU_U<*unit number*>_C<*channel number*>.c
<*unit number*>: 0
<*channel number*>: 0 to 5

RPDL function

R_MTU2_ControlChannel

Details

- Halts the MTU count operation.
- To make the MTU resume counting, call R_PG_Timer_StartCount_MTU_U<*unit number*>_C<*channel number*>(_<*phase*>) or R_PG_Timer_SynchronouslyStartCount_MTU_U<*unit number*>.
- R_PG_Timer_HaltCount_MTU_U0_C5 can stop the count of U, V, and W phase simultaneously.

Example

A case where the setting is made as follows.

- MTU channel 1 was set up
- Mtu1IcCmAIntFunc was specified as the compare match A interrupt notification function name

```
#include "R_PG_default.h" //Include "R_PG_<project name>.h" to use this function.

void func(void)
{
    R_PG_Timer_Set_MTU_U0_C1();       //Set up the MTU1
    R_PG_Timer_StartCount_MTU_U0_C1();       // Start the count operation
}

void Mtu1IcCmAIntFunc(void)
{
    R_PG_Timer_HaltCount_MTU_U0_C1();       //Halt the count operation
    func_cmA();       //Processing in response to a compare match A interrupt
    R_PG_Timer_StartCount_MTU_U0_C1();       //Resume the count operation
}
```

5.11.5 R_PG_Timer_GetCounterValue_MTU_U<*unit number*>_C<*channel number*>

Definition

bool R_PG_Timer_GetCounterValue_MTU_U<*unit number*>_C<*channel number*>
(uint16_t * counter_val)
<*unit number*>: 0
<*channel number*>: 0 to 4

bool R_PG_Timer_GetCounterValue_MTU_U<*unit number*>_C<*channel number*>
(uint16_t * counter_u_val, uint16_t * counter_v_val, uint16_t * counter_w_val)
<*unit number*>: 0
<*channel number*>: 5

Description

Acquire the MTU counter value

Parameter

For MTU0 to MTU4

uint16_t * counter_val	Destination for storage of the counter value

For MTU5

uint16_t * counter_u_val	Destination for storage of the counter U value
uint16_t * counter_v_val	Destination for storage of the counter V value
uint16_t * counter_w_val	Destination for storage of the counter value

Return value

true	Acquisition of the counter value succeeded.
false	Acquisition of the counter value failed.

File for output

R_PG_Timer_MTU_U<*unit number*>_C<*channel number*>.c
<*unit number*>: 0
<*channel number*>: 0 to 5

RPDL function

R_MTU2_ReadChannel

Details

- Acquires the counter value of a MTU.

Example

A case where the setting is made as follows.

- MTU channel 0 was set up
- Set TGRA as an input capture register and enable an input capture A interrupt
- Mtu0IcCmAIntFunc was specified as the input capture A interrupt notification function name

```
#include "R_PG_default.h" //Include "R_PG_<project name>.h" to use this function.

uint16_t counter_val;

void func(void)
{
      R_PG_Timer_Set_MTU_U0_C0();       //Set up the MTU0
      R_PG_Timer_StartCount_MTU_U0_C0();       // Start the count operation
}

void Mtu0IcCmAIntFunc(void)
{
      // Acquire the value of the MTU0 counter
      R_PG_Timer_GetCounterValue_MTU_U0_C0( & counter_val );
}
```

5.11.6 R_PG_Timer_SetCounterValue_MTU_U<*unit number*>_C<*channel number*>(_<*phase*>)

Definition

bool R_PG_Timer_SetCounterValue_MTU_U<*unit number*>_C<*channel number*>
(uint16_t counter_val)

<*unit number*>: 0 <*channel number*>: 0 to 4

bool R_PG_Timer_SetCounterValue_MTU_U<*unit number*>_C<*channel number*>_<*phase*>
(uint16_t counter_val)

<*unit number*>: 0 <*channel number*>: 5 <*phase*>: U, V or W

bool R_PG_Timer_SetCounterValue_MTU_U<*unit number*>_C<*channel number*>
(uint16_t counter_u_val, uint16_t counter_v_val, uint16_t counter_w_val)

<*unit number*>: 0 <*channel number*>: 5

Description

Set the MTU counter value

Parameter

For MTU0 to MTU7

uint16_t counter_val	Value to be written to the counter

For MTU5

uint16_t counter_u_val	Value to be written to the counter U
uint16_t counter_v_val	Value to be written to the counter V
uint16_t counter_w_val	Value to be written to the counter W

Return value

true	Setting of the counter value succeeded.
false	Setting of the counter value failed.

File for output

R_PG_Timer_MTU_U<*unit number*>_C<*channel number*>.c

<*unit number*>: 0

<*channel number*>: 0 to 5

RPDL function

R_MTU2_ControlChannel

Details

- Set the counter value of a MTU.

Example

A case where the setting is made as follows.

- MTU channel 1 was set up
- Set TGRA as an output compare register and enable a compare match A interrupt
- Mtu1IcCmAIntFunc was specified as the compare match A interrupt notification function name

```
#include "R_PG_default.h" //Include "R_PG_<project name>.h" to use this function.

void func (void)
{
    R_PG_Timer_Set_MTU_U0_C1();    //Set up the MTU1
    R_PG_Timer_StartCount_MTU_U0_C1();    // Start the count operation
}

void Mtu1IcCmAIntFunc(void)
{
    R_PG_Timer_SetCounterValue_MTU_U0_C1( 0 );    //Clear the counter
}
```

5.11.7 R_PG_Timer_GetRequestFlag_MTU_U<*unit number*>_C<*channel number*>

Definition

bool R_PG_Timer_GetRequestFlag_MTU_U<*unit number*>_C<*channel number*>
(bool* cm_ic_a, bool* cm_ic_b, bool* cm_ic_c, bool* cm_ic_d,
bool* cm_e, bool* cm_f, bool* ov, bool* un);
<*unit number*>: 0
<*channel number*>: 0 to 4

bool R_PG_Timer_GetRequestFlag_MTU_U<*unit number*>_C<*channel number*>
(bool* cm_ic_u, bool* cm_ic_v, bool* cm_ic_w);
<*unit number*>: 0
<*channel number*>: 5

Description

Acquire and clear the MTU interrupt flags

Parameter

bool* cm_ic_a	The address of storage area for the compare match/input capture A flag
bool* cm_ic_b	The address of storage area for the compare match/input capture B flag
bool* cm_ic_c	The address of storage area for the compare match/input capture C flag
bool* cm_ic_d	The address of storage area for the compare match/input capture D flag
bool* cm_e	The address of storage area for the compare match E flag
bool* cm_f	The address of storage area for the compare match F flag
bool* ov	The address of storage area for the overflow flag
bool* un	The address of storage area for the underflow flag
bool* cm_ic_u	The address of storage area for the compare match/input capture U flag
bool* cm_ic_v	The address of storage area for the compare match/input capture V flag
bool* cm_ic_w	The address of storage area for the compare match/input capture W flag

Available flags for each channel are as follows.

MTU0	cm_ic_a to cm_ic_d, cm_e, cm_f, and ov
MTU1, 2	cm_ic_a, cm_ic_b, ov, and un
MTU3, 4	cm_ic_a to cm_ic_d, and ov
MTU5	cm_ic_u, cm_ic_v, and cm_ic_w
MTU3 (complementary PWM mode and reset-synchronized PWM mode)	cm_ic_a to cm_ic_d
MTU4 (complementary PWM mode and reset-synchronized PWM mode)	cm_ic_a to cm_ic_d, and un

Return value

true	Acquisition of the flags succeeded
false	Acquisition of the flags failed

File for output

R_PG_Timer_MTU_U<*unit number*>_C<*channel number*>.c
<*unit number*>: 0
<*channel number*>: 0 to 5

RPDL function

R_MTU2_ReadChannel

Details

- This function acquires the interrupt flags of MTU.
- All flags will be cleared in this function.
- Specify the address of storage area for the flags to be acquired.
  Specify 0 for a flag that is not required.

Example A case where the setting is made as follows.

- MTU channel 1 was set up
- TGRA is set as an output compare register and the compare match interrupt is enabled
- The priority level of compare match interrupt is set to 0

```
#include "R_PG_default.h" //Include "R_PG_<project name>.h" to use this function.

bool cma_flag;

void func(void)
{
    R_PG_Timer_Set_MTU_U0_C1();    //Set up the MTU1
    R_PG_Timer_StartCount_MTU_U0_C1();    // Start the count operation

    //Wait for the compare match A
    do{
        R_PG_Timer_GetRequestFlag_MTU_U0_C1(
            & cma_flag, //a
            0, //b
            0, //c
            0, //d
            0, //e
            0, //f
            0, //e
            0, //ov
            0 //un
        );
    } while( !cma_flag );

    //Processing in response to a compare match A

}
```

5.11.8 R_PG_Timer_StopModule_MTU_U<*unit number*>

Definition: bool R_PG_Timer_StopModule_MTU_U<*unit number*> (void)
<*unit number*>: 0

Description: Shut down the MTU unit

Parameter: None

Return value:

true	Shutting down succeeded
false	Shutting down failed

File for output: R_PG_Timer_MTU_U<*unit number*>.c
<*unit number*>: 0

RPDL function: R_MTU2_Destroy

Details:

- Stops a MTU and places it in the module-stop state. If two or more channels are running when this function is called, all channels will be stopped. Call R_PG_Timer_HaltCount_MTU_U<*unit number*>_C<*channel number*>(_<*phase*>) to stop a single channel.

Example: A case where the setting is made as follows.

- MTU channel 1 was set up
- Set TGRA as an output compare register and enable a compare match A interrupt
  Mtu1IcCmAIntFunc was specified as the compare match A interrupt notification function name

```
#include "R_PG_default.h" //Include "R_PG_<project name>.h" to use this function.

void func(void)
{
    R_PG_Timer_Set_MTU_U0_C1();    //Set up the MTU1
    R_PG_Timer_StartCount_MTU_U0_C1();    // Start the count operation
}

void Mtu1IcCmAIntFunc(void)
{
    // Stop the MTU unit 0
    R_PG_Timer_StopModule_MTU_U0();
}
```

5.11.9 R_PG_Timer_GetTGR_MTU_U<*unit number*>_C<*channel number*>

Definition

bool R_PG_Timer_GetRequestFlag_MTU_U<*unit number*>_C<*channel number*>
(uint16_t* tgr_a_val, uint16_t* tgr_b_val, uint16_t* tgr_c_val,
uint16_t* tgr_d_val, uint16_t* tgr_e_val, uint16_t* tgr_f_val);
<*unit number*>: 0
<*channel number*>: 0 to 4

bool R_PG_Timer_GetRequestFlag_MTU_U<*unit number*>_C<*channel number*>
(uint16_t * tgr_u_val, uint16_t * tgr_v_val, uint16_t * tgr_w_val);
<*unit number*>: 0
<*channel number*>: 5

Description

Acquire the general register value

Parameter

uint16_t* tgr_a_val	The address of storage area for the general register A value
uint16_t* tgr_b_val	The address of storage area for the general register B value
uint16_t* tgr_c_val	The address of storage area for the general register C value
uint16_t* tgr_d_val	The address of storage area for the general register D value
uint16_t* tgr_e_val	The address of storage area for the general register E value
uint16_t* tgr_f_val	The address of storage area for the general register F value
uint16_t* tgr_u_val	The address of storage area for the general register U value
uint16_t* tgr_v_val	The address of storage area for the general register V value
uint16_t* tgr_w_val	The address of storage area for the general register W value

Available arguments for each channel are as follows.

MTU0	tgr_a_val to tgr_f_val
MTU1, 2	tgr_a_val and tgr_b_val
MTU3, 4	tgr_a_val to tgr_d_val
MTU5	tgr_u_val to tgr_w_val

Return value

true	Acquisition of the flags succeeded
false	Acquisition of the flags failed

File for output

R_PG_Timer_MTU_U<*unit number*>_C<*channel number*>.c
<*unit number*>: 0
<*channel number*>: 0 to 5

RPDL function

R_MTU2_ReadChannel

Details

- This function acquires the general register value.
- Specify the address of storage area for an item to be acquired. Specify 0 for an item that is not required.

Example

A case where the setting is made as follows.

- MTU channel 0 was set up
- Set TGRA as an input capture register and enable an input capture A interrupt
- Mtu0IcCmAIntFunc was specified as the input capture A interrupt notification function name

```
#include "R_PG_default.h" //Include "R_PG_<project name>.h" to use this function.

uint16_t tgr_a_val;

void func(void)
{
	R_PG_Timer_Set_MTU_U0_C0();		//Set up the MTU0
	R_PG_Timer_StartCount_MTU_U0_C0();		// Start the count operation
}

void Mtu0IcCmAIntFunc(void)
{
	// Acquire the value of the TGRA
	R_PG_Timer_GetTGR_MTU_U0_C0(
		& tgr_a_val, //a
		0, //b
		0, //c
		0, //d
		0, //e
		0 //f
	);
}
```

5.11.10 R_PG_Timer_SetTGR_<*general register*>_MTU_U<*unit number*>_C<*channel number*>

Definition bool R_PG_Timer_SetTGR_<*general register*>_MTU_U<*unit number*>_C<*channel number*> (uint16_t value);

<*general register*>:

MTU0	: A, B, C, D, E or F
MTU1, 2	: A or B
MTU3, 4	: A, B, C or D
MTU5	: U, V or W

<*unit number*>: 0

<*channel number*>: 0 to 5

Description Set the general register value

Parameter

uint16_t value	Value to be written to the general register

Return value

true	Setting of the general register succeeded.
false	Setting of the general register failed.

File for output R_PG_Timer_MTU_U<*unit number*>_C<*channel number*>.c

<*unit number*>: 0

<*channel number*>: 0 to 5

RPDL function R_MTU2_ControlChannel

Details

- This function sets the general register value.

Example

A case where the setting is made as follows.

- MTU channel 1 was set up
- Set TGRA as an output compare register and enable a compare match A interrupt
- Mtu1IcCmAIntFunc was specified as the compare match A interrupt notification function name

```
#include "R_PG_default.h" //Include "R_PG_<project name>.h" to use this function.

void func (void)
{
    R_PG_Timer_Set_MTU_U0_C1();    //Set up the MTU1
    R_PG_Timer_StartCount_MTU_U0_C1();    // Start the count operation
}

void Mtu1IcCmAIntFunc(void)
{
    R_PG_Timer_SetTGR_A_MTU_U0_C1( 1000 );    //Set TGRA
}
```

5.11.11 R_PG_Timer_SetBuffer_AD_MTU_U<*unit number*>_C<*channel number*>

Definition

bool R_PG_Timer_SetBuffer_AD_MTU_U<*unit number*>_C<*channel number*>
(uint16_t tadcobr_a_val, uint16_t tadcobr_b_val);
<*unit number*>: 0
<*channel number*>: 4

Description

Set A/D converter start request cycle set buffer registers (TADCOBRA and TADCOBRB)

Conditions for output

The buffer transfer of A/D converter start request cycle value is enabled.

Parameter

uint16_t tadcobr_a_val	Value to be written to TADCOBRA
uint16_t tadcobr_b_val	Value to be written to TADCOBRB

Return value

true	Setting of the counter value succeeded.
false	Setting of the counter value failed.

File for output

R_PG_Timer_MTU_U<*unit number*>_C<*channel number*>.c
<*unit number*>: 0
<*channel number*>: 3(*), 4
(* complementary PWM mode and reset-synchronized PWM mode)

RPDL function

R_MTU2_ControlChannel

Details

- This function sets the TADCOBRA and TADCOBRB values.

Example

A case where the setting is made as follows.

- Buffer transfer of A/D converter start request cycle set register has been enabled

```
#include "R_PG_default.h" //Include "R_PG_<project name>.h" to use this function.

void func (void)
{
      R_PG_Timer_Set_MTU_U0_C4();    //Set up the MTU1
      R_PG_Timer_StartCount_MTU_U0_C4();    // Start the count operation
}

void Mtu1IcCmAIntFunc(void)
{
      // Set TADCOBRA and TADCOBRB
      R_PG_Timer_SetBuffer_AD_MTU_U0_C4( 0x10, 0x20 );
}
```

5.11.12 R_PG_Timer_SetBuffer_CycleData_MTU_U<*unit number*>_<*channels*>

Definition

bool R_PG_Timer_SetBuffer_CycleData_MTU_U<*unit number*>_<*channels*>
(uint16_t tcbr_val);
<*unit number*>: 0
<*channels*>: C3_C4

Description

Set the cycle buffer register

Conditions for output

MTU channels are set to complementary PWM mode

Parameter

uint16_t tcbr_val	Value to be written to the cycle buffer registerr

Return value

true	Setting of the counter value succeeded.
false	Setting of the counter value failed.

File for output

R_PG_Timer_MTU_U<*unit number*>_C<*channel number*>.c
<*unit number*>: 0
<*channel number*>: 3

RPDL function

R_MTU2_ControlUnit

Details

- This function sets the cycle buffer register (TCBR).

Example

```
#include "R_PG_default.h" //Include "R_PG_<project name>.h" to use this function.

void func (void)
{
      R_PG_Timer_SetBuffer_CycleData_MTU_U0_C3_C4(0x1000);
}
```

5.11.13 R_PG_Timer_SetOutputPhaseSwitch_MTU_U<*unit number*>_<*channels*>

Definition

bool R_PG_Timer_SetOutputPhaseSwitch_MTU_U<*unit number*>_<*channels*>
(uint8_t output_level);
<*unit number*>: 0
<*channels*>: C3_C4

Description

Switch PWM output level

Conditions for output

- The MTU channels are set to complementary PWM mode or reset-synchronized PWM mode
- The brushless DC motor control is enabled and the software is selected for the output control method

Parameter

uint8_t output_level	PWM output setting (0 to 5)

The output level for each value is as follows

Value	MTIOC3B U phase	MTIOC4A V phase	MTIOC4B W phase	MTIOC3D U phase	MTIOC4C V phase	MTIOC4D W phase
0	OFF	OFF	OFF	OFF	OFF	OFF
1	ON	OFF	OFF	OFF	OFF	ON
2	OFF	ON	OFF	ON	OFF	OFF
3	OFF	ON	OFF	OFF	OFF	ON
4	OFF	OFF	ON	OFF	ON	OFF
5	ON	OFF	OFF	OFF	ON	OFF
6	OFF	OFF	ON	ON	OFF	OFF
7	OFF	OFF	OFF	OFF	OFF	OFF

Return value

true	Setting of the counter value succeeded.
false	Setting of the counter value failed.

File for output

R_PG_Timer_MTU_U<*unit number*>_C<*channel number*>.c
<*unit number*>: 0
<*channel number*>: 3

RPDL function

R_MTU2_ControlUnit

Details

- This function switches the PWM output level in brushless DC motor control

Example

```
#include "R_PG_default.h" //Include "R_PG_<project name>.h" to use this function.

void func (void)
{
      R_PG_Timer_SetOutputPhaseSwitch_MTU_U0_C3_C4(0x7);
}
```

5.11.14 R_PG_Timer_ControlOutputPin_MTU_U<*unit number*>_<*channels*>

Definition

bool R_PG_Timer_ControlOutputPin_MTU_U<*unit number*>_<*channels*>
(bool p1_enable, bool n1_enable, bool p2_enable, bool n2_enable,
bool p3_enable, bool n3_enable)
<*unit number*>: 0
<*channels*>: C3_C4

Description

Enable or disable the PWM output

Conditions for output

MTU channels are set to complementary PWM mode or reset-synchronized PWM mode

Parameter

bool p1_enable	U positive phase (MTIOCmB) output (0: Disable 1: Enable)
bool n1_enable	U negative phase (MTIOCmD) output (0: Disable 1: Enable)
bool p2_enable	V positive phase (MTIOCnA) output (0: Disable 1: Enable)
bool n2_enable	V negative phase (MTIOCnC) output (0: Disable 1: Enable)
bool p3_enable	W positive phase (MTIOCnB) output (0: Disable 1: Enable)
bool n3_enable	W negative phase (MTIOCnD) output (0: Disable 1: Enable)

m : 3 n : 4

Return value

true	Setting of the counter value succeeded.
false	Setting of the counter value failed.

File for output

R_PG_Timer_MTU_U<*unit number*>_C<*channel number*>.c
<*unit number*>: 0
<*channel number*>: 3

RPDL function

R_MTU2_ControlUnit

Details

- This function enables or disables PWM output in complementary PWM mode or reset-synchronized PWM mode.
- In complementary PWM mode or reset-synchronized PWM mode, PWM output is disabled in the initial state. To enable the pin output, call this function before starting the count operation.

Example

Refer to the example 2 of R_PG_Timer_Set_MTU_U<*unit number*>_<*channels*>

5.11.15 R_PG_Timer_SetBuffer_PWMOutputLevel_MTU_U<*unit number*>_<*channels*>

Definition

bool R_PG_Timer_SetBuffer_PWMOutputLevel_MTU_U<*unit number*>_<*channels*>
(bool p1_high, bool n1_high, bool p2_high, bool n2_high,
bool p3_high, bool n3_high)
<*unit number*>: 0
<*channels*>: C3_C4

Description

Set the PWM output level in the buffer register

Conditions for output

- MTU channels are set to complementary PWM mode or reset-synchronized PWM mode
- Buffer transfer of PWM output level setting is enabled

Parameter

bool p1_high	U positive phase (MTIOCmB) output
bool n1_high	U negative phase (MTIOCmD) output
bool p2_high	V positive phase (MTIOCnA) output
bool n2_high	V negative phase (MTIOCnC) output
bool p3_high	W positive phase (MTIOCnB) output
bool n3_high	W negative phase (MTIOCnD) output

m : 3 n : 4

The output level in each value is as follows

Value	Category	Positive phase	Negative phase
0	Active level	Low	Low
	Initial output	Low	Low
	Compare match when up count	Low	High
	Compare match when down count	High	Low
1	Active level	High	High
	Initial output	High	High
	Compare match when up count	High	Low
	Compare match when down count	Low	High

Return value

true	Setting of the counter value succeeded.
false	Setting of the counter value failed.

File for output

R_PG_Timer_MTU_U<*unit number*>_C<*channel number*>.c
<*unit number*>: 0
<*channel number*>: 3

RPDL function

R_MTU2_ControlUnit

Details

- This function sets the output level settings to the timer output level buffer register (TOLBR)

Example

```
#include "R_PG_default.h" //Include "R_PG_<project name>.h" to use this function.

void func (void)
{
      R_PG_Timer_SetBuffer_PWMOutputLevel_MTU_U0_C3_C4( 0, 0, 0, 0, 0, 0 );
}
```

5.11.16 R_PG_Timer_ControlBufferTransfer_MTU_U<*unit number*>_<*channels*>

Definition

bool R_PG_Timer_ControlBufferTransfer_MTU_U<*unit number*>_<*channels*>
(bool enable)
<*unit number*>: 0
<*channels*>: C3_C4

Description

Enable or disable buffer transfer from the buffer registers to the temporary registers

Conditions for output

- The MTU channels are set to complementary PWM mode
- Interrupt skipping function is set

Parameter

bool enable	Buffer transfer control (0 :Disable 1 :Enable)

Return value

true	Setting of the counter value succeeded.
false	Setting of the counter value failed.

File for output

R_PG_Timer_MTU_U<*unit number*>_C<*channel number*>.c
<*unit number*>: 0
<*channel number*>: 3

RPDL function

R_MTU2_ControlUnit

Details

- This function enables or disables transfer from the buffer registers used in complementary PWM mode to the temporary registers.

Example

```
#include "R_PG_default.h" //Include "R_PG_<project name>.h" to use this function.

void func (void)
{
      R_PG_Timer_ControlBufferTransfer_MTU_U0_C3_C4( 1 );
}
```

5.12 Port Output Enable 2 (POE2a)

5.12.1 R_PG_POE_Set

Definition bool R_PG_POE_Set (void)

Description Set up the POE

Parameter None

Return value

true	Setting was made correctly
false	Setting failed

File for output R_PG_POE.c

RPDL function R_POE_Set, R_POE_Create

- Sets up the output control of MTU0, 3 and 4 pins, the POE pins used for high-impedance request signal input, and the output enable interrupt.
- The MTU module is not set up in this function.
- Do not set pins that are not used for MTU output.
- When the name of the interrupt notification function has been specified in the GUI, if an interrupt occurs in the CPU, the function having the specified name will be called. Create the interrupt notification function as follows:
  void <*name of the interrupt notification function*> (void)
  For the interrupt notification function, note the contents of section Notes on Notification Functions.

Example

A case where the setting is made as follows.

- The output enable interrupt 2(OEI2) has been set
  PoeOei2IntFunc has been specified as an interrupt notification function name

```
//Include “R_PG_<project name>.h” to use this function.
#include "R_PG_default.h"

void func(void)
{
      R_PG_POE_Set();       // Set up the POE
}

void PoeOei2IntFunc (void)
{
      // Processing when the output enable interrupt occurs
}
```

5.12.2 R_PG_POE_SetHiZ_<*Timer channels*>

Definition bool R_PG_POE_SetHiZ_<*Timer channels*>(void)

<*Timer channels*>: MTU3_4, MTU0

Description Place the timer output pins in high-impedance state

Parameter None

Return value

true	Setting was made correctly
false	Setting failed

File for output R_PG_POE.c

RPDL function R_POE_Control

Details Places MTU0, 3, 4 output pins in high-impedance state.

Example A case where the setting is made as follows.

- MTU0 pin output has been set (Setting of MTU)
- MTU0 output pins have been set to be controlled by the high impedance request

```
//Include “R_PG_<project name>.h” to use this function.
#include "R_PG_default.h"

void func1(void)
{
    R_PG_Timer_Set_MTU_U0_C0();     //Set up the MTU0
    R_PG_POE_Set();       // Set up the POE
    R_PG_Timer_StartCount_MTU_U0_C0();      //Start the count operation of MTU0
}

void func2(void)
{
    R_PG_POE_SetHiZ_MTU0();   // Place the MTU0 output pins in high-impedance state
}
```

5.12.3 R_PG_POE_GetRequestFlagHiZ_<*Timer channels/flag*>

Definition

bool R_PG_POE_GetRequestFlagHiZ_MTU3_4
(bool * poe0, bool * poe1, bool * poe2, bool * poe3)

bool R_PG_POE_GetRequestFlagHiZ_MTU0 (bool * poe8)

bool R_PG_POE_GetRequestFlagHiZ_OSTSTF (bool * oststf)

Description

Acquire the high-impedance request flags

Parameter

bool* poe0	The address of storage area for POE0# high-impedance request flags
bool* poe1	The address of storage area for POE1# high-impedance request flags
bool* poe2	The address of storage area for POE2# high-impedance request flags
bool* poe3	The address of storage area for POE3# high-impedance request flags
bool* poe8	The address of storage area for POE8# high-impedance request flags
bool * oststf	The address of storage area for OSTST high-impedance flag

Return value

true	Acquisition succeeded
false	Acquisition failed

File for output

R_PG_POE.c

RPDL function

R_POE_GetStatus

Details

- Acquires the flags of high-impedance request signals input to POEn#pins (POEnF). (n:0 to 3 and 8)
- Specify the address of storage area for the flags to be acquired. Specify 0 for a flag that is not required.
- The flag is valid only when the POE pin is set to a high-impedance request input in GUI.

Example

A case where the setting is made as follows.

- MTU3 and 4 pin output has been set (Setting of MTU)
- MTU3 and 4 output pins have been set to be controlled by the high impedance request
- POE0 has been selected as a high-impedance request signal input

```
//Include “R_PG_<project name>.h” to use this function.
#include "R_PG_default.h"

bool poe0;

void func(void)
{
    R_PG_Timer_Set_MTU_U0_C3();     //Set up the MTU
    R_PG_POE_Set();     // Set up the POE
    R_PG_Timer_StartCount_MTU_U0_C3();     //Start the count operation of MTU

    //Wait for the high-impedance request signal to be input
    do{
        R_PG_POE_GetRequestFlagHiZ_MTU3_4( &poe0, 0, 0, 0 );
    }while( ! poe0 );

    //Processing when the high-impedance request signal is input

    R_PG_POE_ClearFlag_MTU3_4();     //Clear high-impedance request flag
}
```

5.12.4 R_PG_POE_GetShortFlag_<*Timer channels*>

Definition bool R_PG_POE_GetShortFlag_<*Timer channels*> (bool * detected)

<*Timer channels*>: MTU3_4

Description Acquire the MTU output short flags

Parameter

bool* detected	The address of storage area for the output short flag (OSF1)

Return value

true	Acquisition succeeded
false	Acquisition failed

File for output R_PG_POE.c

RPDL function R_POE_GetStatus

Details
- Acquires the MTU3 ,4 complementary PWM output short flags (OSF1).

Example A case where the setting is made as follows.
- The output enable interrupt1(OEI1) has been set.
- PoeOei1IntFunc has been specified as the output enable interrupt 1 notification function name.

```
//Include “R_PG_<project name>.h” to use this function.
#include "R_PG_default.h"

void func(void)
{
    R_PG_POE_Set();     // Set up the POE
}

void PoeOei1IntFunc(void)
{
    bool detected;

    //Acquire the output short flag
    R_PG_POE_GetShortFlag_MTU3_4 (&detected);

    if( detected ){
        //Processing when MTU3,4 output short is detected

        R_PG_POE_ClearFlag_MTU3_4();     // Clear the output short flag(OSF1)
    }
}
```

5.12.5 R_PG_POE_ClearFlag_<*Timer channels/flag*>

Definition bool R_PG_POE_ClearFlag_<*Timer channels/flag*> (void)

<*Timer channels/flag*>: MTU3_4, MTU0, OSTSTF

Description Clear the high-impedance request flags and the output short flags

Parameter None

Return value

true	Clearing succeeded
false	Clearing failed

File for output R_PG_POE.c

RPDL function R_POE_Control

Details

- Clears the high-impedance request flags and the output short flags.
- The flags that shall be cleared by each function are as follows.

Timer channels / flag	Flags
MTU3, 4	POEn request flag (POEnF) (n:0 to 3) MTU3,4 output short flag(OSF1)
MTU0	POE8 request flag (POE8F)
OSTSTF	OSTST high-impedance flag

Example Refer to the example of R_PG_POE_GetShortFlag_<*Timer channels*>

5.13 16-Bit Timer Pulse Unit (TPUa)

5.13.1 R_PG_Timer_Set_TPU_U<*unit number*>

Definition: bool R_PG_Timer_Set_TPU_U<*unit number*>
<*unit number*>: 0 or 1

Description: Set up TPU of two or more channels.

Parameter: None

Return value:

true	Setting was made correctly.
false	Setting failed.

File for output: R_PG_Timer_TPU_U<*unit number*>.c
<*unit number*>: 0 and 1

RPDL function: R_TPU_Create

Details:

- Releases the TPU from the module-stop, makes initial settings of two or more channels.
- Interrupts of the TPU are set by this function. When the name of the interrupt notification function has been specified in the GUI, if an interrupt occurs in the CPU, the function having the specified name will be called. Create the interrupt notification function as follows:
  void <*name of the interrupt notification function*> (void)
  For the interrupt notification function, note the contents of this chapter end, Notes on Notification Functions.
- If a name for the interrupt notification function is not specified in the GUI, an interrupt handler will not be called even if the interrupt occurs. The state of a request flag can be acquired by calling R_PG_Timer_GetRequestFlag_TPU_U<*unit number*>_C<*channel number*>.
- When counting driven by an externally input clock, the external reset signal, input capture, or pulse output is in use, the direction (input or output) and input buffer for the pin to be used is set in this function.

Example: A case where the setting is made as follows.

- TPU unit 1 was set up
- Tpu6IcCmAIntFunc was specified as a compare match A interrupt notification function name

```
#include "R_PG_default.h" //Include "R_PG_<project name>.h" to use this function.

void func(void)
{
    R_PG_Timer_Set_TPU_U1();    //Set up the TPU unit1
}

void Tpu6IcCmAIntFunc(void)
{
    func_cmA();    //Processing in response to a compare match A interrupt
}
```

5.13.2 R_PG_Timer_Start_TPU_U<*unit number*>_C<*channel number*>

Definition: bool R_PG_Timer_Start_TPU_U<*unit number*>_C<*channel number*> (void)
<*unit number*>: 0 or 1
<*channel number*>: 0 to 11

Description: Set up the TPU and start the count

Parameter: None

Return value:

true	Setting was made correctly.
false	Setting failed.

File for output: R_PG_Timer_TPU_U<*unit number*>_C<*channel number*>.c
<*unit number*>: 0 and 1
<*channel number*>: 0 to 11

RPDL function: R_TPU_Create

Details:

- Releases the TPU from the module-stop, makes initial settings, and starts the TPU counting.
- Interrupts of the TPU are set by this function. When the name of the interrupt notification function has been specified in the GUI, if an interrupt occurs in the CPU, the function having the specified name will be called. Create the interrupt notification function as follows:
  void <*name of the interrupt notification function*> (void)
  For the interrupt notification function, note the contents of this chapter end, Notes on Notification Functions.
- If a name for the interrupt notification function is not specified in the GUI, an interrupt handler will not be called even if the interrupt occurs. The state of a request flag can be acquired by calling R_PG_Timer_GetRequestFlag_TPU_U<*unit number*>_C<*channel*
- *number*>.
  When counting driven by an externally input clock, the external reset signal, input capture, or pulse output is in use, the direction (input or output) and input buffer for the pin to be used is set in this function.

Example: A case where the setting is made as follows.

- TPU unit 1 channel 6 was set up
- Tpu6IcCmAIntFunc was specified as a compare match A interrupt notification function name

```
#include "R_PG_default.h" //Include "R_PG_<project name>.h" to use this function.

void func(void)
{
    R_PG_Timer_Start_TPU_U1_C6();    //Set up the TPU6 and start count
}

void Tpu6IcCmAIntFunc(void)
{
    func_cmA();    //Processing in response to a compare match A interrupt
}
```

5.13.3 R_PG_Timer_SynchronouslyStartCount_TPU_U<unit number>

Definition

bool R_PG_Timer_SynchronouslyStartCount_TPU_U<*unit number*>
(bool ch0, bool ch1, bool ch2, bool ch3, bool ch4, bool ch5)
<*unit number*>: 0
bool R_PG_Timer_SynchronouslyStartCount_TPU_U<*unit number*>
(bool ch6, bool ch7, bool ch8, bool ch9, bool ch10, bool ch11)
<*unit number*>: 1

Description

Start the TPU count operation of two or more channels simultaneously

Parameter

Unit 0

bool ch0	Count operation of channel 0 (0:Do not start count 1:Start count)
bool ch1	Count operation of channel 1 (0:Do not start count 1:Start count)
bool ch2	Count operation of channel 2 (0:Do not start count 1:Start count)
bool ch3	Count operation of channel 3 (0:Do not start count 1:Start count)
bool ch4	Count operation of channel 4 (0:Do not start count 1:Start count)
bool ch5	Count operation of channel 5 (0:Do not start count 1:Start count)

Unit 1

bool ch6	Count operation of channel 6 (0:Do not start count 1:Start count)
bool ch7	Count operation of channel 7 (0:Do not start count 1:Start count)
bool ch8	Count operation of channel 8 (0:Do not start count 1:Start count)
bool ch9	Count operation of channel 9 (0:Do not start count 1:Start count)
bool ch10	Count operation of channel 10 (0:Do not start count 1:Start count)
bool ch11	Count operation of channel 11 (0:Do not start count 1:Start count)

Return value

true	Setting was made correctly.
false	Setting failed.

File for output

R_PG_Timer_TPU_U<*unit number*>.c
<*unit number*>: 0 and 1

RPDL function

R_TPU_ControlUnit

Details

- Starts the TPU count operation of two or more channels simultaneously.
- Call R_PG_Timer_Set_TPU_U<*unit number*> to make the initial settings before calling this function.

Example A case where the setting is made as follows.

- TPU unit 1 channel 6 and 7 was set up

```
#include "R_PG_default.h" //Include "R_PG_<project name>.h" to use this function.

void func(void)
{
    //Set up the TPU6 and TPU7
    R_PG_Timer_Set_TPU_U1 ();

    // Start the TPU6 and 7 count operation
    R_PG_Timer_SynchronouslyStartCount_MTU_U1(
        1, //ch6
        1, //ch7
        0, //ch8
        0, //ch9
        0, //ch10
        0 //ch11
    );
}
```

5.13.4 R_PG_Timer_HaltCount_TPU<*unit number*>_C<*channel number*>

Definition

bool R_PG_Timer_HaltCount_TPU_U<*unit number*>_C<*channel number*> (void)

<*unit number*>: 0 or 1

<*channel number*>: 0 to 11

Description

Halt the TPU count

Parameter

None

Return value

true	Halting succeeded.
false	Halting failed.

File for output

R_PG_Timer_TPU_U<*unit number*>_C<*channel number*>.c

<*unit number*>: 0 or 1

<*channel number*>: 0 to 11

RPDL function

R_TPU_ControlChannel

Details

- Halts counting by a TPU. To make the TPU resume counting, call the following function.
  R_PG_Timer_ResumeCount_TPU_U<*unit number*>_C<*channel number*>

Example

A case where the setting is made as follows.

- TPU unit 1 channel 6 was set up
- Tpu6IcCmAIntFunc was specified as the compare match A interrupt notification function name

```
#include "R_PG_default.h" //Include "R_PG_<project name>.h" to use this function.

void func(void)
{
    R_PG_Timer_Start_TPU_U1_C6();       //Set up the TPU6 and start count
}

void Tpu6IcCmAIntFunc(void)
{
    R_PG_Timer_HaltCount_TPU_U1_C6();       //Halt the TPU6 count

    func_cmA();       //Processing in response to a compare match A interrupt

    R_PG_Timer_ResumeCount_TPU_U1_C6();       //Resume the TPU6 count
}
```

5.13.5 R_PG_Timer_ResumeCount_TPU_U<*unit number*>_C<*channel number*>

Definition

bool R_PG_Timer_ResumeCount_TPU_U<*unit number*>_C<*channel number*> (void)

<*unit number*>: 0 or 1

<*channel number*>: 0 to 11

Description

Resume the TPU count

Parameter

None

Return value

true	Resuming count succeeded.
false	Resuming count failed.

File for output

R_PG_Timer_TPU_U<*unit number*>_C<*channel number*>.c

<*unit number*>: 0 or 1

<*channel number*>: 0 to 11

RPDL function

R_TPU_ControlChannel

Details

- Resumes counting by a TPU that was halted by R_PG_Timer_HaltCount_TPU_U<*unit number*>_C<*channel number*>.

Example

A case where the setting is made as follows.

- TPU unit 1 channel 6 was set up
- Tpu6IcCmAIntFunc was specified as the compare match A interrupt notification function name

```
#include "R_PG_default.h" //Include "R_PG_<project name>.h" to use this function.

void func(void)
{
    R_PG_Timer_Start_TPU_U1_C6();       //Set up the TPU6 and start count
}

void Tpu6IcCmAIntFunc(void)
{
    R_PG_Timer_HaltCount_TPU_U1_C6();       //Halt the TPU6 count

    func_cmA();       //Processing in response to a compare match A interrupt

    R_PG_Timer_ResumeCount_TPU_U1_C6();       //Resume the TPU6 count
}
```

5.13.6 R_PG_Timer_GetCounterValue_TPU_U<*unit number*>_C<*channel number*>

Definition

bool R_PG_Timer_GetCounterValue_TPU_U<*unit number*>_C<*channel number*>
(uint16_t * data)

<*unit number*>: 0 or 1

<*channel number*>: 0 to 11

Description

Acquire the TPU counter value

Parameter

uint16_t * data	Destination for storage of the counter value

Return value

true	Acquisition of the counter value succeeded.
false	Acquisition of the counter value failed.

File for output

R_PG_Timer_TPU_U<*unit number*>_C<*channel number*>.c

<*unit number*>: 0 or 1

<*channel number*>: 0 to 11

RPDL function

R_TPU_Read

Details

- Acquires the counter value of a TPU.

Example

A case where the setting is made as follows.

- TPU unit 0 channel 0 was set up
- Set TGRA as an input capture register and enable an input capture interrupt
- Tpu0IcCmAIntFunc was specified as the input capture A interrupt notification function name

```
#include "R_PG_default.h" //Include "R_PG_<project name>.h" to use this function.

uint16_t counter;

void func(void)
{
    R_PG_Timer_Start_TPU_U0_C0();    //Set up the TPU0 and start count
}

void Tpu0IcCmAIntFunc(void)
{
    // Acquire the value of a TPU0 counter
    R_PG_Timer_GetCounterValue_TPU_U0_C0( &counter );
}
```

5.13.7 R_PG_Timer_SetCounterValue_TPU_U<*unit number*>_C<*channel number*>

Definition

bool R_PG_Timer_SetCounterValue_TPU_U<*unit number*>_C<*channel number*>
(uint16_t data)

<*unit number*>: 0 or 1

<*channel number*>: 0 to 11

Description

Set the TPU counter value

Parameter

uint16_t data	Value to be set to the counter

Return value

true	Setting of the counter value succeeded.
false	Setting of the counter value failed.

File for output

R_PG_Timer_TPU_U<*unit number*>_C<*channel number*>.c

<*unit number*>: 0 or 1

<*channel number*>: 0 to 11

RPDL function

R_TPU_ControlChannel

Details

- Set the counter value of a TPU.

Example

A case where the setting is made as follows.

- TPU unit 0 channel 1 was set up
- Set TGRA as an output compare register and enable a compare match interrupt
- Tpu1IcCmAIntFunc was specified as the compare match A interrupt notification function name

```
#include "R_PG_default.h" //Include "R_PG_<project name>.h" to use this function.

void func1(void)
{
    R_PG_Timer_Start_TPU_U0_C1();    //Set up the TPU1 and start count
}

void Tpu1IcCmAIntFunc(void)
{
    R_PG_Timer_SetCounterValue_TPU_U0_C1( 0 );    // Set the value of a TPU1
counter
}
```

5.13.8 R_PG_Timer_GetTGR_TPU_U<*unit number*>_C<*channel number*>

Definition

bool R_PG_Timer_GetTGR_TPU_U<*unit number*>_C<*channel number*>
(uint16_t * tgr_a_val, uint16_t * tgr_b_val, uint16_t * tgr_c_val, uint16_t * tgr_d_val)
<*unit number*>: 0 or 1
<*channel number*>: 0 to 11

Description

Acquire the TPU general register value

Parameter

uint16_t * tgr_a_val	The address of the storage area for TGRA value
uint16_t * tgr_b_val	The address of the storage area for TGRB value
uint16_t * tgr_c_val	The address of the storage area for TGRC value
uint16_t * tgr_d_val	The address of the storage area for TGRD value

Available arguments for each channel are as follows.

TPU0, 3, 6 and 9	tgr_a_val to tgr_d_val
TPU1, 2, 4, 5, 7, 8, 10 and 11	tgr_a_val and tgr_b_val

Return value

true	Acquisition succeeded.
false	Acquisition failed.

File for output

R_PG_Timer_TPU_U<*unit number*>_C<*channel number*>.c
<*unit number*>: 0 or 1
<*channel number*>: 0 to 11

RPDL function

R_TPU_Read

Details

- This function acquires the general register value.
- Specify the address of storage area for an item to be acquired. Specify 0 for an item that is not required.

Example

A case where the setting is made as follows.

- TPU channel 0 was set up
- Set TGRA as an input capture register and enable an input capture A interrupt
- Tpu0IcCmAIntFunc was specified as the input capture A interrupt notification function name

```
#include "R_PG_default.h" //Include "R_PG_<project name>.h" to use this function.
uint16_t tgr_a_val;
void func(void)
{
    R_PG_Timer_Start_TPU_U0_C0();    //Set up the TPU and start the count
}
void Tpu0IcCmAIntFunc(void)
{
    // Acquire the value of the TGRA
    R_PG_Timer_GetTGR_TPU_U0_C0(&tgr_a_val, 0, 0, 0);
}
```

5.13.9 R_PG_Timer_SetTGR_<*general register*>_TPU_U<*unit number*>_C<*channel number*>

Definition bool R_PG_Timer_SetTGR_<*general register*>_TPU_U<*unit number*>_C<*channel number*> (uint16_t value);

<*general register*>:

TPU0, 3, 6 and 9 : A, B, C or D

TPU1, 2, 4, 5, 7, 8, 10 and 11 : A or B

<*unit number*>: 0 or 1

<*channel number*>: 0 to 11

Description Set the TPU general register value

Parameter

uint16_t value	Value to be written to the general register

Return value

true	Setting of the general register succeeded.
false	Setting of the general register failed.

File for output R_PG_Timer_TPU_U<*unit number*>_C<*channel number*>.c

<*unit number*>: 0 or 1

<*channel number*>: 0 to 11

RPDL function R_TPU_ControlChannel

Details

- This function sets the general register value.

Example

A case where the setting is made as follows.

- TPU channel 1 was set up
- Set TGRA as an output compare register and enable a compare match A interrupt
- Tpu1IcCmAIntFunc was specified as the compare match A interrupt notification function name

```
#include "R_PG_default.h" //Include "R_PG_<project name>.h" to use this function.

void func (void)
{
    R_PG_Timer_Start_TPU_U0_C1();    //Set up the TPU and start the count
}

void Tpu1IcCmAIntFunc(void)
{
    R_PG_Timer_SetTGR_A_TPU_U0_C1( 1000 );    //Set TGRA
}
```

5.13.10 R_PG_Timer_GetRequestFlag_TPU_U<*unit number*>_C<*channel number*>

Definition

```
bool R_PG_Timer_GetRequestFlag_TPU_U<unit number>_C<channel number> (
    bool* a,
    bool* b,
    bool* c,
    bool* d,
    bool* ov,
    bool* un
);
```

<*unit number*>: 0 or 1
<*channel number*>: 0 to 11

Description

Acquire and clear the TPU interrupt flags

Parameter

bool* a	The address of storage area for the compare match/input capture A flag
bool* b	The address of storage area for the compare match/input capture B flag
bool* c	The address of storage area for the compare match/input capture C flag
bool* d	The address of storage area for the compare match/input capture D flag
bool* ov	The address of storage area for the overflow flag
bool* un	The address of storage area for the underflow flag

Return value

true	Acquisition of the flags succeeded
false	Acquisition of the flags failed

File for output

R_PG_Timer_TPU_U<*unit number*>.c
<*unit number*>: 0 or 1
<*channel number*>: 0 to 11

RPDL function

R_TPU_Read

Details

- This function acquires the interrupt flags of TPU.
- All flags will be cleared in this function.
- Specify the address of storage area for the flags to be acquired.
  Specify 0 for a flag that is not required.
- The flags of compare match/imput capture C and D are available in channel 0, 3, 6, and 9. Specify 0 for other channels.

Example A case where the setting is made as follows.

- TPU unit 0 channel 1 was set up
- Set TGRA as an output compare register and enable an output compare interrupt

```
#include "R_PG_default.h" //Include "R_PG_<project name>.h" to use this function.

uint16_t counter;

void func(void)
{
    R_PG_Timer_Start_TPU_U0_C1();    //Set up the TPU1 and start count

    //Wait for the compare match A
    do{
        R_PG_Timer_GetRequestFlag_TPU_U0_C1(
            & cma_flag,
            0,
            0,
            0,
            0,
            0
        );
    } while( !cma_flag );

    func_cmA();    //Processing in response to a compare match A

    // Stop the TPU unit 0
    R_PG_Timer_StopModule_TPU_U0( &counter );
}
```

5.13.11 R_PG_Timer_StopModule_TPU_U<*unit number*>

Definition: bool R_PG_Timer_StopModule_TPU_U<*unit number*> (void)
<*unit number*>: 0 or 1

Description: Shut down the TPU unit

Parameter: None

Return value:

true	Shutting down succeeded.
false	Shutting down failed.

File for output: R_PG_Timer_TPU_U<*unit number*>.c
<*unit number*>: 0 or 1

RPDL function: R_TPU_Destroy

Details:

- Stops a TPU unit and places it in the module-stop state per unit. If two or more channels are running when this function is called, all channels are stopped. Call the following function to stop a single channel.
  R_PG_Timer_HaltCount_TPU_U<*unit number*>_C<*channel number*>

Example: A case where the setting is made as follows.

- TPU unit 0 channel 1 was set up
- Tpu1IcCmAIntFunc was specified as the compare match A interrupt notification function name

```
#include "R_PG_default.h" //Include "R_PG_<project name>.h" to use this function.

uint16_t counter;

void func(void)
{
    R_PG_Timer_Start_TPU_U0_C1();    //Set up the TPU1 and start count
}

void Tpu1IcCmAIntFunc(void)
{
    // Stop the TPU unit 0
    R_PG_Timer_StopModule_TPU_U0( &counter );
}
```

5.14 Programmable Pulse Generator (PPG)

5.14.1 R_PG_PPG_StartOutput_U<*unit number*>_G<*group number*>

Definition: bool R_PG_PPG_StartOutput_U<*unit number*>_G<*group number*> (void)
<*unit number*>: 0 or 1
<*group number*>: 0 to 7

Description: Set up the PPG and start outputting

Parameter: None

Return value:

true	Setting was made correctly
false	Setting failed

File for output: R_PG_PPG_U<*unit number*>.c
<*unit number*>: 0 and 1

RPDL function: R_PPG_Create

Details:

- Releases the PPG from the module-stop, makes initial settings, and starts the outputting signals from the selected pins on GUI.
- This function sets initial output value and 2nd output value.
- The MTU and the TPU are not set up in this function. Use MTU or TPU function to set up the MTU or TPU.
- To set up pair of groups Group m and Group n (m:0,2,4,6 n:1,3,5,7), set up Group n first.

Example: A case where the setting is made as follows.

- Pulse output pins PO8 to PO11 on group 2 have been enabled.
- MTU comparematch A interrupt has been enabled and Mtu0IcCmAIntFunc is specified as a interrupt notification function.

```
//Include “R_PG_<project name>.h” to use this function.
#include "R_PG_default.h"

uint8_t output_val;

void func(void)
{
      output_val=1;

      R_PG_Timer_Set_MTU_U0_C0();       //Set up MTU0
      R_PG_PPG_StartOutput_U0_G2();      //Set up PPG and start output
      R_PG_Timer_StartCount_MTU_U0_C0();       // Start MTU0 count operation
}

//MTU0 compare match A interrupt notification function
void Mtu0IcCmAIntFunc (void)
{
      //Set next output value
      R_PG_PPG_SetOutputValue_U0_G2( output_val );
}
```

5.14.2 R_PG_PPG_StopOutput_U<*unit number*>_G<*group number*>

Definition: bool R_PG_PPG_StopOutput_U<*unit number*>_G<*group number*> (void)
<*unit number*>: 0 or 1
<*group number*>: 0 to 7

Description: Stop outputting

Parameter: None

Return value:

true	Setting was made correctly
false	Setting failed

File for output: R_PG_PPG_U<*unit number*>.c
<*unit number*>: 0 and 1

RPDL function: R_PPG_Destroy

Details:
- Stops the PPG output.
- If all the outputs in a unit become disabled, that unit will be put into the stop state to reduce power consumption.

Example: A case where the setting is made as follows.
- Pulse output pins PO8 to PO11 on group 2 have been enabled.
- MTU comparematch A interrupt has been enabled and Mtu0IcCmAIntFunc is specified as a interrupt notification function.

```
//Include “R_PG_<project name>.h” to use this function.
#include "R_PG_default.h"

uint8_t output_val;

void func(void)
{
    output_val=1;

    R_PG_Timer_Set_MTU_U0_C0();       //Set up MTU0
    R_PG_PPG_StartOutput_U0_G2();     //Set up PPG and start output
    R_PG_Timer_StartCount_MTU_U0_C0();      // Start MTU0 count operation
}

//MTU0 compare match A interrupt notification function
void Mtu0IcCmAIntFunc (void)
{
    output_val++;       //Increment the output value

    R_PG_PPG_SetOutputValue_U0_G2( output_val );      //Set the next output value

    if(output_val >= 0x0f){
        R_PG_PPG_StopOutput_U0_G2();      //Stop the output
    }
}
```

5.14.3 R_PG_PPG_SetOutputValue_U<*unit number*>_G<*group number*>

Definition

bool R_PG_PPG_SetOutputValue_ U<*unit number*>_G<*group number*> (uint8_t data)

<*unit number*>: 0 or 1
<*group number*>: 0 to 7

Description

Set the output value of single group

Parameter

uint8_t output_val	Output vale for the next update Group 0,2,4,6 : bit3 to bit0 are avairable Group 1,3,5,7 : bit7 to bit4 are avairable

Parameter

None

Return value

true	Setting was made correctly
false	Setting failed

File for output

R_PG_PPG_U<*unit number*>.c <*unit number*>: 0 and 1

RPDL function

R_PPG_Destroy

Details

- Sets the output value of single group for next update timing (compare match or input capture of MTU or TPU).
- The data is using the format:
  For group 1,3,5, and 7, set the value in upper 4 bits.

	Group 1, 3, 5 or 7				Group 0, 2, 4 or 6			
Group pair	b7	b6	b5	b4	b3	b2	b1	b0
1 & 0	PO7	PO6	PO5	PO4	PO3	PO2	PO1	PO0
3 & 2	PO15	PO14	PO13	PO12	PO11	PO10	PO9	PO8
5 & 4	PO23	PO22	PO21	PO20	PO19	PO18	PO17	PO16
7 & 6	PO31	PO30	PO29	PO28	PO27	PO26	PO25	PO24

Example

A case where the setting is made as follows.

- Pulse output pins PO4 to PO7 on group 1 have been enabled.
- MTU comparematch A interrupt has been enabled and Mtu0IcCmAIntFunc is specified as a interrupt notification function.

```
//Include “R_PG_<project name>.h” to use this function.
#include "R_PG_default.h"

uint8_t output_val;

void func(void)
{
      output_val=1;
      R_PG_Timer_Set_MTU_U0_C0();       //Set up MTU0
      R_PG_PPG_StartOutput_U0_G2();       //Set up PPG and start output
      R_PG_Timer_StartCount_MTU_U0_C0();       // Start MTU0 count operation
}

//MTU0 compare match A interrupt notification function
void Mtu0IcCmAIntFunc (void)
{
      output_val++;      //Increment the output value
      R_PG_PPG_SetOutputValue_U0_G1( output_val << 4 ); //Set the next output value
      if(output_val >= 0x0f){
            R_PG_PPG_StopOutput_U0_G1();       //Stop the output
      }
}
```

5.14.4 R_PG_PPG_SetOutputValue_U<*unit number*>_G<*group number1*>_G<*group number2*>

Definition bool R_PG_PPG_SetOutputValue_ U<*unit number*>_G<*group number1*>_G<*group number2*>
(uint8_t data)
<*unit number*>: 0 or 1
<*group number1*>: 1, 3, 5, 7
<*group number2*>: 0, 2, 4, 6

Description Set the output value for a pair of groups

Conditions for output Pair of groups have been set

Parameter

uint8_t output_val	Output vale for the next update

Return value

true	Setting was made correctly
false	Setting failed

File for output R_PG_PPG_U<*unit number*>.c
<*unit number*>: 0 and 1

RPDL function R_PPG_Destroy

Details

- Sets the output value of a pair of groups (0-1, 2-3, 4-5, or 6-7) for next update timing (compare match or input capture of MTU or TPU).
- The data is using the format:

	Group 1, 3, 5 or 7				Group 0, 2, 4 or 6			
Group pair	b7	b6	b5	b4	b3	b2	b1	b0
1 & 0	PO7	PO6	PO5	PO4	PO3	PO2	PO1	PO0
3 & 2	PO15	PO14	PO13	PO12	PO11	PO10	PO9	PO8
5 & 4	PO23	PO22	PO21	PO20	PO19	PO18	PO17	PO16
7 & 6	PO31	PO30	PO29	PO28	PO27	PO26	PO25	PO24

Example A case where the setting is made as follows.

- Pulse output groups 0 and 1 have been enabled.
- MTU comparematch A interrupt has been enabled and Mtu0IcCmAIntFunc is specified as a interrupt notification function.

```
//Include “R_PG_<project name>.h” to use this function.
#include "R_PG_default.h"

uint8_t output_val;

void func(void)
{
      output_val=1;
      R_PG_Timer_Set_MTU_U0_C0();       //Set up MTU0
      R_PG_PPG_StartOutput_U0_G1();      //Set up PPG and start output from froup 1
      R_PG_PPG_StartOutput_U0_G0();      //Set up PPG and start output from group 0
      R_PG_Timer_StartCount_MTU_U0_C0();       // Start MTU0 count operation
}

void Mtu0IcCmAIntFunc (void)
{
      R_PG_PPG_SetOutputValue_U0_G1_G0( output_val ); //Set the next output value
}
```

5.15 8-Bit Timer (TMR)

5.15.1 R_PG_Timer_Start_TMR_U<*unit number*>(_C<*channel number*>)

Definition
bool R_PG_Timer_Start_TMR_U<*unit number*>(_C<*channel number*>) (void)
<*unit number*>: 0 or 1
<*channel number*>: 0 to 3
((_C<*channel number*>) is added in the 8-bit mode)

Description
Set up the TMR and start the count operation

Parameter
None

Return value

true	Setting was made correctly.
false	Setting failed.

File for output
R_PG_Timer_TMR_U<*unit number*>.c
<*unit number*>: 0 and 1

RPDL function
R_TMR_Set
R_TMR_CreateChannel (8-bit mode)
R_TMR_CreateUnit (16-bit mode)

Details
- Releases the TMR from the module-stop, makes initial settings, and starts the TMR counting. The initial settings are made per channel in the 8-bit mode and per unit in the 16-bit mode (when the two channels of a unit are cascade-connected).
- Interrupts of the TMR are set by this function. When the name of the interrupt notification function has been specified in the GUI, if an interrupt occurs in the CPU, the function having the specified name will be called. Create the interrupt notification function as follows:
  void <*name of the interrupt notification function*> (void)
  For the interrupt notification function, note the contents of this chapter end, Notes on Notification Functions.
  If the interrupt propriety level is set to 0 in the GUI, a CPU interrupt does not occur. The state of a request flag can be acquired by calling R_PG_Timer_GetRequestFlag_TMR_U<*unit number*>(_C<*channel number*>).
- When counting driven by an externally input clock, the external reset signal, or pulse output is in use, sets the pins to be used in this function.

Example1

The 16-bit timer mode has been specified for TMR unit 1.

In this case, the following interrupt notification functions have been set in the GUI.

Overflow interrupt: TmrOf2IntFunc

Compare match A interrupt: TmrCma2IntFunc

Compare match B interrupt: TmrCmb2IntFunc

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

void func(void)
{
    //Place TMR unit 1 in the 16-bit mode.
    R_PG_Timer_Start_TMR_U1();
}

void TmrOf2IntFunc(void)
{
    func_of();      //Processing in response to an overflow interrupt
}

void TmrCma2IntFunc(void)
{
    func_cma();     //Processing in response to a compare match A interrupt
}

void TmrCmb2IntFunc(void)
{
    func_cmb();     //Processing in response to a compare match B interrupt
}
```

Example2

The 8-bit timer mode has been specified for TMR0 in the GUI.

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

void func1(void)
{
    bool cma_flag;

    //Place TMR0 in the 8-bit mode and start it counting.
    R_PG_Timer_Start_TMR_U0_C0();

    while(1){
        bool flag;
        //Acquire the compare match A interrupt request flag.
        R_PG_Timer_GetRequestFlag_TMR_U0_C0( &cma_flag, 0, 0 );

        if( cma_flag ){
            func_cma0();    //Processing of IRQ0
        }
    }
}
```

5.15.2 R_PG_Timer_HaltCount_TMR_U<*unit number*>(_C<*channel number*>)

Definition

bool R_PG_Timer_HaltCount_TMR_U<*unit number*>(_C<*channel number*>) (void)

<*unit number*>: 0 or 1

<*channel number*>: 0 to 3

((_C<*channel number*>) is added in the 8-bit mode.)

Description

Halt the TMR count operation

Parameter

None

Return value

true	Halting succeeded.
false	Halting failed.

File for output

R_PG_Timer_TMR_U<*unit number*>.c

<*unit number*>: 0 or 1

RPDL function

R_TMR_ControlChannel (8-bit mode)

R_TMR_ControlUnit (16-bit mode)

Details

- Halts the TMR count operation. To make the TMR resume counting, call the following function.
  R_PG_Timer_ResumeCount_TMR_U<*unit number*>(_C<*channel number*>)

Example

The 8-bit timer mode was specified for TMR0 in the GUI.
TmrCma0IntFunc was specified as the name of the compare match A interrupt function in the GUI.

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

void func(void)
{
    //Place TMR0 in the 8-bit mode.
    R_PG_Timer_Start_TMR_U0_C0();
}

void TmrCma0IntFunc(void)
{
    //Halt counting by TMR0.
    R_PG_Timer_HaltCount_TMR_U0_C0();

    func_cma();     //Processing in response to a compare match A interrupt

    //Resume counting by TMR0.
    R_PG_Timer_ResumeCount_TMR_U0_C0();
}
```

5.15.3 R_PG_Timer_ResumeCount_TMR_U<*unit number*>(_C<*channel number*>)

Definition
bool R_PG_Timer_ResumeCount_TMR_U<*unit number*>(_C<*channel number*>) (void)
<*unit number*>: 0 or 1
<*channel number*>: 0 to 3
((_C<*channel number*>) is added in the 8-bit mode.)

Description
Resume the TMR count operation

Parameter
None

Return value

true	Resuming count succeeded.
false	Resuming count failed.

File for output
R_PG_Timer_TMR_U<*unit number*>.c
<*unit number*>: 0 or 1

RPDL function
R_TMR_ControlChannel (8-bit mode)
R_TMR_ControlUnit (16-bit mode)

Details
- Resumes counting by a TMR that was halted by R_PG_Timer_HaltCount_TMR_U<*unit number*>(_C<*channel number*>).

Example
Refer to the example of R_PG_Timer_HaltCount_TMR_U<*unit number*>(_C<*channel number*>)

5.15.4 R_PG_Timer_GetCounterValue_TMR_U<*unit number*>(_C<*channel number*>)

Definition

•8-bit mode

bool R_PG_Timer_GetCounterValue_TMR_U<*unit number*>_C<*channel number*>
(uint8_t * data)

<*unit number*>: 0 or 1

<*channel number*>: 0 to 3

•16-bit mode

bool R_PG_Timer_GetCounterValue_TMR_U<*unit number*> (uint16_t * data)

<*unit number*>: 0 or 1

Description

Acquire the TMR counter value

Parameter

uint8_t * data (8-bit mode) uint16_t * data (16-bit mode)	Destination for storage of the counter value

Return value

true	Acquisition of the counter value succeeded.
false	Acquisition of the counter value failed.

File for output

R_PG_Timer_TMR_U<*unit number*>.c

<*unit number*>: 0 or 1

RPDL function

R_TMR_ReadChannel (8-bit mode)

R_TMR_ReadUnit (16-bit mode)

Details

- Acquires the counter value of a TMR.
  The value of the 8-bit counter for the specified channel is stored if the TMR unit is in the 8-bit timer mode. The counter values for both channels are stored as follows if the TMR unit is in the 16-bit mode.

Unit	b15 to b8	b7 to b0
0	TMR0 counter	TMR1 counter
1	TMR2 counter	TMR3 counter

*When the TMR unit is in the 16-bit mode, the higher-order bits are in TMR0 (or TMR2).

Example

The 8-bit timer mode was selected for TMR0 in the GUI.

```
#include "R_PG_default.h" //Include "R_PG_<project name>.h" to use this function.
uint8_t counter_val;
void func1(void)
{
      R_PG_Timer_Start_TMR_U0_C0();      //Place TMR0 in the 8-bit mode.
}
void func2(void)
{
      //Acquire the value of a counter of TMR0.
      R_PG_Timer_GetCounterValue_TMR_U0_C0( &counter_val );
}
```

5.15.5 R_PG_Timer_SetCounterValue_TMR_U<*unit number*>(_C<*channel number*>)

Definition

•8-bit mode

bool R_PG_Timer_SetCounterValue_TMR_U<*unit number*>_C<*channel number*>
(uint8_t data)

<*unit number*>: 0 or 1

<*channel number*>: 0 to 3

•16-bit mode

bool R_PG_Timer_SetCounterValue_TMR_U<*unit number*> (uint16_t data)

<*unit number*>: 0 or 1

Description

Set the TMR counter value

Parameter

uint8_t data (8-bit mode) uint16_t data (16-bit mode)	Value to be set to the counter

Return value

true	Setting of the counter value succeeded.
false	Setting of the counter value failed.

File for output

R_PG_Timer_TMR_U<*unit number*>.c

<*unit number*>: 0 or 1

RPDL function

R_TMR_ControlChannel (8-bit mode)

R_TMR_ControlUnit (16-bit mode)

Details

- Set the counter value of a TMR.
  The value of the 8-bit counter for the specified channel is stored if the TMR unit is in the 8-bit timer mode. The counter values for both channels are stored as follows if the TMR unit is in the 16-bit mode.

Unit	b15 to b8	b7 to b0
0	TMR0 counter	TMR1 counter
1	TMR2 counter	TMR3 counter

*When the TMR unit is in the 16-bit mode, the higher-order bits are in TMR0 (or TMR2).

Example

The 8-bit timer mode was selected for TMR0 in the GUI.

```
#include "R_PG_default.h" //Include "R_PG_<project name>.h" to use this function.
void func1(void)
{
    //Place TMR0 in the 8-bit mode.
    R_PG_Timer_Start_TMR_U0_C0();
}
void func2(void)
{
    //Set the value of a counter of TMR0.
    R_PG_Timer_SetCounterValue_TMR_U0_C0( 0 );
}
```

5.15.6 R_PG_Timer_GetRequestFlag_TMR_U<*unit number*>(_C<*channel number*>)

Definition

bool R_PG_Timer_GetRequestFlag_TMR_U<*unit number*>_C<*channel number*>
(bool* cma, bool* cmb, bool* ov);
<*unit number*>: 0 or 1
<*channel number*>: 0 to 3
((_C<*channel number*>) is added in the 8-bit mode.)

Description

Acquire and clear the TMR interrupt flags

Parameter

bool* cma	The address of storage area for the compare match A flag
bool* cmb	The address of storage area for the compare match B flag
bool* ov	The address of storage area for the overflow flag

Return value

true	Acquisition of the flags succeeded
false	Acquisition of the flags failed

File for output

R_PG_Timer_TMR_U<*unit number*>.c
<*unit number*>: 0 or 1

RPDL function

R_TMR_ReadChannel (8-bit mode)
R_TMR_ReadUnit (16-bit mode)

Details

- This function acquires the interrupt flags of TMR.
- All flags will be cleared in this function.
- Specify the address of storage area for the flags to be acquired.
- Specify 0 for a flag that is not required.

Example

The 8-bit timer mode was selected for TMR0 in the GUI.

```
#include "R_PG_default.h" //Include "R_PG_<project name>.h" to use this function.

uint16_t counter;

void func(void)
{
      //Place TMR0 in the 8-bit mode.
      R_PG_Timer_Start_TMR_U0_C0();

    //Wait for the compare match A
    do{
        R_PG_Timer_GetRequestFlag_TMR_U0_C0(
            & cma_flag,
            0,
            0
        );
    } while( !cma_flag );

    func_cmA();      //Processing in response to a compare match A interrupt
}
```

5.15.7 R_PG_Timer_StopModule_TMR_U<*unit number*>

Definition: bool R_PG_Timer_StopModule_TMR_U<*unit number*> (void)
<*unit number*>: 0 or 1

Description: Shut down a TMR unit

Parameter: None

Return value:

true	Shutting down succeeded.
false	Shutting down failed.

File for output: R_PG_Timer_TMR_U<*unit number*>.c
<*unit number*>: 0 or 1

RPDL function: R_TMR_Destroy

Details:

- Stops a TMR unit and places it in the module-stop state per unit. If both TMR0 and TMR1 of unit 0 (or both TMR2 and TMR3 of unit 1) are running when this function is called, both channels are stopped. Call the following function to stop a single channel.
  R_PG_Timer_HaltCount_TMR_U<*unit number*>_C<*channel number*>

Example: The 8-bit timer mode was selected for TMR0 in the GUI.
TmrCma0IntFunc was specified as the name of the compare match A interrupt function in the GUI.

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

void func(void)
{
    //Place TMR0 in the 8-bit mode.
    R_PG_Timer_Start_TMR_U0_C0();
}

void TmrCma0IntFunc(void)
{
    func_cma();     //Processing in response to a compare match A interrupt

    //Stop TMR unit 0.
    R_PG_Timer_StopModule_TMR_U0();
}
```

5.16 Compare Match Timer (CMT)

5.16.1 R_PG_Timer_Set_CMT_U<*unit number*>_C<*channel number*>

Definition: bool R_PG_Timer_Set_CMT_U<*unit number*>_C<*channel number*> (void)
<*unit number*>: 0 or 1
<*channel number*>: 0 to 3

Description: Set up the CMT

Parameter: None

Return value:

true	Setting was made correctly.
false	Setting failed.

File for output: R_PG_Timer_CMT_U<*unit number*>.c
<*unit number*>: 0 and 1

RPDL function: R_CMT_Create

Details:

- Releases the CMT from the module-stop and makes initial settings.
- R_PG_Timer_StartCount_CMT_U<*unit number*>_C<*channel number*> can be used to start the count operation.
- Function R_PG_Clock_Set must be called before any use of this function.
- Interrupts of the CMT are set by this function. When the name of the interrupt notification function has been specified in the GUI, if an interrupt occurs in the CPU, the function having the specified name will be called. Create the interrupt notification function as follows:
  void <*name of the interrupt notification function*> (void)
  For the interrupt notification function, note the contents of this chapter end, Notes on Notification Functions.

Example: A case where the setting is made as follows.

- Cmt0IntFunc was specified as a compare match interrupt notification function name

```
#include "R_PG_default.h" //Include "R_PG_<project name>.h" to use this function.

void func(void)
{
    R_PG_Clock_Set(); //The clock-generation circuit has to be set first.
    R_PG_Timer_Set_CMT_U0_C0(); //Set up the CMT0
    R_PG_Timer_StartCount_CMT_U0_C0(); //Start the count operation
}

void Cmt0IntFunc(void)
{
    R_PG_Timer_HaltCount_CMT_U0_C0(); //Halt the CMT0 count operation

    func_cmt0();    //Processing in response to a compare match interrupt

    R_PG_Timer_StartCount_CMT_U0_C0(); //Resume the CMT0 count operation
}
```

5.16.2 R_PG_Timer_StartCount_CMT_U<*unit number*>_C<*channel number*>

Definition: bool R_PG_Timer_StartCount_CMT_U<*unit number*>_C<*channel number*> (void)
<*unit number*>: 0 or 1
<*channel number*>: 0 to 3

Description: Start or resume the CMT count operation

Parameter: None

Return value:

True	Starting or resuming count succeeded.
False	Starting or resuming count failed.

File for output: R_PG_Timer_CMT_U<*unit number*>.c
<*unit number*>: 0 or 1

RPDL function: R_CMT_Control

Details:
- Starts counting by a CMT.
- Resumes counting by a CMT that was halted by R_PG_Timer_HaltCount_CMT_U<*unit number*>_C<*channel number*>.

Example: Refer to the example of R_PG_Timer_Set_CMT_U<*unit number*>_C<*channel number*>

5.16.3 R_PG_Timer_HaltCount_CMT_U<*unit number*>_C<*channel number*>

Definition: bool R_PG_Timer_HaltCount_CMT_U<*unit number*>_C<*channel number*> (void)
<*unit number*>: 0 or 1
<*channel number*>: 0 to 3

Description: Halt the CMT count operation

Parameter: None

Return value:

true	Halting succeeded.
false	Halting failed.

File for output: R_PG_Timer_CMT_U<*unit number*>.c
<*unit number*>: 0 or 1

RPDL function: R_CMT_Control

Details:
- Halts the CMT count operation. To make the CMT resume counting, call the following function.
  R_PG_Timer_StartCount_CMT_U<*unit number*>_C<*channel number*>

Example: Refer to the example of R_PG_Timer_Set_CMT_U<*unit number*>_C<*channel number*>

5.16.4 R_PG_Timer_GetCounterValue_CMT_U<*unit number*>_C<*channel number*>

Definition

bool R_PG_Timer_GetCounterValue_CMT_U<*unit number*>_C<*channel number*>
(uint16_t * data)

<*unit number*>: 0 or 1

<*channel number*>: 0 to 3

Description

Acquire the CMT counter value

Parameter

uint16_t * data	Destination for storage of the counter value

Return value

true	Acquisition of the counter value succeeded.
false	Acquisition of the counter value failed.

File for output

R_PG_Timer_CMT_U<*unit number*>.c

<*unit number*>: 0 or 1

RPDL function

R_CMT_Read

Details

- Acquires the counter value of a CMT.

Example

A case where the setting is made as follows.

- CMT unit 0 channel 0 was set up

```
#include "R_PG_default.h" //Include "R_PG_<project name>.h" to use this function.
uint16_t data;
void func1(void)
{
    R_PG_Clock_Set(); //The clock-generation circuit has to be set first.
    R_PG_Timer_Set_CMT_U0_C0(); //Set up the CMT0
    R_PG_Timer_StartCount_CMT_U0_C0(); //Start the count operation
}
void func2(void)
{
    //Acquire the value of a CMT0 counter
    R_PG_Timer_GetCounterValue_CMT_U0_C0( &data );
}
```

5.16.5 R_PG_Timer_SetCounterValue_CMT_U<*unit number*>_C<*channel number*>

Definition

bool R_PG_Timer_SetCounterValue_CMT_U<*unit number*>_C<*channel number*>
(uint16_t data)
<*unit number*>: 0 or 1
<*channel number*>: 0 to 3

Description

Set the CMT counter value

Parameter

uint16_t data	Value to be set to the counter

Return value

true	Setting of the counter value succeeded.
false	Setting of the counter value failed.

File for output

R_PG_Timer_CMT_U<*unit number*>.c
<*unit number*>: 0 or 1

RPDL function

R_CMT_Control

Details

- Set the counter value of a CMT.

Example

A case where the setting is made as follows.

- CMT unit 0 channel 0 was set up

```
#include "R_PG_default.h" //Include "R_PG_<project name>.h" to use this function.
void func1(void)
{
    R_PG_Clock_Set(); //The clock-generation circuit has to be set first.
    R_PG_Timer_Set_CMT_U0_C0(); //Set up the CMT0
    R_PG_Timer_StartCount_CMT_U0_C0(); //Start the count operation
}
void func2(void)
{
    //Set the value of a CMT0 counter
    R_PG_Timer_SetCounterValue_CMT_U0_C0( 0 );
}
```

5.16.6 R_PG_Timer_SetConstantRegister_CMT_U<*unit number*>_C<*channel number*>

Definition

bool R_PG_Timer_SetConstantRegister_CMT_U<*unit number*>_C<*channel number*>
(uint16_t constant_val)
<*unit number*>: 0 or 1
<*channel number*>: 0 to 3

Description

Set the CMT constant register value

Parameter

uint16_t constant_val	Destination for storage of the constant register value.

Return value

true	Setting was made correctly.
false	Setting failed.

File for output

R_PG_Timer_CMT_U<*unit number*>.c
<*unit number*>: 0 or 1

RPDL function

R_CMT_Control

Details

- Set the CMT constant register value.

Example

A case where the setting is made as follows.

- CMT unit 0 channel 0 was set up

```
#include "R_PG_default.h" //Include "R_PG_<project name>.h" to use this function.
void func1(void)
{
    R_PG_Clock_Set(); //The clock-generation circuit has to be set first.
    R_PG_Timer_Set_CMT_U0_C0(); //Set up the CMT0
    R_PG_Timer_StartCount_CMT_U0_C0(); //Start the count operation
}
void func2(void)
{
    //Set the CMT constant register value
    R_PG_Timer_SetConstantRegister_CMT_U0_C0( 0xabcd );
}
```

5.16.7 R_PG_Timer_StopModule_CMT_U<*unit number*>

Definition

bool R_PG_Timer_StopModule_CMT_U<*unit number*> (void)

<*unit number*>: 0 or 1

Description

Shut down the CMT unit

Parameter

None

Return value

true	Shutting down succeeded.
false	Shutting down failed.

File for output

R_PG_Timer_CMT_U<*unit number*>.c

<*unit number*>: 0 or 1

RPDL function

R_CMT_Destroy

Details

- Stops a CMT unit and places it in the module-stop state per unit. If both CMT0 and CMT1 of unit 0 (or both CMT2 and CMT3 of unit 1) are running when this function is called, both channels are stopped. Call the following function to stop a single channel.
  R_PG_Timer_HaltCount_CMT_U<*unit number*>_C<*channel number*>

Example

A case where the setting is made as follows.

- CMT unit 0 channel 0 was set up
- Cmt0IntFunc was specified as the compare match interrupt notification function name

```
#include "R_PG_default.h" //Include "R_PG_<project name>.h" to use this function.

void func(void)
{
    R_PG_Clock_Set(); //The clock-generation circuit has to be set first.
    R_PG_Timer_Set_CMT_U0_C0(); //Set up the CMT0
    R_PG_Timer_StartCount_CMT_U0_C0(); //Start the count operation
}

void Cmt0IntFunc(void)
{
    func_cmt0(); //Processing in response to a compare match interrupt

    //Stop the CMT unit 0
    R_PG_Timer_StopModule_CMT_U0();
}
```

5.17 Realtime Clock (RTCa)

5.17.1 R_PG_RTC_Start

Definition: bool R_PG_RTC_Start (void)

Description: Sets up the RTC and starts its counter

Parameter: None

Return value:

true	Setting was made correctly.
false	Setting failed.

File for output: R_PG_RTC.c

RPDL function: R_RTC_Create

Details:

- Sets up the alarm interrupt, cyclic interrupt, and 1-Hz clock output from the RTCOUT pin and starts the RTC's counter.
- Before calling this function, call R_PG_Clock_Set to set the clock.
- This function does not set the current time. When the alarm interrupt is to be used, call this function before R_PG_RTC_SetCurrentTime, which sets the current time.
- The alarm register is set when the time and date settings for the alarm are made through the GUI.

Example:

```
// Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"
#include "iodefine_RPDL.h"

void func(void)
{
     R_PG_Clock_Set(); // Set the clock.

     if( SYSTEM.RSTSR1.BIT.CWSF == 1 ) { //Check the warm start flag
          R_PG_RTC_Start(); // Set up the RTC and start its counter.
           SYSTEM.RSTSR1.BIT.CWSF = 1; //Set the warm start flag
      }
      else {
           R_PG_RTC_WarmStart(); //Set up the RTC of warm start and start its counter
      }
}
```

5.17.2 R_PG_RTC_WarmStart

Definition: bool R_PG_RTC_WarmStart (void)

Description: Sets up the RTC of warm start and starts its counter

Parameter: None

Return value:

true	Setting was made correctly.
false	Setting failed.

File for output: R_PG_RTC.c

RPDL function: R_RTC_CreateWarm

Details:
- Sets up the alarm interrupt, cyclic interrupt and starts the RTC's counter.
- Before calling this function, call R_PG_Clock_Set to set the clock.

Example: Refer to the example of R_PG_RTC_Start.

5.17.3 R_PG_RTC_Stop

Definition: bool R_PG_RTC_Stop (void)

Description: Suspends counting by the RTC

Parameter: None

Return value:

true	Counting was successfully suspended.
false	Suspension failed.

File for output: R_PG_RTC.c

RPDL function: R_RTC_Control

Details:

- Suspends counting by the RTC.
- To restart counting, call R_PG_RTC_Restart.

Example:

```
// Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

void func1(void)
{
    R_PG_Clock_Set(); // Set the clock.
    R_PG_RTC_Start(); // Set up the RTC and start its counter.
}

void func2(void)
{
    R_PG_RTC_Stop ();   // Suspend counting.
}

void func3(void)
{
    R_PG_RTC_Restart();   // Restart counting.
}
```

5.17.4 R_PG_RTC_Restart

Definition: bool R_PG_RTC_Restart (void)

Description: Restarts counting by the RTC

Parameter: None

Return value:

true	Counting was successfully restarted.
false	Restarting failed.

File for output: R_PG_RTC.c

RPDL function: R_RTC_Control

Details:

- Allows the RTC to restart counting that was suspended by R_PG_RTC_Stop.

Example: Refer to the example of R_PG_RTC_Stop.

5.17.5 R_PG_RTC_SetCurrentTime

Definition

bool R_PG_RTC_SetCurrentTime
(uint8_t seconds, uint8_t minutes, bool pm, uint8_t hours,
uint8_t day, uint8_t month, uint16_t year)

Description

Sets the current time

Parameter

uint8_t seconds	Seconds (valid range of values: 0x00 to 0x59, as BCD values)
uint8_t minutes	Minutes (valid range of values: 0x00 to 0x59, as BCD values)
bool pm	a.m./p.m. 0: a.m. 1: p.m.
uint8_t hours	Hours (valid range of values in 24-hour mode: 0x00 to 0x23, as BCD values; in 12-hour mode: 0x01 to 0x12, as BCD values)
uint8_t day	Date (valid range of values: 0x01 to the number of days in the specified month, as BCD values)
uint8_t month	Month (valid range of values: 0x01 to 0x12, as BCD values)
uint16_t year	Year (valid range of values: 0x0000 to 0x9999, as BCD values)

Return value

true	Setting was made correctly.
false	Setting failed.

File for output

R_PG_RTC.c

RPDL function

R_RTC_Control

Details

- Sets the current time.
- The value of the day-of-the-week counter is is figured out from the specified values for date, month, and year.
- When this function is called during counting, counting is suspended while the current time is set and resumed on completion of the settings.
- Specify valid values even for items that are not to be used for the alarm interrupt.
- In 24-hour mode, specify 0 for p.m.

Example

The following settings have been made through the GUI.

- Set up an alarm interrupt.
- Specify RtcAlmIntFunc as the alarm interrupt notification function.

```
// Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

void func(void)
{
    R_PG_Clock_Set(); // Set the clock.
    R_PG_RTC_Start(); // Set up the RTC and start its counter.

    R_PG_RTC_SetCurrentTime( // Set the current time (03:44:55 on Nov. 22, 2000)
        0x55, // 55 seconds
        0x44, // 44 minutes
        0, // a.m.
        0x03, // 03 o'clock
        0x22, // 22nd
        0x11, // November
        0x2000 // 2000
    );

    R_PG_RTC_SetAlarmTime( // Set the alarm time (03:45:00 on Nov. 22, 2000)
        0x00, // 00 seconds
        0x45, // 45 minutes
        0, // a.m.
        0x03, // 03 o'clock
        0xff, // Day of the week (0xff: Automatically calculated from the date)
        0x22, // 22nd
        0x11, // November
        0x2000 // 2000
    );

    R_PG_RTC_AlarmControl( // Enable the year, month, date, day of the week, hour,
                           // minute, and second alarms.
        1, // Enable the seconds alarm
        1, // Enable the minutes alarm
        1, // Enable the hours alarm
        1, // Enable the day-of-the-week alarm
        1, // Enable the date alarm
        1, // Enable the month alarm
        1 // Enable the year alarm
    );
}

void RtcAlmIntFunc(void)
{
    // Alarm interrupt processing
}
```

5.17.6 R_PG_RTC_GetStatus

Definition

```
bool R_PG_RTC_GetStatus
( bool * hour_mode24,   uint8_t * seconds,      uint8_t * minutes,   bool * pm,
  uint8_t * hours,      uint8_t * day_of_week,  uint8_t * day,       uint8_t * month,
  uint16_t * year,      bool * carry,           bool * alarm,        bool * period,
  bool * adjustment,    bool * reset,           bool * running   )
```

Description

Acquires information on the current state of the RTC

Parameter

bool * hour_mode24	Destination for storage of the hour-mode information (0: 12-hour mode, 1: 24-hour mode)
uint8_t * seconds	Destination for storage of the current seconds-counter value
uint8_t * minutes	Destination for storage of the current minutes-counter value
bool * pm	Destination for storage of the a.m./p.m. value
uint8_t * hours	Destination for storage of the current hours-counter value
uint8_t * day_of_week	Destination for storage of the current day-of-the-week counter value
uint8_t * day	Destination for storage of the current date-counter value
uint8_t * month	Destination for storage of the current month-counter value
uint16_t * year	Destination for storage of the current year-counter value
bool * carry	Destination for storage of the incrementation interrupt flag
bool * alarm	Destination for storage of the alarm interrupt flag
bool * period	Destination for storage of the cyclic interrupt flag
bool * adjustment	Destination for storage of the 30-second unit adjustment bit (0: normal operation, 1: adjustment in progress)
bool * reset	Destination for storage of the reset bit (0: normal operation, 1: resetting in progress)
bool * running	Destination for storage of the start bit (0: clock stopped, 1: clock operating)

Return value

true	Acquisition succeeded.
false	Acquisition failed.

File for output

R_PG_RTC.c

RPDL function

R_RTC_Read

Details

- Acquires information on the current state of the RTC.
- For the parameter that corresponds to each of the items you wish to acquire, specify the address where the value is to be stored. For the items you do not wish to acquire, on the other hand, specify 0.
- The interrupt flag is cleared within this function.
  When the value of the incrementation interrupt flag is 1, the current time will change while the information is being acquired. Read the value again in such cases.

Example

```
// Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

void func1(void)
{
    R_PG_Clock_Set(); // Set the clock.
    R_PG_RTC_Start(); // Set up the RTC and start its counter.
}

void func2(void)
```

```
{
    do{
        // Acquire the values of the current time and incrementation interrupt flag.
            R_PG_RTC_GetStatus(
            &hour_mode24,// 24-hour mode
            &seconds,    // Seconds
            &minutes,    // Minutes
            &pm,         // a.m./p.m.
            &hours,      // Hours
            0,           // Day of the week
            0,           // Date
            0,           // Month
            0,           // Year
            &carry,      // Incrementation interrupt flag
            0,           // Alarm interrupt flag
            0,           // Cyclic interrupt flag
            0,           // 30-second unit adjustment bit
            0,           // Reset bit
            0            // Start bit
        );
    } while( carry );
}
```

5.17.7 R_PG_RTC_Adjust30sec

Definition: bool R_PG_RTC_Adjust30sec (void)

Description: Performs 30-second unit adjustment

Parameter: None

Return value:

true	Setting was made correctly.
false	Setting failed.

File for output: R_PG_RTC.c

RPDL function: R_RTC_Control

Details:

- Performs 30-second unit adjustment (29 or fewer seconds are rounded down to 00 seconds while 30 or more seconds are treated as 1 minute).

Example:

```
// Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

void func1(void)
{
    R_PG_Clock_Set(); // Set the clock.
    R_PG_RTC_Start(); // Set up the RTC and start its counter.
}

void func2(void)
{
    R_PG_RTC_Adjust30sec(); // Perform 30-second unit adjustment.
}
```

5.17.8 R_PG_RTC_ManualErrorAdjust

Definition: bool R_PG_RTC_ManualErrorAdjust (int8_t cycle)

Description: Corrects an error of the timer

Conditions for output: “Sub-clock” is selected as the count source.

Parameter

int8_t cycle	Value (i.e. a number of subclock cycles) for use in correcting an error of the timer -63 to -1 : Put the timer back 0 to 63 : Put the timer forward

Return value

true	Setting was made correctly.
false	Setting failed.

File for output: R_PG_RTC.c

RPDL function: R_RTC_Control

Details

- Corrects an error (earliness or lateness) of the timer due to the precision of subclock oscillation.

Example

```
// Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

int8_t cycle=-1;

void func(void)
{
    R_PG_Clock_Set(); // Set the clock.

    // Set up the RTC and start its counter.
    R_PG_RTC_Start();
}

void RtcPrdIntFunc(void)
{
    // Correct an error of the timer.
    R_PG_RTC_ManualErrorAdjust(cycle);
}
```

5.17.9 R_PG_RTC_Set24HourMode

Definition: bool R_PG_RTC_Set24HourMode (void)

Description: Places the RTC in 24-hour mode

Parameter: None

Return value:

true	Setting was made correctly.
false	Setting failed.

File for output: R_PG_RTC.c

RPDL function: R_RTC_Control

Details:

- Places the RTC in 24-hour mode.

Example:

```
// Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

void func(void)
{
    R_PG_Clock_Set(); // Set the clock.

    // Set up the RTC and start its counter.
    R_PG_RTC_Start();

    // Set the current time (03:44:55 on November 22, 2000)
    R_PG_RTC_SetCurrentTime(
        0x55, // 55 seconds
        0x44, // 44 minutes
        0, // a.m.
        0x03, // 3 o’clock
        0x22, // 22rd
        0x11, // November
        0x2000 // 2000
    );

    // Places the RTC in 24-hour mode.
    R_PG_RTC_Set24HourMode();
}
```

5.17.10 R_PG_RTC_Set12HourMode

Definition: bool R_PG_RTC_Set12HourMode (void)

Description: Places the RTC in 12-hour mode

Parameter: None

Return value:

true	Setting was made correctly.
false	Setting failed.

File for output: R_PG_RTC.c

RPDL function: R_RTC_Control

Details:

- Places the RTC in 12-hour mode.

Example:

```
// Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

void func(void)
{
    R_PG_Clock_Set(); // Set the clock.

    // Set up the RTC and start its counter.
    R_PG_RTC_Start();

    // Set the current time (03:44:55 on November 22, 2000)
    R_PG_RTC_SetCurrentTime(
        0x55, // 55 seconds
        0x44, // 44 minutes
        0, // 24-hour mode
        0x03, // 3 o’clock
        0x22, // 22rd
        0x11, // November
        0x2000 // 2000
    );

    // Places the RTC in 12-hour mode.
    R_PG_RTC_Set12HourMode();
}
```

5.17.11 R_PG_RTC_AutoErrorAdjust_Enable

Definition: bool R_PG_RTC_AutoErrorAdjust_Enable (int8_t cycle)

Description: Enables automatic correction of errors of the timer

Conditions for output: Automatic correction of errors of the timer has been set up.

Parameter

int8_t cycle	Value (i.e. a number of subclock cycles) for use in correcting an error of the timer -63 to -1: Put the timer back 0 to 63: Put the timer forward

Return value

true	Setting was made correctly.
false	Setting failed.

File for output: R_PG_RTC.c

RPDL function: R_RTC_Control

Details

- Enables automatic correction of errors of the timer.
- Automatically corrects errors (earliness or lateness) of the timer due to the precision of subclock oscillation over each adjustment cycle selected through the GUI.

Example

```
// Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

int8_t cycle=-60;

void func(void)
{
    R_PG_Clock_Set(); // Set the clock.

    // Set up the RTC and start its counter.
    R_PG_RTC_Start();

    // Enable automatic correction of errors of the timer.
    R_PG_RTC_AutoErrorAdjust_Enable(cycle);

    // Set the current time (03:44:55 on November 22, 2000)
    R_PG_RTC_SetCurrentTime(
        0x55, // 55 seconds
        0x44, // 44 minutes
        0, // a.m.
        0x03, // 3 o'clock
        0x22, // 22rd
        0x11, // November
        0x2000 // 2000
    );
}
```

5.17.12 R_PG_RTC_AutoErrorAdjust_Disable

Definition	bool R_PG_RTC_AutoErrorAdjust_Disable (void)
Description	Disables automatic correction of errors of the timer
Conditions for output	Automatic correction of errors of the timer has been set up.
Parameter	None

Return value

true	Setting was made correctly.
false	Setting failed.

File for output	R_PG_RTC.c
RPDL function	R_RTC_Control

Details

- Disables automatic correction of errors of the timer.

Example

```
// Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

int8_t cycle=-60;

void func1(void)
{
    R_PG_Clock_Set(); // Set the clock.

    // Set up the RTC and start its counter.
    R_PG_RTC_Start();

    // Enable automatic correction of errors of the timer.
    R_PG_RTC_AutoErrorAdjust_Enable(cycle);

    // Set the current time (03:44:55 on November 22, 2000)
    R_PG_RTC_SetCurrentTime(
        0x55, // 55 seconds
        0x44, // 44 minutes
        0, // a.m.
        0x03, // 3 o'clock
        0x22, // 22rd
        0x11, // November
        0x2000 // 2000
    );
}

void func2(void)
{
    // Disable automatic correction of errors of the timer.
    R_PG_RTC_AutoErrorAdjust_Disable();
}
```

5.17.13 R_PG_RTC_AlarmControl

Definition

bool R_PG_RTC_AlarmControl
(bool sec_enable, bool min_enable, bool hour_enable, bool day_of_week_enable,
bool day_enable, bool month_enable, bool year_enable)

Description

Enables or disables alarms

Conditions for output

An alarm interrupt has been set up.

Parameter

bool sec_enable	Seconds alarm (1: enabled, 0: disabled)
bool min_enable	Minutes alarm (1: enabled, 0: disabled)
bool hour_enable	Hours alarm (1: enabled, 0: disabled)
bool day_of_week_enable	Day-of-the-week alarm (1: enabled, 0: disabled)
bool day_enable	Date alarm (1: enabled, 0: disabled)
bool month_enable	Month alarm (1: enabled, 0: disabled)
bool year_enable	Year alarm (1: enabled, 0: disabled)

Return value

true	Setting was made correctly.
false	Setting failed.

File for output

R_PG_RTC.c

RPDL function

R_RTC_Control

Details

- Enables or disables the seconds, minutes, hours, day-of-the-week, date, month, or year alarms.

Example

Refer to the example of R_PG_RTC_SetCurrentTime.

5.17.14 R_PG_RTC_SetAlarmTime

Definition

```
bool R_PG_RTC_SetAlarmTime
 ( uint8_t seconds,     uint8_t minutes,  bool pm,        uint8_t hours,
   uint8_t day_of_week, uint8_t day,      uint8_t month,  uint16_t year  )
```

Description

Sets the time for an alarm

Conditions for output

An alarm interrupt has been set up.

Parameter

uint8_t seconds	Seconds (valid range of values: 0x00 to 0x59, as BCD values)
uint8_t minutes	Minutes (valid range of values: 0x00 to 0x59, as BCD values)
bool pm	a.m./p.m. 0: a.m. 1: p.m.
uint8_t hours	Hours (valid range of values in 24-hour mode: 0x00 to 0x23, as BCD values; in 12-hour mode: 0x01 to 0x12, as BCD values)
uint8_t day_of_week	Day of the week (valid range of values: 0x00 for Sunday to 0x06 for Saturday) When 0xff is specified, the day of the week is figured out from the values for day, month, and year.
uint8_t day	Date (valid range of values: 0x01 to the number of days in the specified month, as BCD values)
uint8_t month	Month (valid range of values: 0x01 to 0x12, as BCD values)
uint16_t year	Year (valid range of values: 0x0000 to 0x9999, as BCD values)

Return value

true	Setting was made correctly.
false	Setting failed.

File for output

R_PG_RTC.c

RPDL function

R_RTC_Control

Details

- Sets the time for an alarm.
- Specify valid values even for items that are not to be used for an alarm interrupt.
- In 24-hour mode, specify 0 for p.m.

Example

Refer to the example of R_PG_RTC_SetCurrentTime.

5.17.15 R_PG_RTC_SetPeriodicInterrupt

Definition: bool R_PG_RTC_SetPeriodicInterrupt (float frequency)

Description: Specifies the cycle for generating the cyclic interrupt

Conditions for output: The cyclic interrupt has been set up.

Parameter

float frequency	Frequency for the interrupt (Hz; valid values: 0.5, 1, 2, 4, 16, 64, and 256)

Return value

true	Setting was made correctly.
false	Setting failed.

File for output: R_PG_RTC.c

RPDL function: R_RTC_Control

Details

- Changes the cycle for generating the cyclic interrupt.

Example: The following settings have been made through the GUI.

- Setting up the cyclic interrupt.
- Specifying RtcPrdIntFunc as the cyclic interrupt notification function.

```
// Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

void func(void)
{
    R_PG_Clock_Set(); // Set the clock.
    R_PG_RTC_Start(); // Set up the RTC and start its counter.
}

void RtcAlmIntFunc(void)
{
    // Cyclic interrupt processing

    R_PG_RTC_SetPeriodicInterrupt( 4 );   // Specify 1/4 second as the cycle for
                                          // generating the cyclic interrupt.
}
```

5.17.16 R_PG_RTC_ClockOut_Enable

Definition	bool R_PG_RTC_ClockOut_Enable (void)
Description	Enables the clock output
Conditions for output	Output of a 1-Hz clock signal from the RTCOUT pin has been enabled.
Parameter	None

Return value

true	Setting was made correctly.
false	Setting failed.

File for output	R_PG_RTC.c
RPDL function	R_RTC_Control

Details

- Starts 1-Hz clock output from the RTCOUT pin.

Example

The following setting has been made through the GUI.

- Enable 1-Hz clock output from the RTCOUT pin.

```
// Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

void func1(void)
{
    R_PG_Clock_Set(); // Set the clock.
    R_PG_RTC_Start(); // Set up the RTC and start its counter and the clock output.
}

void func2(void)
{
    R_PG_RTC_ClockOut_Disable();   // Stop the clock output.
}

void func3(void)
{
    R_PG_RTC_ClockOut_Enable();   // Restart the clock output.
}
```

5.17.17 R_PG_RTC_ClockOut_Disable

Definition: bool R_PG_RTC_ClockOut_Disable (void)

Description: Disables the clock output

Conditions for output: Output of a 1-Hz clock signal from the RTCOUT pin has been enabled.

Parameter: None

Return value:

true	Setting was made correctly.
false	Setting failed.

File for output: R_PG_RTC.c

RPDL function: R_RTC_Control

Details:

- Stops 1-Hz clock output from the RTCOUT pin.

Example: The following setting has been made through the GUI.

- Enable 1-Hz clock output from the RTCOUT pin.

```
// Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

void func1(void)
{
    R_PG_Clock_Set(); // Set the clock.
    R_PG_RTC_Start(); // Set up the RTC and start its counter and the clock output.
}

void func2(void)
{
    R_PG_RTC_ClockOut_Disable();   // Stop the clock output.
}

void func3(void)
{
    R_PG_RTC_ClockOut_Enable();   // Restart the clock output.
}
```

5.17.18 R_PG_RTC_TimeCapture<*number of the input pin for a time capture event*>_Enable

Definition: bool R_PG_RTC_TimeCapture<*number of the input pin for a time capture event*>_Enable (void)

<*number of the input pin for a time capture event*>: 0 to 2

Description: Enables time capturing

Conditions for output: Time capturing has been set up.

Parameter: None

Return value:

true	Setting was made correctly.
false	Setting failed.

File for output: R_PG_RTC.c

RPDL function: R_RTC_Control

Details

- Enables time capturing.

Example

```
// Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

uint8_t  seconds,minutes,hours,day,month;
bool  pm,event;

void func(void)
{
    R_PG_Clock_Set(); // Set the clock.

    R_PG_RTC_Start(); // Set up the RTC and start its counter.

    // Set the current time (03:44:55 on November 22, 2000)
    R_PG_RTC_SetCurrentTime(
        0x55, // 55 seconds
        0x44, // 44 minutes
        0, // a.m.
        0x03, // 3 o'clock
        0x22, // 22rd
        0x11, // November
        0x2000 // 2000
    );

    R_PG_RTC_TimeCapture0_Enable(); // Enable time capturing.

    do{
        R_PG_RTC_GetCaptureTime0( // Acquire captured time.
            &seconds,
            &minutes,
            &pm,
            &hours,
            &day,
            &month,
            &event
        );
    }while(event == 0);
}
```

5.17.19 R_PG_RTC_TimeCapture<*number of the input pin for a time capture event*>_Disable

Definition: bool R_PG_RTC_TimeCapture<*number of the input pin for a time capture event*>_Disable (void)

<*number of the input pin for a time capture event*>: 0 to 2

Description: Disables time capturing

Conditions for output: Time capturing has been set up.

Parameter: None

Return value:

true	Setting was made correctly.
false	Setting failed.

File for output: R_PG_RTC.c

RPDL function: R_RTC_Control

Details

- Disables time capturing.

Example

```
// Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

uint8_t  seconds,minutes,hours,day,month;
bool  pm,event;

void func(void)
{
    R_PG_Clock_Set(); // Set the clock.
    R_PG_RTC_Start(); // Set up the RTC and start its counter.
    R_PG_RTC_SetCurrentTime( // Set the current time (03:44:55 on November 22, 2000)
        0x55, // 55 seconds
        0x44, // 44 minutes
        0, // a.m.
        0x03, // 3 o'clock
        0x22, // 22rd
        0x11, // November
        0x2000 // 2000
    );
    R_PG_RTC_TimeCapture0_Enable(); // Enable time capturing.

    do{
        R_PG_RTC_GetCaptureTime0( // Acquire captured time.
            &seconds,
            &minutes,
            &pm,
            &hours,
            &day,
            &month,
            &event
        );
    }while(event == 0);

    // Disable time capturing.
    R_PG_RTC_TimeCapture0_Disable();
}
```

5.17.20 R_PG_RTC_GetCaptureTime<*number of the input pin for a time capture event*>

Definition bool R_PG_RTC_GetCaptureTime<*number of the input pin for a time capture event*>
(uint8_t * seconds, uint8_t * minutes, bool * pm, uint8_t * hours,
uint8_t * day, uint8_t * month, bool * event)
<*number of the input pin for a time capture event*>: 0 to 2

Description Acquires the captured time

Conditions for output Time capturing has been set up.

Parameter

uint8_t * seconds	Destination for storage of the current seconds-counter value
uint8_t * minutes	Destination for storage of the current minutes-counter value
bool * pm	Destination for storage of the p.m. value
uint8_t * hours	Destination for storage of the current hours-counter value
uint8_t * day	Destination for storage of the current date-counter value
uint8_t * month	Destination for storage of the current month-counter value
bool * event	Destination for storage of the time capture status bit (0: no events were detected, 1: an event was detected)

Return value

true	Acquisition succeeded.
false	Acquisition failed.

File for output R_PG_RTC.c

RPDL function R_RTC_Read

Details
- Acquires captured time.

Example Refer to the example of R_PG_RTC_TimeCapture<*number of the input pin for a time capture event*>_Enable.

5.18 Watchdog Timer (WDTA)

5.18.1 R_PG_Timer_Start_WDT

Definition	bool R_PG_Timer_Start_WDT (void)
Description	Set up the WDT and start the count operation
Conditions for output	Register start mode is selected. (This function is not output if auto-start mode is selected. A macro for setting option function select registers is output to R_PG_MCU_OFS.c.)
Parameter	None

Return value

true	Setting was made correctly
false	Setting failed

File for output	R_PG_Timer_ WDT.c
RPDL function	R_WDT_Set

Details

- Makes initial settings of WDT and starts the count operation.

Example

A case where the setting is made as follows.

- Start mode: Register start mode

```
//Include “R_PG_<project name>.h” to use this function.
#include "R_PG_default.h"

void func(void)
{
    R_PG_Clock_Set();    //Set up the clocks
    R_PG_Timer_Start_WDT();    //Set up the WDT and start the count operation
}
```

5.18.2 R_PG_Timer_RefreshCounter_WDT

Definition: bool R_PG_Timer_RefreshCounter_WDT(void)

Description: Refresh the counter of WDT

Parameter: None

Return value:

true	Setting was made correctly
false	Setting failed

File for output: R_PG_Timer_ WDT.c

RPDL function: R_WDT_Control

Details:

- Refresh the counter of WDT

Example:

```
//Include “R_PG_<project name>.h” to use this function.
#include "R_PG_default.h"

void func1(void)
{
      R_PG_Clock_Set();    //Set up the clocks
      R_PG_Timer_Start_WDT();    //Set up the WDT and start the count operation
}

void func2(void)
{
      R_PG_Timer_RefreshCounter_WDT();      //Refresh the WDT counter
}
```

5.18.3 R_PG_Timer_GetStatus_WDT

Definition: bool R_PG_Timer_GetStatus_WDT(uint16_t * counter_val, bool * undf, bool * ref_err)

Description: Acquires the status flag and count value of WDT

Parameter

uint16_t * counter_val	The address of storage area for the counter value
bool * undf	The address of storage area for the underflow flag
bool * ref_err	The address of storage area for the refresh error flag

Return value

true	Setting was made correctly
false	Setting failed

File for output: R_PG_Timer_WDT.c

RPDL function: R_WDT_Read

Details

- This function acquires the status flag and count value of WDT.

Example

```
//Include “R_PG_<project name>.h” to use this function.
#include "R_PG_default.h"

uint16_t counter_val;
bool undf;
bool ref_err;

void func(void)
{
    //Acquires the status flag and count value of WDT
    R_PG_Timer_GetStatus_WDT(&counter_val, &undf, &ref_err);
}
```

5.19 Independent Watchdog Timer (IWDTa)

5.19.1 R_PG_Timer_Start_IWDT

Definition: bool R_PG_Timer_Start_IWDT (void)

Description: Sets up the IWDT and starts its timer

Conditions for output: Register start mode is selected.
(This function is not output if auto-start mode is selected. A macro for setting option function select registers is output to R_PG_MCU_OFS.c.)

Parameter: None

Return value:

true	Setting was made correctly
false	Setting failed

File for output: R_PG_Timer_IWDT.c

RPDL function: R_IWDT_Set

Details:
- This function sets up the IWDT and starts its counter.
- Before calling this function, call R_PG_Clock_Set to set the clock.

Example:

```
//Include “R_PG_<project name>.h” to use this function.
#include "R_PG_default.h"

void func(void)
{
    //Set up the clocks
    R_PG_Clock_Set();

    //Sets up the IWDT and starts its timer
    R_PG_Timer_Start_IWDT();
}
```

5.19.2 R_PG_Timer_RefreshCounter_IWDT

Definition: bool R_PG_Timer_RefreshCounter_IWDT (void)

Description: Refresh the counter

Parameter: None

Return value:

true	Refreshing succeeded
false	Refreshing failed

File for output: R_PG_Timer_IWDT.c

RPDL function: R_IWDT_Control

Details:

- Refreshes the IWDT counter
- After starting the count operation, call this function to clear the counter before the counter underflow.

Example:

```
//Include “R_PG_<project name>.h” to use this function.
#include "R_PG_default.h"

void func1(void)
{
    //Set up the clocks
    R_PG_Clock_Set();

    //Sets up the IWDT and starts its timer
    R_PG_Timer_Start_IWDT();
}

void func2(void)
{
    //Refresh the counter
    R_PG_Timer_RefreshCounter_IWDT();
}
```

5.19.3 R_PG_Timer_GetStatus_IWDT

Definition: bool R_PG_Timer_GetStatus_IWDT(uint16_t * counter_val, bool * undf, bool * ref_err)

Description: Acquires the status flag and count value of IWDT

Parameter

uint16_t * counter_val	The address of storage area for the IWDT counter value
bool * undf	The address of storage area for the underflow flag
bool * ref_err	The address of storage area for the refresh error flag

Return value

true	Acquisition succeeded
false	Acquisition failed

File for output: R_PG_Timer_IWDT.c

RPDL function: R_IWDT_Read

Details

- Acquires the IWDT status flag and counter value.
- The underflow flag shall be cleared in this function.

Example

```
//Include “R_PG_<project name>.h” to use this function.
#include "R_PG_default.h"

uint16_t counter_val;
bool undf;
bool ref_err;

void func(void)
{
      //Acquires the IWDT status flag and counter value
      R_PG_Timer_GetStatus_IWDT(&counter_val, &undf, &ref_err);
}
```

5.20 Serial Communications Interface (SCIc, SCId)

5.20.1 R_PG_SCI_Set_C<*channel number*>

Definition: bool R_PG_SCI_Set_C<*channel number*> (void)
<*channel number*>: 0 to 12

Description: Set up a SCI channel

Parameter: None

Return value:

true	Setting was made correctly
false	Setting failed

File for output: R_PG_SCI_C<*channel number*>.c
<*channel number*>: 0 to 12

RPDL function: R_SCI_Create, R_SCI_Set

Details:

- Releases a SCI channel from the module-stop state, makes initial settings.
- Function R_PG_Clock_Set must be called before calling this function.
- When the name of the notification function has been specified in the GUI, if corresponding event occurs, the function having the specified name will be called. Create the notification function as follows:
  void <*name of the notification function*> (void)
  For the notification function, note the contents of this chapter end, Notes on Notification Functions.

Example: SCI0 has been set in the GUI.

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

void func(void)
{
    R_PG_Clock_Set();           //The clock-generation circuit has to be set first.
    R_PG_SCI_Set_C0();          //Set up SCI0.
}
```

5.20.2 R_PG_SCI_SendTargetStationID_C<*channel number*>

Definition: bool R_PG_SCI_SendTargetStationID_C<*channel number*> (uint8_t id)

<*channel number*>: 0 to 12

Description: Transmits the ID code of the receiving station

Conditions for output:
- The function of transmission is selected for a SCI channel
- The multi-processer communications function is enabled in the asynchronous serial communication mode

Parameter

uint8_t id	The ID to be transmitted (0 to 255)

Return value

true	Transmission succeeded
false	Transmission failed

File for output: R_PG_SCI_C<*channel number*>.c

<*channel number*>: 0 to 12

RPDL function: R_SCI_Send

Details:
- Generates an ID transmission cycle to transmit the ID code of the destination receiving station.
- This function waits until the ID transmission cycle has been completed.

Example: A case where the setting is made as follows.
- The function of transmission is selected for a SCI0 channel
- The multi-processer communications function is enabled in the asynchronous serial communication mode
- “Wait at the transmission function until all data has been transmitted” is selected as the data transmission method.

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

uint8_t data[] = “ABCDEFGHIJ”;

void func(void)
{
      R_PG_Clock_Set();              //The clock-generation circuit has to be set first.
      R_PG_SCI_Set_C0();             //Set up SCI0
      R_PG_SCI_SendTargetStationID_C0( 5 );          //Send ID code (ID:5)
      R_PG_SCI_SendAllData_C0( data, 10 );           //Send data
}
```

5.20.3 R_PG_SCI_StartSending_C<*channel number*>

Definition

bool R_PG_SCI_StartSending_C<*channel number*> (uint8_t * data, uint16_t count)

<*channel number*>: 0 to 12

Description

Start the data transmission

Conditions for output

- The function of transmission is selected for a SCI channel in GUI.
- "Notify the transmission completion of all data by function call" is selected as the data transmission method in GUI.

Parameter

uint8_t * data	The start address of the data to be sent.
uint16_t count	The number of the data to be sent. Set this to 0 if the transmit data is a character string (ending with a null character).

Return value

true	Setting was made correctly
false	Setting failed

File for output

R_PG_SCI_C<*channel number*>.c <*channel number*>: 0 to 12

RPDL function

R_SCI_Send

Details

- This function starts the data transmission.
- This function is generated when "Notify the transmission completion of all data by function call" is selected as the data transmission method in GUI. This function returns immediately and the notification function having the specified name will be called when the last byte has been sent. Create the notification function as follows:

  void <*name of the notification function*> (void)

  For the notification function, note the contents of this chapter end, Notes on Notification Functions.
- The number of transmitted data can be acquired by R_PG_SCI_GetSentDataCount_C<*channel number*>. The transmission can be terminated by calling R_PG_SCI_StopCommunication_C<*channel number*> before all bytes have been sent.
- The count of transmitted characters will loop back to 0 if 65536 characters are sent.

Example

SCI0 has been set as transmitter in the GUI.

Sci0TrFunc was specified as the name of the transmit end notification function in the GUI.

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

uint8_t data[255];

void func(void)
{
    R_PG_Clock_Set();              //The clock-generation circuit has to be set first.
    R_PG_SCI_Set_C0();             //Set up SCI0.
    R_PG_SCI_StartSending_C0(data, 255);          //Send 255 bytes of binary data.
}

//Transmit end notification function that called when all bytes have been sent
void Sci0TrFunc(void)
{
    R_PG_SCI_StopModule_C0();          //Shut down the SCI0
}
```

5.20.4 R_PG_SCI_SendAllData_C<*channel number*>

Definition: bool R_PG_SCI_SendAllData_C<*channel number*> (uint8_t * data, uint16_t count)
<*channel number*>: 0 to 12

Description: Transmit all data

Conditions for output:
- The function of transmission is selected for a SCI channel in GUI.
- Other than "Notify the transmission completion of all data by function call" is selected as the data transmission method in GUI.

Parameter

uint8_t * data	The start address of the data to be sent.
uint16_t count	The number of the data to be sent. Set this to 0 if the transmit data is a character string (ending with a null character).

Return value

true	Setting was made correctly
false	Setting failed

File for output: R_PG_SCI_C<*channel number*>.c
<*channel number*>: 0 to 12

RPDL function: R_SCI_Send

Details:
- This function transmits all data.
- This function is generated when other than "Notify the transmission completion of all data by function call" is selected as the transmission method in GUI. This function waits until the last byte has been sent.
- The count of transmitted characters will loop back to 0 if 65536 characters are sent.

Example

SCI0 has been set as transmitter in the GUI.
"Wait at the transmission function until the last byte has been transmitted" is selected as the transmission method in GUI.

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

uint8_t data[255];

void func(void)
{
    R_PG_Clock_Set();              //The clock-generation circuit has to be set first.
    R_PG_SCI_Set_C0();             //Set up SCI0.
    R_PG_SCI_SendAllData_C0(data, 255);          //Send 255 bytes of binary data.
    R_PG_SCI_StopModule_C0();          //Shut down the SCI0
}
```

5.20.5 R_PG_SCI_I2CMode_Send_C<*channel number*>

Definition

bool R_PG_SCI_I2CMode_Send_C<*channel number*>
(bool addr_10bit, uint16_t slave, uint8_t * data, uint16_t count)
<*channel number*>: 0 to 12

Description

Transmit data by simple I^2C bus interface

Conditions for output

- Simple I^2C bus interface is selected for “Mode”.

Parameter

bool addr_10bit	Slave address format (1: 10bit 0: 7bit)
uint16_t slave	Slave address
uint8_t * data	The start address of the data to be sent
uint16_t count	The number of the data to be sent

Return value

true	When [Wait at the transmission function until all data has been transmitted] was selected for data transmission method, the operation completed OK. When except [Wait at the transmission function until all data has been transmitted] is selected for data transmission method, return value is always “true”.
false	When [Wait at the transmission function until all data has been transmitted] was selected for data transmission method, an error was detected.

File for output

R_PG_SCI_C<*channel number*>.c
<*channel number*>: 0 to 12

RPDL function

R_SCI_IIC_Write

Details

- Transmit data by simple I^2C bus interface.

Example

[SCI0]
Mode: Simple I2C mode
Data transmission method: Notify the transmission completion of all data by function call

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

uint8_t data_tr[] = “ABCDEFGHIJ”;
uint16_t tr_count;

void func(void)
{
    R_PG_Clock_Set();           //The clock-generation circuit has to be set first.
    R_PG_SCI_Set_C0();          //Set up SCI0.

    //Transmit data by simple I2C bus interface
    R_PG_SCI_I2CMode_Send_C0(0, 0x0006, data_tr, 10);
}

void Sci0TrFunc(void)
{
    //Acquire the number of transmitted data
    R_PG_SCI_GetSentDataCount_C0(&tr_count);
}
```

5.20.6 R_PG_SCI_I2CMode_SendWithoutStop_C<*channel number*>

Definition
bool R_PG_SCI_I2CMode_SendWithoutStop_C<*channel number*>
(bool addr_10bit, uint16_t slave, uint8_t * data, uint16_t count)
<*channel number*>: 0 to 12

Description
Transmit data by simple I²C bus interface (no stop condition)

Conditions for output
- Simple I²C bus interface is selected for “Mode”.

Parameter

bool addr_10bit	Slave address format (1: 10bit 0: 7bit)
uint16_t slave	Slave address
uint8_t * data	The start address of the data to be sent
uint16_t count	The number of the data to be sent

Return value

true	When [Wait at the transmission function until all data has been transmitted] was selected for data transmission method, the operation completed OK. When except [Wait at the transmission function until all data has been transmitted] is selected for data transmission method, return value is always “true”.
false	When [Wait at the transmission function until all data has been transmitted] was selected for data transmission method, an error was detected.

File for output
R_PG_SCI_C<*channel number*>.c
<*channel number*>: 0 to 12

RPDL function
R_SCI_IIC_Write

Details
- Transmit data by simple I²C bus interface (no stop condition).

Example
[SCI0]
Mode: Simple I2C mode
Data transmission method: Notify the transmission completion of all data by function call

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

uint8_t data_tr[10];
uint8_t data_re[10];

void func(void)
{
    R_PG_Clock_Set();           //The clock-generation circuit has to be set first.
    R_PG_SCI_Set_C0();          //Set up SCI0.

    //Transmit data by simple I2C bus interface (no stop condition)
    R_PG_SCI_I2CMode_SendWithoutStop_C0(0, 0x0006, data_tr, 10);
}

void Sci0TrFunc(void)
{
    //Receive data by simple I2C bus interface (RE-START condition)
    R_PG_SCI_I2CMode_RestartReceive_C0(0, 0x0006, data_re, 10);
}
```

5.20.7 R_PG_SCI_I2CMode_GenerateStopCondition_C<*channel number*>

Definition: bool R_PG_SCI_I2CMode_GenerateStopCondition_C<*channel number*> (void)
<*channel number*>: 0 to 12

Description: Generate a stop condition

Conditions for output:
- Simple I^{2}C bus interface is selected for “Mode”.

Parameter: None

Return value:

true	Setting was made correctly
false	Setting failed

File for output: R_PG_SCI_C<*channel number*>.c
<*channel number*>: 0 to 12

RPDL function: R_SCI_Control

Details:
- This function generates a stop condition.

Example:

[SCI0]

Mode: Simple I2C mode

Data transmission method: Transfer the transmitted serial data by DMAC

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

uint8_t data_tr[]=”ABCDEFGHIJ”;

void func(void)
{
    R_PG_Clock_Set();           //The clock-generation circuit has to be set first.

    //Set up a DMAC channel
    R_PG_DMAC_Set_C0();

    //Set the source address
    R_PG_DMAC_SetSrcAddress_C0(data_tr);

    //Make the DMAC be ready for the start trigger
    R_PG_DMAC_Activate_C0();

    //Set up a SCI channel
    R_PG_SCI_Set_C0();

    //Transmit data by simple I2C bus interface
    R_PG_SCI_I2CMode_Send_C0(0, 0x0006, data_tr, 10);
}

void Dmac0IntFunc(void)
{
    //Generate a stop condition
    R_PG_SCI_I2CMode_GenerateStopCondition_C0();
}
```

5.20.8 R_PG_SCI_I2CMode_Receive_C<*channel number*>

Definition
bool R_PG_SCI_I2CMode_Receive_C<*channel number*>
(bool addr_10bit, uint16_t slave, uint8_t * data, uint16_t count)
<*channel number*>: 0 to 12

Description
Receive data by simple I2C bus interface

Conditions for output
- Simple I2C bus interface is selected for “Mode”.

Parameter

bool addr_10bit	Slave address format (1: 10bit 0: 7bit)
uint16_t slave	Slave address
uint8_t * data	The start address of the storage area for the expected data.
uint16_t count	The number of the data to be received.

Return value

true	When [Wait at the reception function until all data has been received] was selected for data reception method, the operation completed OK. When except [Wait at the reception function until all data has been received] is selected for data reception method, return value is always “true”.
false	When [Wait at the reception function until all data has been received] was selected for data reception method, an error was detected.

File for output
R_PG_SCI_C<*channel number*>.c
<*channel number*>: 0 to 12

RPDL function
R_SCI_IIC_Read

Details
- This function receives data by simple I2C bus interface.

Example
[SCI0]
Mode: Simple I2C mode
Function selection: Transmission and reception

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

uint8_t data_re[10];

void func(void)
{
    R_PG_Clock_Set();           //The clock-generation circuit has to be set first.
    R_PG_SCI_Set_C0();          //Set up SCI0.
    //Receive data by simple I2C bus interface
    R_PG_SCI_I2CMode_Receive_C0(0, 0x0006, data_re, 10);
}
```

5.20.9 R_PG_SCI_I2CMode_RestartReceive_C<*channel number*>

Definition

bool R_PG_SCI_I2CMode_RestartReceive_C<*channel number*>
(bool addr_10bit, uint16_t slave, uint8_t * data, uint16_t count)
<*channel number*>: 0 to 12

Description

Receive data by simple I^2C bus interface (RE-START condition)

Conditions for output

- Simple I^2C bus interface is selected for “Mode”.

Parameter

bool addr_10bit	Slave address format (1: 10bit 0: 7bit)
uint16_t slave	Slave address
uint8_t * data	The start address of the storage area for the expected data.
uint16_t count	The number of the data to be received.

Return value

true	When [Wait at the reception function until all data has been received] was selected for data reception method, the operation completed OK. When except [Wait at the reception function until all data has been received] is selected for data reception method, return value is always “true”.
false	When [Wait at the reception function until all data has been received] was selected for data reception method, an error was detected.

File for output

R_PG_SCI_C<*channel number*>.c <*channel number*>: 0 to 12

RPDL function

R_SCI_IIC_Read

Details

- This function receives data by simple I^2C bus interface. (RE-START condition)

Example

[SCI0]
Mode: Simple I2C mode

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

uint8_t data_re[10];

void func(void)
{
    R_PG_Clock_Set();           //The clock-generation circuit has to be set first.

    R_PG_SCI_Set_C0();          //Set up SCI0.

    //Transmit data by simple I2C bus interface (no stop condition)
    R_PG_SCI_I2CMode_SendWithoutStop_C0(
        1,                      //10 bit address format
        0x0006,                 //Slave address
        PDL_NO_PTR,             //The start address of the data to be sent
        PDL_NO_DATA             //The number of the data to be sent
    );

    //Receive data by simple I2C bus interface (RE-START condition)
    R_PG_SCI_I2CMode_RestartReceive_C0(
        0,          //7 bit address format
        0x00f0,     //Slave address
        data_re,    //The start address of the storage area for the expected data.
        10          //The number of the data to be received.
```

```
    );
}
```

5.20.10 R_PG_SCI_I2CMode_ReceiveLast_C<*channel number*>

Definition: bool R_PG_SCI_I2CMode_ReceiveLast_C<*channel number*> (uint8_t * data)
<*channel number*>: 0 to 12

Description: Making reception complete in simple I^2C bus interface

Conditions for output:
- Simple I^2C bus interface is selected for "Mode".
- "Transfer the received serial data by DMAC" or "Transfer the received serial data by DTC" is selected for data reception method.

Parameter

uint8_t * data	The start address of the storage area for the expected data.

Return value

true	Setting was made correctly
false	Setting failed

File for output: R_PG_SCI_C<*channel number*>.c
<*channel number*>: 0 to 12

RPDL function: R_SCI_IIC_ReadLastByte

Details:
- After received data is transferred by the DMAC or DTC in simple I^2C mode, this function must be called to complete the reception.
- This function must be called from a DMA interrupt notification function or receive end notification function.

Example:
[SCI0]
Mode: Simple I2C mode
Data reception method: Transfer the received serial data by DMAC
[DMAC0]
Transfer request source: RXI0 (SCI0 receive data full interrupt)
Transfer mode: Normal transfer mode
Length of a single data: 1 byte
Number of times: 4
Start address: 8a005h
Notify DMA interrupt (DMACIn)
[DMAC1]
Transfer request source: TXI0 (SCI0 transmit data empty interrupt)
Transfer mode: Normal transfer mode
Length of a single data: 1 byte
Number of times: 3
Source address update mode: Fixed
Start address: 8a003h

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

uint8_t data_re[5];
uint8_t dummy_data=0xFF;

void func(void)
{
    R_PG_Clock_Set();          //The clock-generation circuit has to be set first.
    R_PG_SCI_Set_C0();         //Set up SCI0.
```

```
    R_PG_DMAC_Set_C0();    //Set up DMAC.
    R_PG_DMAC_Set_C1();    //Set up DMAC.
    R_PG_DMAC_SetDestAddress_C0(data_re);    //Set the destination address.
    R_PG_DMAC_SetSrcAddress_C1(&dummy_data);    //Set the source address.
    R_PG_DMAC_Active_C0();    //Make the DMAC be ready for the start trigger.
    R_PG_DMAC_Active_C1();    //Make the DMAC be ready for the start trigger.

    //Receive data by simple I2C bus interface.
    R_PG_SCI_I2CMode_Receive_C0(0, 0x0006, PDL_NO_PTR, 0);
}

void Dmac0IntFunc(void)
{
    //Making reception complete in simple I2C bus interface.
    R_PG_SCI_I2CMode_ReceiveLast_C0(&data_re[4]);
}
```

5.20.11 R_PG_SCI_I2CMode_GetEvent_C<*channel number*>

Definition: bool R_PG_SCI_I2CMode_GetEvent_C<*channel number*> (bool * nack)
<*channel number*>: 0 to 12

Description: Get the detected event in the simple I^2C mode

Conditions for output: Simple I^2C bus interface is selected for “Mode”.

Parameter

bool * nack	The address of the storage area for a NACK detection flag.

Return value

true	Acquisition succeeded
false	Acquisition failed

File for output: R_PG_SCI_C<*channel number*>.c
<*channel number*>: 0 to 12

RPDL function: R_SCI_GetStatus

Details
- This function acquires ACK Reception Data Flag in the simple I^2C mode.

Example

[SCI0]
Mode:Simple I^2C mode
Data transmission method:Notify the transmission completion of all data by function call

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

uint8_t data_tr[]=”ABCDEFGHIJ”;
bool nack;

void func(void)
{
    R_PG_Clock_Set();           //The clock-generation circuit has to be set first.
    R_PG_SCI_Set_C0();          //Set up SCI0.

    //Transmit data by simple I2C bus interface
    R_PG_SCI_I2CMode_Send_C0(0, 0x0006, data_tr, 10);
}

void Sci0TrFunc(void)
{
    //Get the detected event in the simple I2C mode
    R_PG_SCI_I2CMode_GetEvent_C0(&nack);
}
```

5.20.12 R_PG_SCI_SPIMode_Transfer_C<*channel number*>

Definition

bool R_PG_SCI_SPIMode_Transfer_C<*channel number*>
(uint8_t * tx_start, uint8_t * rx_start, uint16_t count)
<*channel number*>: 0 to 12

Description

Transmit data by simple SPI mode

Conditions for output

- Simple SPI mode is selected for “Mode”.

Parameter

uint8_t * tx_start	The start address of the data to be transmitted.
uint8_t * rx_start	The start address of the storage area for the expected data.
uint16_t count	The number of the data to be transferred.

Return value

true	When [Wait at the transmission/reception function until all data has been transmitted/received] was selected for data transmission/reception method, the operation completed OK. When except [Wait at the transmission/reception function until all data has been transmitted/received] is selected for data transmission/reception method, return value is always “true”.
false	When [Wait at the transmission/reception function until all data has been transmitted/received] was selected for data transmission/reception method, an error was detected.

File for output

R_PG_SCI_C<*channel number*>.c
<*channel number*>: 0 to 12

RPDL function

R_SCI_SPI_Transfer

Details

- This function transmits data by simple SPI mode.

Example

[SCI0]
Mode: Simple SPI mode
Function selection: Transmission and reception

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

uint8_t data_tr[10];
uint8_t data_re[10];

void func1(void)
{
    R_PG_Clock_Set();           //The clock-generation circuit has to be set first.
    R_PG_SCI_Set_C0();          //Set up SCI0.
}

void func2(void)
{
    //Transmit data by simple SPI mode
    R_PG_SCI_SPIMode_Transfer_C0(data_tr, data_re, 10);
}
```

5.20.13 R_PG_SCI_SPIMode_GetErrorFlag_C<*channel number*>

Definition: bool R_PG_SCI_SPIMode_GetErrorFlag_C<*channel number*> (bool * overrun)
<*channel number*>: 0 to 12

Description: Get the serial reception error flag in the simple SPI mode

Conditions for output: Simple SPI mode is selected for “Mode”.

Parameter

bool * overrun	The address of the storage area for the overrun error flag.

Return value

true	Acquisition of the flag succeeded
false	Acquisition of the flag failed

File for output: R_PG_SCI_C<*channel number*>.c
<*channel number*>: 0 to 12

RPDL function: R_SCI_GetStatus

Details
- This function acquires the serial reception error flag in the simple SPI mode.
- Specify 0 for a flag that is not required.
- The flags of detected error will be set to 1.

Example

[SCI0]
Mode: Simple SPI mode
Function selection: Transmission and reception
Notify receive error detection by function call

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

uint8_t tx_data[4];
uint8_t rx_data[4];
bool overrun;

void func(void)
{
    R_PG_Clock_Set();           //The clock-generation circuit has to be set first.
    R_PG_SCI_Set_C0();          //Set up SCI0.

    //Transmit data by simple SPI mode
    R_PG_SCI_SPIMode_Transfer_C0(tx_data, rx_data, 4);
}

void Sci0ErFunc(void)
{
    //Get the serial reception error flag in the simple SPI mode
    R_PG_SCI_SPIMode_GetErrorFlag_C0(&overrun);
}
```

5.20.14 R_PG_SCI_GetSentDataCount_C<*channel number*>

Definition: bool R_PG_SCI_GetSentDataCount_C<*channel number*> (uint16_t * count)
<*channel number*>: 0 to 12

Description: Acquire the number of transmitted data

Conditions for output: The function of transmission is selected for a SCI channel and "Notify the transmission completion of all data by function call" is selected as the data transmission method in GUI.

Parameter

uint16_t * count	The storage location for the number of bytes that have been transmitted in the current transmission.

Return value

true	Acquisition of the data count succeeded
false	Acquisition of the data count failed

File for output: R_PG_SCI_C<*channel number*>.c
<*channel number*>: 0 to 12

RPDL function: R_SCI_GetStatus

Details
- When "Notify the transmission completion of all data by function call" is selected as the data transmission method in GUI, the number of transmitted data can be acquired by calling this function.

Example: SCI0 has been set as transmitter in the GUI.
Sci0TrFunc was specified as the name of the transmit end notification function in the GUI.

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

uint8_t data[255];

void func(void)
{
    R_PG_Clock_Set();           //The clock-generation circuit has to be set first.
    R_PG_SCI_Set_C0();          //Set up SCI0.
    R_PG_SCI_StartSending_C0(data, 255);        //Send 255 bytes of binary data.
}

//The transmit end notification function that called when all bytes have been sent
void Sci0TrFunc(void)
{
    R_PG_SCI_StopModule_C0();   //Shut down the SCI0
}

//The function to check the number of transmitted data and terminate the transmission
void func_terminate_SCI(void)
{
    uint16_t count;

    // Acquire the number of transmitted data
    R_PG_SCI_GetSentDataCount_C0(&count);

    if( count > 32 ){
        R_PG_SCI_StopCommunication_C0();    //Terminate the transmission
    }
}
```

5.20.15 R_PG_SCI_ReceiveStationID_C<*channel number*>

Definition
bool R_PG_SCI_ReceiveStationID_C<*channel number*> (void)
<*channel number*>: 0 to 12

Description
Receives the ID code matches the ID of the receiving station itself

Conditions for output
- The function of reception is selected for a SCI channel
- The multi-processer communications function is enabled in the asynchronous serial communication mode

Parameter
None

Return value

true	Reception succeeded
false	Reception failed

File for output
R_PG_SCI_C<*channel number*>.c
<*channel number*>: 0 to 12

RPDL function
R_SCI_Receive

Details
- This function waits until the ID code matches the ID of the receiving station itself has been received.

Example
A case where the setting is made as follows.
- The function of reception is selected for a SCI0 channel
- The multi-processer communications function is enabled in the asynchronous serial communication mode
- "Notify the reception completion of all data by function call" is selected as the data reception method

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

uint8_t data[10];

void func(void)
{
    R_PG_Clock_Set();              //The clock-generation circuit has to be set first.
    R_PG_SCI_Set_C0();             //Set up SCI0
    R_PG_SCI_ReceiveStationID_C0();            //Wait an ID reception
    R_PG_SCI_ReceiveAllData_C0( data, 10 );            //Start receiving
}
```

5.20.16 R_PG_SCI_StartReceiving_C<*channel number*>

Definition: bool R_PG_SCI_StartReceiving_C<*channel number*> (uint8_t * data, uint16_t count)
<*channel number*>: 0 to 12

Description: Start the data reception

Conditions for output:
- The function of reception is selected for a SCI channel in GUI
- "Notify the reception completion of all data by function call" is selected as the data reception method in GUI

Parameter

uint8_t * data	The start address of the storage area for the expected data.
uint16_t count	The number of the data to be received.

Return value

true	Setting was made correctly
false	Setting failed

File for output: R_PG_SCI_C<*channel number*>.c <*channel number*>: 0 to 12

RPDL function: R_SCI_Receive

Details:
- This function starts the data reception.
- This function is generated when "Notify the reception completion of all data by function call" is selected as the data reception method in GUI. This function returns immediately and the notification function having the specified name will be called when the last byte has been received. Create the notification function as follows:
  void <*name of the notification function*> (void)
  For the notification function, note the contents of this chapter end, Notes on Notification Functions.
- The number of received data can be acquired by R_PG_SCI_GetReceivedDataCount_C<*channel number*>. The reception can be terminated by calling R_PG_SCI_StopReceiving_C<*channel number*> before all bytes have been received.
- The maximum number of characters to be received is 65535.

Example
- SCI0 has been set as receiver in the GUI.
- Sci0ReFunc was specified as the name of the receive end notification function in the GUI.

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

uint8_t data[255];

void func(void)
{
    R_PG_Clock_Set();              //The clock-generation circuit has to be set first.
    R_PG_SCI_Set_C0();             //Set up SCI0.
    R_PG_SCI_StartReceiving_C0(data, 255);      //Receive 255 bytes of binary data.
}

//Receive end notification function that called when all bytes have been received
void Sci0ReFunc(void)
{
    R_PG_SCI_StopModule_C0();    //Shut down the SCI0
}
```

5.20.17 R_PG_SCI_ReceiveAllData_C<*channel number*>

Definition

bool R_PG_SCI_ReceiveAllData_C<*channel number*> (uint8_t * data, uint16_t count)

<*channel number*>: 0 to 12

Description

Receive all data

Conditions for output

- The function of reception is selected for a SCI channel in GUI.
- Other than "Notify the reception completion of all data by function call" is selected as the data reception method in GUI

Parameter

uint8_t * data	The start address of the storage area for the expected data.
uint16_t count	The number of the data to be received.

Return value

true	Setting was made correctly
false	Setting failed

File for output

R_PG_SCI_C<*channel number*>.c

<*channel number*>: 0 to 12

RPDL function

R_SCI_Receive

Details

- This function receives all data.
- This function is generated when other than "Notify the reception completion of all data by function call" is selected as the data reception method in GUI. This function waits until the last byte has been received.
- The maximum number of characters to be received is 65535.

Example

SCI0 has been set as receiver in the GUI.

"Wait at the reception function until all data has been transmitted" is selected as the reception method in GUI.

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

uint8_t data[255];

void func(void)
{
    R_PG_Clock_Set();              //The clock-generation circuit has to be set first.
    R_PG_SCI_Set_C0();             //Set up SCI0.
    R_PG_SCI_ReceiveAllData_C0(data, 255);          //Receive 255 bytes of binary
data.
    R_PG_SCI_StopModule_C0();          //Shut down the SCI0
}
```

5.20.18 R_PG_SCI_ControlClockOutput_C<*channel number*>

Definition: bool R_PG_SCI_ControlClockOutput_C<*channel number*> (bool output_enable)
<*channel number*>: 0 to 12

Description: Control the output from the SCKn pin (n: 0 to 12)

Conditions for output:
- "Smart card interface mode" is selected for mode.
- "Enable (GSM mode)" is selected for GSM mode.
- "Output fixed high" or "Output fixed low" is selected for SCKn pin function.

Parameter

bool output_enable	Output from the SCKn pin (1: Clock output, 0: Output fixed)

Return value

true	Setting was made correctly
false	Setting failed

File for output: R_PG_SCI_C<*channel number*>.c
<*channel number*>: 0 to 12

RPDL function: R_SCI_Control

Details:
- This function controls the clock output from the SCKn pin.

Example:

[SCI0]

Mode: Smart card interface mode

GSM mode: Enable

SCKn pin function: Output fixed high

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

void func(void)
{
    R_PG_Clock_Set();           //The clock-generation circuit has to be set first.
    R_PG_SCI_Set_C0();          //Set up SCI0.
    //Control the output from the SCKn pin
    R_PG_SCI_ControlClockOutput_C0( 1 );
}
```

5.20.19 R_PG_SCI_StopCommunication_C<*channel number*>

Definition R_PG_SCI_StopCommunication_C<*channel number*> (void)

<*channel number*>: 0 to 12

Description Stop transmission and reception of serial data

Parameter None

Return value

true	Setting was made correctly
false	Setting failed

File for output R_PG_SCI_C<*channel number*>.c

<*channel number*>: 0 to 12

RPDL function R_SCI_Control

Details

- This function stops data transmission and reception.
- When "Notify the transmission completion of all data by function call" is selected as the data transmission method in GUI, the reception can be terminated by calling this function before the number of bytes specified at R_PG_SCI_StartSending_C<*channel number*> have been received.
- When "Notify the reception completion of all data by function call" is selected as the data reception method in GUI, the reception can be terminated by calling this function before the number of bytes specified at R_PG_SCI_StartReceiving_C<*channel number*> have been received.

Example Refer to the example of R_PG_SCI_GetSentDataCount_C<*channel number*>

5.20.20 R_PG_SCI_GetReceivedDataCount_C<*channel number*>

Definition: bool R_PG_SCI_GetReceivedDataCount_C<*channel number*> (uint16_t * count)
<*channel number*>: 0 to 12

Description: Acquire the number of received data

Conditions for output: The function of reception is selected for a SCI channel and "Notify the reception completion of all data by function call" is selected as the data reception method in GUI.

Parameter

uint16_t * count	The storage location for the number of bytes that have been received in the current reception process.

Return value

true	Acquisition of the data count succeeded
false	Acquisition of the data count failed

File for output: R_PG_SCI_C<*channel number*>.c
<*channel number*>: 0 to 12

RPDL function: R_SCI_GetStatus

Details

- When " Notify the reception completion of all data by function call " is selected as the receive end notification in GUI, the number of received data can be acquired by calling this function.

Example: SCI0 has been set as receiver in the GUI.
Sci0ReFunc was specified as the name of the receive end notification function in the GUI.

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

uint8_t data[255];

void func(void)
{
    R_PG_Clock_Set();              //The clock-generation circuit has to be set first.
    R_PG_SCI_Set_C0();             //Set up SCI0.
    R_PG_SCI_StartReceiving_C0(data, 255);          //Receive 255 bytes of binary data.
}

//The receive end notification function that called when all bytes have been received.
void Sci0ReFunc(void)
{
    R_PG_SCI_StopModule_C0();    //Shut down the SCI0
}

//The function to check the number of received data and terminate the reception
void func_terminate_SCI(void)
{
    uint16_t count;

    //Acquire the number of received data
    R_PG_SCI_GetReceivedDataCount_C0(&count);

    if( count > 32 ){
        R_PG_SCI_StopCommunication_C0();       //Terminate the reception
    }
}
```

5.20.21 R_PG_SCI_GetReceptionErrorFlag_C<*channel number*>

Definition
bool R_PG_SCI_GetReceptionErrorFlag_C<*channel number*>
(bool * parity, bool * framing, bool * overrun)
<*channel number*>: 0 to 12

Description
Get the serial reception error flag

Conditions for output
The function of reception is selected for a SCI channel

Parameter

bool * parity	The address of storage area for the parity error flag
bool * framing	The address of storage area for the framing error flag
bool * overrun	The address of storage area for the overrun error flag

Return value

true	Acquisition of the flags succeeded
false	Acquisition of the flags failed

File for output
R_PG_SCI_C<*channel number*>.c
<*channel number*>: 0 to 12

RPDL function
R_SCI_GetStatus

Details
- This function acquires the reception error flags.
- Specify the address of storage area for the flags to be acquired.
- Specify 0 for a flag that is not required.
- The flags of detected error will be set to 1.

Example
SCI0 has been set as receiver in the GUI.
Sci0ReFunc was specified as the name of the receive end notification function in the GUI.

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

uint8_t data[255];

void func(void)
{
    R_PG_Clock_Set();              //The clock-generation circuit has to be set first.
    R_PG_SCI_Set_C0();             //Set up SCI0.
    R_PG_SCI_StartReceiving_C0(data, 1);          //Receive 1bytes of binary data.
}

//The receive end notification function that called when all bytes have been received.
void Sci0ReFunc(void)
{
    // Acquire the reception error flags
    R_PG_SCI_GetReceptionErrorFlag_C0( &parity, &framing, & overrun );
}
```

5.20.22 R_PG_SCI_ClearReceptionErrorFlag_C<*channel number*>

Definition: bool R_PG_SCI_ClearReceptionErrorFlag_C<*channel number*> (void)
<*channel number*>: 0 to 12

Description: Clear the serial reception error flag

Conditions for output:
- "Asynchronous mode", "Clock synchronous mode" or "Smart card interface mode" is selected for mode.
- "Reception" or "Transmission and reception" is selected for function selection.

Parameter: None

Return value:

true	Setting was made correctly
false	Setting failed

File for output: R_PG_SCI_C<*channel number*>.c
<*channel number*>: 0 to 12

RPDL function: R_SCI_Control

Details:
- This function clears the serial reception error flag.

Example:
Mode: Asynchronous mode
Function selection: Reception
Data reception method: Notify the reception completion of all data by function call

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

uint8_t data_re[10];
bool parity, framing, overrun;

void func(void)
{
    R_PG_Clock_Set();           //The clock-generation circuit has to be set first.

    R_PG_SCI_Set_C0();          //Set up SCI0.

    //Start the data reception
    R_PG_SCI_StartReceiving_C0(data_re, 10);
}

void Sci0ReFunc(void)
{
    //Acquire the reception error flags
    R_PG_SCI_GetReceptionErrorFlag_C0(&parity, &framing, &overrun);

    //Clear the serial reception error flag
    R_PG_SCI_ClearReceptionErrorFlag_C0();
}
```

5.20.23 R_PG_SCI_GetTransmitStatus_C<*channel number*>

Definition: bool R_PG_SCI_GetTransmitStatus_C<*channel number*> (bool * complete)
<*channel number*>: 0 to 12

Description: Get the state of transmission

Conditions for output: The function of transmission is selected for a SCI channel

Parameter

bool * complete	The address of storage area for the transmission completion flag (0: Being transmitted 1:Complete)

Return value

true	Acquisition of the transmission status succeeded
false	Acquisition of the transmission status failed

File for output: R_PG_SCI_C<*channel number*>.c
<*channel number*>: 0 to 12

RPDL function: R_SCI_GetStatus

Details
- This function acquires the state of transmission.

Example

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

bool complete;

void func(void)
{
    //Get the state of transmission
    R_PG_SCI_GetTransmitStatus_C0( &complete );
}
```

5.20.24 R_PG_SCI_StopModule_C<*channel number*>

Definition bool R_PG_SCI_StopModule_C<*channel number*> (void)

<*channel number*>: 0 to 12

Description Shut down a SCI channel

Parameter None

Return value

true	Shutting down succeeded
false	Shutting down failed

File for output R_PG_SCI_C<*channel number*>.c

<*channel number*>: 0 to 12

RPDL function R_SCI_Destroy

Details

- Stops a SCI channel and places it in the module-stop state.

Example

A case where the setting is made as follows.

- SCI0 has been set as receptor in the GUI.
- "Wait at the reception function until all data has been received" is selected as the data reception method instead of specifying the receive end notification function name in GUI.

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

uint8_t data[255];

void func(void)
{
    R_PG_Clock_Set();              //The clock-generation circuit has to be set first.
    R_PG_SCI_Set_C0();             //Set up SCI0.
    R_PG_SCI_ReceiveAllData_C0(data, 255);    //Receive 255 bytes of binary data.
    R_PG_SCI_StopModule_C0();      //Shut down the SCI0
}
```

5.21 I²C Bus Interface (RIIC)

5.21.1 R_PG_I2C_Set_C<*channel number*>

Definition: bool R_PG_I2C_Set_C<*channel number*> (void)

<*channel number*>: 0 to 3

Description: Set up a I²C bus interface channel

Parameter: None

Return value:

true	Setting was made correctly.
false	Setting failed.

File for output: R_PG_I2C_C<*channel number*>.c

<*channel number*>: 0 to 3

RPDL function: R_IIC_Set, R_IIC_Create

Details:

- Releases an I²C bus interface channel from the module-stop state, makes initial settings.
- Function R_PG_Clock_Set must be called before any use of this function.

Example: RIIC0 has been set in the GUI.

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

void func(void)
{
    R_PG_Clock_Set();          //The clock-generation circuit has to be set first
    R_PG_I2C_Set_C0();         //Set up RIIC0
}
```

5.21.2 R_PG_I2C_MasterReceive_C<*channel number*>

Definition
bool R_PG_I2C_MasterReceive_C<*channel number*>
(bool addr_10bit, uint16_t slave, uint8_t* data, uint16_t count)
<*channel number*>: 0 to 3

Description
Master data reception

Conditions for output
The function of master is selected for an I^2C bus interface channel in GUI.

Parameter

bool addr_10bit	Slave address format (1: 10bit 0: 7bit)
uint16_t slave	Target slave address
uint8_t* data	The start address of the storage area for the expected data.
uint16_t count	The number of the data to be received.

Return value

true	Setting was made correctly.
false	Setting failed.

File for output
R_PG_I2C_C<*channel number*>.c <*channel number*>: 0 to 3

RPDL function
R_IIC_MasterReceive

Details
- This function reads data from slave module. The stop condition is generated when the specified number of data has been received and reception completes.
- If "Wait at the reception function until all data has been transmitted" is selected as the master reception method in GUI, this function waits until the last byte has been received.
- If "Notify the reception completion of all data by function call" is selected as the master reception method in GUI, this function returns immediately and the notification function having the specified name will be called when the last byte has been receive.
  Create the notification function as follows:
  void <*name of the notification function*> (void)
  For the notification function, note the contents of this chapter end, Notes on Notification Functions.
- A Start condition will be generated automatically. If the previous transfer did not issue a stop condition, a repeated start condition will be generated.
- In the 7-bit address mode, [7:1] of specified slave address value will be output. In 10-bit address mode, [10:1] of specified slave address will be output.
- The number of received data can be aquired by R_PG_I2C_GetReceivedDataCount_C <*channel number*>.
- When using 10-bit address mode, select other than [Notify the reception completion of all data by function call] for master reception method in the GUI.

Example
A case where the setting is made as follows.
- The function of master is selected for a RIIC0
- "Wait at the reception function until all data has been transmitted" is selected as the master reception method

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

uint8_t iic_data[10];    //The storage area for the received data

void func(void)
{
       R_PG_Clock_Set();    //The clock-generation circuit has to be set first
       R_PG_I2C_Set_C0();    //Set up RIIC0
      R_PG_I2C_MasterReceive_C0(    //Master reception
            0,      //Slave address format
            6,      //Slave address
            iic_data,      // The start address of the storage area for the received data
            10      // The number of the data to be received
      );

      R_PG_I2C_StopModule_C0();    //Stop RIIC0
}
```

5.21.3 R_PG_I2C_MasterReceiveLast_C<*channel number*>

Definition
bool R_PG_I2C_MasterReceiveLast_C< *channel number* >
(uint8_t* data)
< *channel number* >: 0 to 3

Description
Complete a master reception process

Conditions for output
- The function of master is selected for an I^2C bus interface channel in GUI.
- Select DMAC or DTC transfer as a master reception method

Parameter

uint8_t* data	The address of the storage area for the expected data.

Return value

true	Setting was made correctly.
false	Setting failed.

File for output
R_PG_I2C_C<*channel number*>.c
<*channel number*>: 0 to 3

RPDL function
R_IIC_MasterReceiveLast

Details
- This function is genetarted when [Transfer the received serial data by DMAC] or [Transfer the received serial data by DTC] is selected as a master reception method.
- In the master reception process that has used the DMAC or DTC transfer, NACK and
- stop condition will be issued by calling this function and the reception process will be terminated.
- To complete reception process when the DMAC or DTC transfer completes, call this function from DMAC or DTC interrupt notification function.
- Extra 1 byte is acquired from the receive data register in this function.
- The events that has been detected during the reception process or the received data count can be acquired by calling R_PG_I2C_GetEvent_Cn or R_PG_I2C_GetReceivedDataCount_Cn.

Example
A case where the setting is made as follows.
- "Transfer the received serial data by DMAC" is selected as the master reception method in RIIC0 setting.
- DMAC0 is set as follows

  Transfer request source : ICRXI0(receive data full interrupt of TIIC0)
  Transfer system : Single-operand transfer
  Unit data size : 1 byte
  Single operand data count : 1
  Total transfer data size : Number of dtat to be received by RIIC0
  Source start address : Address of RIIC0 received data register
  Destination start address : Destination address of the data transfer
  DMA interrupt notification fuction name : Dmac0IntFunc

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

void Dmac0IntFunc(){
    uint8_t data; //Strage area of extra data

    //Isse NACK and STOP condition and complete the reception
    R_PG_I2C_MasterReceiveLast_C0( &data );
```

```
}

void func(void)
{
      //The clock-generation circuit has to be set first
      R_PG_Clock_Set();

      //Set up RIIC0
      R_PG_I2C_Set_C0();

    //Set up the DMAC0
    R_PG_DMAC_Set_C0();

    //Activate the DMAC0
    R_PG_DMAC_Activate_C0();

      //Master reception
    R_PG_I2C_MasterReceive_C0(
          0,      //Slave address format
          6,      //Slave address
          PDL_NO_PTR, // For DMAC transfer, set PDL_NO_PTR
          10      // The number of the data (For DMAC transfer, set 0)
    );
}
```

5.21.4 R_PG_I2C_MasterSend_C<*channel number*>

Definition
bool R_PG_I2C_MasterSend_C<*channel number*>
(bool addr_10bit, uint16_t slave, uint8_t* data, uint16_t count)
<*channel number*>: 0 to 3

Description
Master data transmission

Conditions for output
The function of master is selected for an I^2C bus interface channel in GUI.

Parameter

bool addr_10bit	Slave address format (1: 10bit 0: 7bit)
uint16_t slave	Target slave address
uint8_t* data	The start address of the data to be sent
uint16_t count	The number of the data to be sent

Return value

true	Setting was made correctly.
false	Setting failed.

File for output
R_PG_I2C_C<*channel number*>.c <*channel number*>: 0 to 3

RPDL function
R_IIC_MasterSend

Details
- This function sends data to the slave module. The stop condition is generated when the specified number of data has been transmitted and transmission completes.
- If "Wait at the transmission function until all data has been transmitted" is selected as the data transmission method in GUI, this function waits until the last byte has been transmitted or other events are detected.
- If "Notify the transmission completion of all data by function call" is selected as the data transmission method in GUI, this function returns immediately and the notification function having the specified name will be called when the last byte has been transmitted. Create the notification function as follows:
  void <*name of the notification function*> (void)
  For the notification function, note the contents of this chapter end, Notes on Notification Functions.
- A Start condition will be generated automatically. If the previous transfer did not issue a stop condition, a repeated start condition will be generated.
- In the 7-bit address mode, [7:1] of specified slave address value will be output. In 10-bit address mode, [10:1] of specified slave address will be output.
- The number of transmitted data can be aquired by R_PG_I2C_GetSentDataCount_C <*channel number*>.
- When using 10-bit address mode, select other than [Notify the transmission completion of all data by function call] for master transmission method in the GUI.

Example
A case where the setting is made as follows.
- The function of master is selected for a RIIC0
- "Wait at the transmission function until all data has been transmitted" is selected as the data transmission method

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

// The storage area for the data to be transmitted
uint8_t iic_data[10];

void func(void)
{
      //The clock-generation circuit has to be set first
      R_PG_Clock_Set();

      //Set up RIIC0
      R_PG_I2C_Set_C0();

      //Master transmission
     R_PG_I2C_MasterSend_C0(
          0,      //Slave address format
          6,      //Slave address
          iic_data,      // The start address of the storage area for the data to be transmitted
          10      // The number of the data to be transmitted
     );

      //Stop RIIC0
     R_PG_I2C_StopModule_C0();
}
```

5.21.5 R_PG_I2C_MasterSendWithoutStop_C<*channel number*>

Definition
bool R_PG_I2C_MasterSendWithoutStop_C<*channel number*>
(bool addr_10bit, uint16_t slave, uint8_t* data, uint16_t count)
<*channel number*>: 0 to 3

Description
Master data transmission (No stop condition)

Conditions for output
The function of master is selected for an I^2C bus interface channel in GUI.

Parameter

bool addr_10bit	Slave address format (1: 10bit 0: 7bit)
uint16_t slave	Target slave address
uint8_t* data	The start address of the data to be sent
uint16_t count	The number of the data to be sent

Return value

true	Setting was made correctly.
false	Setting failed.

File for output
R_PG_I2C_C<*channel number*>.c
<*channel number*>: 0 to 3

RPDL function
R_IIC_MasterSend

Details
- This function sends data to the slave module. The stop condition will not be generated. To generate a stop condition, call R_PG_I2C_GenerateStopCondition_C<*channel number*>.
- If "Wait at the transmission function until all data has been transmitted" is selected as the data transmission method in GUI, this function waits until the last byte has been transmitted or other events are detected.
- If "Notify the transmission completion of all data by function call" is selected as the data transmission method in GUI, this function returns immediately and the notification function having the specified name will be called when the last byte has been transmitted. Create the notification function as follows:
  void <*name of the notification function*> (void)
- For the notification function, note the contents of this chapter end, Notes on Notification Functions.
- A Start condition will be generated automatically. If the previous transfer did not issue a stop condition, a repeated start condition will be generated.
- In the 7-bit address mode, [7:1] of specified slave address value will be output. In 10-bit address mode, [10:1] of specified slave address will be output.
  The number of transmitted data can be aquired by R_PG_I2C_GetSentDataCount_C<*channel number*>.
- When using 10-bit address mode, select other than [Notify the transmission completion of all data by function call] for master transmission method in the GUI.

Example
A case where the setting is made as follows.
- The function of master is selected for a RIIC0
- "Notify the transmission completion of all data by function call" is selected as the data transmission method
- IIC0MasterTrFunc was specified as the name of the transmit end notification function

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

// The storage area for the data to be transmitted
uint8_t iic_data[10];

void func(void)
{
	//The clock-generation circuit has to be set first
	R_PG_Clock_Set();

	//Set up RIIC0
	R_PG_I2C_Set_C0();

	//Master transmission
	R_PG_I2C_MasterSendWithoutStop_C0(
		0,	//Slave address format
		6,	//Slave address
		iic_data,	// The start address of the storage area for the data to be transmitted
		10	// The number of the data to be transmitted
	);
}

void IIC0MasterTrFunc(void){

	//Generate stop condition
	R_PG_I2C_GenerateStopCondition_C0();

	//Stop RIIC0
	R_PG_I2C_StopModule_C0();
}
```

5.21.6 R_PG_I2C_GenerateStopCondition_C<*channel number*>

Definition: bool R_PG_I2C_GenerateStopCondition_C<*channel number*> (void)
<*channel number*>: 0 to 3

Description: Generate a stop condition

Conditions for output: The function of master is selected for an I^2C bus interface channel in GUI.

Parameter: None

Return value:

true	Setting was made correctly.
false	Setting failed.

File for output: R_PG_I2C_C<*channel number*>.c
<*channel number*>: 0 to 3

RPDL function: R_IIC_Control

Details:

- This function generates a stop condition for the transmission started by R_PG_I2C_MasterSendWithoutStop_C<*channel number*>.

Example: RIIC0 has been set in the GUI.

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

// The storage area for the data to be transmitted
uint8_t iic_data[10];

void func(void)
{
      //The clock-generation circuit has to be set first
      R_PG_Clock_Set();

      //Set up RIIC0
      R_PG_I2C_Set_C0();

      //Master transmission
     R_PG_I2C_MasterSendWithoutStop_C0(
          0,     //Slave address format
          6,     //Slave address
          iic_data,     // The start address of the storage area for the data to be transmitted
          10     // The number of the data to be transmitted
     );
}

void IIC0MasterTrFunc(void)
{
      //Generate stop condition
      R_PG_I2C_GenerateStopCondition_C0();

      //Stop RIIC0
     R_PG_I2C_StopModule_C0();
}
```

5.21.7 R_PG_I2C_GetBusState_C<*channel number*>

Definition: bool R_PG_I2C_GetBusState_C<*channel number*> (bool *busy)

<*channel number*>: 0 to 3

Description: Get the bus state

Conditions for output: The function of master is selected for an I^2C bus interface channel in GUI.

Parameter

bool *busy	The address of storage area for the bus busy detection flag

Return value

true	Acquisition of the flag succeeded
false	Acquisition of the flag failed

File for output: R_PG_I2C_C<*channel number*>.c

<*channel number*>: 0 to 3

RPDL function: R_IIC_GetStatus

Details

- This function acquires the bus busy detection flag.

  Bus busy detection flag

0	The I^2C bus is released (bus free state)
1	The I^2C bus is occupied (bus busy state or in the bus free state)

Example: RIIC0 has been set in the GUI.

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"
// The storage area for the data to be transmitted
uint8_t iic_data[10];
//Storage for bus busy detection flag
uint8_t busy;
void func(void)
{
    //The clock-generation circuit has to be set first
    R_PG_Clock_Set();
    //Set up RIIC0
    R_PG_I2C_Set_C0();
    // Wait for the I2C bus to be free
    do{
        R_PG_I2C_GetBusState_C0( & busy );
    } while( busy );
    //Master transmission
    R_PG_I2C_MasterSend_C0(
        0,      //Slave address format
        6,      //Slave address
        iic_data,       // The start address of the storage area for the data to be transmitted
        10      // The number of the data to be transmitted
    );
```

5.21.8 R_PG_I2C_SlaveMonitor_C<*channel number*>

Definition: bool R_PG_I2C_SlaveMonitor_C<*channel number*> (uint8_t *data, uint16_t count)
<*channel number*>: 0 to 3

Description: Slave bus monitor

Conditions for output: The function of slave is selected for an I^2C bus interface channel in GUI.

Parameter

uint8_t* data	The start address of the received data
uint16_t count	The number of the data to be received

Return value

true	Setting was made correctly.
false	Setting failed.

File for output: R_PG_I2C_C<*channel number*>.c
<*channel number*>: 0 to 3

RPDL function: R_IIC_SlaveMonitor

Details

- This function monitors the accesses from master modules.
- If "Notify the reception completion of all data, slave read request, or a stop condition detection by function call" is selected as the slave monitor method in GUI, this function returns immediately and the notification function having the specified name will be called when a read access from master module or a stop condition is detected. Create the notification function as follows:
  void <*name of the notification function*> (void)
  For the notification function, note the contents of this chapter end, Notes on Notification Functions.
- If "Wait at the monitor function until reception completion, slave read request, or a stop condition detection" is selected as the slave monitor method in GUI, this function waits until a read access from master module or a stop condition is detected.
- The received data from a master module is stored in the storage area of specified address. Specify the number of data to not exceed the size of storage area. If the number of the data from the master module exceeds the specified number, NACK shall be generated.
- The transmit/receive mode can be aquired by calling R_PG_I2C_GetRW_C<*channel number*>. The data can be transmitted by calling R_PG_I2C_SlaveSend_C<*channel number*> to respond to a transmission (read) request from the master.
- Call R_PG_I2C_GetDetectedAddress_C<*channel number*> to acquire a detected slave address. Call R_PG_I2C_GetEvent_C<*channel number*> to acquire the detected events (e.g. a stop condition or a start condition).
- When using 10-bit address mode, select other than [Notify the transmission completion of all data, slave read request, or a stop condition detection by function call] for slave monitor method in the GUI.

Example: A case where the setting is made as follows.

- The function of slave is selected for a RIIC0
- IIC0SlaveFunc was specified as the name of the slave monitor function

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"
// The storage area for the data to be received
uint8_t iic_data_re[10];
// The storage area for the data to be transmitted (slave address 0)
uint8_t iic_data_tr_0[10];
// The storage area for the data to be transmitted (slave address 1)
uint8_t iic_data_tr_1[10];
//Storage for bus busy detection flag
uint8_t busy;
void func(void)
{
    //The clock-generation circuit has to be set first
    R_PG_Clock_Set();
    //Set up RIIC0
    R_PG_I2C_Set_C0();
    // Slave monitor
    R_PG_I2C_SlaveMonitor_C0(
        iic_data_re,    // The start address of the storage area for the received data
        10      //The number of the data to be received
    );
}
void IIC0SlaveFunc (void)
{
    bool transmit, start, stop;
    bool addr0, addr1;
    //Get the detected events
    R_PG_I2C_GetEvent_C0(0, &stop, &start, 0, 0);
    //Get an access type
    R_PG_I2C_GetTR_C0(&transmit);
    //Get a detected address
    R_PG_I2C_GetDetectedAddress_C0(&addr0, &addr1, 0, 0, 0, 0);
    if (start && transmit && address0) {
        R_PG_I2C_SlaveSend_C(
            iic_data_tr_0,
            10
        );
    }
    else if (start && read && address1) {
        R_PG_I2C_SlaveSend_C(
            iic_data_tr_1,
            10
        );
    }
}
```

5.21.9 R_PG_I2C_SlaveSend_C<*channel number*>

Definition: bool R_PG_I2C_SlaveSend_C<*channel number*> (uint8_t *data, uint16_t count)
<*channel number*>: 0 to 3

Description: Slave data transmission

Conditions for output: The function of slave is selected for an I^2C bus interface channel in GUI.

Parameter

uint8_t* data	The start address of the data to be transmitted
uint16_t count	The number of the data to be transmitted

Return value

true	Setting was made correctly.
false	Setting failed.

File for output: R_PG_I2C_C<*channel number*>.c
<*channel number*>: 0 to 3

RPDL function: R_IIC_SlaveSend

Details
- This function transmits the data to the master module.
- If the master requires more data than is supplied, this function shall loop back to the start of the data.

Example: Refer to the example of R_PG_I2C_SlaveMonitor_C<*channel number*>

5.21.10 R_PG_I2C_GetDetectedAddress_C<*channel number*>

Definition: bool R_PG_I2C_GetDetectedAddress_C<*channel number*>
(bool *addr0, bool *addr1, bool *addr2, bool *general, bool *device, bool *host)
<*channel number*>: 0 to 3

Description: Get the detected address

Conditions for output: The function of slave is selected for an I^{2}C bus interface channel in GUI.

Parameter

bool *addr0	The address of storage area for slave address 0 detection flag
bool *addr1	The address of storage area for slave address1 detection flag
bool *addr2	The address of storage area for slave address 2 detection flag
bool *general	The address of storage area for general call address detection flag
bool *device	The address of storage area for device-ID command detection flag
bool *host	The address of storage area for host address detection flag

Return value

true	Acquisition succeeded
false	Acquisition failed

File for output: R_PG_I2C_C<*channel number*>.c
<*channel number*>: 0 to 3

RPDL function: R_IIC_GetStatus

Details
- This function acquires the detected address.
- Specify the address of storage area for the flags to be acquired.
- Specify 0 for a flag that is not required.
- 1 is set to detected address

Example: Refer to the example of R_PG_I2C_SlaveMonitor_C<*channel number*>

5.21.11 R_PG_I2C_GetTR_C<*channel number*>

Definition: bool R_PG_I2C_GetTR_PG_C<*channel number*> (bool * transmit)
<*channel number*>: 0 to 3

Description: Get the transmit/receive mode

Conditions for output: The function of slave is selected for an I^2C bus interface channel in GUI.

Parameter

bool * transmit	The address of storage area for the transmit mode flag

Return value

true	Acquisition succeeded
false	Acquisition failed

File for output: R_PG_I2C_C<*channel number*>.c
<*channel number*>: 0 to 3

RPDL function: R_IIC_GetStatus

Details
- This function acquires the detected address.
- Specify the address of storage area for the flags to be acquired.
- Specify 0 for a flag that is not required.
- 1 is set to detected address.
- This function acquires the the transmit/receive mode.

Transmit mode flag

0	Receive mode
1	Transmit mode

Example: Refer to the example of R_PG_I2C_SlaveMonitor_C<*channel number*>

5.21.12 R_PG_I2C_GetEvent_C<*channel number*>

Definition

bool R_PG_I2C_GetEvent_C<*channel number*>
(bool *nack, bool *stop, bool *start, bool *lost, bool *timeout)
<*channel number*>: 0 to 3

Description

Get the detected event

Parameter

bool *nack	The address of storage area for a NACK detection flag
bool *stop	The address of storage area for a stop condition detection flag
bool *start	The address of storage area for a start condition detection flag
bool *lost	The address of storage area for an arbitration lost
bool *timeout	The address of storage area for a timeout detection

Return value

true	Acquisition succeeded
false	Acquisition failed

File for output

R_PG_I2C_C<*channel number*>.c
<*channel number*>: 0 to 3

RPDL function

R_IIC_GetStatus

Details

- This function acquires the detected event.
- Specify 0 for a flag that is not required.
- 1 is set to detected event.

Example

Refer to the example of R_PG_I2C_SlaveMonitor_C<*channel number*>

5.21.13 R_PG_I2C_GetReceivedDataCount_C<*channel number*>

Definition bool R_PG_I2C_GetReceivedDataCount_C<*channel number*> (uint16_t *count)
<*channel number*>: 0 to 3

Description Acquires the count of received data

Parameter

uint16_t *count	The address of storage area for the number of bytes that have been received

Return value

true	Acquisition of the data count succeeded
false	Acquisition of the data count failed

File for output R_PG_I2C_C<*channel number*>.c
<*channel number*>: 0 to 3

RPDL function R_IIC_GetStatus

Details
- This function acquires the number of bytes that have been received in the current reception process.

Example A case where the setting is made as follows.
- The function of master is selected for a RIIC0
- "Notify the reception completion of all data by function call" is selected as the master reception method

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"
// The storage area for the data to be received
uint8_t iic_data[256];
// The storage area for the number of received data
uint16_t count;
void func(void)
{
    //The clock-generation circuit has to be set first
    R_PG_Clock_Set();
    //Set up RIIC0
    R_PG_I2C_Set_C0();
    //Master receive
    R_PG_I2C_MasterReceive_C0(
        0,      //Slave address format
        6,      //Slave address
        iic_data,       // The address of storage area for the data to be received
        256     //The number of data to be received
    );
    //Wait until 64 bytes have been received
    do{
        R_PG_I2C_GetReceivedDataCount_C0( &count );
    } while( count < 64 );
}
```

5.21.14 R_PG_I2C_GetSentDataCount_C<*channel number*>

Definition bool R_PG_I2C_GetSentDataCount_C<*channel number*> (uint16_t *count)

<*channel number*>: 0 to 3

Description Acquires the count of transmitted data

Parameter

uint16_t *count	The address of storage area for the number of bytes that have been transmitted

Return value

true	Acquisition of the data count succeeded
false	Acquisition of the data count failed

File for output R_PG_I2C_C<*channel number*>.c

<*channel number*>: 0 to 3

RPDL function R_IIC_GetStatus

Details

- This function acquires the number of data written in I^2C Bus Transmit Data Register (ICDRT).
- 0 is acquired when the number of transmission specified to the transmitting function is completed.

Example A case where the setting is made as follows.

- The function of master is selected for a RIIC0
- "Notify the transmission completion of all data by function call" is selected as the data transmission method

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"
// The storage area for the data to be transmitted
uint8_t iic_data[256];
// The storage area for the number of transmitted data
uint16_t count;
void func(void)
{
    //The clock-generation circuit has to be set first
    R_PG_Clock_Set();
    //Set up RIIC0
    R_PG_I2C_Set_C0();
    //Master send
    R_PG_I2C_MasterSend_C0(
        0,      //Slave address format
        6,      //Slave address
        iic_data,       // The address of storage area for the data to be transmitted
        256     //The number of data to be transmitted
    );
    //Wait until 64 bytes have been transmitted
    do{
        R_PG_I2C_GetSentDataCount_C0( &count );
    } while( count < 64 );
}
```

5.21.15 R_PG_I2C_Reset_C<*channel number*>

Definition bool R_PG_I2C_Reset_C<*channel number*> (void)

<*channel number*>: 0 to 3

Description Reset the bus

Parameter None

Return value

true	Setting was made correctly.
false	Setting failed.

File for output R_PG_I2C_C<*channel number*>.c

<*channel number*>: 0 to 3

RPDL function R_IIC_Control

Details

- This function resets the module.
- The settings of the module are preserved.

Example A case where the setting is made as follows.

- The function of master is selected for a RIIC0
- "Notify the transmission completion of all data by function call" is selected as the data transmission method

  IIC0MasterTrFunc was specified as the name of the transmit end notification function

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"
// The storage area for the data to be transmitted
uint8_t iic_data[256];
void func(void)
{
    //The clock-generation circuit has to be set first
    R_PG_Clock_Set();
    //Set up RIIC0
    R_PG_I2C_Set_C0();
    //Master send
    R_PG_I2C_MasterSend_C0(
        0,      //Slave address format
        6,      //Slave address
        iic_data,       // The address of storage area for the data to be transmitted
        10      //The number of data to be transmitted
    );
}
void IIC0MasterTrFunc(void)
{
    if ( error ){
        R_PG_I2C_Reset_C0();
    }
}
```

5.21.16 R_PG_I2C_StopModule_C<*channel number*>

Definition bool R_PG_I2C_StopModule_C<*channel number*> (void)

<*channel number*>: 0 to 3

Description Shut down the I^2C bus interface channel

Parameter None

Return value

true	Shutting down succeeded.
false	Shutting down failed.

File for output R_PG_I2C_C<*channel number*>.c

<*channel number*>: 0 to 3

RPDL function R_IIC_Destroy

Details

- Stops an I^2C bus interface channel and places it in the module-stop state.

Example A case where the setting is made as follows.

- The function of master is selected for a RIIC0
- "Wait at the reception function until all data has been transmitted" is selected as the master reception method

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"
// The storage area for the data to be transmitted
uint8_t iic_data[256];
void func(void)
{
    //The clock-generation circuit has to be set first
    R_PG_Clock_Set();
    //Set up RIIC0
    R_PG_I2C_Set_C0();
    //Master receive
    R_PG_I2C_MasterReceive _C0(
        0,      //Slave address format
        6,      //Slave address
        iic_data,       // The address of storage area for the data to be received
        10      //The number of data to be received
    );
    //Stop the RIIC0
    R_PG_I2C_StopModule_C0();
}
```

5.22 Serial Peripheral Interface (RSPI)

5.22.1 R_PG_RSPI_Set_C<*channel number*>

Definition: bool R_PG_RSPI_Set_C<*channel number*> (void)

<*channel number*>: 0 to 2

Description: Set up a RSPI channel

Parameter: None

Return value:

true	Setting was made correctly
false	Setting failed

File for output: R_PG_RSPI_C<*channel number*>.c

<*channel number*>: 0 to 2

RPDL function: R_SPI_Create

Details:

- Releases a serial peripheral interface channel from the module-stop state, makes initial settings, and sets the pins to be used.
- Function R_PG_Clock_Set must be called before calling this function.
- The commands are not set in this function. To set the commands, call R_PG_RSPI_SetCommand_C<*channel number*>.

Example:

```
//Include “R_PG_<project name>.h” to use this function.
#include "R_PG_default.h"

void func(void)
{
      R_PG_Clock_Set();       //Set up the clocks
      R_PG_RSPI_Set_C0();       //Set up RSPI0
      R_PG_RSPI_SetCommand_C0();       //Set commands
}
```

5.22.2 R_PG_RSPI_SetCommand_C<*channel number*>

Definition: bool R_PG_RSPI_SetCommand_C<*channel number*> (void)

<*channel number*>: 0 to 2

Description: Set commands

Parameter: None

Return value:

true	Setting was made correctly
false	Setting failed

File for output: R_PG_RSPI_C<*channel number*>.c

<*channel number*>: 0 to 2

RPDL function: R_SPI_Command

Details:

- Set RSPI commands registers.
- All commands set in GUI (maximum number of commands: 8) shall be set.

Example: Refer to the example of R_PG_RSPI_Set_C<*channel number*>

5.22.3 R_PG_RSPI_StartTransfer_C<*channel number*>

Definition

Transmission and reception operations (Full-duplex synchronous serial communications)

bool R_PG_RSPI_StartTransfer_C<*channel number*>
(uint32_t * tx_start, uint32_t * rx_start, uint16_t sequence_loop_count)
<*channel number*>: 0 to 2

Serial communications consisting of only transmit operations

bool R_PG_RSPI_StartTransfer_C<*channel number*>
(uint32_t * tx_start, uint16_t sequence_loop_count)
<*channel number*>: 0 to 2

Description

Start the data transfer

Conditions for output

"Notify the transfer completion and the error detection by function call" has been selected as the transfer method.

Parameter

uint32_t * tx_start	The start address of the data to be transmitted.
uint32_t * rx_start	The start address of the storage area for the expected data.
uint16_t sequence_loop_count	The number of times that the command sequence will be executed

Return value

true	Setting was made correctly
false	Setting failed

File for output

R_PG_RSPI_C<*channel number*>.c
<*channel number*>: 0 to 2

RPDL function

R_SPI_Transfer

Details

- Starts the data transfer.
- This function is generated when "Notify the transfer completion and the error detection by function call" is selected as the data transfer method in GUI.
- This function returns immediately and the notification function having the specified name will be called when all commands are executed or error is detected.
  Create the notification function as follows:
  void <*name of the notification function*> (void)
  For the notification function, note the contents of this chapter end, Notes on Notification Functions.

Example

A case where the setting is made as follows.

- RSPI has been set to master mode
- "Notify the transfer completion and the error detection by function call" is selected as
- the transfer method
- rsi0_int_func is specified as a notification function name
- Number of commands: 1 Number of frames: 4
  Data lengh of command 0 is 8 bits

```
//Include “R_PG_<project name>.h” to use this function.
#include "R_PG_default.h"

uint32_t tx_data[4] = { 0x11, 0x22, 0x33, 0x44 };
uint32_t rx_data[4] = { 0x00, 0x00, 0x00, 0x00 };
bool    over_run,    mode_fault,    parity_error;

void func(void)
{
      R_PG_Clock_Set();        //Set up the clocks
      R_PG_RSPI_Set_C0();       //Set up RSPI0
      R_PG_RSPI_SetCommand_C0();       //Set commands
      R_PG_RSPI_StartTransfer_C0( tx_data, rx_data, 1 ); //Transfe 4 frames * 8bits
}

void rsi0_int_func (void)
{
      R_PG_RSPI_GetError_C0(&over_run, &mode_fault, &parity_error); //Get error flags
      if( over_run || mode_fault || parity_error ){

            //Processing when an error is detected
      }
      R_PG_RSPI_StopModule_C0();
}
```

5.22.4 R_PG_RSPI_TransferAllData_C<*channel number*>

Definition

Transmission and reception operations (Full-duplex synchronous serial communications)

bool R_PG_RSPI_TransferAllData_C<*channel number*>
(uint32_t * tx_start, uint32_t * rx_start, uint16_t sequence_loop_count)
<*channel number*>: 0 to 2

Serial communications consisting of only transmit operations

bool R_PG_RSPI_TransferAllData_C<*channel number*>
(uint32_t * tx_start, uint16_t sequence_loop_count)
<*channel number*>: 0 to 2

The DTC/DMAC transfer is selected for the transfer method

bool R_PG_RSPI_TransferAllData_C<*channel number*>
(uint16_t sequence_loop_count)
<*channel number*>: 0 to 2

Description

Transfer all data

Conditions for output

Other than “Notify the transfer completion and the error detection by function call” has been selected as the transfer method.

Parameter

uint32_t * tx_start	The start address of the data to be transmitted.
uint32_t * rx_start	The start address of the storage area for the expected data.
uint16_t sequence_loop_count	The number of times that the command sequence will be executed

Return value

true	Setting was made correctly
false	Setting failed

File for output

R_PG_RSPI_C<*channel number*>.c
<*channel number*>: 0 to 2

RPDL function

R_SPI_Transfer

Details

- Transfers all data.
- This function is generated when other than "Notify the transfer completion and the error detection by function call" is selected as the transmission method in GUI.
- This function waits until all commands are executed.

Example

A case where the setting is made as follows.

- RSPI has been set to master mode.
- “Wait until transfer completion” is selected as the transfer method.
- Number of commands: 1 Number of frames: 4
- Data lengh of command 0 is 8 bits

```
//Include “R_PG_<project name>.h” to use this function.
#include "R_PG_default.h"

uint32_t tx_data[4] = { 0x11, 0x22, 0x33, 0x44 };
uint32_t rx_data[4] = { 0x00, 0x00, 0x00, 0x00 };
bool over_run, mode_fault, parity_error;

void func(void)
{
```

```
    R_PG_Clock_Set();       //Set up the clocks
    R_PG_RSPI_Set_C0();     //Set up RSPI0
    R_PG_RSPI_SetCommand_C0();      //Set commands
    R_PG_RSPI_TransferAllData_C0( tx_data, rx_data, 1 ); //Transfe 4 frames * 8bits

    R_PG_RSPI_GetError_C0(&over_run, &mode_fault, &parity_error); //Get error flags
    if( over_run || mode_fault || parity_error ){

        //Processing when an error is detected
    }
    R_PG_RSPI_StopModule_C0();
}
```

5.22.5 R_PG_RSPI_GetStatus_C<*channel number*>

Definition: bool R_PG_RSPI_GetStatus_C<*channel number*> (bool * idle)

<*channel number*>: 0 to 2

Description: Acquire the transfer status

Parameter

bool * idle	The address of storage area for the idle flag (0: Idle state 1: Transfer state)

Return value

true	Acquisition succeeded
false	Acquisition failed

File for output: R_PG_RSPI_C<*channel number*>.c

<*channel number*>: 0 to 2

RPDL function: R_SPI_GetStatus

Details

- Acquires the transfer status.
- The error flags (the overrun error flag, the mode fault error flag, and the parity error flag) are cleared in this function. Call R_PG_RSPI_GetError_C<*channel number*> to acquire the error flags before calling this function if needed.

Example

```
//Include “R_PG_<project name>.h” to use this function.
#include "R_PG_default.h"

bool idle;

void func(void)
{
      do{
         //Get the id
            R_PG_RSPI_GetStatus_C0( & idle );
      }while( idle );
}
```

5.22.6 R_PG_RSPI_GetError_C<*channel number*>

Definition bool R_PG_RSPI_GetError_C<*channel number*>
(bool * over_run, bool * mode_fault, bool * parity_error)
<*channel number*>: 0 to 2

Description Acquire the error flags

Parameter

bool * over_run	The address of storage area for the overrun error flag
bool * mode_fault	The address of storage area for the mode fault error flag
bool * parity_error	The address of storage area for the parity error flag

Return value

true	Acquisition succeeded
false	Acquisition failed

File for output R_PG_RSPI_C<*channel number*>.c
<*channel number*>: 0 to 2

RPDL function R_SPI_GetStatus

Details
- Acquires the error flags.
- Specify the address of storage area for the items to be acquired. Specify 0 for an item that is not required.
- The error flags shall be cleared in this function.

Example Refer to the example of R_PG_RSPI_StartTransfer_C<*channel number*>, R_PG_RSPI_TransferAllData_C<*channel number*>, and R_PG_RSPI_GetCommandStatus_C<*channel number*>

5.22.7 R_PG_RSPI_GetCommandStatus_C<*channel number*>

Definition
bool R_PG_RSPI_GetCommandStatus_C<*channel number*>
(uint8_t * current_command, uint8_t * error_command)
<*channel number*>: 0 to 2

Description
Acquire the command status

Conditions for output
A RSPI channel has been set to the master mode

Parameter

uint8_t * current_command	The address of storage area for the current command pointer value (0 to 7)
uint8_t * error_command	The address of storage area for the value of command pointer when an error is detected (0 to 7)

Return value

true	Acquisition succeeded
false	Acquisition failed

File for output
R_PG_RSPI_C<*channel number*>.c
<*channel number*>: 0 to 2

RPDL function
R_SPI_GetStatus

Details
- Acquires the current command pointer value (0 to 7) and the value of command pointer when an error is detected (0 to 7).
- Specify the address of storage area for the items to be acquired. Specify 0 for an item that is not required.
- The error flags (the overrun error flag, the mode fault error flag, and the parity error flag) are cleared in this function. Call R_PG_RSPI_GetError_C<*channel number*> to acquire the error flags before calling this function if needed.

Example
A case where the setting is made as follows.
- RSPI has been set to the master mode

```
//Include “R_PG_<project name>.h” to use this function.
#include "R_PG_default.h"

bool over_run, mode_fault, parity_error;
uint8_t error_command;

void func(void)
{
    R_PG_RSPI_GetError_C0(&over_run, &mode_fault, &parity_error); //Get error flags
    if( over_run || mode_fault || parity_error ){
        R_PG_RSPI_GetCommandStatus_C0( 0, &error_command );

        // Processing when an error is detected
    }
}
```

5.22.8 R_PG_RSPI_LoopBack<*loopback mode*>_C<*channel number*>

Definition: bool R_PG_RSPI_LoopBack<*loopback mode*>_C<*channel number*> (void)
<*loopback mode*>: Direct, Reversed, Disable
<*channel number*>: 0 to 2

Description: Set loopback mode

Conditions for output: The loopback mode has been set

Parameter: None

Return value:

true	Setting was made correctly
false	Setting failed

File for output: R_PG_RSPI_C<*channel number*>.c
<*channel number*>: 0 to 2

RPDL function: R_SPI_Control

Details:

- Sets or disables RSPI pins to loopback mode.
- By calling R_PG_RSPI_LoopBackDirect_C<*channel number*>, the input path and output path for the shift register are connected. (transmit data = receive data)
- By calling R_PG_RSPI_LoopBackReversed _C<*channel number*>, the reversed input path and output path for the shift register are connected. (reversed transmit data = receive data)
- By calling R_PG_RSPI_LoopBackDisable_C<*channel number*>, the loopback mode is disabled.

Example:

```
//Include “R_PG_<project name>.h” to use this function.
#include "R_PG_default.h"

void func(void)
{
      R_PG_RSPI_LoopBackDirect_C0();    //Set loopback mode
}
```

5.22.9 R_PG_RSPI_StopModule_C<*channel number*>

Definition bool R_PG_RSPI_StopModule_C<*channel number*> (void)

<*channel number*>: 0 to 2

Description Shut down a RSPI channel

Parameter None

Return value

true	Shutting down succeeded
false	Shutting down failed

File for output R_PG_RSPI_C<*channel number*>.c

<*channel number*>: 0 to 2

RPDL function R_SPI_Destroy

Details

- Stops RSPI channel and places it in the module-stop state.

Example Refer to the example of R_PG_RSPI_StartTransfer_C<*channel number*> and R_PG_RSPI_TransferAllData_C<*channel number*>.

5.23 IEBus Controller (IEB)

5.23.1 R_PG_IEB_Set_C<*channel number*>

Definition: bool R_PG_IEB_Set_C<*channel number*>(void)

<*channel number*> : 0

Description: Set up the IEBus interface channel

Parameter:

None

Return value:

true	Setting was made correctly
false	Setting failed

File for output: R_PG_IEB_C<*channel number*>.c <*channel number*> : 0

RPDL function: R_IEB_Set, R_IEB_Create

Details:

- Sets up the IEBus controller.

Example:

```
//Include “R_PG_<project name>.h” to use this function.
#include “R_PG_default.h”

void func(void)
{
      //Set up the IEBus interface channel
      R_PG_IEB_Set_C0();
}
```

5.23.2 R_PG_IEB_MasterReceiveStatus_C<*channel number*>

Definition

bool R_PG_IEB_MasterReceiveStatus_C<*channel number*>
(uint16_t slave, uint8_t *data, uint8_t *count, bool unlock)
<*channel number*> : 0

Description

Read the slave status and unlock

Conditions for output

[Master and slave] was selected as the device attribute and the receive function was set to be enabled on GUI.

Parameter

uint16_t slave	Slave address
uint8_t *data	The address of storage area for the received data
uint8_t *count	The address of storage area for the received message length
bool unlock	Unlock (1:Cancel the lock 0:Lock is not canceled)

Return value

true	Acquisition of the slave status succeeded
false	Acquisition of the slave status failed

File for output

R_PG_IEB_C<*channel number*>.c <*channel number*> : 0

RPDL function

R_IEB_MasterReceive

Details

- Reads the slave status and unlock
- If “Notify the reception completion of all data or an error detection by function call” is selected as the master reception method in GUI, this function returns immediately and the notification function having the specified name will be called when the reception is completed or another event occurs.
  Create the notification function as follows:
  void <name of the notification function> (void)
  For the notification function, note the contents of this chapter end, Notes on Notification Functions.
- If “Wait at the reception function until all data has been received or an error has been detected” is selected as the master reception method in GUI, this function waits until the reception is completed or another event occurs.
- Use R_PG_IEB_GetReceiveStatus_C<channel number> and R_PG_IEB_GetTransmitStatus_C<channel number> to identify the detected event.

Example

A case where the setting has been made in the GUI as follows.

Example

```
//Include “R_PG_<project name>.h” to use this function.
#include “R_PG_default.h”

uint8_t ssr;
uint8_t re_count;

void func(void)
{
      //Read the slave status
      R_PG_IEB_MasterReceiveStatus_C0(
            0x0123,
            &ssr,
```

```
            &re_count,
            0
        );
}
```

5.23.3 R_PG_IEB_MasterReceiveLockAddress_C<*channel number*>

Definition

bool R_PG_IEB_MasterReceiveLockAddress_C<*channel number*>
(uint16_t slave, uint8_t *data, uint8_t *count, bool upper)
<*channel number*> : 0

Description

Read the locked address

Conditions for output

[Master and slave] was selected as the device attribute and the receive function was set to be enabled on GUI.

Parameter

uint16_t slave	Slave address
uint8_t *data	The address of storage area for the received data
uint8_t *count	The address of storage area for the received message length
bool upper	Required bits of locked address to be read (1:upper 4 bits 0:lower 8 bits)

Return value

true	Acquisition of the locked address succeeded
false	Acquisition of the locked address failed

File for output

R_PG_IEB_C<*channel number*>.c <*channel number*> : 0

RPDL function

R_IEB_MasterReceive

Details

- Reads the slave locked address
- If “Notify the reception completion of all data or an error detection by function call” is selected as the master reception method in GUI, this function returns immediately and the notification function having the specified name will be called when the reception is completed or another event occurs.
  Create the notification function as follows:
  void <name of the notification function> (void)
  For the notification function, note the contents of this chapter end, Notes on Notification Functions.
- If “Wait at the reception function until all data has been received or an error has been detected” is selected as the master reception method in GUI, this function waits until the reception is completed or another event occurs.
- Use R_PG_IEB_GetReceiveStatus_C<channel number> and R_PG_IEB_GetTransmitStatus_C<channel number> to identify the detected event.

Example

A case where the setting has been made in the GUI as follows.

Example

```
//Include “R_PG_<project name>.h” to use this function.
#include “R_PG_default.h”

uint8_t lock_addr_l;
uint8_t re_count;

void func(void)
{
    // Read the lock address (lower 8 bits)
      R_PG_IEB_MasterReceiveLockAddress_C0(
            0x0123,
```

```
            &lock_addr_l,
            &re_count,
            0    //lower 8 bits
        );
}
```

5.23.4 R_PG_IEB_MasterReceiveData_C<*channel number*>

Definition

bool R_PG_IEB_MasterReceiveData_C<*channel number*>
(uint16_t slave, uint8_t *data, uint8_t *count)
<*channel number*> : 0

Description

Master data reception

Conditions for output

[Master and slave] was selected as the device attribute and the receive function was set to be enabled on GUI.

Parameter

uint16_t slave	Slave address
uint8_t *data	The address of storage area for the received data
uint8_t *count	The address of storage area for the received message length

Return value

true	Data reception succeeded
false	Data reception failed

File for output

R_PG_IEB_C<*channel number*>.c <*channel number*> : 0

RPDL function

R_IEB_MasterReceive

Details

- Reads the data
- If “Notify the reception completion of all data or an error detection by function call” is selected as the master reception method in GUI, this function returns immediately and the notification function having the specified name will be called when the reception is completed or another event occurs.
  Create the notification function as follows:
  void <name of the notification function> (void)
  For the notification function, note the contents of this chapter end, Notes on Notification Functions.
- If “Wait at the reception function until all data has been received or an error has been detected” is selected as the master reception method in GUI, this function waits until the reception is completed or another event occurs.
- Use R_PG_IEB_GetReceiveStatus_C<channel number> and R_PG_IEB_GetTransmitStatus_C<channel number> to identify the detected event.

Example

A case where the setting has been made in the GUI as follows.

- IebMasterReFunc has been specified as the notification function name

```
//Include “R_PG_<project name>.h” to use this function.
#include “R_PG_default.h”

uint8_t re_data[32];
uint8_t re_count;

void func(void)
{
      //Master data reception
      R_PG_IEB_MasterReceiveData_C0(
            0x0123,
            re_data,
```

```
            &re_count
        );
}
```

5.23.5 R_PG_IEB_MasterSendCmd_C<*channel number*>

Definition
bool R_PG_IEB_MasterSendCmd_C<*channel number*>
(uint16_t slave_unit, uint8_t *cmd, uint8_t count)
<*channel number*> : 0

Description
Master command transmission

Conditions for output
[Master and slave] was selected as the device attribute

Parameter

uint16_t slave_unit	Slave address
uint8_t *cmd	The start address of the command to be sent
uint8_t count	The message length

Return value

true	Command transmission succeeded
false	Command transmission failed

File for output
R_PG_IEB_C<*channel number*>.c <*channel number*> : 0

RPDL function
R_IEB_MasterSend

Details
- Sends the commands
- If “Notify the transmission completion of all data or an error detection by function call” is selected as the master reception method in GUI, this function returns immediately and the notification function having the specified name will be called when the transmission is completed or another event occurs.
  Create the notification function as follows:
  void <name of the notification function> (void)
  For the notification function, note the contents of this chapter end, Notes on Notification Functions.
- If “Wait at the transmission function until all data has been transmitted or an error has been detected” is selected as the master transmission method in GUI, this function waits until the transmission is completed or another event occurs.
- Use R_PG_IEB_GetReceiveStatus_C<channel number> and R_PG_IEB_GetTransmitStatus_C<channel number> to identify the detected event.

Example
A case where the setting has been made in the GUI as follows.
- IebMasterTrFunc has been specified as the notification function name

```
//Include “R_PG_<project name>.h” to use this function.
#include “R_PG_default.h”

uint8_t cmd[10]=“ABCDEFGHIJ”;

void func(void)
{
      //Master command transmission
      R_PG_IEB_MasterSendCmd_C0(
            0x0123,
            cmd,
            10
      );
}
```

5.23.6 R_PG_IEB_MasterSendData_C<*channel number*>

Definition

bool R_PG_IEB_MasterSendData_C<*channel number*>
(uint16_t slave_unit, uint8_t *data, uint8_t count)
<*channel number*> : 0

Description

Master data transmission

Conditions for output

[Master and slave] was selected as the device attribute

Parameter

uint16_t slave_unit	Slave address
uint8_t *data	The start address of the data to be sent
uint8_t count	The message length

Return value

true	Data transmission succeeded
false	Data transmission failed

File for output

R_PG_IEB_C<*channel number*>.c <*channel number*> : 0

RPDL function

R_IEB_MasterSend

Details

- Sends the data
- If “Notify the transmission completion of all data or an error detection by function call” is selected as the master reception method in GUI, this function returns immediately and the notification function having the specified name will be called when the transmission is completed or another event occurs.
  Create the notification function as follows:
  void <name of the notification function> (void)
  For the notification function, note the contents of this chapter end, Notes on Notification Functions.
- If “Wait at the transmission function until all data has been transmitted or an error has been detected” is selected as the master transmission method in GUI, this function waits until the transmission is completed or another event occurs.
- Use R_PG_IEB_GetReceiveStatus_C<channel number> and R_PG_IEB_GetTransmitStatus_C<channel number> to identify the detected event.

Example

A case where the setting has been made in the GUI as follows.

- IebMasterTrFunc has been specified as the notification function name

```
//Include “R_PG_<project name>.h” to use this function.
#include “R_PG_default.h”

uint8_t tr_data[10]=“ABCDEFGHIJ”;

void func(void)
{
      //Master data transmission
      R_PG_IEB_MasterSendData_C0(
            0x0123,
            tr_data,
            10
      );
}
```

5.23.7 R_PG_IEB_MasterSendCmdBroadcast_C<*channel number*>

Definition

bool R_PG_IEB_MasterSendCmdBroadcast_C<*channel number*>
(uint16_t slave_group, uint8_t *cmd, uint8_t count)
<*channel number*> : 0

Description

Master command transmission (Broadcast)

Conditions for output

[Master and slave] was selected as the device attribute

Parameter

uint16_t slave_group	Slave address (FFFh:General broadcast communications Other than FFFh:Group broadcast communications)
uint8_t *cmd	The start address of the command to be sent
uint8_t count	The message length

Return value

true	Command transmission succeeded
false	Command transmission failed

File for output

R_PG_IEB_C<*channel number*>.c <*channel number*> : 0

RPDL function

R_IEB_MasterSend

Details

- Sends the commands in broadcast communications
- If “Notify the transmission completion of all data or an error detection by function call” is selected as the master reception method in GUI, this function returns immediately and the notification function having the specified name will be called when the transmission is completed or another event occurs.
  Create the notification function as follows:
  void <name of the notification function> (void)
  For the notification function, note the contents of this chapter end, Notes on Notification Functions.
- If “Wait at the transmission function until all data has been transmitted or an error has been detected” is selected as the master transmission method in GUI, this function waits until the transmission is completed or another event occurs.
- Use R_PG_IEB_GetReceiveStatus_C<channel number> and R_PG_IEB_GetTransmitStatus_C<channel number> to identify the detected event.

Example

A case where the setting has been made in the GUI as follows.

- IebMasterTrFunc has been specified as the notification function name

```
//Include “R_PG_<project name>.h” to use this function.
#include “R_PG_default.h”

uint8_t cmd[10]=“ABCDEFGHIJ”;

void func(void)
{
      //Master command transmission ( Broadcast )
      R_PG_IEB_MasterSendCmdBroadcast_C0(
            0x0fff,    //Broadcast Communications
```

```
            cmd,
            10
        );
}
```

5.23.8 R_PG_IEB_MasterSendDataBroadcast_C<*channel number*>

Definition
bool R_PG_IEB_MasterSendDataBroadcast_C<*channel number*>
(uint16_t slave_group, uint8_t *data, uint8_t count)
<*channel number*> : 0

Description
Master data transmission (Broadcast)

Conditions for output
[Master and slave] was selected as the device attribute

Parameter

uint16_t slave_group	Slave address (FFFh:General broadcast communications Other than FFFh:Group broadcast communications)
uint8_t *data	The start address of the data to be sent
uint8_t count	The message length

Return value

true	Data transmission succeeded
false	Data transmission failed

File for output
R_PG_IEB_C<*channel number*>.c <*channel number*> : 0

RPDL function
R_IEB_MasterSend

Details
- Sends the data in broadcast communications
- If “Notify the transmission completion of all data or an error detection by function call” is selected as the master reception method in GUI, this function returns immediately and the notification function having the specified name will be called when the transmission is completed or another event occurs.
  Create the notification function as follows:
  void <name of the notification function> (void)
  For the notification function, note the contents of this chapter end, Notes on Notification Functions.
- If “Wait at the transmission function until all data has been transmitted or an error has been detected” is selected as the master transmission method in GUI, this function waits until the transmission is completed or another event occurs.
- Use R_PG_IEB_GetReceiveStatus_C<channel number> and R_PG_IEB_GetTransmitStatus_C<channel number> to identify the detected event.

Example
A case where the setting has been made in the GUI as follows.
- IebMasterTrFunc has been specified as the notification function name

```
//Include “R_PG_<project name>.h” to use this function.
#include “R_PG_default.h”

uint8_t tr_data[10]=“ABCDEFGHIJ”;

void func(void)
{
      //Master data transmission ( Broadcast )
      R_PG_IEB_MasterSendDataBroadcast_C0(
            0x0fff,    //Broadcast Communications
```

```
            tr_data,
            10
        );
}
```

5.23.9 R_PG_IEB_SlaveMonitor_C<*channel number*>

Definition
bool R_PG_IEB_SlaveMonitor_C<*channel number*>
(uint8_t *data, uint8_t *count)
<*channel number*> : 0

Description
Slave bus monitor

Conditions for output
[Slave] or [Master and slave] was selected as the device attribute

Parameter

uint8_t *data	The address of storage area for the received data
uint8_t *count	The address of storage area for the received message length

Return value

true	Bus monitoring succeeded
false	Bus monitoring failed

File for output
R_PG_IEB_C<*channel number*>.c <*channel number*> : 0

RPDL function
R_IEB_SlaveMonitor

Details
- Monitors the bus
- If “Notify the transmission completion of all data or an error detection by function call” is selected as the master reception method in GUI, this function returns immediately and the notification function having the specified name will be called when the transmission is completed or another event occurs.
  Create the notification function as follows:
  void <name of the notification function> (void)
  For the notification function, note the contents of this chapter end, Notes on Notification Functions.
- If “Wait at the transmission function until all data has been transmitted or an error has been detected” is selected as the master transmission method in GUI, this function waits until the transmission is completed or another event occurs.
- Use R_PG_IEB_GetReceiveStatus_C<channel number> and R_PG_IEB_GetTransmitStatus_C<channel number> to identify the detected event.

Example
A case where the setting has been made in the GUI as follows.
- IebMasterReFunc has been specified as the notification function name

```
//Include “R_PG_<project name>.h” to use this function.
#include “R_PG_default.h”

uint8_t re_data[32];
uint8_t re_count;

void func(void)
{
    //Slave bus monitor
    R_PG_IEB_SlaveMonitor_C0(
        re_data,
        &re_count
    );
}
```

5.23.10 R_PG_IEB_SlaveWrite_C<*channel number*>

Definition

bool R_PG_IEB_SlaveWrite_C<*channel number*>
(uint8_t *data, uint8_t count)
<*channel number*> : 0

Description

Set the slave transmit data

Conditions for output

[Slave] or [Master and slave] was selected as the device attribute

Parameter

uint8_t *data	The address of storage area for the data to be transmitted
uint8_t count	The message length

Return value

true	Data transmission succeeded
false	Data transmission failed

File for output

R_PG_IEB_C<*channel number*>.c <*channel number*> : 0

RPDL function

R_IEB_SlaveWrite

Details

- Writes the slave transmit data to the transmit data buffer registers.

Example

A case where the setting has been made in the GUI as follows.

- IebMasterReFunc has been specified as the notification function name

```
//Include “R_PG_<project name>.h” to use this function.
#include “R_PG_default.h”

uint8_t tr_data[10]=“ABCDEFGHIJ”;
uint8_t re_data[32];
uint8_t re_count;

void func(void)
{
	//Set the slave transmit data
	R_PG_IEB_SlaveWrite_C0(
		tr_data,
		10
	);

	//Slave bus monitor
	R_PG_IEB_SlaveMonitor_C0(
		re_data,
		&re_count
	);
}
```

5.23.11 R_PG_IEB_GetReceivedMasterAddress_C<*channel number*>

Definition

bool R_PG_IEB_GetReceivedMasterAddress_C<*channel number*>
(uint16_t *addr)
<*channel number*> : 0

Description

Get the master address

Conditions for output

[Slave] or [Master and slave] was selected as the device attribute

Parameter

uint16_t *addr	The address of storage area for the received master address

Return value

true	Acquisition of the master address succeeded
false	Acquisition of the master address failed

File for output

R_PG_IEB_C<*channel number*>.c <*channel number*> : 0

RPDL function

R_IEB_GetStatus

Details

- Gets the master unit address

Example

```
//Include “R_PG_<project name>.h” to use this function.
#include “R_PG_default.h”

uint16_t addr;

void func(void)
{
      //Get the master address
      R_PG_IEB_GetReceivedMasterAddress_C0(
            &addr
      );
}
```

5.23.12 R_PG_IEB_GetReceivedCmd_C<*channel number*>

Definition: bool R_PG_IEB_GetReceivedCmd_C<*channel number*>(uint8_t *cmd)
<*channel number*> : 0

Description: Get the receive command

Conditions for output: [Slave] or [Master and slave] was selected as the device attribute

Parameter

uint8_t *cmd	The address of storage area for the received command

Return value

true	Acquisition of the receive command succeeded
false	Acquisition of the receive command failed

File for output: R_PG_IEB_C<*channel number*>.c <*channel number*> : 0

RPDL function: R_IEB_GetStatus

Details

- Gets the received commands

Example

```
//Include “R_PG_<project name>.h” to use this function.
#include “R_PG_default.h”

uint8_t cmd;

void func(void)
{
	//Get the receive command
	R_PG_IEB_GetReceivedCmd_C0(
		&cmd
	);
}
```

5.23.13 R_PG_IEB_GetReceivedDataCount_C<*channel number*>

Definition

bool R_PG_IEB_GetReceivedDataCount_C<*channel number*>
(uint8_t *count)
<*channel number*> : 0

Description

Get the message length of receive data

Parameter

uint8_t *count	The address of storage area for the received message length

Return value

true	Acquisition of the count of received data succeeded
false	Acquisition of the count of received data failed

File for output

R_PG_IEB_C<*channel number*>.c <*channel number*> : 0

RPDL function

R_IEB_GetStatus

Details

- Gets the message length of receive data

Example

```
//Include “R_PG_<project name>.h” to use this function.
#include “R_PG_default.h”

uint8_t count;

void func(void)
{
      //Get the message length of receive data
      R_PG_IEB_GetReceivedDataCount_C0(
            &count
      );
}
```

5.23.14 R_PG_IEB_GetLockMasterAddress_C<*channel number*>

Definition

bool R_PG_IEB_GetLockMasterAddress_C<*channel number*>
(uint16_t *addr)
<*channel number*> : 0

Description

Get the lock address

Conditions for output

[Slave] or [Master and slave] was selected as the device attribute

Parameter

uint16_t *addr	The pointer to the storage area for the address of the master unit that has issued a lock request

Return value

true	Acquisition of the master address succeeded
false	Acquisition of the master address failed

File for output

R_PG_IEB_C<*channel number*>.c <*channel number*> : 0

RPDL function

R_IEB_GetStatus

Details

- Gets the address of the master unit that has issued a lock request

Example

```
//Include “R_PG_<project name>.h” to use this function.
#include “R_PG_default.h”

uint16_t addr;

void func(void)
{
	//Get the lock address
	R_PG_IEB_GetLockMasterAddress_C0(
		&addr
	);
}
```

5.23.15 R_PG_IEB_GetGeneralFlag_C<*channel number*>

Definition

bool R_PG_IEB_GetGeneralFlag_C<*channel number*>
(bool *cmd_exe, bool *master_comm, bool *slave_comm_trans, bool *slave_comm_recv, bool *lock, bool *comm_type, bool *broadcast)
<*channel number*> : 0

Description

Get the general flags

Parameter

bool *cmd_exe	The address of storage area for the command execution status flag
bool *master_comm	The address of storage area for the master communications request flag
bool *slave_comm_trans	The address of storage area for the slave transmission request flag
bool *slave_comm_recv	The address of storage area for the slave receive status flag
bool *lock	The address of storage area for the lock status indication flag
bool *comm_type	The address of storage area for the receive broadcast flag
bool *broadcast	The address of storage area for the general broadcast reception acknowledgement flag

Return value

true	Acquisition of the flags succeeded
false	Acquisition of the flags failed

File for output

R_PG_IEB_C<*channel number*>.c <*channel number*> : 0

RPDL function

R_IEB_GetStatus

Details

- Gets the flags in IEBus general flag register

Example

```
//Include “R_PG_<project name>.h” to use this function.
#include “R_PG_default.h”

bool cmd_exe, master_comm, slave_comm_trans, slave_comm_recv, lock, comm_type,
broadcast;

void func(void)
{
      //Get the general flag
      R_PG_IEB_GetGeneralFlag_C0(
            &cmd_exe,
            &master_comm,
            &slave_comm_trans,
            &slave_comm_recv,
            &lock,
            &comm_type,
            &broadcast
      );
}
```

5.23.16 R_PG_IEB_GetTransmitStatus_C<*channel number*>

Definition

bool R_PG_IEB_GetTransmitStatus_C<*channel number*>
(bool *send, bool *complete, bool *arbitration, bool *timing, bool *overflow, bool *nack)
<*channel number*> : 0

Description

Get the transmit status

Parameter

bool *send	The address of storage area for the transmit start flag
bool *complete	The address of storage area for the transmit normal completion flag
bool *arbitration	The address of storage area for the arbitration loss flag
bool *timing	The address of storage area for the transmit timing error flag
bool *overflow	The address of storage area for the transmit-frame maximum transfer byte overflow flag
bool *nack	The address of storage area for the acknowledge flag

Return value

true	Acquisition of the flags succeeded
false	Acquisition of the flags failed

File for output

R_PG_IEB_C<*channel number*>.c <*channel number*> : 0

RPDL function

R_IEB_GetStatus

Details

- Gets the flags in IEBus transmit status register

Example

```
//Include “R_PG_<project name>.h” to use this function.
#include “R_PG_default.h”

bool send, complete, arbitration, timing, overflow, nack;

void func(void)
{
	//Get the transmit status
	R_PG_IEB_GetTransmitStatus_C0(
		&send,
		&complete,
		&arbitration,
		&timing,
		&overflow,
		&nack
	);
}
```

5.23.17 R_PG_IEB_GetReceiveStatus_C<*channel number*>

Definition

bool R_PG_IEB_GetReceiveStatus_C<*channel number*>
(bool *busy, bool *reception, bool *complete, bool *broadcast, bool *overrun, bool *timing, bool *overflow, bool *parity)
<*channel number*> : 0

Description

Get the receive status

Parameter

bool *busy	The address of storage area for the receive busy flag
bool *reception	The address of storage area for the receive start flag
bool *complete	The address of storage area for the receive normal completion flag
bool *broadcast	The address of storage area for the broadcast receive error flag
bool *overrun	The address of storage area for the receive overrun flag
bool *timing	The address of storage area for the receive timing error flag
bool *overflow	The address of storage area for the receive-frame maximum transfer byte overflow flag
bool *parity	The address of storage area for the parity error flag

Return value

true	Acquisition of the flags succeeded
false	Acquisition of the flags failed

File for output

R_PG_IEB_C<*channel number*>.c <*channel number*> : 0

RPDL function

R_IEB_GetStatus

Details

- Gets the flags in IEBus receive status register

Example

```
//Include “R_PG_<project name>.h” to use this function.
#include “R_PG_default.h”

bool busy, reception, complete, broadcast, overrun, timing, overflow, parity;

void func(void)
{
    //Get the receive status
    R_PG_IEB_GetReceiveStatus_C0(
        &busy,
        &reception,
        &complete,
        &broadcast,
        &overrun,
        &timing,
        &overflow,
        &parity
    );
}
```

5.23.18 R_PG_IEB_Reset_C<*channel number*>

Definition bool R_PG_IEB_Reset_C<*channel number*>(bool reset)
<*channel number*> : 0

Description Reset the bus

Parameter

bool reset	Software reset (1:Software reset is asserted 0:Software reset is negated)

Return value

true	Resetting was made correctly
false	Resetting failed

File for output R_PG_IEB_C<*channel number*>.c <*channel number*> : 0

RPDL function R_IEB_Control

Details

- Asserts or negates a software reset

Example

```
//Include “R_PG_<project name>.h” to use this function.
#include “R_PG_default.h”

void func(void)
{
      //Assert a software reset
      R_PG_IEB_Reset_C0( 1 );
}
```

5.23.19 R_PG_IEB_SetSlaveStatus_C<*channel number*>

Definition

bool R_PG_IEB_SetSlaveStatus_C<*channel number*>(bool enable)

<*channel number*> : 0

Description

Set the slave transmission status

Conditions for output

[Slave] or [Master and slave] was selected as the device attribute

Parameter

bool enable	Slave transmission setting (1:slave transmission enabled state 0:slave transmission stop state)

Return value

true	Transmission setting was made correctly
false	Transmission setting failed

File for output

R_PG_IEB_C<*channel number*>.c <*channel number*> : 0

RPDL function

R_IEB_Control

Details

- Sets the slave transmission status

Example

```
//Include “R_PG_<project name>.h” to use this function.
#include “R_PG_default.h”

void func(void)
{
      //Set the slave transmission status
      R_PG_IEB_SetSlaveStatus_C0( 1 ); //transmission enabled
}
```

5.23.20 R_PG_IEB_CancelLock_C<*channel number*>

Definition: bool R_PG_IEB_CancelLock_C<*channel number*>(void)
<*channel number*> : 0

Description: Cancel the slave lock status

Conditions for output: [Slave] or [Master and slave] was selected as the device attribute

Parameter:

None

Return value:

true	Setting was made correctly
false	Setting failed

File for output: R_PG_IEB_C<*channel number*>.c <*channel number*> : 0

RPDL function: R_IEB_Control

Details:

- Cancels the slave lock status

Example:

```
//Include “R_PG_<project name>.h” to use this function.
#include “R_PG_default.h”

void func(void)
{
      //Cancel the lock.
      R_PG_IEB_CancelLock_C0();
}
```

5.23.21 R_PG_IEB_StopCommunication_C<*channel number*>

Definition bool R_PG_IEB_StopCommunication_C<*channel number*>(void)
<*channel number*> : 0

Description Stop the communication

Parameter

None

Return value

true	Setting was made correctly
false	Setting failed

File for output R_PG_IEB_C<*channel number*>.c <*channel number*> : 0

RPDL function R_IEB_Control

Details
- Stops the communication

Example

```
//Include “R_PG_<project name>.h” to use this function.
#include “R_PG_default.h”

void func(void)
{
      //Stop the communication.
      R_PG_IEB_StopCommunication_C0();
}
```

5.23.22 R_PG_IEB_StopModule_C<*channel number*>

Definition bool R_PG_IEB_StopModule_C<*channel number*>(void)
<*channel number*> : 0

Description Shut down the IEBus interface channel

Parameter

None

Return value

true	Stopping succeeded
false	Stopping failed

File for output R_PG_IEB_C<*channel number*>.c <*channel number*> : 0

RPDL function R_IEB_Destroy

Details

- Stops IEBus controller and places it in the module-stop state.

Example

```
//Include “R_PG_<project name>.h” to use this function.
#include “R_PG_default.h”

void func(void)
{
      //Shut down the IEBus interface channel.
      R_PG_IEB_StopModule_C0();
}
```

5.24 CRC Calculator (CRC)

5.24.1 R_PG_CRC_Set

Definition: bool R_PG_CRC_Set(void)

Description: Set up CRC calculator

Parameter: None

Return value:

true	Setting was made correctly.
false	Setting failed.

File for output: R_PG_CRC.c

RPDL function: R_CRC_Create

Details:

- Releases the CRC calculator from the module-stop state, makes initial settings.

5.24.2 R_PG_CRC_InputData

Definition: bool R_PG_CRC_InputData (uint8_t data)

Description: Input a data to CRC calculator

Parameter

uint8_t data	The data to be used for the calculation

Return value

true	Setting was made correctly.
false	Setting failed.

File for output: R_PG_CRC.c

RPDL function: R_CRC_Write

Details

- This function writes the data into the CRC calculation register

5.24.3 R_PG_CRC_GetResult

Definition bool R_PG_CRC_GetResult (uint16_t * data)

Description Get the the result of calculation

Parameter

uint16_t * data	The address of the location where the result shall be stored.

Return value

true	Acquisition succeeded
false	Acquisition failed

File for output R_PG_CRC.c

RPDL function R_CRC_Read

Details

- This function acquires the the result of calculation

5.24.4 R_PG_CRC_StopModule

Definition bool R_CRC_Destroy (uint16_t * data)

Description Shut down CRC calculator

Parameter None

Return value

true	Acquisition succeeded
false	Acquisition failed

File for output R_PG_CRC.c

RPDL function R_CRC_Destroy

Details

- Stops the CRC calculator and places it in the module-stop state.

5.25 12-Bit A/D Converter (S12ADa)

5.25.1 R_PG_ADC_12_Set_S12AD0

Definition: bool R_PG_ADC_12_Set_S12AD0 (void)

Description: Sets up the 12-bit A/D converter

Parameter: None

Return value:

true	Setting was made correctly.
false	Setting failed.

File for output: R_PG_ADC_12_S12AD0.c

RPDL function: R_ADC_12_Create

Details:

- Releases the 12-bit A/D converter from the module-stop state, makes initial settings, and places the converter in the conversion-start trigger-input wait state. When the software trigger is selected to start conversion, conversion is started by calling R_PG_ADC_12_StartConversionSW_S12AD0.
- Before calling this function, call R_PG_Clock_Set to set the clock.
- The A/D-conversion end interrupt is set in this function. When the name of the interrupt notification function has been specified in the GUI, the function having the specified name will be called when an interrupt request is conveyed to the CPU. Create the interrupt notification function as follows:

  void <name of the interrupt notification function> (void)

  For notes on interrupt notification functions, refer to “Notes on Notification Functions” provided at the end of this section.

Example:

```
// Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

void func(void)
{
    R_PG_Clock_Set();              // The clock-generation circuit has to be set first.
    R_PG_ADC_12_Set_S12AD0();      // Set up the 12-bit A/D converter (S12AD0).
}
```

5.25.2 R_PG_ADC_12_StartConversionSW_S12AD0

Definition bool R_PG_ADC_12_StartConversionSW_S12AD0(void)

Description Starts A/D conversion (by a software trigger)

Parameter None

Return value

true	Setting was made correctly.
false	Setting failed.

File for output R_PG_ADC_12_S12AD0.c

RPDL function R_ADC_12_Control

Details

- Starts A/D conversion by an A/D converter for which the software trigger has been selected as the activation source.

Example The following setting has been made through the GUI.

- Select the software trigger as the activation source.

```
// Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

void func(void)
{
    R_PG_Clock_Set();              // The clock-generation circuit has to be set first.
    R_PG_ADC_12_Set_S12AD0();      // Set up the 12-bit A/D converter (S12AD0).

    // A software trigger starts A/D conversion.
    R_PG_ADC_12_StartConversionSW_S12AD0();
}
```

5.25.3 R_PG_ADC_12_StopConversion_S12AD0

Definition: bool R_PG_ADC_12_StopConversion_S12AD0(void)

Description: Stops A/D conversion

Parameter: None

Return value:

true	Stopping conversion succeeded.
false	Stopping conversion failed.

File for output: R_PG_ADC_12_S12AD0.c

RPDL function: R_ADC_12_Control

Details:

- Stops A/D conversion in the continuous scan mode. In other modes, this function need not be called after A/D conversion has ended.
- After this function has stopped A/D conversion, continuous scanning is resumed on input of the A/D-conversion start trigger. To end continuous scanning, stop the A/D conversion unit by calling R_PG_ADC_12_StopModule_S12AD0.

Example: The following setting has been made through the GUI.

- Select the continuous scan mode as the operating mode.

```
// Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

void func1(void)
{
    R_PG_Clock_Set();              // The clock-generation circuit has to be set first.
    R_PG_ADC_12_Set_S12AD0();      // Set up the 12-bit A/D converter (S12AD0).
}

void func2(void)
{
    // Stop continuous scanning.
    R_PG_ADC_12_StopConversion_S12AD0();
}
```

5.25.4 R_PG_ADC_12_GetResult_S12AD0

Definition: bool R_PG_ADC_12_GetResult_S12AD0(uint16_t * result)

Description: Gets the result of A/D conversion of an analog input, output from the temperature sensor, or internal reference voltage

Parameter

uint16_t * result	Destination for storage of the result of A/D conversion

Return value

true	Acquisition of the result succeeded.
false	Acquisition of the result failed.

File for output: R_PG_ADC_12_S12AD0.c

RPDL function: R_ADC_12_Read

Details

- At least two 21-byte spaces are needed for storage of the acquired result of A/D conversion of an analog input.
- When A/D conversion is in progress at the time of calling this function and a name for the interrupt notification function has not been specified through the GUI, the function waits until the end of A/D conversion before reading the result.

Example: The following settings have been made through the GUI.

- Analog input channel was selected as conversion target in the GUI.
- S12ad0IntFunc was specified as A/D conversion end interrupt notification function name in the GUI.

```
// Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

void func(void)
{
    R_PG_Clock_Set();              // The clock-generation circuit has to be set first.
    R_PG_ADC_12_Set_S12AD0();      // Set up the 12-bit A/D converter (S12AD0).
}

// A/D-conversion end interrupt notification function
void S12ad0IntFunc(void)
{
    uint16_t result[21];      // Destination for storing the result of A/D conversion.

    // Acquire the results of A/D conversion.
    R_PG_ADC_12_GetResult_S12AD0( result );
}
```

5.25.5 R_PG_ADC_12_StopModule_S12AD0

Definition: bool R_PG_ADC_12_StopModule_S12AD0(void)

Description: Shuts down the 12-bit A/D converter

Parameter: None

Return value:

true	Shutting down succeeded.
false	Shutting down failed.

File for output: R_PG_ADC_12_S12AD0.c

RPDL function: R_ADC_12_Destroy

Details:

- Stops the 12-bit A/D converter and places it in the module-stop state.

Example:

```
// Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

uint16_t result[21];   // Destination for storage of the result of A/D conversion

void func1(void)
{
    R_PG_Clock_Set();              // The clock-generation circuit has to be set first.
    R_PG_ADC_12_Set_S12AD0();      // Set up the 12-bit A/D converter (S12AD0).
}

void func2(void)
{
    // Stop continuous scanning.
    R_PG_ADC_12_StopConversion_S12AD0();

    // Acquire the result of A/D conversion.
    R_PG_ADC_12_GetResult_S12AD0( result );

    // Stop the 12-bit A/D converter (S12AD0).
    R_PG_ADC_12_StopModule_S12AD0();
}
```

5.26 10-Bit A/D Converter (ADb)

5.26.1 R_PG_ADC_10_Set_AD<*unit number*>

Definition: bool R_PG_ADC_10_Set_AD<*unit number*> (void) <*unit number*>: 0

Description: Set up the 10-Bit A/D Converter (ADA)

Parameter: None

Return value:

true	Setting was made correctly
false	Setting failed

File for output: R_PG_ADC_10_AD<*unit number*>.c <*unit number*>: 0

RPDL function: R_ADC_10_Set and R_ADC_10_Create

Details:

- Releases an A/D converter from the module-stop state, makes initial settings, and places it in the conversion-start trigger-input wait state.
- Function R_PG_Clock_Set must be called before calling this function.
- The input direction is set for pins used as analog inputs and the input buffers for the pins are disabled.
- Call R_PG_ADC_10_StartConversionSW_AD<*unit number*> to start the A/D-conversion by the software trigger.
- The A/D-conversion end interrupt is set in this function. When the name of the interrupt notification function has been specified in the GUI, if an interrupt request is conveyed to the CPU, the function having the specified name will be called. Create the interrupt notification function as follows:

  void <name of the interrupt notification function> (void)

  For the interrupt notification function, note the contents of this chapter end, Notes on Notification Functions.

Example: The hardware trigger has been specified in the GUI.

Ad0IntFunc has been specified as the name of the A/D-conversion end interrupt notification function in the GUI.

```
#include "R_PG_default.h" //Include "R_PG_<project name>.h" to use this function.
uint16_t data;    //Destination for storage of the result of A/D conversion

void func(void)
{
      R_PG_Clock_Set();              //The clock-generation circuit has to be set first.
      R_PG_ADC_10_Set_AD0();         //Set up ADA.
}

//AD-conversion end interrupt notification function
void Ad0IntFunc(void)
{
      R_PG_ADC_10_GetResult_AD0(&data)       //Acquire the result of A/D conversion.
}
```

5.26.2 R_PG_ADC_10_SetSelfDiag_VREF_<*voltage*>_AD<*unit number*>

Definition: bool R_PG_ADC_10_SetSelfDiag_VREF_<*voltage*>_AD<*unit number*> (void)
<*voltage*>: 0, 0_5, 1 (0:Vref*0, 0_5:Vref/2, 1:Vref) <*unit number*>: 0

Description: Set up the A/D self-diagnostic function

Conditions for output: The self-diagnostic function is enabled

Parameter: None

Return value:

true	Setting was made correctly
false	Setting failed

File for output: R_PG_ADC_10_AD<*unit number*>.c <*unit number*>: 0

RPDL function: R_ADC_10_Create

Details:

- Setsup the A/D self-diagnostic function.
- In this function, the A/D conversion mode is set to the single channel mode and the conversion start trigger is set to the software trigger.
- To re-set the A/D converter, call R_PG_ADC_10_Set_AD<*unit number*>.
- To start the self-diagnostic, call R_PG_ADC_10_StartSelfDiag_AD<*unit number*> and to get the result of self-diagnostic, call R_PG_ADC_10_GetResult_AD<*unit number*>.

Example:

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"
uint16_t SelfDiagnostic_0()
{
    uint16_t result;
    R_PG_ADC_10_SetSelfDiag_VREF_0_AD0();
    R_PG_ADC_10_StartSelfDiag_AD0();
    R_PG_ADC_10_GetResult_AD0 (&result);
    return result;
}
uint16_t SelfDiagnostic_0_5()
{
    uint16_t result;
    R_PG_ADC_10_SetSelfDiag_VREF_0_5_AD0();
    R_PG_ADC_10_StartSelfDiag_AD0();
    R_PG_ADC_10_GetResult_AD0 (&result);
    return result;
}
uint16_t SelfDiagnostic_1()
{
    uint16_t result;
    R_PG_ADC_10_SetSelfDiag_VREF_1_AD0();
    R_PG_ADC_10_StartSelfDiag_AD0();
    R_PG_ADC_10_GetResult_AD0 (&result);
    return result;
}
```

5.26.3 R_PG_ADC_10_StartConversionSW_AD<*unit number*>

Definition: bool R_PG_ADC_10_StartConversionSW_AD<*unit number*> (void)
<*unit number*>: 0

Description: Start the A/D conversion (Software trigger)

Conditions for output: Software trigger is selected as conversion start trigger

Parameter: None

Return value:

true	Triggering the conversion succeeded.
false	Triggering the conversion failed.

File for output: R_PG_ADC_10_AD<*unit number*>.c
<*unit number*>: 0

RPDL function: R_ADC_10_Control

Details:

- Call this function when you use the software trigger.

Example: The software trigger has been specified in the GUI.

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

void func(void)
{
    R_PG_Clock_Set();              //The clock-generation circuit has to be set first.
    R_PG_ADC_10_Set_AD0();         //Set up AD0.

    //Start A/D conversion by the software trigger
    R_PG_ADC_10_StartConversionSW_AD0();
}
```

5.26.4 R_PG_ADC_10_StartSelfDiag_AD<*unit number*>

Definition	bool R_PG_ADC_10_StartSelfDiag_AD<*unit number*> (void) <*unit number*>: 0
Description	Start the A/D conversion (Self-diagnostic function)
Conditions for output	The self-diagnostic function is enabled
Parameter	None

Return value

true	Triggering the conversion succeeded.
false	Triggering the conversion failed.

File for output	R_PG_ADC_10_AD<*unit number*>.c <*unit number*>: 0
RPDL function	R_ADC_10_Control

Details

- Start the A/D conversion (Self-diagnostic function).

Example

Refer to the example of R_PG_ADC_10_SetSelfDiag_VREF_<*voltage*>_AD<*unit number*>.

5.26.5 R_PG_ADC_10_StopConversion_AD<*unit number*>

Definition bool R_PG_ADC_10_StopConversion_AD<*unit number*> (void)
<*unit number*>: 0

Description Stop the A/D conversion

Parameter None

Return value

true	Stopping the conversion succeeded.
false	Stopping the conversion failed.

File for output R_PG_ADC_10_AD<*unit number*>.c
<*unit number*>: 0

RPDL function R_ADC_10_Control

Details
- A/D conversion can be stopped in the continuous scan mode. In the single channel mode and single scan mode, this function need not be called after A/D conversion has ended.
- After this function has stopped A/D conversion, continuous scanning is resumed on input of the A/D-conversion start trigger. To end continuous scanning, stop the A/D conversion unit by calling R_PG_ADC_10_StopModule_AD<*unit number*>.

Example The continuous scan mode has been specified in the GUI.

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

uint16_t data;    //Destination for storage of the result of A/D conversion

void func1(void)
{
      R_PG_Clock_Set();                 //The clock-generation circuit has to be set first.
      R_PG_ADC_10_Set_AD0();        //Set up AD0.
}

void func2(void)
{
      //Stop continuous scanning.
      R_PG_ADC_10_StopConversion_AD0();

      //Acquire the result of A/D conversion.
      R_PG_ADC_10_GetResult_AD0(&data);

      //Stop the A/D converter.
      R_PG_ADC_10_StopModule_AD0();
}
```

5.26.6 R_PG_ADC_10_GetResult_AD<*unit number*>

Definition bool R_PG_ADC_10_GetResult_AD<*unit number*> (uint16_t * result)

<*unit number*>: 0

Description Get the result of A/D conversion

Parameter

uint16_t * result	Destination for storage of the result of A/D conversion

Return value

true	Acquisition of the result succeeded.
false	Acquisition of the result failed.

File for output R_PG_ADC_10_AD<*unit number*>.c <*unit number*>: 0

RPDL function R_ADC_10_Read

Details

- The amount of data to be acquired depends on the number of A/D-conversion channels that are in use. Reserve the area required for storing the result of A/D conversion for the given number of channels.
- When A/D conversion is in progress at the time of calling this function and a name for the interrupt notification function has not been specified through the GUI, the function waits until the end of A/D conversion before reading the result.

Example Four channels (AN0 to AN3) are in use.
Ad0IntFunc has been specified as the name of the A/D-conversion end interrupt notification function in the GUI.

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"
void func(void)
{
    R_PG_Clock_Set();              //The clock-generation circuit has to be set first.
    R_PG_ADC_10_Set_AD0();         //Set up AD0.
}
//AD-conversion end interrupt notification function
void Ad0IntFunc(void)
{
    uint16_t data[4];    //Result of A/D conversion on all channels
    uint16_t data_an0;   //Result of A/D conversion on AN0
    uint16_t data_an1;   //Result of A/D conversion on AN1
    uint16_t data_an2;   //Result of A/D conversion on AN2
    uint16_t data_an3;   //Result of A/D conversion on AN3

    R_PG_ADC_10_GetResult_AD0(data);     //Acquire the results of A/D conversion.

    data_an0 = data[0];
    data_an1 = data[1];
    data_an2 = data[2];
    data_an3 = data[3];
}
```

5.26.7 R_PG_ADC_10_StopModule_AD<*unit number*>

Definition: bool R_PG_ADC_10_StopModule_AD<*unit number*> (void)
<*unit number*>: 0

Description: Shut down the 10-Bit A/D Converter (ADA)

Parameter: None

Return value:

true	Shutting down succeeded
false	Shutting down failed

File for output: R_PG_ADC_10_AD<*unit number*>.c
<*unit number*>: 0

RPDL function: R_ADC_10_Destroy

Details:
- Stops an A/D converter and places it in the module-stop state. (Power consumption decrease functoin)

Example: Refer to the example of R_PG_ADC_10_StopConversion_AD<*unit number*>

5.27 D/A Converter (DAa)

5.27.1 R_PG_DAC_Set_C<*channel number*>

Definition: bool R_PG_DAC_Set_C<*channel number*> (void) <*channel number*>: 0 or 1

Description: Set up a D/A converter channel

Parameter: None

Return value:

true	Setting was made correctly.
false	Setting failed.

File for output: R_PG_DAC_C<*channel number*>.c <*channel number*>: 0 or 1

RPDL function: R_DAC_10_Create

Details:

- Sets up a D/A converter channel.
- Releases the D/A converter from the module-stop state.
- The conversion result of data register's initial value (=0) after the module-stop state is released is output from the analog output pin.
- If the output begins after specifying an initial value, use R_DAC_SetWithInitialValue_C<*channel number*>.
- If [D/A converter operation synchronizes with 10-bit A/D converter operation] is selected in GUI, do not call this function when a 10-bit A/D conversion is in progress.

Example:

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

void func1(void)
{
      //Set up DA0 pin.
      R_PG_DAC_Set_C0();
}

void func2( uint16_t output_val )
{
      //Change D/A conversion value
      R_PG_DAC_ControlOutput_C0( output_val );
}
```

5.27.2 R_PG_DAC_SetWithInitialValue_C<*channel number*>

Definition bool R_PG_DAC_SetWithInitialValue_C<*channel number*> (uint16_t data)

<*channel number*>: 0 or 1

Description An initial value of the data register is specified, and D/A converter channel is set.

Parameter

uint16_t data	An initial value of the data register

Return value

true	Setting was made correctly.
false	Setting failed.

File for output R_PG_DAC_C<*channel number*>.c <*channel number*>: 0 or 1

RPDL function R_DAC_10_Create

Details

- The D/A converter channel is set specifying an initial value of the data register, and the output begins.
- Releases the D/A converter from the module-stop state.
- If [D/A converter operation synchronizes with 10-bit A/D converter operation] is selected in GUI, do not call this function when a 10-bit A/D conversion is in progress.

Example

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

void func1(uint16_t initial_val)
{
      //Setting up DA0 pin, and output begins.
      R_PG_DAC_SetWithInitialValue_C0( initial_val );
}

void func2( uint16_t output_val )
{
      //Change D/A conversion value
      R_PG_DAC_ControlOutput_C0( output_val );
}
```

5.27.3 R_PG_DAC_ControlOutput_C<*channel number*>

Definition bool R_PG_DAC_ControlOutput_C<*channel number*> (uint16_t data)

<*channel number*>: 0 or 1

Description Setting the data register

Parameter

uint16_t data	The value to be written to the data register

Return value

true	Setting was made correctly.
false	Setting failed.

File for output R_PG_DAC_C<*channel number*>.c <*channel number*>: 0 or 1

RPDL function R_DAC_10_Write

Details

- Writes the D/A conversion value to the data register.

Example Refer to the example of R_PG_DAC_Set_C<*channel number*>

5.27.4 R_PG_DAC_StopOutput_C<*channel number*>

Definition bool R_PG_DAC_StopOutput_C<*channel number*> (void)

<*channel number*>: 0 or 1

Description Stop the analog signal output.

Parameter None

Return value

true	Stopping succeeded.
false	Stopping failed.

File for output R_PG_DAC_C<*channel number*>.c <*channel number*>: 0 or 1

RPDL function R_DAC_10_Destroy

Details

- Disables D/A converter channel.
- Once both channels are disabled, the module is put into the power-down state.

Example

```
//Include "R_PG_<project name>.h" to use this function.
#include "R_PG_default.h"

void func1(uint16_t initial_val)
{
    //Setting up DA0 pin, and output begins.
    R_PG_DAC_SetWithInitialValue_C0( initial_val );
}

void func2(void)
{
    //Stop the analog signal output.
    R_PG_DAC_StopOutput_C0();
}
```

5.28 Temperature Sensor (TS)

5.28.1 R_PG_TS_Set

Definition: bool R_PG_TS_Set(void)

Description: Set up the temperature sensor

Parameter:

None

Return value:

true	Setting was made correctly
false	Setting failed

File for output: R_PG_TS.c

RPDL function: R_TS_Create

Details:

- Releases the temperature sensor from the module-stop state, makes initial settings.

Example:

```
//Include “R_PG_<project name>.h” to use this function.
#include “R_PG_default.h”

void func1(void)
{
      //Set the clock-generation circuit.
      R_PG_Clock_Set();

      //Sets up the 12-bit A/D converter
      R_PG_ADC_12_Set_S12AD0();

      //Set up the temperature sensor
      R_PG_TS_Set();

      //Enable the temperature sensor output
      R_PG_TS_EnableOutput();
}

void func2(void)
{
      //Disable the temperature sensor output
      R_PG_TS_DisableOutput();
}

void func3(void)
{
      //Shut down the temperature sensor
      R_PG_TS_StopModule();
}
```

5.28.2 R_PG_TS_EnableOutput

Definition: bool R_PG_TS_EnableOutput(void)

Description: Enable the temperature sensor output

Parameter:

None

Return value:

true	Setting was made correctly
false	Setting failed

File for output: R_PG_TS.c

RPDL function: R_TS_Control

Details:

- Enables output from the temperature sensor to the 12-bit A/D converter.

Example:

Refer to the example of R_PG_TS_Set.

5.28.3 R_PG_TS_DisableOutput

Definition: bool R_PG_TS_DisableOutput(void)

Description: Disable the temperature sensor output

Parameter:

None

Return value:

true	Setting was made correctly
false	Setting failed

File for output: R_PG_TS.c

RPDL function: R_TS_Control

Details:

- Disables output from the temperature sensor to the 12-bit A/D converter.

Example:

Refer to the example of R_PG_TS_Set.

5.28.4 R_PG_TS_StopModule

Definition: bool R_PG_TS_StopModule(void)

Description: Shut down the temperature sensor

Parameter:

None

Return value:

true	Setting was made correctly
false	Setting failed

File for output: R_PG_TS.c

RPDL function: R_TS_Destroy

Details:

- Disables output from the temperature sensor to the 12-bit A/D converter.
- Stops the temperature sensor and places it in the module-stop state.

Example: Refer to the example of R_PG_TS_Set.

5.29 Notes on Notification Functions

5.29.1 Interrupts and processor mode

The RX CPU has two processor modes; supervisor and user.The driver functions will be executed by the CPU in user mode.However any notification functions which are called by the interrupt handlers in Renesas Peripheral Driver Library will be executed by the CPU in supervisor mode.This means that the privileged CPU instructions (RTFI, RTE and WAIT) can be executed by the notification function and any function that is called by the notification function.
The user must:

1. Avoid using the RTFI and RTE instructions.
   These instructions are issued by the API interrupt handlers, so there should be no need for the user's code to use these instructions.
2. Use the wait() intrinsic function with caution.
   This instruction is used by some API functions as part of power management, so there should be no need for the user's code to use this instruction.

More information on the processor modes can be found in §1.4 of the RX Family software manual.

5.29.2 Interrupts and DSP instructions

The accumulator (ACC) register is modified by the following instructions:

• DSP (MACHI, MACLO, MULHI, MULLO, MVTACHI, MVTACLO and RACW).
• Multiply and multiply-and-accumulate (EMUL, EMULU, FMUL, MUL, and RMPA)

The accumulator (ACC) register is not pushed onto the stack by the interrupt handlers in Renesas Peripheral Driver Library.
If DSP instructions are being utilised in the users' code, notification functions which are called by the interrupt handlers in Renesas Peripheral Driver Library should either

1. Avoid using instructions which modify the ACC register.
2. Take a copy of the ACC register and restore it before exiting the callback function.

6. Registering Files with the IDE and Building Them

Note the following points when registering the files generated by the Peripheral Driver Generator with the IDE(High-performance Embedded Workshop, CubeSuite+ or e2 studio) and building them.

(1) Source files generated by the Peripheral Driver Generator do not include a startup program. For this reason, you need to create a startup program by specifying [Application] as the project type during the process of creating a IDE project.

(2) Source files registered by the Peripheral Driver Generator with the IDE include an interrupt handler and vector table. Since the interrupt handler and vector table must not overlap with those included in the startup program created by using the IDE, intprg.c and vecttbl.c are excluded from the set of files that are included in the build. Interrupt_handler.c and vector_table.c are made the target in case of e2studio.

(3) Source files Interrupt_xxx.c, which includes the interrupt handler that the Peripheral Driver Generator registers with the IDE, is overwritten when the Peripheral Driver Generator generates source files.

(4) The Renesas Peripheral Driver Library is produced using the default compiler options (except that [Double precision] is selected for [Precision of double]). If you specify the compiler options other than the defaults in your project, you have to utilize Renesas Peripheral Driver Library source under your responsibility.

(5) The Renesas Peripheral Driver Library has been built specifying double-precision floating point. Therefore, to build the user program with Peripheral Driver Generator-generated files, specify double-precision floating point option in builder settings of IDE as follows. It's unnecessary at the time of e2 studio use.

CubeSuite+

1. Open the [CC-RX Property] by double-clicking [CC-RX(Build Tool)] in project tree.
2. In the [CPU] category, select [Handles in double precision] for [Precision of the double type and long double type].

High-performance Embedded Workshop

1. Select [Build]->[RX Standard Toolchain] from main menu to open the [RX Standard Toolchain] dialog box.
2. Select the [CPU] tab.
3. Click the [Details] button to open the [CPU details] dialog box.
4. Select [Double precision] for [Precision of double].

(6) The Renesas Peripheral Driver Library use FIXEDVECT section that address is 0xFFFFFFD0. Therefore, to build the user program with Peripheral Driver Generator-generated files, specify the linker option in builder setting of IDE as follows. It’s necessary at the time of e2 studio use.

1. Select the project on Project Explorer.
2. Select [File]->[Properties] from main menu to open the [Properties] window.
3. Select [C/C++ build] ->[Settings]
4. Select [All configurations] for [Configuration]
5. Select [Linker] -> [Section] to show [Section viewer]
6. Set the address of the FIXEDVECT section as 0xFFFFFFD0.

Appendix 1. Pin Functions for which the Allocation Can be Changed

Table a-1.1 177-pin TFLGA, 176-pin LFBGA (the Upper Row of Each Pair is the Default Selection)

Peripheral module	Pin function	Selection of assignment	Pin No.
Address bus	A16	PC0/A16/MTIOC3C/TCLKC/PO17/CTS5#/RTS5#/SS5#/SSLA1/SCL3/IRQ14	N13
		P90/A16/D16/TXD7/SMOSI7/SSDA7/AN014	C6
	A17	PC1/A17/MTIOC3A/TCLKD/PO18/SCK5/SSLA2/SDA3/IRQ12	R15
		P91/A17/D17/SCK7/AN015	B6
	A18	PC2/A18/MTIOC4B/TCLKA/PO21/RXD5/SMISO5/SSCL5/SSLA3/IERXD	P13
		P92/A18/D18/RXD7/SMISO7/SSCL7/AN016	B7
	A19	PC3/A19/MTIOC4D/TCLKB/PO24/TXD5/SMOSI5/SSDA5/IETXD	N12
		P93/A19/D19/CTS7#/RTS7#/SS7#/AN017	D6
	A20	PC4/A20/CS3#/MTIOC3D/MTCLKC/TIOCC6/TCLKE/TMCI1/PO25/POE0#/SCK5/CTS8#/RTS8#/SS8#/SSLA0	P12
		P94/A20/D20	A8
	A21	PC5/A21/CS2#/WAIT#/MTIOC3B/MTCLKD/TIOCD6/TCLKF/TMRI2/PO29/SCK8/RSPCKA	M10
		P95/A21/D21	D7
	A22	PC6/A22/CS1#/MTIOC3C/MTCLKA/TIOCA6/TMCI2/PO30/RXD8/SMISO8/SSCL8/MOSIA/IRQ13	P11
		P96/A22/D22	B9
	A23	PC7/A23/CS0#/MTIOC3A/MTCLKB/TIOCB6/TMO2/PO31/TXD8/SMOSI8/SSDA8/MISOA/IRQ14	N10
		P97/A23/D23	A10
Bus control	WAIT#	P57/WAIT#/WR3#/BC3#	N6
		P55/WAIT#/MTIOC4D/TMO3/CRX1/IRQ10	N7
		PC5/A21/CS2#/WAIT#/MTIOC3B/MTCLKD/TIOCD6/TCLKF/TMRI2/PO29/SCK8/RSPCKA	M10
		P51/WR1#/BC1#/WAIT#/SCK2/SSLB2	N9
	CS0#	P60/CS0#/SCK9	A12
		PC7/A23/CS0#/MTIOC3A/MTCLKB/TIOCB6/TMO2/PO31/TXD8/SMOSI8/SSDA8/MISOA/IRQ14	N10
	CS1#	P61/CS1#/CTS9#/RTS9#/SS9#	D11
		P71/CS1#	K13
		PC6/A22/CS1#/MTIOC3C/MTCLKA/TIOCA6/TMCI2/PO30/RXD8/SMISO8/SSCL8/MOSIA/IRQ13	P11
	CS2#	P62/CS2#	C12
		P72/CS2#	L15
		PC5/A21/CS2#/WAIT#/MTIOC3B/MTCLKD/TIOCD6/TCLKF/TMRI2/PO29/SCK8/RSPCKA	M10
	CS3#	P63/CS3#	A13
		P73/CS3#/PO16	N14
		PC4/A20/CS3#/MTIOC3D/MTCLKC/TIOCC6/TCLKE/TMCI1/PO25/POE0#/SCK5/CTS8#/RTS8#/SS8#/SSLA0	P12

	CS4#	P64/CS4#	B13
		P74/CS4#/PO19/CTS11#/RTS11#/SS11#	R14
		P24/CS4#/MTIOC4A/MTCLKA/TIOCB4/TMRI1/PO4/SCK3	M3
	CS5#	P65/CS5#	D15
		P75/CS5#/PO20/SCK11	P14
		P25/CS5#/MTIOC4C/MTCLKB/TIOCA4/PO5/RXD3/SMISO3/SSCL3/ADTRG0#	L4
	CS6#	P66/CS6#/CTX2	F12
		P76/CS6#/PO22/RXD11/SMISO11/SSCL11	R13
		P26/CS6#/MTIOC2A/TMO1/PO6/TXD1/CTS3#/RTS3#/SMOSI1/SS3#/SSDA1/MOSIB	M2
	CS7#	P67/CS7#/CRX2/IRQ15	E15
		P77/CS7#/PO23/TXD11/SMOSI11/SSDA11	M12
		P27/CS7#/MTIOC2B/TMCI3/PO7/SCK1/RSPCKB	M1
Interrupts	IRQ0	P30/MTIOC4B/TMRI3/PO8/RTCIC0/POE8#/RXD1/SMISO1/SSCL1/MISOB/IRQ0-DS	L2
		P10/MTIC5W/TMRI3/IRQ0	P5
		PD0/D0[A0/D0]/TIOCA7/IRQ0/AN008	C7
	IRQ1	P31/MTIOC4D/TMCI2/PO9/RTCIC1/CTS1#/RTS1#/SS1#/SSLB0/IRQ1-DS	L1
		P11/MTIC5V/TMCI3/SCK2/IRQ1	R5
		PD1/D1[A1/D1]/MTIOC4B/TIOCB7/TCLKG/MOSIC/CTX0/IRQ1/AN009	B8
	IRQ2	P32/MTIOC0C/TIOCC0/TMO3/PO10/RTCOUT/RTCIC2/TXD6/TXD0/SMOSI6/SMOSI0/SSDA6/SSDA0/CTX0/IRQ2-DS	K2
		P12/MTIC5U/TMCI1/RXD2/SMISO2/SSCL2/SCL0[FM+]/IRQ2	N5
		PD2/D2[A2/D2]/MTIOC4D/TIOCA8/MISOC/CRX0/IRQ2/AN010	C8
	IRQ3	P33/MTIOC0D/TIOCD0/TMRI3/PO11/POE3#/RXD6/RXD0/SMISO6/SMISO0/SSCL6/SSCL0/CRX0/IRQ3-DS	K1
		P13/MTIOC0B/TIOCA5/TMO3/PO13/TXD2/SMOSI2/SSDA2/SDA0[FM+]/IRQ3/ADTRG#	M5
		PD3/D3[A3/D3]/TIOCB8/TCLKH/POE8#/RSPCKC/IRQ3/AN011	C9
	IRQ4	PB1/A9/MTIOC0C/MTIOC4C/TIOCB3/TMCI0/PO25/TXD4/TXD6/SMOSI4/SMOSI6/SSDA4/SSDA6/IRQ4-DS	L14
		P14/MTIOC3A/MTCLKA/TIOCB5/TCLKA/TMRI2/PO15/CTS1#/RTS1#/SS1#/CTX1/USB0_DPUPE/IRQ4	P4
		P34/MTIOC0A/TMCI3/PO12/POE2#/SCK6/SCK0/IRQ4	J3
		PD4/D4[A4/D4]/POE3#/SSLC0/IRQ4/AN012	B10
		PF5/IRQ4	E3
	IRQ5	PA4/A4/MTIC5U/MTCLKA/TIOCA1/TMRI0/PO20/TXD5/SMOSI5/SSDA5/SSLA0/IRQ5-DS	H12
		P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SCK3/SMISO1/SSCL1/CRX1-DS/IRQ5	N4
		PD5/D5[A5/D5]/MTIC5W/POE2#/SSLC1/IRQ5/AN013	D9
		PE5/D13[A13/D13]/MTIOC4C/MTIOC2B/TIOCB10/RSPCKB/IRQ5/AN3	D12
	IRQ6	PA3/A3/MTIOC0D/MTCLKD/TIOCD0/TCLKB/PO19/RXD5/SMISO5/SSCL5/IRQ6-DS	H13

		P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	R3
		PD6/D6[A6/D6]/MTIC5V/POE1#/SSLC2/IRQ6/AN6	A11
		PE6/D14[A14/D14]/TIOCA11/CTS4#/RTS4#/SS4#/MOSIB/IRQ6/AN4	E12
	IRQ7	PE2/D10[A10/D10]/MTIOC4A/TIOCA9/PO23/RXD12/SMISO12/SSCL12/RXDX12/SSLB3/MOSIB/IRQ7-DS/AN0	A15
		P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	P2
		PD7/D7[A7/D7]/MTIC5U/POE0#/SSLC3/IRQ7/AN7	D10
		PE7/D15[A15/D15]/TIOCB11/MISOB/IRQ7/AN5	D14
	IRQ8	P40/IRQ8-DS/AN000	B3
		P00/TMRI0/TXD6/SMOSI6/SSDA6/IRQ8/AN018	D4
		P20/MTIOC1A/TIOCB3/TMRI0/PO0/TXD0/SMOSI0/SSDA0/SDA1/IRQ8	R2
	IRQ9	P41/IRQ9-DS/AN001	B4
		P01/TMCI0/RXD6/SMISO6/SSCL6/IRQ9/AN019	D1
		P21/MTIOC1B/TIOCA3/TMCI0/PO1/RXD0/SMISO0/SSCL0/SCL1/IRQ9	R1
	IRQ10	P42/IRQ10-DS/AN002	A4
		P02/TMCI1/SCK6/IRQ10/AN020	D2
		P55/WAIT#/MTIOC4D/TMO3/CRX1/IRQ10	N7
	IRQ11	P43/IRQ11-DS/AN003	C4
		P03/IRQ11/DA0	D3
		PA1/A1/MTIOC0B/MTCLKC/TIOCB0/PO17/SCK5/SSLA2/IRQ11	G14
	IRQ12	P44/IRQ12-DS/AN004	D5
		PB0/A8/MTIC5W/TIOCA3/PO24/RXD4/RXD6/SMISO4/SMISO6/SSCL4/SSCL6/RSPCKA/IRQ12	K15
		PC1/A17/MTIOC3A/TCLKD/PO18/SCK5/SSLA2/SDA3/IRQ12	R15
	IRQ13	P45/IRQ13-DS/AN005	C5
		P05/IRQ13/DA1	B1
		PC6/A22/CS1#/MTIOC3C/MTCLKA/TIOCA6/TMCI2/PO30/RXD8/SMISO8/SSCL8/MOSIA/IRQ13	P11
	IRQ14	P46/IRQ14-DS/AN006	A5
		PC0/A16/MTIOC3C/TCLKC/PO17/CTS5#/RTS5#/SS5#/SSLA1/SCL3/IRQ14	N13
		PC7/A23/CS0#/MTIOC3A/MTCLKB/TIOCB6/TMO2/PO31/TXD8/SMOSI8/SSDA8/MISOA/IRQ14	N10
	IRQ15	P47/IRQ15-DS/AN007	B5
		P07/IRQ15/ADTRG0#	B2
		P67/CS7#/CRX2/IRQ15	E15
MTU0-5	MTCLKA	P14/MTIOC3A/MTCLKA/TIOCB5/TCLKA/TMRI2/PO15/CTS1#/RTS1#/SS1#/CTX1/USB0_DPUPE/IRQ4	P4
		P24/CS4#/MTIOC4A/MTCLKA/TIOCB4/TMRI1/PO4/SCK3	M3
		PA4/A4/MTIC5U/MTCLKA/TIOCA1/TMRI0/PO20/TXD5/SMOSI5/SSDA5/SSLA0/IRQ5-DS	H12
		PC6/A22/CS1#/MTIOC3C/MTCLKA/TIOCA6/TMCI2/PO30/RXD8/SMISO8/SSCL8/MOSIA/IRQ13	P11

	MTCLKB	P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SCK3/SMISO1/SSCL1/CRX1-DS/IRQ5	N4
		P25/CS5#/MTIOC4C/MTCLKB/TIOCA4/PO5/RXD3/SMISO3/SSCL3/ADTRG0#	L4
		PA6/A6/MTIC5V/MTCLKB/TIOCA2/TMCI3/PO22/POE2#/CTS5#/RTS5#/SS5#/MOSIA	J15
		PC7/A23/CS0#/MTIOC3A/MTCLKB/TIOCB6/TMO2/PO31/TXD8/SMOSI8/SSDA8/MISOA/IRQ14	N10
	MTCLKC	P22/MTIOC3B/MTCLKC/TIOCC3/TMO0/PO2/SCK0	N3
		PA1/A1/MTIOC0B/MTCLKC/TIOCB0/PO17/SCK5/SSLA2/IRQ11	G14
		PC4/A20/CS3#/MTIOC3D/MTCLKC/TIOCC6/TCLKE/TMCI1/PO25/POE0#/SCK5/CTS8#/RTS8#/SS8#/SSLA0	P12
	MTCLKD	P23/MTIOC3D/MTCLKD/TIOCD3/PO3/TXD3/CTS0#/RTS0#/SMOSI3/SS0#/SSDA3	N2
		PA3/A3/MTIOC0D/MTCLKD/TIOCD0/TCLKB/PO19/RXD5/SMISO5/SSCL5/IRQ6-DS	H13
		PC5/A21/CS2#/WAIT#/MTIOC3B/MTCLKD/TIOCD6/TCLKF/TMRI2/PO29/SCK8/RSPCKA	M10
MTU0	MTIOC0A	P34/MTIOC0A/TMCI3/PO12/POE2#/SCK6/SCK0/IRQ4	J3
		PB3/A11/MTIOC0A/MTIOC4A/TIOCD3/TCLKD/TMO0/PO27/POE3#/SCK4/SCK6	L13
	MTIOC0B	P13/MTIOC0B/TIOCA5/TMO3/PO13/TXD2/SMOSI2/SSDA2/SDA0[FM+]/IRQ3/ADTRG#	M5
		P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SCK3/SMISO1/SSCL1/CRX1-DS/IRQ5	N4
		PA1/A1/MTIOC0B/MTCLKC/TIOCB0/PO17/SCK5/SSLA2/IRQ11	G14
	MTIOC0C	P32/MTIOC0C/TIOCC0/TMO3/PO10/RTCOUT/RTCIC2/TXD6/TXD0/SMOSI6/SMOSI0/SSDA6/SSDA0/CTX0/IRQ2-DS	K2
		PB1/A9/MTIOC0C/MTIOC4C/TIOCB3/TMCI0/PO25/TXD4/TXD6/SMOSI4/SMOSI6/SSDA4/SSDA6/IRQ4-DS	L14
	MTIOC0D	P33/MTIOC0D/TIOCD0/TMRI3/PO11/POE3#/RXD6/RXD0/SMISO6/SMISO0/SSCL6/SSCL0/CRX0/IRQ3-DS	K1
		PA3/A3/MTIOC0D/MTCLKD/TIOCD0/TCLKB/PO19/RXD5/SMISO5/SSCL5/IRQ6-DS	H13
MTU1	MTIOC1A	P20/MTIOC1A/TIOCB3/TMRI0/PO0/TXD0/SMOSI0/SSDA0/SDA1/IRQ8	R2
		PE4/D12[A12/D12]/MTIOC4D/MTIOC1A/TIOCA10/PO28/SSLB0/AN2	C13
	MTIOC1B	P21/MTIOC1B/TIOCA3/TMCI0/PO1/RXD0/SMISO0/SSCL0/SCL1/IRQ9	R1
		PB5/A13/MTIOC2A/MTIOC1B/TIOCB4/TMRI1/PO29/POE1#/SCK9	M14
MTU2	MTIOC2A	P26/CS6#/MTIOC2A/TMO1/PO6/TXD1/CTS3#/RTS3#/SMOSI1/SS3#/SSDA1/MOSIB	M2
		PB5/A13/MTIOC2A/MTIOC1B/TIOCB4/TMRI1/PO29/POE1#/SCK9	M14
	MTIOC2B	P27/CS7#/MTIOC2B/TMCI3/PO7/SCK1/RSPCKB	M1
		PE5/D13[A13/D13]/MTIOC4C/MTIOC2B/TIOCB10/RSPCKB/IRQ5/AN3	D12
MTU3	MTIOC3A	P14/MTIOC3A/MTCLKA/TIOCB5/TCLKA/TMRI2/PO15/CTS1#/RTS1#/SS1#/CTX1/USB0_DPUPE/IRQ4	P4
		P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	P2

		PC1/A17/MTIOC3A/TCLKD/PO18/SCK5/SSLA2/SDA3/IRQ12	R15
		PC7/A23/CS0#/MTIOC3A/MTCLKB/TIOCB6/TMO2/PO31/TXD8/SMOSI8/SSDA8/MISOA/IRQ14	N10
	MTIOC3B	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	P2
		P22/MTIOC3B/MTCLKC/TIOCC3/TMO0/PO2/SCK0	N3
		P80/MTIOC3B/PO26/SCK10	R12
		PB7/A15/MTIOC3B/TIOCB5/PO31/TXD9/SMOSI9/SSDA9	M13
		PC5/A21/CS2#/WAIT#/MTIOC3B/MTCLKD/TIOCD6/TCLKF/TMRI2/PO29/SCK8/RSPCKA	M10
	MTIOC3C	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	R3
		P56/WR2#/BC2#/MTIOC3C/TIOCA1	M6
		PC0/A16/MTIOC3C/TCLKC/PO17/CTS5#/RTS5#/SS5#/SSLA1/SCL3/IRQ14	N13
		PC6/A22/CS1#/MTIOC3C/MTCLKA/TIOCA6/TMCI2/PO30/RXD8/SMISO8/SSCL8/MOSIA/IRQ13	P11
		PJ3/MTIOC3C/CTS6#/RTS6#/CTS0#/RTS0#/SS6#/SS0#	F3
	MTIOC3D	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	R3
		P23/MTIOC3D/MTCLKD/TIOCD3/PO3/TXD3/CTS0#/RTS0#/SMOSI3/SS0#/SSDA3	N2
		P81/MTIOC3D/PO27/RXD10/SMISO10/SSCL10	M11
		PB6/A14/MTIOC3D/TIOCA5/PO30/RXD9/SMISO9/SSCL9	L12
		PC4/A20/CS3#/MTIOC3D/MTCLKC/TIOCC6/TCLKE/TMCI1/PO25/POE0#/SCK5/CTS8#/RTS8#/SS8#/SSLA0	P12
MTU4	MTIOC4A	P24/CS4#/MTIOC4A/MTCLKA/TIOCB4/TMRI1/PO4/SCK3	M3
		P82/MTIOC4A/PO28/TXD10/SMOSI10/SSDA10	N11
		PA0/A0/BC0#/MTIOC4A/TIOCA0/PO16/SSLA1	F14
		PB3/A11/MTIOC0A/MTIOC4A/TIOCD3/TCLKD/TMO0/PO27/POE3#/SCK4/SCK6	L13
		PE2/D10[A10/D10]/MTIOC4A/TIOCA9/PO23/RXD12/SMISO12/SSCL12/RXDX12/SSLB3/MOSIB/IRQ7-DS/AN0	A15
	MTIOC4B	P30/MTIOC4B/TMRI3/PO8/RTCIC0/POE8#/RXD1/SMISO1/SSCL1/MISOB/IRQ0-DS	L2
		P54/ALE/MTIOC4B/TMCI1/CTS2#/RTS2#/SS2#/CTX1	M7
		PC2/A18/MTIOC4B/TCLKA/PO21/RXD5/SMISO5/SSCL5/SSLA3/IERXD	P13
		PD1/D1[A1/D1]/MTIOC4B/TIOCB7/TCLKG/MOSIC/CTX0/IRQ1/AN009	B8
		PE3/D11[A11/D11]/MTIOC4B/TIOCB9/PO26/POE8#/CTS12#/RTS12#/SS12#/MISOB/AN1	B15
	MTIOC4C	P25/CS5#/MTIOC4C/MTCLKB/TIOCA4/PO5/RXD3/SMISO3/SSCL3/ADTRG0#	L4
		P83/MTIOC4C/CTS10#/RTS10#/SS10#	P10
		PB1/A9/MTIOC0C/MTIOC4C/TIOCB3/TMCI0/PO25/TXD4/TXD6/SMOSI4/SMOSI6/SSDA4/SSDA6/IRQ4-DS	L14

		PE1/D9[A9/D9]/MTIOC4C/TIOCD9/PO18/TXD12/SMOSI12/SSDA12/TXDX12/SIOX12/SSLB2/RSPCKB/ANEX1	A14
		PE5/D13[A13/D13]/MTIOC4C/MTIOC2B/TIOCB10/RSPCKB/IRQ5/AN3	D12
	MTIOC4D	P31/MTIOC4D/TMCI2/PO9/RTCIC1/CTS1#/RTS1#/SS1#/SSLB0/IRQ1-DS	L1
		P55/WAIT#/MTIOC4D/TMO3/CRX1/IRQ10	N7
		PC3/A19/MTIOC4D/TCLKB/PO24/TXD5/SMOSI5/SSDA5/IETXD	N12
		PD2/D2[A2/D2]/MTIOC4D/TIOCA8/MISOC/CRX0/IRQ2/AN010	C8
		PE4/D12[A12/D12]/MTIOC4D/MTIOC1A/TIOCA10/PO28/SSLB0/AN2	C13
MTU5	MTIC5U	P12/MTIC5U/TMCI1/RXD2/SMISO2/SSCL2/SCL0[FM+]/IRQ2	N5
		PA4/A4/MTIC5U/MTCLKA/TIOCA1/TMRI0/PO20/TXD5/SMOSI5/SSDA5/SSLA0/IRQ5-DS	H12
		PD7/D7[A7/D7]/MTIC5U/POE0#/SSLC3/IRQ7/AN7	D10
	MTIC5V	P11/MTIC5V/TMCI3/SCK2/IRQ1	R5
		PA6/A6/MTIC5V/MTCLKB/TIOCA2/TMCI3/PO22/POE2#/CTS5#/RTS5#/SS5#/MOSIA	J15
		PD6/D6[A6/D6]/MTIC5V/POE1#/SSLC2/IRQ6/AN6	A11
	MTIC5W	P10/MTIC5W/TMRI3/IRQ0	P5
		PB0/A8/MTIC5W/TIOCA3/PO24/RXD4/RXD6/SMISO4/SMISO6/SSCL4/SSCL6/RSPCKA/IRQ12	K15
		PD5/D5[A5/D5]/MTIC5W/POE2#/SSLC1/IRQ5/AN013	D9
POE	POE0#	PC4/A20/CS3#/MTIOC3D/MTCLKC/TIOCC6/TCLKE/TMCI1/PO25/POE0#/SCK5/CTS8#/RTS8#/SS8#/SSLA0	P12
		PD7/D7[A7/D7]/MTIC5U/POE0#/SSLC3/IRQ7/AN7	D10
	POE1#	PB5/A13/MTIOC2A/MTIOC1B/TIOCB4/TMRI1/PO29/POE1#/SCK9	M14
		PD6/D6[A6/D6]/MTIC5V/POE1#/SSLC2/IRQ6/AN6	A11
	POE2#	P34/MTIOC0A/TMCI3/PO12/POE2#/SCK6/SCK0/IRQ4	J3
		PA6/A6/MTIC5V/MTCLKB/TIOCA2/TMCI3/PO22/POE2#/CTS5#/RTS5#/SS5#/MOSIA	J15
		PD5/D5[A5/D5]/MTIC5W/POE2#/SSLC1/IRQ5/AN013	D9
	POE3#	P33/MTIOC0D/TIOCD0/TMRI3/PO11/POE3#/RXD6/RXD0/SMISO6/SMISO0/SSCL6/SSCL0/CRX0/IRQ3-DS	K1
		PB3/A11/MTIOC0A/MTIOC4A/TIOCD3/TCLKD/TMO0/PO27/POE3#/SCK4/SCK6	L13
		PD4/D4[A4/D4]/POE3#/SSLC0/IRQ4/AN012	B10
	POE8#	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	P2
		P30/MTIOC4B/TMRI3/PO8/RTCIC0/POE8#/RXD1/SMISO1/SSCL1/MISOB/IRQ0-DS	L2
		PD3/D3[A3/D3]/TIOCB8/TCLKH/POE8#/RSPCKC/IRQ3/AN011	C9
		PE3/D11[A11/D11]/MTIOC4B/TIOCB9/PO26/POE8#/CTS12#/RTS12#/SS12#/MISOB/AN1	B15
TPU0-5	TCLKA	P14/MTIOC3A/MTCLKA/TIOCB5/TCLKA/TMRI2/PO15/CTS1#/RTS1#/SS1#/CTX1/USB0_DPUPE/IRQ4	P4
		PC2/A18/MTIOC4B/TCLKA/PO21/RXD5/SMISO5/SSCL5/SSLA3/IERXD	P13
	TCLKB	P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SCK3/SMISO1/SSCL1/CRX1-DS/IRQ5	N4

		PA3/A3/MTIOC0D/MTCLKD/TIOCD0/TCLKB/PO19/RXD5/SMISO5/SSCL5/IRQ6-DS	H13
		PC3/A19/MTIOC4D/TCLKB/PO24/TXD5/SMOSI5/SSDA5/IETXD	N12
	TCLKC	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	R3
		PB2/A10/TIOCC3/TCLKC/PO26/CTS4#/RTS4#/CTS6#/RTS6#/SS4#/SS6#	K12
		PC0/A16/MTIOC3C/TCLKC/PO17/CTS5#/RTS5#/SS5#/SSLA1/SCL3/IRQ14	N13
	TCLKD	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	P2
		PB3/A11/MTIOC0A/MTIOC4A/TIOCD3/TCLKD/TMO0/PO27/POE3#/SCK4/SCK6	L13
		PC1/A17/MTIOC3A/TCLKD/PO18/SCK5/SSLA2/SDA3/IRQ12	R15
TPU0	TIOCA0	P86/TIOCA0	M4
		PA0/A0/BC0#/MTIOC4A/TIOCA0/PO16/SSLA1	F14
	TIOCB0	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	P2
		PA1/A1/MTIOC0B/MTCLKC/TIOCB0/PO17/SCK5/SSLA2/IRQ11	G14
	TIOCD0	P33/MTIOC0D/TIOCD0/TMRI3/PO11/POE3#/RXD6/RXD0/SMISO6/SMISO0/SSCL6/SSCL0/CRX0/IRQ3-DS	K1
		PA3/A3/MTIOC0D/MTCLKD/TIOCD0/TCLKB/PO19/RXD5/SMISO5/SSCL5/IRQ6-DS	H13
TPU1	TIOCA1	P56/WR2#/BC2#/MTIOC3C/TIOCA1	M6
		PA4/A4/MTIC5U/MTCLKA/TIOCA1/TMRI0/PO20/TXD5/SMOSI5/SSDA5/SSLA0/IRQ5-DS	H12
	TIOCB1	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	R3
		PA5/A5/TIOCB1/PO21/RSPCKA	J12
TPU2	TIOCA2	P87/TIOCA2	P3
		PA6/A6/MTIC5V/MTCLKB/TIOCA2/TMCI3/PO22/POE2#/CTS5#/RTS5#/SS5#/MOSIA	J15
	TIOCB2	P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SCK3/SMISO1/SSCL1/CRX1-DS/IRQ5	N4
		PA7/A7/TIOCB2/PO23/MISOA	J14
TPU3	TIOCA3	P21/MTIOC1B/TIOCA3/TMCI0/PO1/RXD0/SMISO0/SSCL0/SCL1/IRQ9	R1
		PB0/A8/MTIC5W/TIOCA3/PO24/RXD4/RXD6/SMISO4/SMISO6/SSCL4/SSCL6/RSPCKA/IRQ12	K15
	TIOCB3	P20/MTIOC1A/TIOCB3/TMRI0/PO0/TXD0/SMOSI0/SSDA0/SDA1/IRQ8	R2
		PB1/A9/MTIOC0C/MTIOC4C/TIOCB3/TMCI0/PO25/TXD4/TXD6/SMOSI4/SMOSI6/SSDA4/SSDA6/IRQ4-DS	L14
	TIOCC3	P22/MTIOC3B/MTCLKC/TIOCC3/TMO0/PO2/SCK0	N3
		PB2/A10/TIOCC3/TCLKC/PO26/CTS4#/RTS4#/CTS6#/RTS6#/SS4#/SS6#	K12
	TIOCD3	P23/MTIOC3D/MTCLKD/TIOCD3/PO3/TXD3/CTS0#/RTS0#/SMOSI3/SS	N2

		0#/SSDA3	
		PB3/A11/MTIOC0A/MTIOC4A/TIOCD3/TCLKD/TMO0/PO27/POE3#/SCK4/SCK6	L13
TPU4	TIOCA4	P25/CS5#/MTIOC4C/MTCLKB/TIOCA4/PO5/RXD3/SMISO3/SSCL3/ADTRG0#	L4
		PB4/A12/TIOCA4/PO28/CTS9#/RTS9#/SS9#	M15
	TIOCB4	P24/CS4#/MTIOC4A/MTCLKA/TIOCB4/TMRI1/PO4/SCK3	M3
		PB5/A13/MTIOC2A/MTIOC1B/TIOCB4/TMRI1/PO29/POE1#/SCK9	M14
TPU5	TIOCA5	P13/MTIOC0B/TIOCA5/TMO3/PO13/TXD2/SMOSI2/SSDA2/SDA0[FM+]/IRQ3/ADTRG#	M5
		PB6/A14/MTIOC3D/TIOCA5/PO30/RXD9/SMISO9/SSCL9	L12
	TIOCB5	P14/MTIOC3A/MTCLKA/TIOCB5/TCLKA/TMRI2/PO15/CTS1#/RTS1#/SS1#/CTX1/USB0_DPUPE/IRQ4	P4
		PB7/A15/MTIOC3B/TIOCB5/PO31/TXD9/SMOSI9/SSDA9	M13
PPG Group3	PO13	P13/MTIOC0B/TIOCA5/TMO3/PO13/TXD2/SMOSI2/SSDA2/SDA0[FM+]/IRQ3/ADTRG#	M5
		P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SCK3/SMISO1/SSCL1/CRX1-DS/IRQ5	N4
	PO15	P14/MTIOC3A/MTCLKA/TIOCB5/TCLKA/TMRI2/PO15/CTS1#/RTS1#/SS1#/CTX1/USB0_DPUPE/IRQ4	P4
		P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	P2
PPG Group4	PO16	P73/CS3#/PO16	N14
		PA0/A0/BC0#/MTIOC4A/TIOCA0/PO16/SSLA1	F14
	PO17	PA1/A1/MTIOC0B/MTCLKC/TIOCB0/PO17/SCK5/SSLA2/IRQ11	G14
		PC0/A16/MTIOC3C/TCLKC/PO17/CTS5#/RTS5#/SS5#/SSLA1/SCL3/IRQ14	N13
	PO18	PA2/A2/PO18/RXD5/SMISO5/SSCL5/SSLA3	H14
		PC1/A17/MTIOC3A/TCLKD/PO18/SCK5/SSLA2/SDA3/IRQ12	R15
		PE1/D9[A9/D9]/MTIOC4C/TIOCD9/PO18/TXD12/SMOSI12/SSDA12/TXDX12/SIOX12/SSLB2/RSPCKB/ANEX1	A14
	PO19	P74/CS4#/PO19/CTS11#/RTS11#/SS11#	R14
		PA3/A3/MTIOC0D/MTCLKD/TIOCD0/TCLKB/PO19/RXD5/SMISO5/SSCL5/IRQ6-DS	H13
PPG Group5	PO20	P75/CS5#/PO20/SCK11	P14
		PA4/A4/MTIC5U/MTCLKA/TIOCA1/TMRI0/PO20/TXD5/SMOSI5/SSDA5/SSLA0/IRQ5-DS	H12
	PO21	PA5/A5/TIOCB1/PO21/RSPCKA	J12
		PC2/A18/MTIOC4B/TCLKA/PO21/RXD5/SMISO5/SSCL5/SSLA3/IERXD	P13
	PO22	P76/CS6#/PO22/RXD11/SMISO11/SSCL11	R13
		PA6/A6/MTIC5V/MTCLKB/TIOCA2/TMCI3/PO22/POE2#/CTS5#/RTS5#/SS5#/MOSIA	J15
	PO23	P77/CS7#/PO23/TXD11/SMOSI11/SSDA11	M12
		PA7/A7/TIOCB2/PO23/MISOA	J14
		PE2/D10[A10/D10]/MTIOC4A/TIOCA9/PO23/RXD12/SMISO12/SSCL12/RXDX12/SSLB3/MOSIB/IRQ7-DS/AN0	A15
PPG	PO24	PB0/A8/MTIC5W/TIOCA3/PO24/RXD4/RXD6/SMISO4/SMISO6/SSCL4/	K15

Group6		SSCL6/RSPCKA/IRQ12	
		PC3/A19/MTIOC4D/TCLKB/PO24/TXD5/SMOSI5/SSDA5/IETXD	N12
	PO25	PB1/A9/MTIOC0C/MTIOC4C/TIOCB3/TMCI0/PO25/TXD4/TXD6/SMOSI4/SMOSI6/SSDA4/SSDA6/IRQ4-DS	L14
		PC4/A20/CS3#/MTIOC3D/MTCLKC/TIOCC6/TCLKE/TMCI1/PO25/POE0#/SCK5/CTS8#/RTS8#/SS8#/SSLA0	P12
	PO26	P80/MTIOC3B/PO26/SCK10	R12
		PB2/A10/TIOCC3/TCLKC/PO26/CTS4#/RTS4#/CTS6#/RTS6#/SS4#/SS6#	K12
		PE3/D11[A11/D11]/MTIOC4B/TIOCB9/PO26/POE8#/CTS12#/RTS12#/SS12#/MISOB/AN1	B15
	PO27	P81/MTIOC3D/PO27/RXD10/SMISO10/SSCL10	M11
		PB3/A11/MTIOC0A/MTIOC4A/TIOCD3/TCLKD/TMO0/PO27/POE3#/SCK4/SCK6	L13
PPG Group7	PO28	P82/MTIOC4A/PO28/TXD10/SMOSI10/SSDA10	N11
		PB4/A12/TIOCA4/PO28/CTS9#/RTS9#/SS9#	M15
		PE4/D12[A12/D12]/MTIOC4D/MTIOC1A/TIOCA10/PO28/SSLB0/AN2	C13
	PO29	PB5/A13/MTIOC2A/MTIOC1B/TIOCB4/TMRI1/PO29/POE1#/SCK9	M14
		PC5/A21/CS2#/WAIT#/MTIOC3B/MTCLKD/TIOCD6/TCLKF/TMRI2/PO29/SCK8/RSPCKA	M10
	PO30	PB6/A14/MTIOC3D/TIOCA5/PO30/RXD9/SMISO9/SSCL9	L12
		PC6/A22/CS1#/MTIOC3C/MTCLKA/TIOCA6/TMCI2/PO30/RXD8/SMISO8/SSCL8/MOSIA/IRQ13	P11
	PO31	PB7/A15/MTIOC3B/TIOCB5/PO31/TXD9/SMOSI9/SSDA9	M13
		PC7/A23/CS0#/MTIOC3A/MTCLKB/TIOCB6/TMO2/PO31/TXD8/SMOSI8/SSDA8/MISOA/IRQ14	N10
TMR0	TMO0	P22/MTIOC3B/MTCLKC/TIOCC3/TMO0/PO2/SCK0	N3
		PB3/A11/MTIOC0A/MTIOC4A/TIOCD3/TCLKD/TMO0/PO27/POE3#/SCK4/SCK6	L13
	TMCI0	P01/TMCI0/RXD6/SMISO6/SSCL6/IRQ9/AN019	D1
		P21/MTIOC1B/TIOCA3/TMCI0/PO1/RXD0/SMISO0/SSCL0/SCL1/IRQ9	R1
		PB1/A9/MTIOC0C/MTIOC4C/TIOCB3/TMCI0/PO25/TXD4/TXD6/SMOSI4/SMOSI6/SSDA4/SSDA6/IRQ4-DS	L14
	TMRI0	P00/TMRI0/TXD6/SMOSI6/SSDA6/IRQ8/AN018	D4
		P20/MTIOC1A/TIOCB3/TMRI0/PO0/TXD0/SMOSI0/SSDA0/SDA1/IRQ8	R2
		PA4/A4/MTIC5U/MTCLKA/TIOCA1/TMRI0/PO20/TXD5/SMOSI5/SSDA5/SSLA0/IRQ5-DS	H12
TMR1	TMO1	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	P2
		P26/CS6#/MTIOC2A/TMO1/PO6/TXD1/CTS3#/RTS3#/SMOSI1/SS3#/SSDA1/MOSIB	M2
	TMCI1	P02/TMCI1/SCK6/IRQ10/AN020	D2
		P12/MTIC5U/TMCI1/RXD2/SMISO2/SSCL2/SCL0[FM+]/IRQ2	N5
		P54/ALE/MTIOC4B/TMCI1/CTS2#/RTS2#/SS2#/CTX1	M7
		PC4/A20/CS3#/MTIOC3D/MTCLKC/TIOCC6/TCLKE/TMCI1/PO25/POE0#/SCK5/CTS8#/RTS8#/SS8#/SSLA0	P12
	TMRI1	P24/CS4#/MTIOC4A/MTCLKA/TIOCB4/TMRI1/PO4/SCK3	M3

		PB5/A13/MTIOC2A/MTIOC1B/TIOCB4/TMRI1/PO29/POE1#/SCK9	M14
TMR2	TMO2	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	R3
		PC7/A23/CS0#/MTIOC3A/MTCLKB/TIOCB6/TMO2/PO31/TXD8/SMOSI8/SSDA8/MISOA/IRQ14	N10
	TMCI2	P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SCK3/SMISO1/SSCL1/CRX1-DS/IRQ5	N4
		P31/MTIOC4D/TMCI2/PO9/RTCIC1/CTS1#/RTS1#/SS1#/SSLB0/IRQ1-DS	L1
		PC6/A22/CS1#/MTIOC3C/MTCLKA/TIOCA6/TMCI2/PO30/RXD8/SMISO8/SSCL8/MOSIA/IRQ13	P11
	TMRI2	P14/MTIOC3A/MTCLKA/TIOCB5/TCLKA/TMRI2/PO15/CTS1#/RTS1#/SS1#/CTX1/USB0_DPUPE/IRQ4	P4
		PC5/A21/CS2#/WAIT#/MTIOC3B/MTCLKD/TIOCD6/TCLKF/TMRI2/PO29/SCK8/RSPCKA	M10
TMR3	TMO3	P13/MTIOC0B/TIOCA5/TMO3/PO13/TXD2/SMOSI2/SSDA2/SDA0[FM+]/IRQ3/ADTRG#	M5
		P32/MTIOC0C/TIOCC0/TMO3/PO10/RTCOUT/RTCIC2/TXD6/TXD0/SMOSI6/SMOSI0/SSDA6/SSDA0/CTX0/IRQ2-DS	K2
		P55/WAIT#/MTIOC4D/TMO3/CRX1/IRQ10	N7
	TMCI3	P11/MTIC5V/TMCI3/SCK2/IRQ1	R5
		P27/CS7#/MTIOC2B/TMCI3/PO7/SCK1/RSPCKB	M1
		P34/MTIOC0A/TMCI3/PO12/POE2#/SCK6/SCK0/IRQ4	J3
		PA6/A6/MTIC5V/MTCLKB/TIOCA2/TMCI3/PO22/POE2#/CTS5#/RTS5#/SS5#/MOSIA	J15
	TMRI3	P10/MTIC5W/TMRI3/IRQ0	P5
		P30/MTIOC4B/TMRI3/PO8/RTCIC0/POE8#/RXD1/SMISO1/SSCL1/MISOB/IRQ0-DS	L2
		P33/MTIOC0D/TIOCD0/TMRI3/PO11/POE3#/RXD6/RXD0/SMISO6/SMISO0/SSCL6/SSCL0/CRX0/IRQ3-DS	K1
SCI0	RXD0 SSCL0 SMISO0	P21/MTIOC1B/TIOCA3/TMCI0/PO1/RXD0/SMISO0/SSCL0/SCL1/IRQ9	R1
		P33/MTIOC0D/TIOCD0/TMRI3/PO11/POE3#/RXD6/RXD0/SMISO6/SMISO0/SSCL6/SSCL0/CRX0/IRQ3-DS	K1
	TXD0 SSDA0 SMOSI0	P20/MTIOC1A/TIOCB3/TMRI0/PO0/TXD0/SMOSI0/SSDA0/SDA1/IRQ8	R2
		P32/MTIOC0C/TIOCC0/TMO3/PO10/RTCOUT/RTCIC2/TXD6/TXD0/SMOSI6/SMOSI0/SSDA6/SSDA0/CTX0/IRQ2-DS	K2
	SCK0	P22/MTIOC3B/MTCLKC/TIOCC3/TMO0/PO2/SCK0	N3
		P34/MTIOC0A/TMCI3/PO12/POE2#/SCK6/SCK0/IRQ4	J3
	CTS0# RTS0# SS0#	P23/MTIOC3D/MTCLKD/TIOCD3/PO3/TXD3/CTS0#/RTS0#/SMOSI3/SS0#/SSDA3	N2
		PJ3/MTIOC3C/CTS6#/RTS6#/CTS0#/RTS0#/SS6#/SS0#	F3
SCI1	RXD1 SSCL1 SMISO1	P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SCK3/SMISO1/SSCL1/CRX1-DS/IRQ5	N4
		P30/MTIOC4B/TMRI3/PO8/RTCIC0/POE8#/RXD1/SMISO1/SSCL1/MISOB/IRQ0-DS	L2
		TDI/PF2/RXD1/SMISO1/SSCL1	K3

	TXD1 SSDA1 SMOSI1	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	R3
		P26/CS6#/MTIOC2A/TMO1/PO6/TXD1/CTS3#/RTS3#/SMOSI1/SS3#/SSDA1/MOSIB	M2
		TDO/PF0/TXD1/SMOSI1/SSDA1	L3
	SCK1	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	P2
		P27/CS7#/MTIOC2B/TMCI3/PO7/SCK1/RSPCKB	M1
		TCK/FINEC/PF1/SCK1	K4
	CTS1# RTS1# SS1#	P14/MTIOC3A/MTCLKA/TIOCB5/TCLKA/TMRI2/PO15/CTS1#/RTS1#/SS1#/CTX1/USB0_DPUPE/IRQ4	P4
		P31/MTIOC4D/TMCI2/PO9/RTCIC1/CTS1#/RTS1#/SS1#/SSLB0/IRQ1-DS	L1
SCI2	RXD2 SSCL2 SMISO2	P12/MTIC5U/TMCI1/RXD2/SMISO2/SSCL2/SCL0[FM+]/IRQ2	N5
		P52/RD#/RXD2/SMISO2/SSCL2/SSLB3	P9
	TXD2 SSDA2 SMOSI2	P13/MTIOC0B/TIOCA5/TMO3/PO13/TXD2/SMOSI2/SSDA2/SDA0[FM+]/IRQ3/ADTRG#	M5
		P50/WR0#/WR#/TXD2/SMOSI2/SSDA2/SSLB1	M9
	SCK2	P11/MTIC5V/TMCI3/SCK2/IRQ1	R5
		P51/WR1#/BC1#/WAIT#/SCK2/SSLB2	N9
SCI3	RXD3 SSCL3 SMISO3	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	R3
		P25/CS5#/MTIOC4C/MTCLKB/TIOCA4/PO5/RXD3/SMISO3/SSCL3/ADTRG0#	L4
	TXD3 SSDA3 SMOSI3	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	P2
		P23/MTIOC3D/MTCLKD/TIOCD3/PO3/TXD3/CTS0#/RTS0#/SMOSI3/SS0#/SSDA3	N2
	SCK3	P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SCK3/SMISO1/SSCL1/CRX1-DS/IRQ5	N4
		P24/CS4#/MTIOC4A/MTCLKA/TIOCB4/TMRI1/PO4/SCK3	M3
SCI4	RXD4 SSCL4 SMISO4	PB0/A8/MTIC5W/TIOCA3/PO24/RXD4/RXD6/SMISO4/SMISO6/SSCL4/SSCL6/RSPCKA/IRQ12	K15
		PK4/RXD4/SMISO4/SSCL4	C14
	TXD4 SSDA4 SMOSI4	PB1/A9/MTIOC0C/MTIOC4C/TIOCB3/TMCI0/PO25/TXD4/TXD6/SMOSI4/SMOSI6/SSDA4/SSDA6/IRQ4-DS	L14
		PK5/TXD4/SMOSI4/SSDA4	D13
	SCK4	P70/SCK4	C15
		PB3/A11/MTIOC0A/MTIOC4A/TIOCD3/TCLKD/TMO0/PO27/POE3#/SCK4/SCK6	L13
	CTS4# RTS4# SS4#	PB2/A10/TIOCC3/TCLKC/PO26/CTS4#/RTS4#/CTS6#/RTS6#/SS4#/SS6#	K12
		PE6/D14[A14/D14]/TIOCA11/CTS4#/RTS4#/SS4#/MOSIB/IRQ6/AN4	E12

SCI5	RXD5 SSCL5 SMISO5	PA2/A2/PO18/RXD5/SMISO5/SSCL5/SSLA3	H14
		PA3/A3/MTIOC0D/MTCLKD/TIOCD0/TCLKB/PO19/RXD5/SMISO5/SSCL5/IRQ6-DS	H13
		PC2/A18/MTIOC4B/TCLKA/PO21/RXD5/SMISO5/SSCL5/SSLA3/IERXD	P13
	TXD5 SSDA5 SMOSI5	PA4/A4/MTIC5U/MTCLKA/TIOCA1/TMRI0/PO20/TXD5/SMOSI5/SSDA5/SSLA0/IRQ5-DS	H12
		PC3/A19/MTIOC4D/TCLKB/PO24/TXD5/SMOSI5/SSDA5/IETXD	N12
	SCK5	PA1/A1/MTIOC0B/MTCLKC/TIOCB0/PO17/SCK5/SSLA2/IRQ11	G14
		PC1/A17/MTIOC3A/TCLKD/PO18/SCK5/SSLA2/SDA3/IRQ12	R15
		PC4/A20/CS3#/MTIOC3D/MTCLKC/TIOCC6/TCLKE/TMCI1/PO25/POE0#/SCK5/CTS8#/RTS8#/SS8#/SSLA0	P12
	CTS5# RTS5# SS5#	PA6/A6/MTIC5V/MTCLKB/TIOCA2/TMCI3/PO22/POE2#/CTS5#/RTS5#/SS5#/MOSIA	J15
		PC0/A16/MTIOC3C/TCLKC/PO17/CTS5#/RTS5#/SS5#/SSLA1/SCL3/IRQ14	N13
SCI6	RXD6 SSCL6 SMISO6	P01/TMCI0/RXD6/SMISO6/SSCL6/IRQ9/AN019	D1
		P33/MTIOC0D/TIOCD0/TMRI3/PO11/POE3#/RXD6/RXD0/SMISO6/SMISO0/SSCL6/SSCL0/CRX0/IRQ3-DS	K1
		PB0/A8/MTIC5W/TIOCA3/PO24/RXD4/RXD6/SMISO4/SMISO6/SSCL4/SSCL6/RSPCKA/IRQ12	K15
	TXD6 SSDA6 SMOSI6	P00/TMRI0/TXD6/SMOSI6/SSDA6/IRQ8/AN018	D4
		P32/MTIOC0C/TIOCC0/TMO3/PO10/RTCOUT/RTCIC2/TXD6/TXD0/SMOSI6/SMOSI0/SSDA6/SSDA0/CTX0/IRQ2-DS	K2
		PB1/A9/MTIOC0C/MTIOC4C/TIOCB3/TMCI0/PO25/TXD4/TXD6/SMOSI4/SMOSI6/SSDA4/SSDA6/IRQ4-DS	L14
	SCK6	P02/TMCI1/SCK6/IRQ10/AN020	D2
		P34/MTIOC0A/TMCI3/PO12/POE2#/SCK6/SCK0/IRQ4	J3
		PB3/A11/MTIOC0A/MTIOC4A/TIOCD3/TCLKD/TMO0/PO27/POE3#/SCK4/SCK6	L13
	CTS6# RTS6# SS6#	PB2/A10/TIOCC3/TCLKC/PO26/CTS4#/RTS4#/CTS6#/RTS6#/SS4#/SS6#	K12
		PJ3/MTIOC3C/CTS6#/RTS6#/CTS0#/RTS0#/SS6#/SS0#	F3
SCI9	RXD9 SSCL9 SMISO9	PB6/A14/MTIOC3D/TIOCA5/PO30/RXD9/SMISO9/SSCL9	L12
		PK3/RXD9/SMISO9/SSCL9	B12
	TXD9 SSDA9 SMOSI9	PB7/A15/MTIOC3B/TIOCB5/PO31/TXD9/SMOSI9/SSDA9	M13
		PK2/TXD9/SMOSI9/SSDA9	C11
	SCK9	P60/CS0#/SCK9	A12
		PB5/A13/MTIOC2A/MTIOC1B/TIOCB4/TMRI1/PO29/POE1#/SCK9	M14
	CTS9# RTS9# SS9#	P61/CS1#/CTS9#/RTS9#/SS9#	D11
		PB4/A12/TIOCA4/PO28/CTS9#/RTS9#/SS9#	M15
RSPI0	RSPCKA	PA5/A5/TIOCB1/PO21/RSPCKA	J12
		PB0/A8/MTIC5W/TIOCA3/PO24/RXD4/RXD6/SMISO4/SMISO6/SSCL4/SSCL6/RSPCKA/IRQ12	K15
		PC5/A21/CS2#/WAIT#/MTIOC3B/MTCLKD/TIOCD6/TCLKF/TMRI2/PO2	M10

		9/SCK8/RSPCKA	
	MOSIA	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	R3
		PA6/A6/MTIC5V/MTCLKB/TIOCA2/TMCI3/PO22/POE2#/CTS5#/RTS5#/SS5#/MOSIA	J15
		PC6/A22/CS1#/MTIOC3C/MTCLKA/TIOCA6/TMCI2/PO30/RXD8/SMISO8/SSCL8/MOSIA/IRQ13	P11
	MISOA	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	P2
		PA7/A7/TIOCB2/PO23/MISOA	J14
		PC7/A23/CS0#/MTIOC3A/MTCLKB/TIOCB6/TMO2/PO31/TXD8/SMOSI8/SSDA8/MISOA/IRQ14	N10
	SSLA0	PA4/A4/MTIC5U/MTCLKA/TIOCA1/TMRI0/PO20/TXD5/SMOSI5/SSDA5/SSLA0/IRQ5-DS	H12
		PC4/A20/CS3#/MTIOC3D/MTCLKC/TIOCC6/TCLKE/TMCI1/PO25/POE0#/SCK5/CTS8#/RTS8#/SS8#/SSLA0	P12
	SSLA1	PA0/A0/BC0#/MTIOC4A/TIOCA0/PO16/SSLA1	F14
		PC0/A16/MTIOC3C/TCLKC/PO17/CTS5#/RTS5#/SS5#/SSLA1/SCL3/IRQ14	N13
	SSLA2	PA1/A1/MTIOC0B/MTCLKC/TIOCB0/PO17/SCK5/SSLA2/IRQ11	G14
		PC1/A17/MTIOC3A/TCLKD/PO18/SCK5/SSLA2/SDA3/IRQ12	R15
	SSLA3	PA2/A2/PO18/RXD5/SMISO5/SSCL5/SSLA3	H14
		PC2/A18/MTIOC4B/TCLKA/PO21/RXD5/SMISO5/SSCL5/SSLA3/IERXD	P13
RSPI1	RSPCKB	P27/CS7#/MTIOC2B/TMCI3/PO7/SCK1/RSPCKB	M1
		PE1/D9[A9/D9]/MTIOC4C/TIOCD9/PO18/TXD12/SMOSI12/SSDA12/TXDX12/SIOX12/SSLB2/RSPCKB/ANEX1	A14
		PE5/D13[A13/D13]/MTIOC4C/MTIOC2B/TIOCB10/RSPCKB/IRQ5/AN3	D12
	MOSIB	P26/CS6#/MTIOC2A/TMO1/PO6/TXD1/CTS3#/RTS3#/SMOSI1/SS3#/SSDA1/MOSIB	M2
		PE2/D10[A10/D10]/MTIOC4A/TIOCA9/PO23/RXD12/SMISO12/SSCL12/RXDX12/SSLB3/MOSIB/IRQ7-DS/AN0	A15
		PE6/D14[A14/D14]/TIOCA11/CTS4#/RTS4#/SS4#/MOSIB/IRQ6/AN4	E12
	MISOB	P30/MTIOC4B/TMRI3/PO8/RTCIC0/POE8#/RXD1/SMISO1/SSCL1/MISOB/IRQ0-DS	L2
		PE3/D11[A11/D11]/MTIOC4B/TIOCB9/PO26/POE8#/CTS12#/RTS12#/SS12#/MISOB/AN1	B15
		PE7/D15[A15/D15]/TIOCB11/MISOB/IRQ7/AN5	D14
	SSLB0	P31/MTIOC4D/TMCI2/PO9/RTCIC1/CTS1#/RTS1#/SS1#/SSLB0/IRQ1-DS	L1
		PE4/D12[A12/D12]/MTIOC4D/MTIOC1A/TIOCA10/PO28/SSLB0/AN2	C13
	SSLB1	P50/WR0#/WR#/TXD2/SMOSI2/SSDA2/SSLB1	M9
		PE0/D8[A8/D8]/TIOCC9/SCK12/SSLB1/ANEX0	B14
	SSLB2	P51/WR1#/BC1#/WAIT#/SCK2/SSLB2	N9
		PE1/D9[A9/D9]/MTIOC4C/TIOCD9/PO18/TXD12/SMOSI12/SSDA12/TXDX12/SIOX12/SSLB2/RSPCKB/ANEX1	A14
	SSLB3	P52/RD#/RXD2/SMISO2/SSCL2/SSLB3	P9

		PE2/D10[A10/D10]/MTIOC4A/TIOCA9/PO23/RXD12/SMISO12/SSCL12/RXDX12/SSLB3/MOSIB/IRQ7-DS/AN0	A15
IEB0	IERXD	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	R3
		PC2/A18/MTIOC4B/TCLKA/PO21/RXD5/SMISO5/SSCL5/SSLA3/IERXD	P13
	IETXD	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	P2
		PC3/A19/MTIOC4D/TCLKB/PO24/TXD5/SMOSI5/SSDA5/IETXD	N12
RTCA	RTCOUT	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	R3
		P32/MTIOC0C/TIOCC0/TMO3/PO10/RTCOUT/RTCIC2/TXD6/TXD0/SMOSI6/SMOSI0/SSDA6/SSDA0/CTX0/IRQ2-DS	K2
S12AD	ADTRG0#	P07/IRQ15/ADTRG0#	B2
		P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	R3
		P25/CS5#/MTIOC4C/MTCLKB/TIOCA4/PO5/RXD3/SMISO3/SSCL3/ADTRG0#	L4
ADA0	ADTRG#	P13/MTIOC0B/TIOCA5/TMO3/PO13/TXD2/SMOSI2/SSDA2/SDA0[FM+]/IRQ3/ADTRG#	M5
		P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	P2

Table a-1.2 176-pin LQFP (the Upper Row of Each Pair is the Default Selection)

Peripheral module	Pin function	Selection of assignment	Pin No.
Address bus	A16	PC0/A16/MTIOC3C/TCLKC/PO17/CTS5#/RTS5#/SS5#/SSLA1/SCL3/IRQ14	91
		P90/A16/D16/TXD7/SMOSI7/SSDA7/AN014	163
	A17	PC1/A17/MTIOC3A/TCLKD/PO18/SCK5/SSLA2/SDA3/IRQ12	89
		P91/A17/D17/SCK7/AN015	161
	A18	PC2/A18/MTIOC4B/TCLKA/PO21/RXD5/SMISO5/SSCL5/SSLA3/IERXD	86
		P92/A18/D18/RXD7/SMISO7/SSCL7/AN016	160
	A19	PC3/A19/MTIOC4D/TCLKB/PO24/TXD5/SMOSI5/SSDA5/IETXD	83
		P93/A19/D19/CTS7#/RTS7#/SS7#/AN017	159
	A20	PC4/A20/CS3#/MTIOC3D/MTCLKC/TIOCC6/TCLKE/TMCI1/PO25/POE0#/SCK5/CTS8#/RTS8#/SS8#/SSLA0	82
		P94/A20/D20	157
	A21	PC5/A21/CS2#/WAIT#/MTIOC3B/MTCLKD/TIOCD6/TCLKF/TMRI2/PO29/SCK8/RSPCKA	78
		P95/A21/D21	155
	A22	PC6/A22/CS1#/MTIOC3C/MTCLKA/TIOCA6/TMCI2/PO30/RXD8/SMISO8/SSCL8/MOSIA/IRQ13	77
		P96/A22/D22	152
	A23	PC7/A23/CS0#/MTIOC3A/MTCLKB/TIOCB6/TMO2/PO31/TXD8/SMOSI8	76

		/SSDA8/MISOA/IRQ14	
		P97/A23/D23	149
Bus control	WAIT#	P57/WAIT#/WR3#/BC3#	61
		P55/WAIT#/MTIOC4D/TMO3/CRX1/IRQ10	66
		PC5/A21/CS2#/WAIT#/MTIOC3B/MTCLKD/TIOCD6/TCLKF/TMRI2/PO29/SCK8/RSPCKA	78
		P51/WR1#/BC1#/WAIT#/SCK2/SSLB2	71
	CS0#	P60/CS0#/SCK9	141
		PC7/A23/CS0#/MTIOC3A/MTCLKB/TIOCB6/TMO2/PO31/TXD8/SMOSI8/SSDA8/MISOA/IRQ14	76
	CS1#	P61/CS1#/CTS9#/RTS9#/SS9#	139
		P71/CS1#	102
		PC6/A22/CS1#/MTIOC3C/MTCLKA/TIOCA6/TMCI2/PO30/RXD8/SMISO8/SSCL8/MOSIA/IRQ13	77
	CS2#	P62/CS2#	138
		P72/CS2#	101
		PC5/A21/CS2#/WAIT#/MTIOC3B/MTCLKD/TIOCD6/TCLKF/TMRI2/PO29/SCK8/RSPCKA	78
	CS3#	P63/CS3#	137
		P73/CS3#/PO16	93
		PC4/A20/CS3#/MTIOC3D/MTCLKC/TIOCC6/TCLKE/TMCI1/PO25/POE0#/SCK5/CTS8#/RTS8#/SS8#/SSLA0	82
	CS4#	P64/CS4#	136
		P74/CS4#/PO19/CTS11#/RTS11#/SS11#	88
		P24/CS4#/MTIOC4A/MTCLKA/TIOCB4/TMRI1/PO4/SCK3	40
	CS5#	P65/CS5#	124
		P75/CS5#/PO20/SCK11	87
		P25/CS5#/MTIOC4C/MTCLKB/TIOCA4/PO5/RXD3/SMISO3/SSCL3/ADTRG0#	38
	CS6#	P66/CS6#/CTX2	122
		P76/CS6#/PO22/RXD11/SMISO11/SSCL11	85
		P26/CS6#/MTIOC2A/TMO1/PO6/TXD1/CTS3#/RTS3#/SMOSI1/SS3#/SSDA1/MOSIB	37
	CS7#	P67/CS7#/CRX2/IRQ15	120
		P77/CS7#/PO23/TXD11/SMOSI11/SSDA11	84
		P27/CS7#/MTIOC2B/TMCI3/PO7/SCK1/RSPCKB	36
Interrupts	IRQ0	P30/MTIOC4B/TMRI3/PO8/RTCIC0/POE8#/RXD1/SMISO1/SSCL1/MISOB/IRQ0-DS	33
		P10/MTIC5W/TMRI3/IRQ0	56
		PD0/D0[A0/D0]/TIOCA7/IRQ0/AN008	158
	IRQ1	P31/MTIOC4D/TMCI2/PO9/RTCIC1/CTS1#/RTS1#/SS1#/SSLB0/IRQ1-DS	32
		P11/MTIC5V/TMCI3/SCK2/IRQ1	55
		PD1/D1[A1/D1]/MTIOC4B/TIOCB7/TCLKG/MOSIC/CTX0/IRQ1/AN009	156
	IRQ2	P32/MTIOC0C/TIOCC0/TMO3/PO10/RTCOUT/RTCIC2/TXD6/TXD0/SMOSI6/SMOSI0/SSDA6/SSDA0/CTX0/IRQ2-DS	29

		P12/MTIC5U/TMCI1/RXD2/SMISO2/SSCL2/SCL0[FM+]/IRQ2	54
		PD2/D2[A2/D2]/MTIOC4D/TIOCA8/MISOC/CRX0/IRQ2/AN010	154
	IRQ3	P33/MTIOC0D/TIOCD0/TMRI3/PO11/POE3#/RXD6/RXD0/SMISO6/SMISO0/SSCL6/SSCL0/CRX0/IRQ3-DS	28
		P13/MTIOC0B/TIOCA5/TMO3/PO13/TXD2/SMOSI2/SSDA2/SDA0[FM+]/IRQ3/ADTRG#	53
		PD3/D3[A3/D3]/TIOCB8/TCLKH/POE8#/RSPCKC/IRQ3/AN011	150
	IRQ4	PB1/A9/MTIOC0C/MTIOC4C/TIOCB3/TMCI0/PO25/TXD4/TXD6/SMOSI4/SMOSI6/SSDA4/SSDA6/IRQ4	100
		P14/MTIOC3A/MTCLKA/TIOCB5/TCLKA/TMRI2/PO15/CTS1#/RTS1#/SS1#/CTX1/USB0_DPUPE/IRQ4	51
		P34/MTIOC0A/TMCI3/PO12/POE2#/SCK6/SCK0/IRQ4	27
		PD4/D4/POE3#/SSLC0/IRQ4/AN012	148
		PF5/IRQ4	9
	IRQ5	PA4/A4/MTIC5U/MTCLKA/TIOCA1/TMRI0/PO20/TXD5/SMOSI5/SSDA5/SSLA0/IRQ5	109
		P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SCK3/SMISO1/SSCL1/CRX1/IRQ5	50
		PD5/D5/MTIC5W/POE2#/SSLC1/IRQ5/AN013	147
		PE5/D13/MTIOC4C/MTIOC2B/TIOCB10/RSPCKB/IRQ5/AN3	130
	IRQ6	PA3/A3/MTIOC0D/MTCLKD/TIOCD0/TCLKB/PO19/RXD5/SMISO5/SSCL5/IRQ6	110
		P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2/IERXD/USB0_VBUS/IRQ6/ADTRG0#	48
		PD6/D6/MTIC5V/POE1#/SSLC2/IRQ6/AN6	145
		PE6/D14/TIOCA11/CTS4#/RTS4#/SS4#/MOSIB/IRQ6/AN4	126
	IRQ7	PE2/D10/MTIOC4A/TIOCA9/PO23/RXD12/SMISO12/SSCL12/RXDX12/SSLB3/MOSIB/IRQ7/AN0	133
		P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2/IETXD/IRQ7/ADTRG#	46
		PD7/D7/MTIC5U/POE0#/SSLC3/IRQ7/AN7	143
		PE7/D15/TIOCB11/MISOB/IRQ7/AN5	125
	IRQ8	P40/IRQ8-DS/AN000	173
		P00/TMRI0/TXD6/SMOSI6/SSDA6/IRQ8/AN018	8
		P20/MTIOC1A/TIOCB3/TMRI0/PO0/TXD0/SMOSI0/SSDA0/SDA1/IRQ8	45
	IRQ9	P41/IRQ9-DS/AN001	171
		P01/TMCI0/RXD6/SMISO6/SSCL6/IRQ9/AN019	7
		P21/MTIOC1B/TIOCA3/TMCI0/PO1/RXD0/SMISO0/SSCL0/SCL1/IRQ9	44
	IRQ10	P42/IRQ10-DS/AN002	170
		P02/TMCI1/SCK6/IRQ10/AN020	6
		P55/WAIT#/MTIOC4D/TMO3/CRX1/IRQ10	66
	IRQ11	P43/IRQ11-DS/AN003	169
		P03/IRQ11/DA0	4
		PA1/A1/MTIOC0B/MTCLKC/TIOCB0/PO17/SCK5/SSLA2/IRQ11	114
	IRQ12	P44/IRQ12-DS/AN004	168

		PB0/A8/MTIC5W/TIOCA3/PO24/RXD4/RXD6/SMISO4/SMISO6/SSCL4/SSCL6/RSPCKA/IRQ12	104
		PC1/A17/MTIOC3A/TCLKD/PO18/SCK5/SSLA2/SDA3/IRQ12	89
	IRQ13	P45/IRQ13-DS/AN005	167
		P05/IRQ13/DA1	2
		PC6/A22/CS1#/MTIOC3C/MTCLKA/TIOCA6/TMCI2/PO30/RXD8/SMISO8/SSCL8/MOSIA/IRQ13	77
	IRQ14	P46/IRQ14-DS/AN006	166
		PC0/A16/MTIOC3C/TCLKC/PO17/CTS5#/RTS5#/SS5#/SSLA1/SCL3/IRQ14	91
		PC7/A23/CS0#/MTIOC3A/MTCLKB/TIOCB6/TMO2/PO31/TXD8/SMOSI8/SSDA8/MISOA/IRQ14	76
	IRQ15	P47/IRQ15-DS/AN007	165
		P07/IRQ15/ADTRG0#	176
		P67/CS7#/CRX2/IRQ15	120
MTU0-5	MTCLKA	P14/MTIOC3A/MTCLKA/TIOCB5/TCLKA/TMRI2/PO15/CTS1#/RTS1#/SS1#/CTX1/USB0_DPUPE/IRQ4	51
		P24/CS4#/MTIOC4A/MTCLKA/TIOCB4/TMRI1/PO4/SCK3	40
		PA4/A4/MTIC5U/MTCLKA/TIOCA1/TMRI0/PO20/TXD5/SMOSI5/SSDA5/SSLA0/IRQ5-DS	109
		PC6/A22/CS1#/MTIOC3C/MTCLKA/TIOCA6/TMCI2/PO30/RXD8/SMISO8/SSCL8/MOSIA/IRQ13	77
	MTCLKB	P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SCK3/SMISO1/SSCL1/CRX1-DS/IRQ5	50
		P25/CS5#/MTIOC4C/MTCLKB/TIOCA4/PO5/RXD3/SMISO3/SSCL3/ADTRG0#	38
		PA6/A6/MTIC5V/MTCLKB/TIOCA2/TMCI3/PO22/POE2#/CTS5#/RTS5#/SS5#/MOSIA	107
		PC7/A23/CS0#/MTIOC3A/MTCLKB/TIOCB6/TMO2/PO31/TXD8/SMOSI8/SSDA8/MISOA/IRQ14	76
	MTCLKC	P22/MTIOC3B/MTCLKC/TIOCC3/TMO0/PO2/SCK0	43
		PA1/A1/MTIOC0B/MTCLKC/TIOCB0/PO17/SCK5/SSLA2/IRQ11	114
		PC4/A20/CS3#/MTIOC3D/MTCLKC/TIOCC6/TCLKE/TMCI1/PO25/POE0#/SCK5/CTS8#/RTS8#/SS8#/SSLA0	82
	MTCLKD	P23/MTIOC3D/MTCLKD/TIOCD3/PO3/TXD3/CTS0#/RTS0#/SMOSI3/SS0#/SSDA3	42
		PA3/A3/MTIOC0D/MTCLKD/TIOCD0/TCLKB/PO19/RXD5/SMISO5/SSCL5/IRQ6-DS	110
		PC5/A21/CS2#/WAIT#/MTIOC3B/MTCLKD/TIOCD6/TCLKF/TMRI2/PO29/SCK8/RSPCKA	78
MTU0	MTIOC0A	P34/MTIOC0A/TMCI3/PO12/POE2#/SCK6/SCK0/IRQ4	27
		PB3/A11/MTIOC0A/MTIOC4A/TIOCD3/TCLKD/TMO0/PO27/POE3#/SCK4/SCK6	98
	MTIOC0B	P13/MTIOC0B/TIOCA5/TMO3/PO13/TXD2/SMOSI2/SSDA2/SDA0[FM+]/IRQ3/ADTRG#	53
		P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SCK3/SMISO1/SSCL1/CRX1-DS/IRQ5	50

		PA1/A1/MTIOC0B/MTCLKC/TIOCB0/PO17/SCK5/SSLA2/IRQ11	114
	MTIOC0C	P32/MTIOC0C/TIOCC0/TMO3/PO10/RTCOUT/RTCIC2/TXD6/TXD0/SMOSI6/SMOSI0/SSDA6/SSDA0/CTX0/IRQ2-DS	29
		PB1/A9/MTIOC0C/MTIOC4C/TIOCB3/TMCI0/PO25/TXD4/TXD6/SMOSI4/SMOSI6/SSDA4/SSDA6/IRQ4-DS	100
	MTIOC0D	P33/MTIOC0D/TIOCD0/TMRI3/PO11/POE3#/RXD6/RXD0/SMISO6/SMISO0/SSCL6/SSCL0/CRX0/IRQ3-DS	28
		PA3/A3/MTIOC0D/MTCLKD/TIOCD0/TCLKB/PO19/RXD5/SMISO5/SSCL5/IRQ6-DS	110
MTU1	MTIOC1A	P20/MTIOC1A/TIOCB3/TMRI0/PO0/TXD0/SMOSI0/SSDA0/SDA1/IRQ8	45
		PE4/D12[A12/D12]/MTIOC4D/MTIOC1A/TIOCA10/PO28/SSLB0/AN2	131
	MTIOC1B	P21/MTIOC1B/TIOCA3/TMCI0/PO1/RXD0/SMISO0/SSCL0/SCL1/IRQ9	44
		PB5/A13/MTIOC2A/MTIOC1B/TIOCB4/TMRI1/PO29/POE1#/SCK9	96
MTU2	MTIOC2A	P26/CS6#/MTIOC2A/TMO1/PO6/TXD1/CTS3#/RTS3#/SMOSI1/SS3#/SSDA1/MOSIB	37
		PB5/A13/MTIOC2A/MTIOC1B/TIOCB4/TMRI1/PO29/POE1#/SCK9	96
	MTIOC2B	P27/CS7#/MTIOC2B/TMCI3/PO7/SCK1/RSPCKB	36
		PE5/D13[A13/D13]/MTIOC4C/MTIOC2B/TIOCB10/RSPCKB/IRQ5/AN3	130
MTU3	MTIOC3A	P14/MTIOC3A/MTCLKA/TIOCB5/TCLKA/TMRI2/PO15/CTS1#/RTS1#/SS1#/CTX1/USB0_DPUPE/IRQ4	51
		P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	46
		PC1/A17/MTIOC3A/TCLKD/PO18/SCK5/SSLA2/SDA3/IRQ12	89
		PC7/A23/CS0#/MTIOC3A/MTCLKB/TIOCB6/TMO2/PO31/TXD8/SMOSI8/SSDA8/MISOA/IRQ14	76
	MTIOC3B	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	46
		P22/MTIOC3B/MTCLKC/TIOCC3/TMO0/PO2/SCK0	43
		P80/MTIOC3B/PO26/SCK10	81
		PB7/A15/MTIOC3B/TIOCB5/PO31/TXD9/SMOSI9/SSDA9	94
		PC5/A21/CS2#/WAIT#/MTIOC3B/MTCLKD/TIOCD6/TCLKF/TMRI2/PO29/SCK8/RSPCKA	78
	MTIOC3C	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	48
		P56/WR2#/BC2#/MTIOC3C/TIOCA1	62
		PC0/A16/MTIOC3C/TCLKC/PO17/CTS5#/RTS5#/SS5#/SSLA1/SCL3/IRQ14	91
		PC6/A22/CS1#/MTIOC3C/MTCLKA/TIOCA6/TMCI2/PO30/RXD8/SMISO8/SSCL8/MOSIA/IRQ13	77
		PJ3/MTIOC3C/CTS6#/RTS6#/CTS0#/RTS0#/SS6#/SS0#	13
	MTIOC3D	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	48
		P23/MTIOC3D/MTCLKD/TIOCD3/PO3/TXD3/CTS0#/RTS0#/SMOSI3/SS0#/SSDA3	42
		P81/MTIOC3D/PO27/RXD10/SMISO10/SSCL10	80

		PB6/A14/MTIOC3D/TIOCA5/PO30/RXD9/SMISO9/SSCL9	95
		PC4/A20/CS3#/MTIOC3D/MTCLKC/TIOCC6/TCLKE/TMCI1/PO25/POE0#/SCK5/CTS8#/RTS8#/SS8#/SSLA0	82
MTU4	MTIOC4A	P24/CS4#/MTIOC4A/MTCLKA/TIOCB4/TMRI1/PO4/SCK3	40
		P82/MTIOC4A/PO28/TXD10/SMOSI10/SSDA10	79
		PA0/A0/BC0#/MTIOC4A/TIOCA0/PO16/SSLA1	118
		PB3/A11/MTIOC0A/MTIOC4A/TIOCD3/TCLKD/TMO0/PO27/POE3#/SCK4/SCK6	98
		PE2/D10[A10/D10]/MTIOC4A/TIOCA9/PO23/RXD12/SMISO12/SSCL12/RXDX12/SSLB3/MOSIB/IRQ7-DS/AN0	133
	MTIOC4B	P30/MTIOC4B/TMRI3/PO8/RTCIC0/POE8#/RXD1/SMISO1/SSCL1/MISOB/IRQ0-DS	33
		P54/ALE/MTIOC4B/TMCI1/CTS2#/RTS2#/SS2#/CTX1	67
		PC2/A18/MTIOC4B/TCLKA/PO21/RXD5/SMISO5/SSCL5/SSLA3/IERXD	86
		PD1/D1[A1/D1]/MTIOC4B/TIOCB7/TCLKG/MOSIC/CTX0/IRQ1/AN009	156
		PE3/D11[A11/D11]/MTIOC4B/TIOCB9/PO26/POE8#/CTS12#/RTS12#/SS12#/MISOB/AN1	132
	MTIOC4C	P25/CS5#/MTIOC4C/MTCLKB/TIOCA4/PO5/RXD3/SMISO3/SSCL3/ADTRG0#	38
		P83/MTIOC4C/CTS10#/RTS10#/SS10#	74
		PB1/A9/MTIOC0C/MTIOC4C/TIOCB3/TMCI0/PO25/TXD4/TXD6/SMOSI4/SMOSI6/SSDA4/SSDA6/IRQ4-DS	100
		PE1/D9[A9/D9]/MTIOC4C/TIOCD9/PO18/TXD12/SMOSI12/SSDA12/TXDX12/SIOX12/SSLB2/RSPCKB/ANEX1	134
		PE5/D13[A13/D13]/MTIOC4C/MTIOC2B/TIOCB10/RSPCKB/IRQ5/AN3	130
	MTIOC4D	P31/MTIOC4D/TMCI2/PO9/RTCIC1/CTS1#/RTS1#/SS1#/SSLB0/IRQ1-DS	32
		P55/WAIT#/MTIOC4D/TMO3/CRX1/IRQ10	66
		PC3/A19/MTIOC4D/TCLKB/PO24/TXD5/SMOSI5/SSDA5/IETXD	83
		PD2/D2[A2/D2]/MTIOC4D/TIOCA8/MISOC/CRX0/IRQ2/AN010	154
		PE4/D12[A12/D12]/MTIOC4D/MTIOC1A/TIOCA10/PO28/SSLB0/AN2	131
MTU5	MTIC5U	P12/MTIC5U/TMCI1/RXD2/SMISO2/SSCL2/SCL0[FM+]/IRQ2	54
		PA4/A4/MTIC5U/MTCLKA/TIOCA1/TMRI0/PO20/TXD5/SMOSI5/SSDA5/SSLA0/IRQ5-DS	109
		PD7/D7[A7/D7]/MTIC5U/POE0#/SSLC3/IRQ7/AN7	143
	MTIC5V	P11/MTIC5V/TMCI3/SCK2/IRQ1	55
		PA6/A6/MTIC5V/MTCLKB/TIOCA2/TMCI3/PO22/POE2#/CTS5#/RTS5#/SS5#/MOSIA	107
		PD6/D6[A6/D6]/MTIC5V/POE1#/SSLC2/IRQ6/AN6	145
	MTIC5W	P10/MTIC5W/TMRI3/IRQ0	56
		PB0/A8/MTIC5W/TIOCA3/PO24/RXD4/RXD6/SMISO4/SMISO6/SSCL4/SSCL6/RSPCKA/IRQ12	104
		PD5/D5[A5/D5]/MTIC5W/POE2#/SSLC1/IRQ5/AN013	147
POE	POE0#	PC4/A20/CS3#/MTIOC3D/MTCLKC/TIOCC6/TCLKE/TMCI1/PO25/POE0#/SCK5/CTS8#/RTS8#/SS8#/SSLA0	82
		PD7/D7[A7/D7]/MTIC5U/POE0#/SSLC3/IRQ7/AN7	143

	POE1#	PB5/A13/MTIOC2A/MTIOC1B/TIOCB4/TMRI1/PO29/POE1#/SCK9	96
		PD6/D6[A6/D6]/MTIC5V/POE1#/SSLC2/IRQ6/AN6	145
	POE2#	P34/MTIOC0A/TMCI3/PO12/POE2#/SCK6/SCK0/IRQ4	27
		PA6/A6/MTIC5V/MTCLKB/TIOCA2/TMCI3/PO22/POE2#/CTS5#/RTS5#/SS5#/MOSIA	107
		PD5/D5[A5/D5]/MTIC5W/POE2#/SSLC1/IRQ5/AN013	147
	POE3#	P33/MTIOC0D/TIOCD0/TMRI3/PO11/POE3#/RXD6/RXD0/SMISO6/SMISO0/SSCL6/SSCL0/CRX0/IRQ3-DS	28
		PB3/A11/MTIOC0A/MTIOC4A/TIOCD3/TCLKD/TMO0/PO27/POE3#/SCK4/SCK6	98
		PD4/D4[A4/D4]/POE3#/SSLC0/IRQ4/AN012	148
	POE8#	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	46
		P30/MTIOC4B/TMRI3/PO8/RTCIC0/POE8#/RXD1/SMISO1/SSCL1/MISOB/IRQ0-DS	33
		PD3/D3[A3/D3]/TIOCB8/TCLKH/POE8#/RSPCKC/IRQ3/AN011	150
		PE3/D11[A11/D11]/MTIOC4B/TIOCB9/PO26/POE8#/CTS12#/RTS12#/SS12#/MISOB/AN1	132
TPU0-5	TCLKA	P14/MTIOC3A/MTCLKA/TIOCB5/TCLKA/TMRI2/PO15/CTS1#/RTS1#/SS1#/CTX1/USB0_DPUPE/IRQ4	51
		PC2/A18/MTIOC4B/TCLKA/PO21/RXD5/SMISO5/SSCL5/SSLA3/IERXD	86
	TCLKB	P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SCK3/SMISO1/SSCL1/CRX1-DS/IRQ5	50
		PA3/A3/MTIOC0D/MTCLKD/TIOCD0/TCLKB/PO19/RXD5/SMISO5/SSCL5/IRQ6-DS	110
		PC3/A19/MTIOC4D/TCLKB/PO24/TXD5/SMOSI5/SSDA5/IETXD	83
	TCLKC	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	48
		PB2/A10/TIOCC3/TCLKC/PO26/CTS4#/RTS4#/CTS6#/RTS6#/SS4#/SS6#	99
		PC0/A16/MTIOC3C/TCLKC/PO17/CTS5#/RTS5#/SS5#/SSLA1/SCL3/IRQ14	91
	TCLKD	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	46
		PB3/A11/MTIOC0A/MTIOC4A/TIOCD3/TCLKD/TMO0/PO27/POE3#/SCK4/SCK6	98
		PC1/A17/MTIOC3A/TCLKD/PO18/SCK5/SSLA2/SDA3/IRQ12	89
TPU0	TIOCA0	P86/TIOCA0	49
		PA0/A0/BC0#/MTIOC4A/TIOCA0/PO16/SSLA1	118
	TIOCB0	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	46
		PA1/A1/MTIOC0B/MTCLKC/TIOCB0/PO17/SCK5/SSLA2/IRQ11	114
	TIOCD0	P33/MTIOC0D/TIOCD0/TMRI3/PO11/POE3#/RXD6/RXD0/SMISO6/SMISO0/SSCL6/SSCL0/CRX0/IRQ3-DS	28
		PA3/A3/MTIOC0D/MTCLKD/TIOCD0/TCLKB/PO19/RXD5/SMISO5/SSCL5/IRQ6-DS	110

TPU1	TIOCA1	P56/WR2#/BC2#/MTIOC3C/TIOCA1	62
		PA4/A4/MTIC5U/MTCLKA/TIOCA1/TMRI0/PO20/TXD5/SMOSI5/SSDA5/SSLA0/IRQ5-DS	109
	TIOCB1	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	48
		PA5/A5/TIOCB1/PO21/RSPCKA	108
TPU2	TIOCA2	P87/TIOCA2	47
		PA6/A6/MTIC5V/MTCLKB/TIOCA2/TMCI3/PO22/POE2#/CTS5#/RTS5#/SS5#/MOSIA	107
	TIOCB2	P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SCK3/SMISO1/SSCL1/CRX1-DS/IRQ5	50
		PA7/A7/TIOCB2/PO23/MISOA	106
TPU3	TIOCA3	P21/MTIOC1B/TIOCA3/TMCI0/PO1/RXD0/SMISO0/SSCL0/SCL1/IRQ9	44
		PB0/A8/MTIC5W/TIOCA3/PO24/RXD4/RXD6/SMISO4/SMISO6/SSCL4/SSCL6/RSPCKA/IRQ12	104
	TIOCB3	P20/MTIOC1A/TIOCB3/TMRI0/PO0/TXD0/SMOSI0/SSDA0/SDA1/IRQ8	45
		PB1/A9/MTIOC0C/MTIOC4C/TIOCB3/TMCI0/PO25/TXD4/TXD6/SMOSI4/SMOSI6/SSDA4/SSDA6/IRQ4-DS	100
	TIOCC3	P22/MTIOC3B/MTCLKC/TIOCC3/TMO0/PO2/SCK0	43
		PB2/A10/TIOCC3/TCLKC/PO26/CTS4#/RTS4#/CTS6#/RTS6#/SS4#/SS6#	99
	TIOCD3	P23/MTIOC3D/MTCLKD/TIOCD3/PO3/TXD3/CTS0#/RTS0#/SMOSI3/SS0#/SSDA3	42
		PB3/A11/MTIOC0A/MTIOC4A/TIOCD3/TCLKD/TMO0/PO27/POE3#/SCK4/SCK6	98
TPU4	TIOCA4	P25/CS5#/MTIOC4C/MTCLKB/TIOCA4/PO5/RXD3/SMISO3/SSCL3/ADTRG0#	38
		PB4/A12/TIOCA4/PO28/CTS9#/RTS9#/SS9#	97
	TIOCB4	P24/CS4#/MTIOC4A/MTCLKA/TIOCB4/TMRI1/PO4/SCK3	40
		PB5/A13/MTIOC2A/MTIOC1B/TIOCB4/TMRI1/PO29/POE1#/SCK9	96
TPU5	TIOCA5	P13/MTIOC0B/TIOCA5/TMO3/PO13/TXD2/SMOSI2/SSDA2/SDA0[FM+]/IRQ3/ADTRG#	53
		PB6/A14/MTIOC3D/TIOCA5/PO30/RXD9/SMISO9/SSCL9	95
	TIOCB5	P14/MTIOC3A/MTCLKA/TIOCB5/TCLKA/TMRI2/PO15/CTS1#/RTS1#/SS1#/CTX1/USB0_DPUPE/IRQ4	51
		PB7/A15/MTIOC3B/TIOCB5/PO31/TXD9/SMOSI9/SSDA9	94
PPG Group3	PO13	P13/MTIOC0B/TIOCA5/TMO3/PO13/TXD2/SMOSI2/SSDA2/SDA0[FM+]/IRQ3/ADTRG#	53
		P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SCK3/SMISO1/SSCL1/CRX1-DS/IRQ5	50
	PO15	P14/MTIOC3A/MTCLKA/TIOCB5/TCLKA/TMRI2/PO15/CTS1#/RTS1#/SS1#/CTX1/USB0_DPUPE/IRQ4	51
		P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	46
PPG Group4	PO16	P73/CS3#/PO16	93
		PA0/A0/BC0#/MTIOC4A/TIOCA0/PO16/SSLA1	118

	PO17	PA1/A1/MTIOC0B/MTCLKC/TIOCB0/PO17/SCK5/SSLA2/IRQ11	114
		PC0/A16/MTIOC3C/TCLKC/PO17/CTS5#/RTS5#/SS5#/SSLA1/SCL3/IRQ14	91
	PO18	PA2/A2/PO18/RXD5/SMISO5/SSCL5/SSLA3	112
		PC1/A17/MTIOC3A/TCLKD/PO18/SCK5/SSLA2/SDA3/IRQ12	89
		PE1/D9[A9/D9]/MTIOC4C/TIOCD9/PO18/TXD12/SMOSI12/SSDA12/TXDX12/SIOX12/SSLB2/RSPCKB/ANEX1	134
	PO19	P74/CS4#/PO19/CTS11#/RTS11#/SS11#	88
		PA3/A3/MTIOC0D/MTCLKD/TIOCD0/TCLKB/PO19/RXD5/SMISO5/SSCL5/IRQ6-DS	110
PPG Group5	PO20	P75/CS5#/PO20/SCK11	87
		PA4/A4/MTIC5U/MTCLKA/TIOCA1/TMRI0/PO20/TXD5/SMOSI5/SSDA5/SSLA0/IRQ5-DS	109
	PO21	PA5/A5/TIOCB1/PO21/RSPCKA	108
		PC2/A18/MTIOC4B/TCLKA/PO21/RXD5/SMISO5/SSCL5/SSLA3/IERXD	86
	PO22	P76/CS6#/PO22/RXD11/SMISO11/SSCL11	85
		PA6/A6/MTIC5V/MTCLKB/TIOCA2/TMCI3/PO22/POE2#/CTS5#/RTS5#/SS5#/MOSIA	107
	PO23	P77/CS7#/PO23/TXD11/SMOSI11/SSDA11	84
		PA7/A7/TIOCB2/PO23/MISOA	106
		PE2/D10[A10/D10]/MTIOC4A/TIOCA9/PO23/RXD12/SMISO12/SSCL12/RXDX12/SSLB3/MOSIB/IRQ7-DS/AN0	133
PPG Group6	PO24	PB0/A8/MTIC5W/TIOCA3/PO24/RXD4/RXD6/SMISO4/SMISO6/SSCL4/SSCL6/RSPCKA/IRQ12	104
		PC3/A19/MTIOC4D/TCLKB/PO24/TXD5/SMOSI5/SSDA5/IETXD	83
	PO25	PB1/A9/MTIOC0C/MTIOC4C/TIOCB3/TMCI0/PO25/TXD4/TXD6/SMOSI4/SMOSI6/SSDA4/SSDA6/IRQ4-DS	100
		PC4/A20/CS3#/MTIOC3D/MTCLKC/TIOCC6/TCLKE/TMCI1/PO25/POE0#/SCK5/CTS8#/RTS8#/SS8#/SSLA0	82
	PO26	P80/MTIOC3B/PO26/SCK10	81
		PB2/A10/TIOCC3/TCLKC/PO26/CTS4#/RTS4#/CTS6#/RTS6#/SS4#/SS6#	99
		PE3/D11[A11/D11]/MTIOC4B/TIOCB9/PO26/POE8#/CTS12#/RTS12#/SS12#/MISOB/AN1	132
	PO27	P81/MTIOC3D/PO27/RXD10/SMISO10/SSCL10	80
		PB3/A11/MTIOC0A/MTIOC4A/TIOCD3/TCLKD/TMO0/PO27/POE3#/SCK4/SCK6	98
PPG Group7	PO28	P82/MTIOC4A/PO28/TXD10/SMOSI10/SSDA10	79
		PB4/A12/TIOCA4/PO28/CTS9#/RTS9#/SS9#	97
		PE4/D12[A12/D12]/MTIOC4D/MTIOC1A/TIOCA10/PO28/SSLB0/AN2	131
	PO29	PB5/A13/MTIOC2A/MTIOC1B/TIOCB4/TMRI1/PO29/POE1#/SCK9	96
		PC5/A21/CS2#/WAIT#/MTIOC3B/MTCLKD/TIOCD6/TCLKF/TMRI2/PO29/SCK8/RSPCKA	78
	PO30	PB6/A14/MTIOC3D/TIOCA5/PO30/RXD9/SMISO9/SSCL9	95
		PC6/A22/CS1#/MTIOC3C/MTCLKA/TIOCA6/TMCI2/PO30/RXD8/SMISO8/SSCL8/MOSIA/IRQ13	77
	PO31	PB7/A15/MTIOC3B/TIOCB5/PO31/TXD9/SMOSI9/SSDA9	94

		PC7/A23/CS0#/MTIOC3A/MTCLKB/TIOCB6/TMO2/PO31/TXD8/SMOSI8/SSDA8/MISOA/IRQ14	76
TMR0	TMO0	P22/MTIOC3B/MTCLKC/TIOCC3/TMO0/PO2/SCK0	43
		PB3/A11/MTIOC0A/MTIOC4A/TIOCD3/TCLKD/TMO0/PO27/POE3#/SCK4/SCK6	98
	TMCI0	P01/TMCI0/RXD6/SMISO6/SSCL6/IRQ9/AN019	7
		P21/MTIOC1B/TIOCA3/TMCI0/PO1/RXD0/SMISO0/SSCL0/SCL1/IRQ9	44
		PB1/A9/MTIOC0C/MTIOC4C/TIOCB3/TMCI0/PO25/TXD4/TXD6/SMOSI4/SMOSI6/SSDA4/SSDA6/IRQ4-DS	100
	TMRI0	P00/TMRI0/TXD6/SMOSI6/SSDA6/IRQ8/AN018	8
		P20/MTIOC1A/TIOCB3/TMRI0/PO0/TXD0/SMOSI0/SSDA0/SDA1/IRQ8	45
		PA4/A4/MTIC5U/MTCLKA/TIOCA1/TMRI0/PO20/TXD5/SMOSI5/SSDA5/SSLA0/IRQ5-DS	109
TMR1	TMO1	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	46
		P26/CS6#/MTIOC2A/TMO1/PO6/TXD1/CTS3#/RTS3#/SMOSI1/SS3#/SSDA1/MOSIB	37
	TMCI1	P02/TMCI1/SCK6/IRQ10/AN020	6
		P12/MTIC5U/TMCI1/RXD2/SMISO2/SSCL2/SCL0[FM+]/IRQ2	54
		P54/ALE/MTIOC4B/TMCI1/CTS2#/RTS2#/SS2#/CTX1	67
		PC4/A20/CS3#/MTIOC3D/MTCLKC/TIOCC6/TCLKE/TMCI1/PO25/POE0#/SCK5/CTS8#/RTS8#/SS8#/SSLA0	82
	TMRI1	P24/CS4#/MTIOC4A/MTCLKA/TIOCB4/TMRI1/PO4/SCK3	40
		PB5/A13/MTIOC2A/MTIOC1B/TIOCB4/TMRI1/PO29/POE1#/SCK9	96
TMR2	TMO2	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	48
		PC7/A23/CS0#/MTIOC3A/MTCLKB/TIOCB6/TMO2/PO31/TXD8/SMOSI8/SSDA8/MISOA/IRQ14	76
	TMCI2	P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SCK3/SMISO1/SSCL1/CRX1-DS/IRQ5	50
		P31/MTIOC4D/TMCI2/PO9/RTCIC1/CTS1#/RTS1#/SS1#/SSLB0/IRQ1-DS	32
		PC6/A22/CS1#/MTIOC3C/MTCLKA/TIOCA6/TMCI2/PO30/RXD8/SMISO8/SSCL8/MOSIA/IRQ13	77
	TMRI2	P14/MTIOC3A/MTCLKA/TIOCB5/TCLKA/TMRI2/PO15/CTS1#/RTS1#/SS1#/CTX1/USB0_DPUPE/IRQ4	51
		PC5/A21/CS2#/WAIT#/MTIOC3B/MTCLKD/TIOCD6/TCLKF/TMRI2/PO29/SCK8/RSPCKA	78
TMR3	TMO3	P13/MTIOC0B/TIOCA5/TMO3/PO13/TXD2/SMOSI2/SSDA2/SDA0[FM+]/IRQ3/ADTRG#	53
		P32/MTIOC0C/TIOCC0/TMO3/PO10/RTCOUT/RTCIC2/TXD6/TXD0/SMOSI6/SMOSI0/SSDA6/SSDA0/CTX0/IRQ2-DS	29
		P55/WAIT#/MTIOC4D/TMO3/CRX1/IRQ10	66
	TMCI3	P11/MTIC5V/TMCI3/SCK2/IRQ1	55
		P27/CS7#/MTIOC2B/TMCI3/PO7/SCK1/RSPCKB	36
		P34/MTIOC0A/TMCI3/PO12/POE2#/SCK6/SCK0/IRQ4	27

		PA6/A6/MTIC5V/MTCLKB/TIOCA2/TMCI3/PO22/POE2#/CTS5#/RTS5#/SS5#/MOSIA	107
	TMRI3	P10/MTIC5W/TMRI3/IRQ0	56
		P30/MTIOC4B/TMRI3/PO8/RTCIC0/POE8#/RXD1/SMISO1/SSCL1/MISOB/IRQ0-DS	33
		P33/MTIOC0D/TIOCD0/TMRI3/PO11/POE3#/RXD6/RXD0/SMISO6/SMISO0/SSCL6/SSCL0/CRX0/IRQ3-DS	28
SCI0	RXD0 SSCL0 SMISO0	P21/MTIOC1B/TIOCA3/TMCI0/PO1/RXD0/SMISO0/SSCL0/SCL1/IRQ9	44
		P33/MTIOC0D/TIOCD0/TMRI3/PO11/POE3#/RXD6/RXD0/SMISO6/SMISO0/SSCL6/SSCL0/CRX0/IRQ3-DS	28
	TXD0 SSDA0 SMOSI0	P20/MTIOC1A/TIOCB3/TMRI0/PO0/TXD0/SMOSI0/SSDA0/SDA1/IRQ8	45
		P32/MTIOC0C/TIOCC0/TMO3/PO10/RTCOUT/RTCIC2/TXD6/TXD0/SMOSI6/SMOSI0/SSDA6/SSDA0/CTX0/IRQ2-DS	29
	SCK0	P22/MTIOC3B/MTCLKC/TIOCC3/TMO0/PO2/SCK0	43
		P34/MTIOC0A/TMCI3/PO12/POE2#/SCK6/SCK0/IRQ4	27
	CTS0# RTS0# SS0#	P23/MTIOC3D/MTCLKD/TIOCD3/PO3/TXD3/CTS0#/RTS0#/SMOSI3/SS0#/SSDA3	42
		PJ3/MTIOC3C/CTS6#/RTS6#/CTS0#/RTS0#/SS6#/SS0#	13
SCI1	RXD1 SSCL1 SMISO1	P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SCK3/SMISO1/SSCL1/CRX1-DS/IRQ5	50
		P30/MTIOC4B/TMRI3/PO8/RTCIC0/POE8#/RXD1/SMISO1/SSCL1/MISOB/IRQ0-DS	33
		TDI/PF2/RXD1/SMISO1/SSCL1	31
	TXD1 SSDA1 SMOSI1	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	48
		P26/CS6#/MTIOC2A/TMO1/PO6/TXD1/CTS3#/RTS3#/SMOSI1/SS3#/SSDA1/MOSIB	37
		TDO/PF0/TXD1/SMOSI1/SSDA1	35
	SCK1	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	46
		P27/CS7#/MTIOC2B/TMCI3/PO7/SCK1/RSPCKB	36
		TCK/FINEC/PF1/SCK1	34
	CTS1# RTS1# SS1#	P14/MTIOC3A/MTCLKA/TIOCB5/TCLKA/TMRI2/PO15/CTS1#/RTS1#/SS1#/CTX1/USB0_DPUPE/IRQ4	51
		P31/MTIOC4D/TMCI2/PO9/RTCIC1/CTS1#/RTS1#/SS1#/SSLB0/IRQ1-DS	32
SCI2	RXD2 SSCL2 SMISO2	P12/MTIC5U/TMCI1/RXD2/SMISO2/SSCL2/SCL0[FM+]/IRQ2	54
		P52/RD#/RXD2/SMISO2/SSCL2/SSLB3	70
	TXD2 SSDA2 SMOSI2	P13/MTIOC0B/TIOCA5/TMO3/PO13/TXD2/SMOSI2/SSDA2/SDA0[FM+]/IRQ3/ADTRG#	53
		P50/WR0#/WR#/TXD2/SMOSI2/SSDA2/SSLB1	72
	SCK2	P11/MTIC5V/TMCI3/SCK2/IRQ1	55
		P51/WR1#/BC1#/WAIT#/SCK2/SSLB2	71
SCI3	RXD3	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/	48

	SSCL3 SMISO3	RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	
		P25/CS5#/MTIOC4C/MTCLKB/TIOCA4/PO5/RXD3/SMISO3/SSCL3/ADTRG0#	38
	TXD3 SSDA3 SMOSI3	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	46
		P23/MTIOC3D/MTCLKD/TIOCD3/PO3/TXD3/CTS0#/RTS0#/SMOSI3/SS0#/SSDA3	42
	SCK3	P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SCK3/SMISO1/SSCL1/CRX1-DS/IRQ5	50
		P24/CS4#/MTIOC4A/MTCLKA/TIOCB4/TMRI1/PO4/SCK3	40
SCI4	RXD4 SSCL4 SMISO4	PB0/A8/MTIC5W/TIOCA3/PO24/RXD4/RXD6/SMISO4/SMISO6/SSCL4/SSCL6/RSPCKA/IRQ12	104
		PK4/RXD4/SMISO4/SSCL4	129
	TXD4 SSDA4 SMOSI4	PB1/A9/MTIOC0C/MTIOC4C/TIOCB3/TMCI0/PO25/TXD4/TXD6/SMOSI4/SMOSI6/SSDA4/SSDA6/IRQ4-DS	100
		PK5/TXD4/SMOSI4/SSDA4	127
	SCK4	P70/SCK4	128
		PB3/A11/MTIOC0A/MTIOC4A/TIOCD3/TCLKD/TMO0/PO27/POE3#/SCK4/SCK6	98
	CTS4# RTS4# SS4#	PB2/A10/TIOCC3/TCLKC/PO26/CTS4#/RTS4#/CTS6#/RTS6#/SS4#/SS6#	99
		PE6/D14[A14/D14]/TIOCA11/CTS4#/RTS4#/SS4#/MOSIB/IRQ6/AN4	126
SCI5	RXD5 SSCL5 SMISO5	PA2/A2/PO18/RXD5/SMISO5/SSCL5/SSLA3	112
		PA3/A3/MTIOC0D/MTCLKD/TIOCD0/TCLKB/PO19/RXD5/SMISO5/SSCL5/IRQ6-DS	110
		PC2/A18/MTIOC4B/TCLKA/PO21/RXD5/SMISO5/SSCL5/SSLA3/IERXD	86
	TXD5 SSDA5 SMOSI5	PA4/A4/MTIC5U/MTCLKA/TIOCA1/TMRI0/PO20/TXD5/SMOSI5/SSDA5/SSLA0/IRQ5-DS	109
		PC3/A19/MTIOC4D/TCLKB/PO24/TXD5/SMOSI5/SSDA5/IETXD	83
	SCK5	PA1/A1/MTIOC0B/MTCLKC/TIOCB0/PO17/SCK5/SSLA2/IRQ11	114
		PC1/A17/MTIOC3A/TCLKD/PO18/SCK5/SSLA2/SDA3/IRQ12	89
		PC4/A20/CS3#/MTIOC3D/MTCLKC/TIOCC6/TCLKE/TMCI1/PO25/POE0#/SCK5/CTS8#/RTS8#/SS8#/SSLA0	82
	CTS5# RTS5# SS5#	PA6/A6/MTIC5V/MTCLKB/TIOCA2/TMCI3/PO22/POE2#/CTS5#/RTS5#/SS5#/MOSIA	107
		PC0/A16/MTIOC3C/TCLKC/PO17/CTS5#/RTS5#/SS5#/SSLA1/SCL3/IRQ14	91
SCI6	RXD6 SSCL6 SMISO6	P01/TMCI0/RXD6/SMISO6/SSCL6/IRQ9/AN019	7
		P33/MTIOC0D/TIOCD0/TMRI3/PO11/POE3#/RXD6/RXD0/SMISO6/SMISO0/SSCL6/SSCL0/CRX0/IRQ3-DS	28
		PB0/A8/MTIC5W/TIOCA3/PO24/RXD4/RXD6/SMISO4/SMISO6/SSCL4/SSCL6/RSPCKA/IRQ12	104
	TXD6 SSDA6	P00/TMRI0/TXD6/SMOSI6/SSDA6/IRQ8/AN018	8
		P32/MTIOC0C/TIOCC0/TMO3/PO10/RTCOUT/RTCIC2/TXD6/TXD0/SM	29

	SMOSI6	OSI6/SMOSI0/SSDA6/SSDA0/CTX0/IRQ2-DS	
		PB1/A9/MTIOC0C/MTIOC4C/TIOCB3/TMCI0/PO25/TXD4/TXD6/SMOSI4/SMOSI6/SSDA4/SSDA6/IRQ4-DS	100
	SCK6	P02/TMCI1/SCK6/IRQ10/AN020	6
		P34/MTIOC0A/TMCI3/PO12/POE2#/SCK6/SCK0/IRQ4	27
		PB3/A11/MTIOC0A/MTIOC4A/TIOCD3/TCLKD/TMO0/PO27/POE3#/SCK4/SCK6	98
	CTS6# RTS6# SS6#	PB2/A10/TIOCC3/TCLKC/PO26/CTS4#/RTS4#/CTS6#/RTS6#/SS4#/SS6#	99
		PJ3/MTIOC3C/CTS6#/RTS6#/CTS0#/RTS0#/SS6#/SS0#	13
SCI9	RXD9 SSCL9 SMISO9	PB6/A14/MTIOC3D/TIOCA5/PO30/RXD9/SMISO9/SSCL9	95
		PK3/RXD9/SMISO9/SSCL9	140
	TXD9 SSDA9 SMOSI9	PB7/A15/MTIOC3B/TIOCB5/PO31/TXD9/SMOSI9/SSDA9	94
		PK2/TXD9/SMOSI9/SSDA9	142
	SCK9	P60/CS0#/SCK9	141
		PB5/A13/MTIOC2A/MTIOC1B/TIOCB4/TMRI1/PO29/POE1#/SCK9	96
	CTS9# RTS9# SS9#	P61/CS1#/CTS9#/RTS9#/SS9#	139
		PB4/A12/TIOCA4/PO28/CTS9#/RTS9#/SS9#	97
RSPI0	RSPCKA	PA5/A5/TIOCB1/PO21/RSPCKA	108
		PB0/A8/MTIC5W/TIOCA3/PO24/RXD4/RXD6/SMISO4/SMISO6/SSCL4/SSCL6/RSPCKA/IRQ12	104
		PC5/A21/CS2#/WAIT#/MTIOC3B/MTCLKD/TIOCD6/TCLKF/TMRI2/PO29/SCK8/RSPCKA	78
	MOSIA	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	48
		PA6/A6/MTIC5V/MTCLKB/TIOCA2/TMCI3/PO22/POE2#/CTS5#/RTS5#/SS5#/MOSIA	107
		PC6/A22/CS1#/MTIOC3C/MTCLKA/TIOCA6/TMCI2/PO30/RXD8/SMISO8/SSCL8/MOSIA/IRQ13	77
	MISOA	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	46
		PA7/A7/TIOCB2/PO23/MISOA	106
		PC7/A23/CS0#/MTIOC3A/MTCLKB/TIOCB6/TMO2/PO31/TXD8/SMOSI8/SSDA8/MISOA/IRQ14	76
	SSLA0	PA4/A4/MTIC5U/MTCLKA/TIOCA1/TMRI0/PO20/TXD5/SMOSI5/SSDA5/SSLA0/IRQ5-DS	109
		PC4/A20/CS3#/MTIOC3D/MTCLKC/TIOCC6/TCLKE/TMCI1/PO25/POE0#/SCK5/CTS8#/RTS8#/SS8#/SSLA0	82
	SSLA1	PA0/A0/BC0#/MTIOC4A/TIOCA0/PO16/SSLA1	118
		PC0/A16/MTIOC3C/TCLKC/PO17/CTS5#/RTS5#/SS5#/SSLA1/SCL3/IRQ14	91
	SSLA2	PA1/A1/MTIOC0B/MTCLKC/TIOCB0/PO17/SCK5/SSLA2/IRQ11	114
		PC1/A17/MTIOC3A/TCLKD/PO18/SCK5/SSLA2/SDA3/IRQ12	89

	SSLA3	PA2/A2/PO18/RXD5/SMISO5/SSCL5/SSLA3	112
		PC2/A18/MTIOC4B/TCLKA/PO21/RXD5/SMISO5/SSCL5/SSLA3/IERXD	86
RSPI1	RSPCKB	P27/CS7#/MTIOC2B/TMCI3/PO7/SCK1/RSPCKB	36
		PE1/D9[A9/D9]/MTIOC4C/TIOCD9/PO18/TXD12/SMOSI12/SSDA12/TXDX12/SIOX12/SSLB2/RSPCKB/ANEX1	134
		PE5/D13[A13/D13]/MTIOC4C/MTIOC2B/TIOCB10/RSPCKB/IRQ5/AN3	130
	MOSIB	P26/CS6#/MTIOC2A/TMO1/PO6/TXD1/CTS3#/RTS3#/SMOSI1/SS3#/SSDA1/MOSIB	37
		PE2/D10[A10/D10]/MTIOC4A/TIOCA9/PO23/RXD12/SMISO12/SSCL12/RXDX12/SSLB3/MOSIB/IRQ7-DS/AN0	133
		PE6/D14[A14/D14]/TIOCA11/CTS4#/RTS4#/SS4#/MOSIB/IRQ6/AN4	126
	MISOB	P30/MTIOC4B/TMRI3/PO8/RTCIC0/POE8#/RXD1/SMISO1/SSCL1/MISOB/IRQ0-DS	33
		PE3/D11[A11/D11]/MTIOC4B/TIOCB9/PO26/POE8#/CTS12#/RTS12#/SS12#/MISOB/AN1	132
		PE7/D15[A15/D15]/TIOCB11/MISOB/IRQ7/AN5	125
	SSLB0	P31/MTIOC4D/TMCI2/PO9/RTCIC1/CTS1#/RTS1#/SS1#/SSLB0/IRQ1-DS	32
		PE4/D12[A12/D12]/MTIOC4D/MTIOC1A/TIOCA10/PO28/SSLB0/AN2	131
	SSLB1	P50/WR0#/WR#/TXD2/SMOSI2/SSDA2/SSLB1	72
		PE0/D8[A8/D8]/TIOCC9/SCK12/SSLB1/ANEX0	135
	SSLB2	P51/WR1#/BC1#/WAIT#/SCK2/SSLB2	71
		PE1/D9[A9/D9]/MTIOC4C/TIOCD9/PO18/TXD12/SMOSI12/SSDA12/TXDX12/SIOX12/SSLB2/RSPCKB/ANEX1	134
	SSLB3	P52/RD#/RXD2/SMISO2/SSCL2/SSLB3	70
		PE2/D10[A10/D10]/MTIOC4A/TIOCA9/PO23/RXD12/SMISO12/SSCL12/RXDX12/SSLB3/MOSIB/IRQ7-DS/AN0	133
IEB0	IERXD	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	48
		PC2/A18/MTIOC4B/TCLKA/PO21/RXD5/SMISO5/SSCL5/SSLA3/IERXD	86
	IETXD	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	46
		PC3/A19/MTIOC4D/TCLKB/PO24/TXD5/SMOSI5/SSDA5/IETXD	83
RTCA	RTCOUT	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	48
		P32/MTIOC0C/TIOCC0/TMO3/PO10/RTCOUT/RTCIC2/TXD6/TXD0/SMOSI6/SMOSI0/SSDA6/SSDA0/CTX0/IRQ2-DS	29
S12AD	ADTRG0#	P07/IRQ15/ADTRG0#	176
		P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	48
		P25/CS5#/MTIOC4C/MTCLKB/TIOCA4/PO5/RXD3/SMISO3/SSCL3/ADTRG0#	38
ADA0	ADTRG#	P13/MTIOC0B/TIOCA5/TMO3/PO13/TXD2/SMOSI2/SSDA2/SDA0[FM+]/IRQ3/ADTRG#	53

		P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	46

Table a-1.3 145-pin TFLGA (the Upper Row of Each Pair is the Default Selection)

Peripheral module	Pin function	Selection of assignment	Pin No.
Address bus	A16	PC0/A16/MTIOC3C/TCLKC/PO17/CTS5#/RTS5#/SS5#/SSLA1/SCL3/IRQ14	M11
		P90/A16/TXD7/SMOSI7/SSDA7/AN014	A6
	A17	PC1/A17/MTIOC3A/TCLKD/PO18/SCK5/SSLA2/SDA3/IRQ12	M12
		P91/A17/SCK7/AN015	B7
	A18	PC2/A18/MTIOC4B/TCLKA/PO21/RXD5/SMISO5/SSCL5/SSLA3/IERXD	L11
		P92/A18/RXD7/SMISO7/SSCL7/AN016	A7
	A19	PC3/A19/MTIOC4D/TCLKB/PO24/TXD5/SMOSI5/SSDA5/IETXD	N11
		P93/A19/CTS7#/RTS7#/SS7#/AN017	D6
Bus control	WAIT#	TRDATA3/P55/WAIT#/MTIOC4D/TMO3/CRX1/IRQ10	N7
		PC5/A21/CS2#/WAIT#/MTIOC3B/MTCLKD/TIOCD6/TCLKF/TMRI2/PO29/SCK8/RSPCKA	L9
		P51/WR1#/BC1#/WAIT#/SCK2/SSLB2	K7
	CS0#	P60/CS0#/SCK9	D8
		PC7/A23/CS0#/MTIOC3A/MTCLKB/TIOCB6/TMO2/PO31/TXD8/SMOSI8/SSDA8/MISOA/IRQ14	N9
	CS1#	P61/CS1#/CTS9#/RTS9#/SS9#	B11
		P71/CS1#	H11
		PC6/A22/CS1#/MTIOC3C/MTCLKA/TIOCA6/TMCI2/PO30/RXD8/SMISO8/SSCL8/MOSIA/IRQ13	M8
	CS2#	P62/CS2#	A11
		P72/CS2#	H10
		PC5/A21/CS2#/WAIT#/MTIOC3B/MTCLKD/TIOCD6/TCLKF/TMRI2/PO29/SCK8/RSPCKA	L9
	CS3#	P63/CS3#	C10
		P73/CS3#/PO16	L12
		PC4/A20/CS3#/MTIOC3D/MTCLKC/TIOCC6/TCLKE/TMCI1/PO25/POE0#/SCK5/CTS8#/RTS8#/SS8#/SSLA0	L10
	CS4#	P64/CS4#	D9
		P74/CS4#/PO19/CTS11#/RTS11#/SS11#	N13
		P24/CS4#/MTIOC4A/MTCLKA/TIOCB4/TMRI1/PO4/SCK3	L4
	CS5#	P65/CS5#	E12
		P75/CS5#/PO20/SCK11	N12
		P25/CS5#/MTIOC4C/MTCLKB/TIOCA4/PO5/RXD3/SMISO3/SSCL3/ADTRG0#	L1
	CS6#	P66/CS6#/CTX2	E11
		P76/CS6#/PO22/RXD11/SMISO11/SSCL11	K10
		TDO/P26/CS6#/MTIOC2A/TMO1/PO6/TXD1/CTS3#/RTS3#/SMOSI1/SS3#/SSDA1/MOSIB	K2
	CS7#	P67/CS7#/CRX2/IRQ15	E13

		P77/CS7#/PO23/TXD11/SMOSI11/SSDA11	M10
		TCK/FINEC/P27/CS7#/MTIOC2B/TMCI3/PO7/SCK1/RSPCKB	K1
Interrupts	IRQ0	TDI/P30/MTIOC4B/TMRI3/PO8/RTCIC0/POE8#/RXD1/SMISO1/SSCL1/MISOB/IRQ0-DS	J4
		PD0/D0[A0/D0]/TIOCA7/IRQ0/AN008	B8
	IRQ1	TMS/P31/MTIOC4D/TMCI2/PO9/RTCIC1/CTS1#/RTS1#/SS1#/SSLB0/IRQ1-DS	K3
		PD1/D1[A1/D1]/MTIOC4B/TIOCB7/TCLKG/MOSIC/CTX0/IRQ1/AN009	C7
	IRQ2	P32/MTIOC0C/TIOCC0/TMO3/PO10/RTCOUT/RTCIC2/TXD6/TXD0/SMOSI6/SMOSI0/SSDA6/SSDA0/CTX0/IRQ2-DS	J3
		P12/TMCI1/RXD2/SMISO2/SSCL2/SCL0[FM+]/IRQ2	M4
		PD2/D2[A2/D2]/MTIOC4D/TIOCA8/MISOC/CRX0/IRQ2/AN010	A8
	IRQ3	P33/MTIOC0D/TIOCD0/TMRI3/PO11/POE3#/RXD6/RXD0/SMISO6/SMISO0/SSCL6/SSCL0/CRX0/IRQ3-DS	J2
		P13/MTIOC0B/TIOCA5/TMO3/PO13/TXD2/SMOSI2/SSDA2/SDA0[FM+]/IRQ3/ADTRG#	L5
		PD3/D3[A3/D3]/TIOCB8/TCLKH/POE8#/RSPCKC/IRQ3/AN011	C8
	IRQ4	PB1/A9/MTIOC0C/MTIOC4C/TIOCB3/TMCI0/PO25/TXD4/TXD6/SMOSI4/SMOSI6/SSDA4/SSDA6/IRQ4-DS	J13
		P14/MTIOC3A/MTCLKA/TIOCB5/TCLKA/TMRI2/PO15/CTS1#/RTS1#/SS1#/CTX1/USB0_DPUPE/IRQ4	N4
		TRST#/P34/MTIOC0A/TMCI3/PO12/POE2#/SCK6/SCK0/IRQ4	J1
		PD4/D4[A4/D4]/POE3#/SSLC0/IRQ4/AN012	B9
		PF5/IRQ4	D2
	IRQ5	PA4/A4/MTIC5U/MTCLKA/TIOCA1/TMRI0/PO20/TXD5/SMOSI5/SSDA5/SSLA0/IRQ5-DS	G13
		P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SCK3/SMISO1/SSCL1/CRX1-DS/IRQ5	K4
		PD5/D5[A5/D5]/MTIC5W/POE2#/SSLC1/IRQ5/AN013	D7
		PE5/D13[A13/D13]/MTIOC4C/MTIOC2B/TIOCB10/RSPCKB/IRQ5/AN3	D12
	IRQ6	PA3/A3/MTIOC0D/MTCLKD/TIOCD0/TCLKB/PO19/RXD5/SMISO5/SSCL5/IRQ6-DS	F10
		P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	L3
		PD6/D6[A6/D6]/MTIC5V/POE1#/SSLC2/IRQ6/AN6	A9
		PE6/D14[A14/D14]/TIOCA11/CTS4#/RTS4#/SS4#/MOSIB/IRQ6/AN4	D13
	IRQ7	PE2/D10[A10/D10]/MTIOC4A/TIOCA9/PO23/RXD12/SMISO12/SSCL12/RXDX12/SSLB3/MOSIB/IRQ7-DS/AN0	B12
		P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	M2
		PD7/D7[A7/D7]/MTIC5U/POE0#/SSLC3/IRQ7/AN7	C9
		PE7/D15[A15/D15]/TIOCB11/MISOB/IRQ7/AN5	D10
	IRQ8	P40/IRQ8-DS/AN000	A3
		P00/TMRI0/TXD6/SMOSI6/SSDA6/IRQ8/AN018	D1
		P20/MTIOC1A/TIOCB3/TMRI0/PO0/TXD0/SMOSI0/SSDA0/SDA1/IRQ8	N2
	IRQ9	P41/IRQ9-DS/AN001	C4

		P01/TMCI0/RXD6/SMISO6/SSCL6/IRQ9/AN019	D4
		P21/MTIOC1B/TIOCA3/TMCI0/PO1/RXD0/SMISO0/SSCL0/SCL1/IRQ9	N1
	IRQ10	P42/IRQ10-DS/AN002	A4
		P02/TMCI1/SCK6/IRQ10/AN020	C2
		TRDATA3/P55/WAIT#/MTIOC4D/TMO3/CRX1/IRQ10	N7
	IRQ11	P43/IRQ11-DS/AN003	B5
		P03/IRQ11/DA0	D3
		PA1/A1/MTIOC0B/MTCLKC/TIOCB0/PO17/SCK5/SSLA2/IRQ11	F12
	IRQ12	P44/IRQ12-DS/AN004	E5
		PB0/A8/MTIC5W/TIOCA3/PO24/RXD4/RXD6/SMISO4/SMISO6/SSCL4/SSCL6/RSPCKA/IRQ12	H12
		PC1/A17/MTIOC3A/TCLKD/PO18/SCK5/SSLA2/SDA3/IRQ12	M12
	IRQ13	P45/IRQ13-DS/AN005	A5
		P05/IRQ13/DA1	B3
		PC6/A22/CS1#/MTIOC3C/MTCLKA/TIOCA6/TMCI2/PO30/RXD8/SMISO8/SSCL8/MOSIA/IRQ13	M8
	IRQ14	P46/IRQ14-DS/AN006	C5
		PC0/A16/MTIOC3C/TCLKC/PO17/CTS5#/RTS5#/SS5#/SSLA1/SCL3/IRQ14	M11
		PC7/A23/CS0#/MTIOC3A/MTCLKB/TIOCB6/TMO2/PO31/TXD8/SMOSI8/SSDA8/MISOA/IRQ14	N9
	IRQ15	P47/IRQ15-DS/AN007	B6
		P07/IRQ15/ADTRG0#	A2
		P67/CS7#/CRX2/IRQ15	E13
MTU0-5	MTCLKA	P14/MTIOC3A/MTCLKA/TIOCB5/TCLKA/TMRI2/PO15/CTS1#/RTS1#/SS1#/CTX1/USB0_DPUPE/IRQ4	N4
		P24/CS4#/MTIOC4A/MTCLKA/TIOCB4/TMRI1/PO4/SCK3	L4
		PA4/A4/MTIC5U/MTCLKA/TIOCA1/TMRI0/PO20/TXD5/SMOSI5/SSDA5/SSLA0/IRQ5-DS	G13
		PC6/A22/CS1#/MTIOC3C/MTCLKA/TIOCA6/TMCI2/PO30/RXD8/SMISO8/SSCL8/MOSIA/IRQ13	M8
	MTCLKB	P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SCK3/SMISO1/SSCL1/CRX1-DS/IRQ5	K4
		P25/CS5#/MTIOC4C/MTCLKB/TIOCA4/PO5/RXD3/SMISO3/SSCL3/ADTRG0#	L1
		PA6/A6/MTIC5V/MTCLKB/TIOCA2/TMCI3/PO22/POE2#/CTS5#/RTS5#/SS5#/MOSIA	G11
		PC7/A23/CS0#/MTIOC3A/MTCLKB/TIOCB6/TMO2/PO31/TXD8/SMOSI8/SSDA8/MISOA/IRQ14	N9
	MTCLKC	P22/MTIOC3B/MTCLKC/TIOCC3/TMO0/PO2/SCK0	M1
		PA1/A1/MTIOC0B/MTCLKC/TIOCB0/PO17/SCK5/SSLA2/IRQ11	F12
		PC4/A20/CS3#/MTIOC3D/MTCLKC/TIOCC6/TCLKE/TMCI1/PO25/POE0#/SCK5/CTS8#/RTS8#/SS8#/SSLA0	L10
	MTCLKD	P23/MTIOC3D/MTCLKD/TIOCD3/PO3/TXD3/CTS0#/RTS0#/SMOSI3/SS0#/SSDA3	L2
		PA3/A3/MTIOC0D/MTCLKD/TIOCD0/TCLKB/PO19/RXD5/SMISO5/SSCL5/IRQ6-DS	F10

		PC5/A21/CS2#/WAIT#/MTIOC3B/MTCLKD/TIOCD6/TCLKF/TMRI2/PO29/SCK8/RSPCKA	L9
MTU0	MTIOC0A	TRST#/P34/MTIOC0A/TMCI3/PO12/POE2#/SCK6/SCK0/IRQ4	J1
		PB3/A11/MTIOC0A/MTIOC4A/TIOCD3/TCLKD/TMO0/PO27/POE3#/SCK4/SCK6	J10
	MTIOC0B	P13/MTIOC0B/TIOCA5/TMO3/PO13/TXD2/SMOSI2/SSDA2/SDA0[FM+]/IRQ3/ADTRG#	L5
		P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SCK3/SMISO1/SSCL1/CRX1-DS/IRQ5	K4
		PA1/A1/MTIOC0B/MTCLKC/TIOCB0/PO17/SCK5/SSLA2/IRQ11	F12
	MTIOC0C	P32/MTIOC0C/TIOCC0/TMO3/PO10/RTCOUT/RTCIC2/TXD6/TXD0/SMOSI6/SMOSI0/SSDA6/SSDA0/CTX0/IRQ2-DS	J3
		PB1/A9/MTIOC0C/MTIOC4C/TIOCB3/TMCI0/PO25/TXD4/TXD6/SMOSI4/SMOSI6/SSDA4/SSDA6/IRQ4-DS	J13
	MTIOC0D	P33/MTIOC0D/TIOCD0/TMRI3/PO11/POE3#/RXD6/RXD0/SMISO6/SMISO0/SSCL6/SSCL0/CRX0/IRQ3-DS	J2
		PA3/A3/MTIOC0D/MTCLKD/TIOCD0/TCLKB/PO19/RXD5/SMISO5/SSCL5/IRQ6-DS	F10
MTU1	MTIOC1A	P20/MTIOC1A/TIOCB3/TMRI0/PO0/TXD0/SMOSI0/SSDA0/SDA1/IRQ8	N2
		PE4/D12[A12/D12]/MTIOC4D/MTIOC1A/TIOCA10/PO28/SSLB0/AN2	B13
	MTIOC1B	P21/MTIOC1B/TIOCA3/TMCI0/PO1/RXD0/SMISO0/SSCL0/SCL1/IRQ9	N1
		PB5/A13/MTIOC2A/MTIOC1B/TIOCB4/TMRI1/PO29/POE1#/SCK9	K13
MTU2	MTIOC2A	TDO/P26/CS6#/MTIOC2A/TMO1/PO6/TXD1/CTS3#/RTS3#/SMOSI1/SS3#/SSDA1/MOSIB	K2
		PB5/A13/MTIOC2A/MTIOC1B/TIOCB4/TMRI1/PO29/POE1#/SCK9	K13
	MTIOC2B	TCK/FINEC/P27/CS7#/MTIOC2B/TMCI3/PO7/SCK1/RSPCKB	K1
		PE5/D13[A13/D13]/MTIOC4C/MTIOC2B/TIOCB10/RSPCKB/IRQ5/AN3	D12
MTU3	MTIOC3A	P14/MTIOC3A/MTCLKA/TIOCB5/TCLKA/TMRI2/PO15/CTS1#/RTS1#/SS1#/CTX1/USB0_DPUPE/IRQ4	N4
		P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	M2
		PC1/A17/MTIOC3A/TCLKD/PO18/SCK5/SSLA2/SDA3/IRQ12	M12
		PC7/A23/CS0#/MTIOC3A/MTCLKB/TIOCB6/TMO2/PO31/TXD8/SMOSI8/SSDA8/MISOA/IRQ14	N9
	MTIOC3B	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	M2
		P22/MTIOC3B/MTCLKC/TIOCC3/TMO0/PO2/SCK0	M1
		TRDATA0/P80/MTIOC3B/PO26/SCK10	K9
		PB7/A15/MTIOC3B/TIOCB5/PO31/TXD9/SMOSI9/SSDA9	K11
		PC5/A21/CS2#/WAIT#/MTIOC3B/MTCLKD/TIOCD6/TCLKF/TMRI2/PO29/SCK8/RSPCKA	L9
	MTIOC3C	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	L3
		P56/MTIOC3C/TIOCA1	L6
		PC0/A16/MTIOC3C/TCLKC/PO17/CTS5#/RTS5#/SS5#/SSLA1/SCL3/IRQ14	M11

		PC6/A22/CS1#/MTIOC3C/MTCLKA/TIOCA6/TMCI2/PO30/RXD8/SMISO8/SSCL8/MOSIA/IRQ13	M8
		PJ3/MTIOC3C/CTS6#/RTS6#/CTS0#/RTS0#/SS6#/SS0#	F3
	MTIOC3D	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	L3
		P23/MTIOC3D/MTCLKD/TIOCD3/PO3/TXD3/CTS0#/RTS0#/SMOSI3/SS0#/SSDA3	L2
		TRDATA1/P81/MTIOC3D/PO27/RXD10/SMISO10/SSCL10	M9
		PB6/A14/MTIOC3D/TIOCA5/PO30/RXD9/SMISO9/SSCL9	K12
		PC4/A20/CS3#/MTIOC3D/MTCLKC/TIOCC6/TCLKE/TMCI1/PO25/POE0#/SCK5/CTS8#/RTS8#/SS8#/SSLA0	L10
MTU4	MTIOC4A	P24/CS4#/MTIOC4A/MTCLKA/TIOCB4/TMRI1/PO4/SCK3	L4
		TRSYNC#/P82/MTIOC4A/PO28/TXD10/SMOSI10/SSDA10	N10
		PA0/A0/BC0#/MTIOC4A/TIOCA0/PO16/SSLA1	E10
		PB3/A11/MTIOC0A/MTIOC4A/TIOCD3/TCLKD/TMO0/PO27/POE3#/SCK4/SCK6	J10
		PE2/D10[A10/D10]/MTIOC4A/TIOCA9/PO23/RXD12/SMISO12/SSCL12/RXDX12/SSLB3/MOSIB/IRQ7-DS/AN0	B12
	MTIOC4B	TDI/P30/MTIOC4B/TMRI3/PO8/RTCIC0/POE8#/RXD1/SMISO1/SSCL1/MISOB/IRQ0-DS	J4
		TRDATA2/P54/ALE/MTIOC4B/TMCI1/CTS2#/RTS2#/SS2#/CTX1	K5
		PC2/A18/MTIOC4B/TCLKA/PO21/RXD5/SMISO5/SSCL5/SSLA3/IERXD	L11
		PD1/D1[A1/D1]/MTIOC4B/TIOCB7/TCLKG/MOSIC/CTX0/IRQ1/AN009	C7
		PE3/D11[A11/D11]/MTIOC4B/TIOCB9/PO26/POE8#/CTS12#/RTS12#/SS12#/MISOB/AN1	A13
	MTIOC4C	P25/CS5#/MTIOC4C/MTCLKB/TIOCA4/PO5/RXD3/SMISO3/SSCL3/ADTRG0#	L1
		TRCLK/P83/MTIOC4C/CTS10#/RTS10#/SS10#	L8
		PB1/A9/MTIOC0C/MTIOC4C/TIOCB3/TMCI0/PO25/TXD4/TXD6/SMOSI4/SMOSI6/SSDA4/SSDA6/IRQ4-DS	J13
		PE1/D9[A9/D9]/MTIOC4C/TIOCD9/PO18/TXD12/SMOSI12/SSDA12/TXDX12/SIOX12/SSLB2/RSPCKB/ANEX1	A12
		PE5/D13[A13/D13]/MTIOC4C/MTIOC2B/TIOCB10/RSPCKB/IRQ5/AN3	D12
	MTIOC4D	TMS/P31/MTIOC4D/TMCI2/PO9/RTCIC1/CTS1#/RTS1#/SS1#/SSLB0/IRQ1-DS	K3
		TRDATA3/P55/WAIT#/MTIOC4D/TMO3/CRX1/IRQ10	N7
		PC3/A19/MTIOC4D/TCLKB/PO24/TXD5/SMOSI5/SSDA5/IETXD	N11
		PD2/D2[A2/D2]/MTIOC4D/TIOCA8/MISOC/CRX0/IRQ2/AN010	A8
		PE4/D12[A12/D12]/MTIOC4D/MTIOC1A/TIOCA10/PO28/SSLB0/AN2	B13
MTU5	MTIC5U	PA4/A4/MTIC5U/MTCLKA/TIOCA1/TMRI0/PO20/TXD5/SMOSI5/SSDA5/SSLA0/IRQ5-DS	G13
		PD7/D7[A7/D7]/MTIC5U/POE0#/SSLC3/IRQ7/AN7	C9
	MTIC5V	PA6/A6/MTIC5V/MTCLKB/TIOCA2/TMCI3/PO22/POE2#/CTS5#/RTS5#/SS5#/MOSIA	G11
		PD6/D6[A6/D6]/MTIC5V/POE1#/SSLC2/IRQ6/AN6	A9
	MTIC5W	PB0/A8/MTIC5W/TIOCA3/PO24/RXD4/RXD6/SMISO4/SMISO6/SSCL4/	H12

		SSCL6/RSPCKA/IRQ12	
		PD5/D5[A5/D5]/MTIC5W/POE2#/SSLC1/IRQ5/AN013	D7
POE	POE0#	PC4/A20/CS3#/MTIOC3D/MTCLKC/TIOCC6/TCLKE/TMCI1/PO25/POE0#/SCK5/CTS8#/RTS8#/SS8#/SSLA0	L10
		PD7/D7[A7/D7]/MTIC5U/POE0#/SSLC3/IRQ7/AN7	C9
	POE1#	PB5/A13/MTIOC2A/MTIOC1B/TIOCB4/TMRI1/PO29/POE1#/SCK9	K13
		PD6/D6[A6/D6]/MTIC5V/POE1#/SSLC2/IRQ6/AN6	A9
	POE2#	TRST#/P34/MTIOC0A/TMCI3/PO12/POE2#/SCK6/SCK0/IRQ4	J1
		PA6/A6/MTIC5V/MTCLKB/TIOCA2/TMCI3/PO22/POE2#/CTS5#/RTS5#/SS5#/MOSIA	G11
		PD5/D5[A5/D5]/MTIC5W/POE2#/SSLC1/IRQ5/AN013	D7
	POE3#	P33/MTIOC0D/TIOCD0/TMRI3/PO11/POE3#/RXD6/RXD0/SMISO6/SMISO0/SSCL6/SSCL0/CRX0/IRQ3-DS	J2
		PB3/A11/MTIOC0A/MTIOC4A/TIOCD3/TCLKD/TMO0/PO27/POE3#/SCK4/SCK6	J10
		PD4/D4[A4/D4]/POE3#/SSLC0/IRQ4/AN012	B9
	POE8#	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	M2
		TDI/P30/MTIOC4B/TMRI3/PO8/RTCIC0/POE8#/RXD1/SMISO1/SSCL1/MISOB/IRQ0-DS	J4
		PD3/D3[A3/D3]/TIOCB8/TCLKH/POE8#/RSPCKC/IRQ3/AN011	C8
		PE3/D11[A11/D11]/MTIOC4B/TIOCB9/PO26/POE8#/CTS12#/RTS12#/SS12#/MISOB/AN1	A13
TPU0-5	TCLKA	P14/MTIOC3A/MTCLKA/TIOCB5/TCLKA/TMRI2/PO15/CTS1#/RTS1#/SS1#/CTX1/USB0_DPUPE/IRQ4	N4
		PC2/A18/MTIOC4B/TCLKA/PO21/RXD5/SMISO5/SSCL5/SSLA3/IERXD	L11
	TCLKB	P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SCK3/SMISO1/SSCL1/CRX1-DS/IRQ5	K4
		PA3/A3/MTIOC0D/MTCLKD/TIOCD0/TCLKB/PO19/RXD5/SMISO5/SSCL5/IRQ6-DS	F10
		PC3/A19/MTIOC4D/TCLKB/PO24/TXD5/SMOSI5/SSDA5/IETXD	N11
	TCLKC	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	L3
		PB2/A10/TIOCC3/TCLKC/PO26/CTS4#/RTS4#/CTS6#/RTS6#/SS4#/SS6#	J12
		PC0/A16/MTIOC3C/TCLKC/PO17/CTS5#/RTS5#/SS5#/SSLA1/SCL3/IRQ14	M11
	TCLKD	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	M2
		PB3/A11/MTIOC0A/MTIOC4A/TIOCD3/TCLKD/TMO0/PO27/POE3#/SCK4/SCK6	J10
		PC1/A17/MTIOC3A/TCLKD/PO18/SCK5/SSLA2/SDA3/IRQ12	M12
TPU0	TIOCA0	P86/TIOCA0	M3
		PA0/A0/BC0#/MTIOC4A/TIOCA0/PO16/SSLA1	E10
	TIOCB0	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	M2

		PA1/A1/MTIOC0B/MTCLKC/TIOCB0/PO17/SCK5/SSLA2/IRQ11	F12
	TIOCD0	P33/MTIOC0D/TIOCD0/TMRI3/PO11/POE3#/RXD6/RXD0/SMISO6/SMISO0/SSCL6/SSCL0/CRX0/IRQ3-DS	J2
		PA3/A3/MTIOC0D/MTCLKD/TIOCD0/TCLKB/PO19/RXD5/SMISO5/SSCL5/IRQ6-DS	F10
TPU1	TIOCA1	P56/MTIOC3C/TIOCA1	L6
		PA4/A4/MTIC5U/MTCLKA/TIOCA1/TMRI0/PO20/TXD5/SMOSI5/SSDA5/SSLA0/IRQ5-DS	G13
	TIOCB1	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	L3
		PA5/A5/TIOCB1/PO21/RSPCKA	G10
TPU2	TIOCA2	P87/TIOCA2	N3
		PA6/A6/MTIC5V/MTCLKB/TIOCA2/TMCI3/PO22/POE2#/CTS5#/RTS5#/SS5#/MOSIA	G11
	TIOCB2	P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SCK3/SMISO1/SSCL1/CRX1-DS/IRQ5	K4
		PA7/A7/TIOCB2/PO23/MISOA	H13
TPU3	TIOCA3	P21/MTIOC1B/TIOCA3/TMCI0/PO1/RXD0/SMISO0/SSCL0/SCL1/IRQ9	N1
		PB0/A8/MTIC5W/TIOCA3/PO24/RXD4/RXD6/SMISO4/SMISO6/SSCL4/SSCL6/RSPCKA/IRQ12	H12
	TIOCB3	P20/MTIOC1A/TIOCB3/TMRI0/PO0/TXD0/SMOSI0/SSDA0/SDA1/IRQ8	N2
		PB1/A9/MTIOC0C/MTIOC4C/TIOCB3/TMCI0/PO25/TXD4/TXD6/SMOSI4/SMOSI6/SSDA4/SSDA6/IRQ4-DS	J13
	TIOCC3	P22/MTIOC3B/MTCLKC/TIOCC3/TMO0/PO2/SCK0	M1
		PB2/A10/TIOCC3/TCLKC/PO26/CTS4#/RTS4#/CTS6#/RTS6#/SS4#/SS6#	J12
	TIOCD3	P23/MTIOC3D/MTCLKD/TIOCD3/PO3/TXD3/CTS0#/RTS0#/SMOSI3/SS0#/SSDA3	L2
		PB3/A11/MTIOC0A/MTIOC4A/TIOCD3/TCLKD/TMO0/PO27/POE3#/SCK4/SCK6	J10
TPU4	TIOCA4	P25/CS5#/MTIOC4C/MTCLKB/TIOCA4/PO5/RXD3/SMISO3/SSCL3/ADTRG0#	L1
		PB4/A12/TIOCA4/PO28/CTS9#/RTS9#/SS9#	J11
	TIOCB4	P24/CS4#/MTIOC4A/MTCLKA/TIOCB4/TMRI1/PO4/SCK3	L4
		PB5/A13/MTIOC2A/MTIOC1B/TIOCB4/TMRI1/PO29/POE1#/SCK9	K13
TPU5	TIOCA5	P13/MTIOC0B/TIOCA5/TMO3/PO13/TXD2/SMOSI2/SSDA2/SDA0[FM+]/IRQ3/ADTRG#	L5
		PB6/A14/MTIOC3D/TIOCA5/PO30/RXD9/SMISO9/SSCL9	K12
	TIOCB5	P14/MTIOC3A/MTCLKA/TIOCB5/TCLKA/TMRI2/PO15/CTS1#/RTS1#/SS1#/CTX1/USB0_DPUPE/IRQ4	N4
		PB7/A15/MTIOC3B/TIOCB5/PO31/TXD9/SMOSI9/SSDA9	K11
PPG Group3	PO13	P13/MTIOC0B/TIOCA5/TMO3/PO13/TXD2/SMOSI2/SSDA2/SDA0[FM+]/IRQ3/ADTRG#	L5
		P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SCK3/SMISO1/SSCL1/CRX1-DS/IRQ5	K4
	PO15	P14/MTIOC3A/MTCLKA/TIOCB5/TCLKA/TMRI2/PO15/CTS1#/RTS1#/S	N4

		S1#/CTX1/USB0_DPUPE/IRQ4	
		P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	M2
PPG Group4	PO16	P73/CS3#/PO16	L12
		PA0/A0/BC0#/MTIOC4A/TIOCA0/PO16/SSLA1	E10
	PO17	PA1/A1/MTIOC0B/MTCLKC/TIOCB0/PO17/SCK5/SSLA2/IRQ11	F12
		PC0/A16/MTIOC3C/TCLKC/PO17/CTS5#/RTS5#/SS5#/SSLA1/SCL3/IRQ14	M11
	PO18	PA2/A2/PO18/RXD5/SMISO5/SSCL5/SSLA3	F13
		PC1/A17/MTIOC3A/TCLKD/PO18/SCK5/SSLA2/SDA3/IRQ12	M12
		PE1/D9[A9/D9]/MTIOC4C/TIOCD9/PO18/TXD12/SMOSI12/SSDA12/TXDX12/SIOX12/SSLB2/RSPCKB/ANEX1	A12
	PO19	P74/CS4#/PO19/CTS11#/RTS11#/SS11#	N13
		PA3/A3/MTIOC0D/MTCLKD/TIOCD0/TCLKB/PO19/RXD5/SMISO5/SSCL5/IRQ6-DS	F10
PPG Group5	PO20	P75/CS5#/PO20/SCK11	N12
		PA4/A4/MTIC5U/MTCLKA/TIOCA1/TMRI0/PO20/TXD5/SMOSI5/SSDA5/SSLA0/IRQ5-DS	G13
	PO21	PA5/A5/TIOCB1/PO21/RSPCKA	G10
		PC2/A18/MTIOC4B/TCLKA/PO21/RXD5/SMISO5/SSCL5/SSLA3/IERXD	L11
	PO22	P76/CS6#/PO22/RXD11/SMISO11/SSCL11	K10
		PA6/A6/MTIC5V/MTCLKB/TIOCA2/TMCI3/PO22/POE2#/CTS5#/RTS5#/SS5#/MOSIA	G11
	PO23	P77/CS7#/PO23/TXD11/SMOSI11/SSDA11	M10
		PA7/A7/TIOCB2/PO23/MISOA	H13
		PE2/D10[A10/D10]/MTIOC4A/TIOCA9/PO23/RXD12/SMISO12/SSCL12/RXDX12/SSLB3/MOSIB/IRQ7-DS/AN0	B12
PPG Group6	PO24	PB0/A8/MTIC5W/TIOCA3/PO24/RXD4/RXD6/SMISO4/SMISO6/SSCL4/SSCL6/RSPCKA/IRQ12	H12
		PC3/A19/MTIOC4D/TCLKB/PO24/TXD5/SMOSI5/SSDA5/IETXD	N11
	PO25	PB1/A9/MTIOC0C/MTIOC4C/TIOCB3/TMCI0/PO25/TXD4/TXD6/SMOSI4/SMOSI6/SSDA4/SSDA6/IRQ4-DS	J13
		PC4/A20/CS3#/MTIOC3D/MTCLKC/TIOCC6/TCLKE/TMCI1/PO25/POE0#/SCK5/CTS8#/RTS8#/SS8#/SSLA0	L10
	PO26	TRDATA0/P80/MTIOC3B/PO26/SCK10	K9
		PB2/A10/TIOCC3/TCLKC/PO26/CTS4#/RTS4#/CTS6#/RTS6#/SS4#/SS6#	J12
		PE3/D11[A11/D11]/MTIOC4B/TIOCB9/PO26/POE8#/CTS12#/RTS12#/SS12#/MISOB/AN1	A13
	PO27	TRDATA1/P81/MTIOC3D/PO27/RXD10/SMISO10/SSCL10	M9
		PB3/A11/MTIOC0A/MTIOC4A/TIOCD3/TCLKD/TMO0/PO27/POE3#/SCK4/SCK6	J10
PPG Group7	PO28	TRSYNC#/P82/MTIOC4A/PO28/TXD10/SMOSI10/SSDA10	N10
		PB4/A12/TIOCA4/PO28/CTS9#/RTS9#/SS9#	J11
		PE4/D12[A12/D12]/MTIOC4D/MTIOC1A/TIOCA10/PO28/SSLB0/AN2	B13
	PO29	PB5/A13/MTIOC2A/MTIOC1B/TIOCB4/TMRI1/PO29/POE1#/SCK9	K13

		PC5/A21/CS2#/WAIT#/MTIOC3B/MTCLKD/TIOCD6/TCLKF/TMRI2/PO29/SCK8/RSPCKA	L9
	PO30	PB6/A14/MTIOC3D/TIOCA5/PO30/RXD9/SMISO9/SSCL9	K12
		PC6/A22/CS1#/MTIOC3C/MTCLKA/TIOCA6/TMCI2/PO30/RXD8/SMISO8/SSCL8/MOSIA/IRQ13	M8
	PO31	PB7/A15/MTIOC3B/TIOCB5/PO31/TXD9/SMOSI9/SSDA9	K11
		PC7/A23/CS0#/MTIOC3A/MTCLKB/TIOCB6/TMO2/PO31/TXD8/SMOSI8/SSDA8/MISOA/IRQ14	N9
TMR0	TMO0	P22/MTIOC3B/MTCLKC/TIOCC3/TMO0/PO2/SCK0	M1
		PB3/A11/MTIOC0A/MTIOC4A/TIOCD3/TCLKD/TMO0/PO27/POE3#/SCK4/SCK6	J10
	TMCI0	P01/TMCI0/RXD6/SMISO6/SSCL6/IRQ9/AN019	D4
		P21/MTIOC1B/TIOCA3/TMCI0/PO1/RXD0/SMISO0/SSCL0/SCL1/IRQ9	N1
		PB1/A9/MTIOC0C/MTIOC4C/TIOCB3/TMCI0/PO25/TXD4/TXD6/SMOSI4/SMOSI6/SSDA4/SSDA6/IRQ4-DS	J13
	TMRI0	P00/TMRI0/TXD6/SMOSI6/SSDA6/IRQ8/AN018	D1
		P20/MTIOC1A/TIOCB3/TMRI0/PO0/TXD0/SMOSI0/SSDA0/SDA1/IRQ8	N2
		PA4/A4/MTIC5U/MTCLKA/TIOCA1/TMRI0/PO20/TXD5/SMOSI5/SSDA5/SSLA0/IRQ5-DS	G13
TMR1	TMO1	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	M2
		TDO/P26/CS6#/MTIOC2A/TMO1/PO6/TXD1/CTS3#/RTS3#/SMOSI1/SS3#/SSDA1/MOSIB	K2
	TMCI1	P02/TMCI1/SCK6/IRQ10/AN020	C2
		P12/TMCI1/RXD2/SMISO2/SSCL2/SCL0[FM+]/IRQ2	M4
		TRDATA2/P54/ALE/MTIOC4B/TMCI1/CTS2#/RTS2#/SS2#/CTX1	K5
		PC4/A20/CS3#/MTIOC3D/MTCLKC/TIOCC6/TCLKE/TMCI1/PO25/POE0#/SCK5/CTS8#/RTS8#/SS8#/SSLA0	L10
	TMRI1	P24/CS4#/MTIOC4A/MTCLKA/TIOCB4/TMRI1/PO4/SCK3	L4
		PB5/A13/MTIOC2A/MTIOC1B/TIOCB4/TMRI1/PO29/POE1#/SCK9	K13
TMR2	TMO2	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	L3
		PC7/A23/CS0#/MTIOC3A/MTCLKB/TIOCB6/TMO2/PO31/TXD8/SMOSI8/SSDA8/MISOA/IRQ14	N9
	TMCI2	P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SCK3/SMISO1/SSCL1/CRX1-DS/IRQ5	K4
		TMS/P31/MTIOC4D/TMCI2/PO9/RTCIC1/CTS1#/RTS1#/SS1#/SSLB0/IRQ1-DS	K3
		PC6/A22/CS1#/MTIOC3C/MTCLKA/TIOCA6/TMCI2/PO30/RXD8/SMISO8/SSCL8/MOSIA/IRQ13	M8
	TMRI2	P14/MTIOC3A/MTCLKA/TIOCB5/TCLKA/TMRI2/PO15/CTS1#/RTS1#/SS1#/CTX1/USB0_DPUPE/IRQ4	N4
		PC5/A21/CS2#/WAIT#/MTIOC3B/MTCLKD/TIOCD6/TCLKF/TMRI2/PO29/SCK8/RSPCKA	L9
TMR3	TMO3	P13/MTIOC0B/TIOCA5/TMO3/PO13/TXD2/SMOSI2/SSDA2/SDA0[FM+]/IRQ3/ADTRG#	L5

		P32/MTIOC0C/TIOCC0/TMO3/PO10/RTCOUT/RTCIC2/TXD6/TXD0/SMOSI6/SMOSI0/SSDA6/SSDA0/CTX0/IRQ2-DS	J3
		TRDATA3/P55/WAIT#/MTIOC4D/TMO3/CRX1/IRQ10	N7
	TMCI3	TCK/FINEC/P27/CS7#/MTIOC2B/TMCI3/PO7/SCK1/RSPCKB	K1
		TRST#/P34/MTIOC0A/TMCI3/PO12/POE2#/SCK6/SCK0/IRQ4	J1
		PA6/A6/MTIC5V/MTCLKB/TIOCA2/TMCI3/PO22/POE2#/CTS5#/RTS5#/SS5#/MOSIA	G11
	TMRI3	TDI/P30/MTIOC4B/TMRI3/PO8/RTCIC0/POE8#/RXD1/SMISO1/SSCL1/MISOB/IRQ0-DS	J4
		P33/MTIOC0D/TIOCD0/TMRI3/PO11/POE3#/RXD6/RXD0/SMISO6/SMISO0/SSCL6/SSCL0/CRX0/IRQ3-DS	J2
SCI0	RXD0 SSCL0 SMISO0	P21/MTIOC1B/TIOCA3/TMCI0/PO1/RXD0/SMISO0/SSCL0/SCL1/IRQ9	N1
		P33/MTIOC0D/TIOCD0/TMRI3/PO11/POE3#/RXD6/RXD0/SMISO6/SMISO0/SSCL6/SSCL0/CRX0/IRQ3-DS	J2
	TXD0 SSDA0 SMOSI0	P20/MTIOC1A/TIOCB3/TMRI0/PO0/TXD0/SMOSI0/SSDA0/SDA1/IRQ8	N2
		P32/MTIOC0C/TIOCC0/TMO3/PO10/RTCOUT/RTCIC2/TXD6/TXD0/SMOSI6/SMOSI0/SSDA6/SSDA0/CTX0/IRQ2-DS	J3
	SCK0	P22/MTIOC3B/MTCLKC/TIOCC3/TMO0/PO2/SCK0	M1
		TRST#/P34/MTIOC0A/TMCI3/PO12/POE2#/SCK6/SCK0/IRQ4	J1
	CTS0# RTS0# SS0#	P23/MTIOC3D/MTCLKD/TIOCD3/PO3/TXD3/CTS0#/RTS0#/SMOSI3/SS0#/SSDA3	L2
		PJ3/MTIOC3C/CTS6#/RTS6#/CTS0#/RTS0#/SS6#/SS0#	F3
SCI1	RXD1 SSCL1 SMISO1	P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SCK3/SMISO1/SSCL1/CRX1-DS/IRQ5	K4
		TDI/P30/MTIOC4B/TMRI3/PO8/RTCIC0/POE8#/RXD1/SMISO1/SSCL1/MISOB/IRQ0-DS	J4
	TXD1 SSDA1 SMOSI1	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	L3
		TDO/P26/CS6#/MTIOC2A/TMO1/PO6/TXD1/CTS3#/RTS3#/SMOSI1/SS3#/SSDA1/MOSIB	K2
	SCK1	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	M2
		TCK/FINEC/P27/CS7#/MTIOC2B/TMCI3/PO7/SCK1/RSPCKB	K1
	CTS1# RTS1# SS1#	P14/MTIOC3A/MTCLKA/TIOCB5/TCLKA/TMRI2/PO15/CTS1#/RTS1#/SS1#/CTX1/USB0_DPUPE/IRQ4	N4
		TMS/P31/MTIOC4D/TMCI2/PO9/RTCIC1/CTS1#/RTS1#/SS1#/SSLB0/IRQ1-DS	K3
SCI2	RXD2 SSCL2 SMISO2	P12/TMCI1/RXD2/SMISO2/SSCL2/SCL0[FM+]/IRQ2	M4
		P52/RD#/RXD2/SMISO2/SSCL2/SSLB3	L7
	TXD2 SSDA2 SMOSI2	P13/MTIOC0B/TIOCA5/TMO3/PO13/TXD2/SMOSI2/SSDA2/SDA0[FM+]/IRQ3/ADTRG#	L5
		P50/WR0#/WR#/TXD2/SMOSI2/SSDA2/SSLB1	M7
SCI3	RXD3 SSCL3	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_	L3

	SMISO3	VBUS/IRQ6/ADTRG0#	
		P25/CS5#/MTIOC4C/MTCLKB/TIOCA4/PO5/RXD3/SMISO3/SSCL3/ADTRG0#	L1
	TXD3 SSDA3 SMOSI3	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	M2
		P23/MTIOC3D/MTCLKD/TIOCD3/PO3/TXD3/CTS0#/RTS0#/SMOSI3/SS0#/SSDA3	L2
	SCK3	P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SCK3/SMISO1/SSCL1/CRX1-DS/IRQ5	K4
		P24/CS4#/MTIOC4A/MTCLKA/TIOCB4/TMRI1/PO4/SCK3	L4
SCI4	RXD4 SSCL4 SMISO4	PB0/A8/MTIC5W/TIOCA3/PO24/RXD4/RXD6/SMISO4/SMISO6/SSCL4/SSCL6/RSPCKA/IRQ12	H12
		PK4/RXD4/SMISO4/SSCL4	C13
	TXD4 SSDA4 SMOSI4	PB1/A9/MTIOC0C/MTIOC4C/TIOCB3/TMCI0/PO25/TXD4/TXD6/SMOSI4/SMOSI6/SSDA4/SSDA6/IRQ4-DS	J13
		PK5/TXD4/SMOSI4/SSDA4	D11
	SCK4	P70/SCK4	C12
		PB3/A11/MTIOC0A/MTIOC4A/TIOCD3/TCLKD/TMO0/PO27/POE3#/SCK4/SCK6	J10
	CTS4# RTS4# SS4#	PB2/A10/TIOCC3/TCLKC/PO26/CTS4#/RTS4#/CTS6#/RTS6#/SS4#/SS6#	J12
		PE6/D14[A14/D14]/TIOCA11/CTS4#/RTS4#/SS4#/MOSIB/IRQ6/AN4	D13
SCI5	RXD5 SSCL5 SMISO5	PA2/A2/PO18/RXD5/SMISO5/SSCL5/SSLA3	F13
		PA3/A3/MTIOC0D/MTCLKD/TIOCD0/TCLKB/PO19/RXD5/SMISO5/SSCL5/IRQ6-DS	F10
		PC2/A18/MTIOC4B/TCLKA/PO21/RXD5/SMISO5/SSCL5/SSLA3/IERXD	L11
	TXD5 SSDA5 SMOSI5	PA4/A4/MTIC5U/MTCLKA/TIOCA1/TMRI0/PO20/TXD5/SMOSI5/SSDA5/SSLA0/IRQ5-DS	G13
		PC3/A19/MTIOC4D/TCLKB/PO24/TXD5/SMOSI5/SSDA5/IETXD	N11
	SCK5	PA1/A1/MTIOC0B/MTCLKC/TIOCB0/PO17/SCK5/SSLA2/IRQ11	F12
		PC1/A17/MTIOC3A/TCLKD/PO18/SCK5/SSLA2/SDA3/IRQ12	M12
		PC4/A20/CS3#/MTIOC3D/MTCLKC/TIOCC6/TCLKE/TMCI1/PO25/POE0#/SCK5/CTS8#/RTS8#/SS8#/SSLA0	L10
	CTS5# RTS5# SS5#	PA6/A6/MTIC5V/MTCLKB/TIOCA2/TMCI3/PO22/POE2#/CTS5#/RTS5#/SS5#/MOSIA	G11
		PC0/A16/MTIOC3C/TCLKC/PO17/CTS5#/RTS5#/SS5#/SSLA1/SCL3/IRQ14	M11
SCI6	RXD6 SSCL6 SMISO6	P01/TMCI0/RXD6/SMISO6/SSCL6/IRQ9/AN019	D4
		P33/MTIOC0D/TIOCD0/TMRI3/PO11/POE3#/RXD6/RXD0/SMISO6/SMISO0/SSCL6/SSCL0/CRX0/IRQ3-DS	J2
		PB0/A8/MTIC5W/TIOCA3/PO24/RXD4/RXD6/SMISO4/SMISO6/SSCL4/SSCL6/RSPCKA/IRQ12	H12
	TXD6 SSDA6 SMOSI6	P00/TMRI0/TXD6/SMOSI6/SSDA6/IRQ8/AN018	D1
		P32/MTIOC0C/TIOCC0/TMO3/PO10/RTCOUT/RTCIC2/TXD6/TXD0/SMOSI6/SMOSI0/SSDA6/SSDA0/CTX0/IRQ2-DS	J3
		PB1/A9/MTIOC0C/MTIOC4C/TIOCB3/TMCI0/PO25/TXD4/TXD6/SMOSI4/SMOSI6/SSDA4/SSDA6/IRQ4-DS	J13

	SCK6	P02/TMCI1/SCK6/IRQ10/AN020	C2
		TRST#/P34/MTIOC0A/TMCI3/PO12/POE2#/SCK6/SCK0/IRQ4	J1
		PB3/A11/MTIOC0A/MTIOC4A/TIOCD3/TCLKD/TMO0/PO27/POE3#/SCK4/SCK6	J10
	CTS6# RTS6# SS6#	PB2/A10/TIOCC3/TCLKC/PO26/CTS4#/RTS4#/CTS6#/RTS6#/SS4#/SS6#	J12
		PJ3/MTIOC3C/CTS6#/RTS6#/CTS0#/RTS0#/SS6#/SS0#	F3
SCI9	RXD9 SSCL9 SMISO9	PB6/A14/MTIOC3D/TIOCA5/PO30/RXD9/SMISO9/SSCL9	K12
		PK3/RXD9/SMISO9/SSCL9	A10
	TXD9 SSDA9 SMOSI9	PB7/A15/MTIOC3B/TIOCB5/PO31/TXD9/SMOSI9/SSDA9	K11
		PK2/TXD9/SMOSI9/SSDA9	B10
	SCK9	P60/CS0#/SCK9	D8
		PB5/A13/MTIOC2A/MTIOC1B/TIOCB4/TMRI1/PO29/POE1#/SCK9	K13
	CTS9# RTS9# SS9#	P61/CS1#/CTS9#/RTS9#/SS9#	B11
		PB4/A12/TIOCA4/PO28/CTS9#/RTS9#/SS9#	J11
RSPI0	RSPCKA	PA5/A5/TIOCB1/PO21/RSPCKA	G10
		PB0/A8/MTIC5W/TIOCA3/PO24/RXD4/RXD6/SMISO4/SMISO6/SSCL4/SSCL6/RSPCKA/IRQ12	H12
		PC5/A21/CS2#/WAIT#/MTIOC3B/MTCLKD/TIOCD6/TCLKF/TMRI2/PO29/SCK8/RSPCKA	L9
	MOSIA	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	L3
		PA6/A6/MTIC5V/MTCLKB/TIOCA2/TMCI3/PO22/POE2#/CTS5#/RTS5#/SS5#/MOSIA	G11
		PC6/A22/CS1#/MTIOC3C/MTCLKA/TIOCA6/TMCI2/PO30/RXD8/SMISO8/SSCL8/MOSIA/IRQ13	M8
	MISOA	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	M2
		PA7/A7/TIOCB2/PO23/MISOA	H13
		PC7/A23/CS0#/MTIOC3A/MTCLKB/TIOCB6/TMO2/PO31/TXD8/SMOSI8/SSDA8/MISOA/IRQ14	N9
	SSLA0	PA4/A4/MTIC5U/MTCLKA/TIOCA1/TMRI0/PO20/TXD5/SMOSI5/SSDA5/SSLA0/IRQ5-DS	G13
		PC4/A20/CS3#/MTIOC3D/MTCLKC/TIOCC6/TCLKE/TMCI1/PO25/POE0#/SCK5/CTS8#/RTS8#/SS8#/SSLA0	L10
	SSLA1	PA0/A0/BC0#/MTIOC4A/TIOCA0/PO16/SSLA1	E10
		PC0/A16/MTIOC3C/TCLKC/PO17/CTS5#/RTS5#/SS5#/SSLA1/SCL3/IRQ14	M11
	SSLA2	PA1/A1/MTIOC0B/MTCLKC/TIOCB0/PO17/SCK5/SSLA2/IRQ11	F12
		PC1/A17/MTIOC3A/TCLKD/PO18/SCK5/SSLA2/SDA3/IRQ12	M12
	SSLA3	PA2/A2/PO18/RXD5/SMISO5/SSCL5/SSLA3	F13
		PC2/A18/MTIOC4B/TCLKA/PO21/RXD5/SMISO5/SSCL5/SSLA3/IERXD	L11

RSPI1	RSPCKB	TCK/FINEC/P27/CS7#/MTIOC2B/TMCI3/PO7/SCK1/RSPCKB	K1
		PE1/D9[A9/D9]/MTIOC4C/TIOCD9/PO18/TXD12/SMOSI12/SSDA12/TXDX12/SIOX12/SSLB2/RSPCKB/ANEX1	A12
		PE5/D13[A13/D13]/MTIOC4C/MTIOC2B/TIOCB10/RSPCKB/IRQ5/AN3	D12
	MOSIB	TDO/P26/CS6#/MTIOC2A/TMO1/PO6/TXD1/CTS3#/RTS3#/SMOSI1/SS3#/SSDA1/MOSIB	K2
		PE2/D10[A10/D10]/MTIOC4A/TIOCA9/PO23/RXD12/SMISO12/SSCL12/RXDX12/SSLB3/MOSIB/IRQ7-DS/AN0	B12
		PE6/D14[A14/D14]/TIOCA11/CTS4#/RTS4#/SS4#/MOSIB/IRQ6/AN4	D13
	MISOB	TDI/P30/MTIOC4B/TMRI3/PO8/RTCIC0/POE8#/RXD1/SMISO1/SSCL1/MISOB/IRQ0-DS	J4
		PE3/D11[A11/D11]/MTIOC4B/TIOCB9/PO26/POE8#/CTS12#/RTS12#/SS12#/MISOB/AN1	A13
		PE7/D15[A15/D15]/TIOCB11/MISOB/IRQ7/AN5	D10
	SSLB0	TMS/P31/MTIOC4D/TMCI2/PO9/RTCIC1/CTS1#/RTS1#/SS1#/SSLB0/IRQ1-DS	K3
		PE4/D12[A12/D12]/MTIOC4D/MTIOC1A/TIOCA10/PO28/SSLB0/AN2	B13
	SSLB1	P50/WR0#/WR#/TXD2/SMOSI2/SSDA2/SSLB1	M7
		PE0/D8[A8/D8]/TIOCC9/SCK12/SSLB1/ANEX0	C11
	SSLB2	P51/WR1#/BC1#/WAIT#/SCK2/SSLB2	K7
		PE1/D9[A9/D9]/MTIOC4C/TIOCD9/PO18/TXD12/SMOSI12/SSDA12/TXDX12/SIOX12/SSLB2/RSPCKB/ANEX1	A12
	SSLB3	P52/RD#/RXD2/SMISO2/SSCL2/SSLB3	L7
		PE2/D10[A10/D10]/MTIOC4A/TIOCA9/PO23/RXD12/SMISO12/SSCL12/RXDX12/SSLB3/MOSIB/IRQ7-DS/AN0	B12
IEB0	IERXD	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	L3
		PC2/A18/MTIOC4B/TCLKA/PO21/RXD5/SMISO5/SSCL5/SSLA3/IERXD	L11
	IETXD	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	M2
		PC3/A19/MTIOC4D/TCLKB/PO24/TXD5/SMOSI5/SSDA5/IETXD	N11
RTCA	RTCOUT	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	L3
		P32/MTIOC0C/TIOCC0/TMO3/PO10/RTCOUT/RTCIC2/TXD6/TXD0/SMOSI6/SMOSI0/SSDA6/SSDA0/CTX0/IRQ2-DS	J3
S12AD	ADTRG0#	P07/IRQ15/ADTRG0#	A2
		P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	L3
		P25/CS5#/MTIOC4C/MTCLKB/TIOCA4/PO5/RXD3/SMISO3/SSCL3/ADTRG0#	L1
ADA0	ADTRG#	P13/MTIOC0B/TIOCA5/TMO3/PO13/TXD2/SMOSI2/SSDA2/SDA0[FM+]/IRQ3/ADTRG#	L5
		P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	M2

Table a-1.4 144-pin LQFP (the Upper Row of Each Pair is the Default Selection)

Peripheral module	Pin function	Selection of assignment	Pin No.
Address bus	A16	PC0/A16/MTIOC3C/TCLKC/PO17/CTS5#/RTS5#/SS5#/SSLA1/SCL3/IRQ14	75
		P90/A16/TXD7/SMOSI7/SSDA7/AN014	131
	A17	PC1/A17/MTIOC3A/TCLKD/PO18/SCK5/SSLA2/SDA3/IRQ12	73
		P91/A17/SCK7/AN015	129
	A18	PC2/A18/MTIOC4B/TCLKA/PO21/RXD5/SMISO5/SSCL5/SSLA3/IERXD	70
		P92/A18/RXD7/SMISO7/SSCL7/AN016	128
	A19	PC3/A19/MTIOC4D/TCLKB/PO24/TXD5/SMOSI5/SSDA5/IETXD	67
		P93/A19/CTS7#/RTS7#/SS7#/AN017	127
Bus control	WAIT#	TRDATA3/P55/WAIT#/MTIOC4D/TMO3/CRX1/IRQ10	51
		PC5/A21/CS2#/WAIT#/MTIOC3B/MTCLKD/TIOCD6/TCLKF/TMRI2/PO29/SCK8/RSPCKA	62
		P51/WR1#/BC1#/WAIT#/SCK2/SSLB2	55
	CS0#	P60/CS0#/SCK9	117
		PC7/A23/CS0#/MTIOC3A/MTCLKB/TIOCB6/TMO2/PO31/TXD8/SMOSI8/SSDA8/MISOA/IRQ14	60
	CS1#	P61/CS1#/CTS9#/RTS9#/SS9#	115
		P71/CS1#	86
		PC6/A22/CS1#/MTIOC3C/MTCLKA/TIOCA6/TMCI2/PO30/RXD8/SMISO8/SSCL8/MOSIA/IRQ13	61
	CS2#	P62/CS2#	114
		P72/CS2#	85
		PC5/A21/CS2#/WAIT#/MTIOC3B/MTCLKD/TIOCD6/TCLKF/TMRI2/PO29/SCK8/RSPCKA	62
	CS3#	P63/CS3#	113
		P73/CS3#/PO16	77
		PC4/A20/CS3#/MTIOC3D/MTCLKC/TIOCC6/TCLKE/TMCI1/PO25/POE0#/SCK5/CTS8#/RTS8#/SS8#/SSLA0	66
	CS4#	P64/CS4#	112
		P74/CS4#/PO19/CTS11#/RTS11#/SS11#	72
		P24/CS4#/MTIOC4A/MTCLKA/TIOCB4/TMRI1/PO4/SCK3	33
	CS5#	P65/CS5#	100
		P75/CS5#/PO20/SCK11	71
		P25/CS5#/MTIOC4C/MTCLKB/TIOCA4/PO5/RXD3/SMISO3/SSCL3/ADTRG0#	32
	CS6#	P66/CS6#/CTX2	99
		P76/CS6#/PO22/RXD11/SMISO11/SSCL11	69
		TDO/P26/CS6#/MTIOC2A/TMO1/PO6/TXD1/CTS3#/RTS3#/SMOSI1/SS3#/SSDA1/MOSIB	31
	CS7#	P67/CS7#/CRX2/IRQ15	98
		P77/CS7#/PO23/TXD11/SMOSI11/SSDA11	68
		TCK/FINEC/P27/CS7#/MTIOC2B/TMCI3/PO7/SCK1/RSPCKB	30
Interrupts	IRQ0	TDI/P30/MTIOC4B/TMRI3/PO8/RTCIC0/POE8#/RXD1/SMISO1/SSCL1/	29

		MISOB/IRQ0-DS	
		PD0/D0[A0/D0]/TIOCA7/IRQ0/AN008	126
	IRQ1	TMS/P31/MTIOC4D/TMCI2/PO9/RTCIC1/CTS1#/RTS1#/SS1#/SSLB0/IRQ1-DS	28
		PD1/D1[A1/D1]/MTIOC4B/TIOCB7/TCLKG/MOSIC/CTX0/IRQ1/AN009	125
	IRQ2	P32/MTIOC0C/TIOCC0/TMO3/PO10/RTCOUT/RTCIC2/TXD6/TXD0/SMOSI6/SMOSI0/SSDA6/SSDA0/CTX0/IRQ2-DS	27
		P12/TMCI1/RXD2/SMISO2/SSCL2/SCL0[FM+]/IRQ2	45
		PD2/D2[A2/D2]/MTIOC4D/TIOCA8/MISOC/CRX0/IRQ2/AN010	124
	IRQ3	P33/MTIOC0D/TIOCD0/TMRI3/PO11/POE3#/RXD6/RXD0/SMISO6/SMISO0/SSCL6/SSCL0/CRX0/IRQ3-DS	26
		P13/MTIOC0B/TIOCA5/TMO3/PO13/TXD2/SMOSI2/SSDA2/SDA0[FM+]/IRQ3/ADTRG#	44
		PD3/D3[A3/D3]/TIOCB8/TCLKH/POE8#/RSPCKC/IRQ3/AN011	123
	IRQ4	PB1/A9/MTIOC0C/MTIOC4C/TIOCB3/TMCI0/PO25/TXD4/TXD6/SMOSI4/SMOSI6/SSDA4/SSDA6/IRQ4-DS	84
		P14/MTIOC3A/MTCLKA/TIOCB5/TCLKA/TMRI2/PO15/CTS1#/RTS1#/SS1#/CTX1/USB0_DPUPE/IRQ4	43
		TRST#/P34/MTIOC0A/TMCI3/PO12/POE2#/SCK6/SCK0/IRQ4	25
		PD4/D4[A4/D4]/POE3#/SSLC0/IRQ4/AN012	122
		PF5/IRQ4	9
	IRQ5	PA4/A4/MTIC5U/MTCLKA/TIOCA1/TMRI0/PO20/TXD5/SMOSI5/SSDA5/SSLA0/IRQ5-DS	92
		P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SCK3/SMISO1/SSCL1/CRX1-DS/IRQ5	42
		PD5/D5[A5/D5]/MTIC5W/POE2#/SSLC1/IRQ5/AN013	121
		PE5/D13[A13/D13]/MTIOC4C/MTIOC2B/TIOCB10/RSPCKB/IRQ5/AN3	106
	IRQ6	PA3/A3/MTIOC0D/MTCLKD/TIOCD0/TCLKB/PO19/RXD5/SMISO5/SSCL5/IRQ6-DS	94
		P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	40
		PD6/D6[A6/D6]/MTIC5V/POE1#/SSLC2/IRQ6/AN6	120
		PE6/D14[A14/D14]/TIOCA11/CTS4#/RTS4#/SS4#/MOSIB/IRQ6/AN4	102
	IRQ7	PE2/D10[A10/D10]/MTIOC4A/TIOCA9/PO23/RXD12/SMISO12/SSCL12/RXDX12/SSLB3/MOSIB/IRQ7-DS/AN0	109
		P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	38
		PD7/D7[A7/D7]/MTIC5U/POE0#/SSLC3/IRQ7/AN7	119
		PE7/D15[A15/D15]/TIOCB11/MISOB/IRQ7/AN5	101
	IRQ8	P40/IRQ8-DS/AN000	141
		P00/TMRI0/TXD6/SMOSI6/SSDA6/IRQ8/AN018	8
		P20/MTIOC1A/TIOCB3/TMRI0/PO0/TXD0/SMOSI0/SSDA0/SDA1/IRQ8	37
	IRQ9	P41/IRQ9-DS/AN001	139
		P01/TMCI0/RXD6/SMISO6/SSCL6/IRQ9/AN019	7
		P21/MTIOC1B/TIOCA3/TMCI0/PO1/RXD0/SMISO0/SSCL0/SCL1/IRQ9	36

	IRQ10	P42/IRQ10-DS/AN002	138
		P02/TMCI1/SCK6/IRQ10/AN020	6
		TRDATA3/P55/WAIT#/MTIOC4D/TMO3/CRX1/IRQ10	51
	IRQ11	P43/IRQ11-DS/AN003	137
		P03/IRQ11/DA0	4
		PA1/A1/MTIOC0B/MTCLKC/TIOCB0/PO17/SCK5/SSLA2/IRQ11	96
	IRQ12	P44/IRQ12-DS/AN004	136
		PB0/A8/MTIC5W/TIOCA3/PO24/RXD4/RXD6/SMISO4/SMISO6/SSCL4/SSCL6/RSPCKA/IRQ12	87
		PC1/A17/MTIOC3A/TCLKD/PO18/SCK5/SSLA2/SDA3/IRQ12	73
	IRQ13	P45/IRQ13-DS/AN005	135
		P05/IRQ13/DA1	2
		PC6/A22/CS1#/MTIOC3C/MTCLKA/TIOCA6/TMCI2/PO30/RXD8/SMISO8/SSCL8/MOSIA/IRQ13	61
	IRQ14	P46/IRQ14-DS/AN006	134
		PC0/A16/MTIOC3C/TCLKC/PO17/CTS5#/RTS5#/SS5#/SSLA1/SCL3/IRQ14	75
		PC7/A23/CS0#/MTIOC3A/MTCLKB/TIOCB6/TMO2/PO31/TXD8/SMOSI8/SSDA8/MISOA/IRQ14	60
	IRQ15	P47/IRQ15-DS/AN007	133
		P07/IRQ15/ADTRG0#	144
		P67/CS7#/CRX2/IRQ15	98
MTU0-5	MTCLKA	P14/MTIOC3A/MTCLKA/TIOCB5/TCLKA/TMRI2/PO15/CTS1#/RTS1#/SS1#/CTX1/USB0_DPUPE/IRQ4	43
		P24/CS4#/MTIOC4A/MTCLKA/TIOCB4/TMRI1/PO4/SCK3	33
		PA4/A4/MTIC5U/MTCLKA/TIOCA1/TMRI0/PO20/TXD5/SMOSI5/SSDA5/SSLA0/IRQ5-DS	92
		PC6/A22/CS1#/MTIOC3C/MTCLKA/TIOCA6/TMCI2/PO30/RXD8/SMISO8/SSCL8/MOSIA/IRQ13	61
	MTCLKB	P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SCK3/SMISO1/SSCL1/CRX1-DS/IRQ5	42
		P25/CS5#/MTIOC4C/MTCLKB/TIOCA4/PO5/RXD3/SMISO3/SSCL3/ADTRG0#	32
		PA6/A6/MTIC5V/MTCLKB/TIOCA2/TMCI3/PO22/POE2#/CTS5#/RTS5#/SS5#/MOSIA	89
		PC7/A23/CS0#/MTIOC3A/MTCLKB/TIOCB6/TMO2/PO31/TXD8/SMOSI8/SSDA8/MISOA/IRQ14	60
	MTCLKC	P22/MTIOC3B/MTCLKC/TIOCC3/TMO0/PO2/SCK0	35
		PA1/A1/MTIOC0B/MTCLKC/TIOCB0/PO17/SCK5/SSLA2/IRQ11	96
		PC4/A20/CS3#/MTIOC3D/MTCLKC/TIOCC6/TCLKE/TMCI1/PO25/POE0#/SCK5/CTS8#/RTS8#/SS8#/SSLA0	66
	MTCLKD	P23/MTIOC3D/MTCLKD/TIOCD3/PO3/TXD3/CTS0#/RTS0#/SMOSI3/SS0#/SSDA3	34
		PA3/A3/MTIOC0D/MTCLKD/TIOCD0/TCLKB/PO19/RXD5/SMISO5/SSCL5/IRQ6-DS	94
		PC5/A21/CS2#/WAIT#/MTIOC3B/MTCLKD/TIOCD6/TCLKF/TMRI2/PO29/SCK8/RSPCKA	62

MTU0	MTIOC0A	TRST#/P34/MTIOC0A/TMCI3/PO12/POE2#/SCK6/SCK0/IRQ4	25
		PB3/A11/MTIOC0A/MTIOC4A/TIOCD3/TCLKD/TMO0/PO27/POE3#/SCK4/SCK6	82
	MTIOC0B	P13/MTIOC0B/TIOCA5/TMO3/PO13/TXD2/SMOSI2/SSDA2/SDA0[FM+]/IRQ3/ADTRG#	44
		P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SCK3/SMISO1/SSCL1/CRX1-DS/IRQ5	42
		PA1/A1/MTIOC0B/MTCLKC/TIOCB0/PO17/SCK5/SSLA2/IRQ11	96
	MTIOC0C	P32/MTIOC0C/TIOCC0/TMO3/PO10/RTCOUT/RTCIC2/TXD6/TXD0/SMOSI6/SMOSI0/SSDA6/SSDA0/CTX0/IRQ2-DS	27
		PB1/A9/MTIOC0C/MTIOC4C/TIOCB3/TMCI0/PO25/TXD4/TXD6/SMOSI4/SMOSI6/SSDA4/SSDA6/IRQ4-DS	84
	MTIOC0D	P33/MTIOC0D/TIOCD0/TMRI3/PO11/POE3#/RXD6/RXD0/SMISO6/SMISO0/SSCL6/SSCL0/CRX0/IRQ3-DS	26
		PA3/A3/MTIOC0D/MTCLKD/TIOCD0/TCLKB/PO19/RXD5/SMISO5/SSCL5/IRQ6-DS	94
MTU1	MTIOC1A	P20/MTIOC1A/TIOCB3/TMRI0/PO0/TXD0/SMOSI0/SSDA0/SDA1/IRQ8	37
		PE4/D12[A12/D12]/MTIOC4D/MTIOC1A/TIOCA10/PO28/SSLB0/AN2	107
	MTIOC1B	P21/MTIOC1B/TIOCA3/TMCI0/PO1/RXD0/SMISO0/SSCL0/SCL1/IRQ9	36
		PB5/A13/MTIOC2A/MTIOC1B/TIOCB4/TMRI1/PO29/POE1#/SCK9	80
MTU2	MTIOC2A	TDO/P26/CS6#/MTIOC2A/TMO1/PO6/TXD1/CTS3#/RTS3#/SMOSI1/SS3#/SSDA1/MOSIB	31
		PB5/A13/MTIOC2A/MTIOC1B/TIOCB4/TMRI1/PO29/POE1#/SCK9	80
	MTIOC2B	TCK/FINEC/P27/CS7#/MTIOC2B/TMCI3/PO7/SCK1/RSPCKB	30
		PE5/D13[A13/D13]/MTIOC4C/MTIOC2B/TIOCB10/RSPCKB/IRQ5/AN3	106
MTU3	MTIOC3A	P14/MTIOC3A/MTCLKA/TIOCB5/TCLKA/TMRI2/PO15/CTS1#/RTS1#/SS1#/CTX1/USB0_DPUPE/IRQ4	43
		P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	38
		PC1/A17/MTIOC3A/TCLKD/PO18/SCK5/SSLA2/SDA3/IRQ12	73
		PC7/A23/CS0#/MTIOC3A/MTCLKB/TIOCB6/TMO2/PO31/TXD8/SMOSI8/SSDA8/MISOA/IRQ14	60
	MTIOC3B	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	38
		P22/MTIOC3B/MTCLKC/TIOCC3/TMO0/PO2/SCK0	35
		TRDATA0/P80/MTIOC3B/PO26/SCK10	65
		PB7/A15/MTIOC3B/TIOCB5/PO31/TXD9/SMOSI9/SSDA9	78
		PC5/A21/CS2#/WAIT#/MTIOC3B/MTCLKD/TIOCD6/TCLKF/TMRI2/PO29/SCK8/RSPCKA	62
	MTIOC3C	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	40
		P56/MTIOC3C/TIOCA1	50
		PC0/A16/MTIOC3C/TCLKC/PO17/CTS5#/RTS5#/SS5#/SSLA1/SCL3/IRQ14	75
		PC6/A22/CS1#/MTIOC3C/MTCLKA/TIOCA6/TMCI2/PO30/RXD8/SMISO8/SSCL8/MOSIA/IRQ13	61

		PJ3/MTIOC3C/CTS6#/RTS6#/CTS0#/RTS0#/SS6#/SS0#	13
	MTIOC3D	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	40
		P23/MTIOC3D/MTCLKD/TIOCD3/PO3/TXD3/CTS0#/RTS0#/SMOSI3/SS0#/SSDA3	34
		TRDATA1/P81/MTIOC3D/PO27/RXD10/SMISO10/SSCL10	64
		PB6/A14/MTIOC3D/TIOCA5/PO30/RXD9/SMISO9/SSCL9	79
		PC4/A20/CS3#/MTIOC3D/MTCLKC/TIOCC6/TCLKE/TMCI1/PO25/POE0#/SCK5/CTS8#/RTS8#/SS8#/SSLA0	66
MTU4	MTIOC4A	P24/CS4#/MTIOC4A/MTCLKA/TIOCB4/TMRI1/PO4/SCK3	33
		TRSYNC#/P82/MTIOC4A/PO28/TXD10/SMOSI10/SSDA10	63
		PA0/A0/BC0#/MTIOC4A/TIOCA0/PO16/SSLA1	97
		PB3/A11/MTIOC0A/MTIOC4A/TIOCD3/TCLKD/TMO0/PO27/POE3#/SCK4/SCK6	82
		PE2/D10[A10/D10]/MTIOC4A/TIOCA9/PO23/RXD12/SMISO12/SSCL12/RXDX12/SSLB3/MOSIB/IRQ7-DS/AN0	109
	MTIOC4B	TDI/P30/MTIOC4B/TMRI3/PO8/RTCIC0/POE8#/RXD1/SMISO1/SSCL1/MISOB/IRQ0-DS	29
		TRDATA2/P54/ALE/MTIOC4B/TMCI1/CTS2#/RTS2#/SS2#/CTX1	52
		PC2/A18/MTIOC4B/TCLKA/PO21/RXD5/SMISO5/SSCL5/SSLA3/IERXD	70
		PD1/D1[A1/D1]/MTIOC4B/TIOCB7/TCLKG/MOSIC/CTX0/IRQ1/AN009	125
		PE3/D11[A11/D11]/MTIOC4B/TIOCB9/PO26/POE8#/CTS12#/RTS12#/SS12#/MISOB/AN1	108
	MTIOC4C	P25/CS5#/MTIOC4C/MTCLKB/TIOCA4/PO5/RXD3/SMISO3/SSCL3/ADTRG0#	32
		TRCLK/P83/MTIOC4C/CTS10#/RTS10#/SS10#	58
		PB1/A9/MTIOC0C/MTIOC4C/TIOCB3/TMCI0/PO25/TXD4/TXD6/SMOSI4/SMOSI6/SSDA4/SSDA6/IRQ4-DS	84
		PE1/D9[A9/D9]/MTIOC4C/TIOCD9/PO18/TXD12/SMOSI12/SSDA12/TXDX12/SIOX12/SSLB2/RSPCKB/ANEX1	110
		PE5/D13[A13/D13]/MTIOC4C/MTIOC2B/TIOCB10/RSPCKB/IRQ5/AN3	106
	MTIOC4D	TMS/P31/MTIOC4D/TMCI2/PO9/RTCIC1/CTS1#/RTS1#/SS1#/SSLB0/IRQ1-DS	28
		TRDATA3/P55/WAIT#/MTIOC4D/TMO3/CRX1/IRQ10	51
		PC3/A19/MTIOC4D/TCLKB/PO24/TXD5/SMOSI5/SSDA5/IETXD	67
		PD2/D2[A2/D2]/MTIOC4D/TIOCA8/MISOC/CRX0/IRQ2/AN010	124
		PE4/D12[A12/D12]/MTIOC4D/MTIOC1A/TIOCA10/PO28/SSLB0/AN2	107
MTU5	MTIC5U	PA4/A4/MTIC5U/MTCLKA/TIOCA1/TMRI0/PO20/TXD5/SMOSI5/SSDA5/SSLA0/IRQ5-DS	92
		PD7/D7[A7/D7]/MTIC5U/POE0#/SSLC3/IRQ7/AN7	119
	MTIC5V	PA6/A6/MTIC5V/MTCLKB/TIOCA2/TMCI3/PO22/POE2#/CTS5#/RTS5#/SS5#/MOSIA	89
		PD6/D6[A6/D6]/MTIC5V/POE1#/SSLC2/IRQ6/AN6	120
	MTIC5W	PB0/A8/MTIC5W/TIOCA3/PO24/RXD4/RXD6/SMISO4/SMISO6/SSCL4/SSCL6/RSPCKA/IRQ12	87
		PD5/D5[A5/D5]/MTIC5W/POE2#/SSLC1/IRQ5/AN013	121

POE	POE0#	PC4/A20/CS3#/MTIOC3D/MTCLKC/TIOCC6/TCLKE/TMCI1/PO25/POE0#/SCK5/CTS8#/RTS8#/SS8#/SSLA0	66
		PD7/D7[A7/D7]/MTIC5U/POE0#/SSLC3/IRQ7/AN7	119
	POE1#	PB5/A13/MTIOC2A/MTIOC1B/TIOCB4/TMRI1/PO29/POE1#/SCK9	80
		PD6/D6[A6/D6]/MTIC5V/POE1#/SSLC2/IRQ6/AN6	120
	POE2#	TRST#/P34/MTIOC0A/TMCI3/PO12/POE2#/SCK6/SCK0/IRQ4	25
		PA6/A6/MTIC5V/MTCLKB/TIOCA2/TMCI3/PO22/POE2#/CTS5#/RTS5#/SS5#/MOSIA	89
		PD5/D5[A5/D5]/MTIC5W/POE2#/SSLC1/IRQ5/AN013	121
	POE3#	P33/MTIOC0D/TIOCD0/TMRI3/PO11/POE3#/RXD6/RXD0/SMISO6/SMISO0/SSCL6/SSCL0/CRX0/IRQ3-DS	26
		PB3/A11/MTIOC0A/MTIOC4A/TIOCD3/TCLKD/TMO0/PO27/POE3#/SCK4/SCK6	82
		PD4/D4[A4/D4]/POE3#/SSLC0/IRQ4/AN012	122
	POE8#	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	38
		TDI/P30/MTIOC4B/TMRI3/PO8/RTCIC0/POE8#/RXD1/SMISO1/SSCL1/MISOB/IRQ0-DS	29
		PD3/D3[A3/D3]/TIOCB8/TCLKH/POE8#/RSPCKC/IRQ3/AN011	123
		PE3/D11[A11/D11]/MTIOC4B/TIOCB9/PO26/POE8#/CTS12#/RTS12#/SS12#/MISOB/AN1	108
TPU0-5	TCLKA	P14/MTIOC3A/MTCLKA/TIOCB5/TCLKA/TMRI2/PO15/CTS1#/RTS1#/SS1#/CTX1/USB0_DPUPE/IRQ4	43
		PC2/A18/MTIOC4B/TCLKA/PO21/RXD5/SMISO5/SSCL5/SSLA3/IERXD	70
	TCLKB	P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SCK3/SMISO1/SSCL1/CRX1-DS/IRQ5	42
		PA3/A3/MTIOC0D/MTCLKD/TIOCD0/TCLKB/PO19/RXD5/SMISO5/SSCL5/IRQ6-DS	94
		PC3/A19/MTIOC4D/TCLKB/PO24/TXD5/SMOSI5/SSDA5/IETXD	67
	TCLKC	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	40
		PB2/A10/TIOCC3/TCLKC/PO26/CTS4#/RTS4#/CTS6#/RTS6#/SS4#/SS6#	83
		PC0/A16/MTIOC3C/TCLKC/PO17/CTS5#/RTS5#/SS5#/SSLA1/SCL3/IRQ14	75
	TCLKD	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	38
		PB3/A11/MTIOC0A/MTIOC4A/TIOCD3/TCLKD/TMO0/PO27/POE3#/SCK4/SCK6	82
		PC1/A17/MTIOC3A/TCLKD/PO18/SCK5/SSLA2/SDA3/IRQ12	73
TPU0	TIOCA0	P86/TIOCA0	41
		PA0/A0/BC0#/MTIOC4A/TIOCA0/PO16/SSLA1	97
	TIOCB0	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	38
		PA1/A1/MTIOC0B/MTCLKC/TIOCB0/PO17/SCK5/SSLA2/IRQ11	96
	TIOCD0	P33/MTIOC0D/TIOCD0/TMRI3/PO11/POE3#/RXD6/RXD0/SMISO6/SMI	26

		SO0/SSCL6/SSCL0/CRX0/IRQ3-DS	
		PA3/A3/MTIOC0D/MTCLKD/TIOCD0/TCLKB/PO19/RXD5/SMISO5/SSCL5/IRQ6-DS	94
TPU1	TIOCA1	P56/MTIOC3C/TIOCA1	50
		PA4/A4/MTIC5U/MTCLKA/TIOCA1/TMRI0/PO20/TXD5/SMOSI5/SSDA5/SSLA0/IRQ5-DS	92
	TIOCB1	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	40
		PA5/A5/TIOCB1/PO21/RSPCKA	90
TPU2	TIOCA2	P87/TIOCA2	39
		PA6/A6/MTIC5V/MTCLKB/TIOCA2/TMCI3/PO22/POE2#/CTS5#/RTS5#/SS5#/MOSIA	89
	TIOCB2	P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SCK3/SMISO1/SSCL1/CRX1-DS/IRQ5	42
		PA7/A7/TIOCB2/PO23/MISOA	88
TPU3	TIOCA3	P21/MTIOC1B/TIOCA3/TMCI0/PO1/RXD0/SMISO0/SSCL0/SCL1/IRQ9	36
		PB0/A8/MTIC5W/TIOCA3/PO24/RXD4/RXD6/SMISO4/SMISO6/SSCL4/SSCL6/RSPCKA/IRQ12	87
	TIOCB3	P20/MTIOC1A/TIOCB3/TMRI0/PO0/TXD0/SMOSI0/SSDA0/SDA1/IRQ8	37
		PB1/A9/MTIOC0C/MTIOC4C/TIOCB3/TMCI0/PO25/TXD4/TXD6/SMOSI4/SMOSI6/SSDA4/SSDA6/IRQ4-DS	84
	TIOCC3	P22/MTIOC3B/MTCLKC/TIOCC3/TMO0/PO2/SCK0	35
		PB2/A10/TIOCC3/TCLKC/PO26/CTS4#/RTS4#/CTS6#/RTS6#/SS4#/SS6#	83
	TIOCD3	P23/MTIOC3D/MTCLKD/TIOCD3/PO3/TXD3/CTS0#/RTS0#/SMOSI3/SS0#/SSDA3	34
		PB3/A11/MTIOC0A/MTIOC4A/TIOCD3/TCLKD/TMO0/PO27/POE3#/SCK4/SCK6	82
TPU4	TIOCA4	P25/CS5#/MTIOC4C/MTCLKB/TIOCA4/PO5/RXD3/SMISO3/SSCL3/ADTRG0#	32
		PB4/A12/TIOCA4/PO28/CTS9#/RTS9#/SS9#	81
	TIOCB4	P24/CS4#/MTIOC4A/MTCLKA/TIOCB4/TMRI1/PO4/SCK3	33
		PB5/A13/MTIOC2A/MTIOC1B/TIOCB4/TMRI1/PO29/POE1#/SCK9	80
TPU5	TIOCA5	P13/MTIOC0B/TIOCA5/TMO3/PO13/TXD2/SMOSI2/SSDA2/SDA0[FM+]/IRQ3/ADTRG#	44
		PB6/A14/MTIOC3D/TIOCA5/PO30/RXD9/SMISO9/SSCL9	79
	TIOCB5	P14/MTIOC3A/MTCLKA/TIOCB5/TCLKA/TMRI2/PO15/CTS1#/RTS1#/SS1#/CTX1/USB0_DPUPE/IRQ4	43
		PB7/A15/MTIOC3B/TIOCB5/PO31/TXD9/SMOSI9/SSDA9	78
PPG Group3	PO13	P13/MTIOC0B/TIOCA5/TMO3/PO13/TXD2/SMOSI2/SSDA2/SDA0[FM+]/IRQ3/ADTRG#	44
		P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SCK3/SMISO1/SSCL1/CRX1-DS/IRQ5	42
	PO15	P14/MTIOC3A/MTCLKA/TIOCB5/TCLKA/TMRI2/PO15/CTS1#/RTS1#/SS1#/CTX1/USB0_DPUPE/IRQ4	43
		P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TX	38

		D3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	
PPG Group4	PO16	P73/CS3#/PO16	77
		PA0/A0/BC0#/MTIOC4A/TIOCA0/PO16/SSLA1	97
	PO17	PA1/A1/MTIOC0B/MTCLKC/TIOCB0/PO17/SCK5/SSLA2/IRQ11	96
		PC0/A16/MTIOC3C/TCLKC/PO17/CTS5#/RTS5#/SS5#/SSLA1/SCL3/IRQ14	75
	PO18	PA2/A2/PO18/RXD5/SMISO5/SSCL5/SSLA3	95
		PC1/A17/MTIOC3A/TCLKD/PO18/SCK5/SSLA2/SDA3/IRQ12	73
		PE1/D9[A9/D9]/MTIOC4C/TIOCD9/PO18/TXD12/SMOSI12/SSDA12/TXDX12/SIOX12/SSLB2/RSPCKB/ANEX1	110
	PO19	P74/CS4#/PO19/CTS11#/RTS11#/SS11#	72
		PA3/A3/MTIOC0D/MTCLKD/TIOCD0/TCLKB/PO19/RXD5/SMISO5/SSCL5/IRQ6-DS	94
PPG Group5	PO20	P75/CS5#/PO20/SCK11	71
		PA4/A4/MTIC5U/MTCLKA/TIOCA1/TMRI0/PO20/TXD5/SMOSI5/SSDA5/SSLA0/IRQ5-DS	92
	PO21	PA5/A5/TIOCB1/PO21/RSPCKA	90
		PC2/A18/MTIOC4B/TCLKA/PO21/RXD5/SMISO5/SSCL5/SSLA3/IERXD	70
	PO22	P76/CS6#/PO22/RXD11/SMISO11/SSCL11	69
		PA6/A6/MTIC5V/MTCLKB/TIOCA2/TMCI3/PO22/POE2#/CTS5#/RTS5#/SS5#/MOSIA	89
	PO23	P77/CS7#/PO23/TXD11/SMOSI11/SSDA11	68
		PA7/A7/TIOCB2/PO23/MISOA	88
		PE2/D10[A10/D10]/MTIOC4A/TIOCA9/PO23/RXD12/SMISO12/SSCL12/RXDX12/SSLB3/MOSIB/IRQ7-DS/AN0	109
PPG Group6	PO24	PB0/A8/MTIC5W/TIOCA3/PO24/RXD4/RXD6/SMISO4/SMISO6/SSCL4/SSCL6/RSPCKA/IRQ12	87
		PC3/A19/MTIOC4D/TCLKB/PO24/TXD5/SMOSI5/SSDA5/IETXD	67
	PO25	PB1/A9/MTIOC0C/MTIOC4C/TIOCB3/TMCI0/PO25/TXD4/TXD6/SMOSI4/SMOSI6/SSDA4/SSDA6/IRQ4-DS	84
		PC4/A20/CS3#/MTIOC3D/MTCLKC/TIOCC6/TCLKE/TMCI1/PO25/POE0#/SCK5/CTS8#/RTS8#/SS8#/SSLA0	66
	PO26	TRDATA0/P80/MTIOC3B/PO26/SCK10	65
		PB2/A10/TIOCC3/TCLKC/PO26/CTS4#/RTS4#/CTS6#/RTS6#/SS4#/SS6#	83
		PE3/D11[A11/D11]/MTIOC4B/TIOCB9/PO26/POE8#/CTS12#/RTS12#/SS12#/MISOB/AN1	108
	PO27	TRDATA1/P81/MTIOC3D/PO27/RXD10/SMISO10/SSCL10	64
		PB3/A11/MTIOC0A/MTIOC4A/TIOCD3/TCLKD/TMO0/PO27/POE3#/SCK4/SCK6	82
PPG Group7	PO28	TRSYNC#/P82/MTIOC4A/PO28/TXD10/SMOSI10/SSDA10	63
		PB4/A12/TIOCA4/PO28/CTS9#/RTS9#/SS9#	81
		PE4/D12[A12/D12]/MTIOC4D/MTIOC1A/TIOCA10/PO28/SSLB0/AN2	107
	PO29	PB5/A13/MTIOC2A/MTIOC1B/TIOCB4/TMRI1/PO29/POE1#/SCK9	80
		PC5/A21/CS2#/WAIT#/MTIOC3B/MTCLKD/TIOCD6/TCLKF/TMRI2/PO29/SCK8/RSPCKA	62

	PO30	PB6/A14/MTIOC3D/TIOCA5/PO30/RXD9/SMISO9/SSCL9	79
		PC6/A22/CS1#/MTIOC3C/MTCLKA/TIOCA6/TMCI2/PO30/RXD8/SMISO8/SSCL8/MOSIA/IRQ13	61
	PO31	PB7/A15/MTIOC3B/TIOCB5/PO31/TXD9/SMOSI9/SSDA9	78
		PC7/A23/CS0#/MTIOC3A/MTCLKB/TIOCB6/TMO2/PO31/TXD8/SMOSI8/SSDA8/MISOA/IRQ14	60
TMR0	TMO0	P22/MTIOC3B/MTCLKC/TIOCC3/TMO0/PO2/SCK0	35
		PB3/A11/MTIOC0A/MTIOC4A/TIOCD3/TCLKD/TMO0/PO27/POE3#/SCK4/SCK6	82
	TMCI0	P01/TMCI0/RXD6/SMISO6/SSCL6/IRQ9/AN019	7
		P21/MTIOC1B/TIOCA3/TMCI0/PO1/RXD0/SMISO0/SSCL0/SCL1/IRQ9	36
		PB1/A9/MTIOC0C/MTIOC4C/TIOCB3/TMCI0/PO25/TXD4/TXD6/SMOSI4/SMOSI6/SSDA4/SSDA6/IRQ4-DS	84
	TMRI0	P00/TMRI0/TXD6/SMOSI6/SSDA6/IRQ8/AN018	8
		P20/MTIOC1A/TIOCB3/TMRI0/PO0/TXD0/SMOSI0/SSDA0/SDA1/IRQ8	37
		PA4/A4/MTIC5U/MTCLKA/TIOCA1/TMRI0/PO20/TXD5/SMOSI5/SSDA5/SSLA0/IRQ5-DS	92
TMR1	TMO1	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	38
		TDO/P26/CS6#/MTIOC2A/TMO1/PO6/TXD1/CTS3#/RTS3#/SMOSI1/SS3#/SSDA1/MOSIB	31
	TMCI1	P02/TMCI1/SCK6/IRQ10/AN020	6
		P12/TMCI1/RXD2/SMISO2/SSCL2/SCL0[FM+]/IRQ2	45
		TRDATA2/P54/ALE/MTIOC4B/TMCI1/CTS2#/RTS2#/SS2#/CTX1	52
		PC4/A20/CS3#/MTIOC3D/MTCLKC/TIOCC6/TCLKE/TMCI1/PO25/POE0#/SCK5/CTS8#/RTS8#/SS8#/SSLA0	66
	TMRI1	P24/CS4#/MTIOC4A/MTCLKA/TIOCB4/TMRI1/PO4/SCK3	33
		PB5/A13/MTIOC2A/MTIOC1B/TIOCB4/TMRI1/PO29/POE1#/SCK9	80
TMR2	TMO2	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	40
		PC7/A23/CS0#/MTIOC3A/MTCLKB/TIOCB6/TMO2/PO31/TXD8/SMOSI8/SSDA8/MISOA/IRQ14	60
	TMCI2	P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SCK3/SMISO1/SSCL1/CRX1-DS/IRQ5	42
		TMS/P31/MTIOC4D/TMCI2/PO9/RTCIC1/CTS1#/RTS1#/SS1#/SSLB0/IRQ1-DS	28
		PC6/A22/CS1#/MTIOC3C/MTCLKA/TIOCA6/TMCI2/PO30/RXD8/SMISO8/SSCL8/MOSIA/IRQ13	61
	TMRI2	P14/MTIOC3A/MTCLKA/TIOCB5/TCLKA/TMRI2/PO15/CTS1#/RTS1#/SS1#/CTX1/USB0_DPUPE/IRQ4	43
		PC5/A21/CS2#/WAIT#/MTIOC3B/MTCLKD/TIOCD6/TCLKF/TMRI2/PO29/SCK8/RSPCKA	62
TMR3	TMO3	P13/MTIOC0B/TIOCA5/TMO3/PO13/TXD2/SMOSI2/SSDA2/SDA0[FM+]/IRQ3/ADTRG#	44
		P32/MTIOC0C/TIOCC0/TMO3/PO10/RTCOUT/RTCIC2/TXD6/TXD0/SMOSI6/SMOSI0/SSDA6/SSDA0/CTX0/IRQ2-DS	27

		TRDATA3/P55/WAIT#/MTIOC4D/TMO3/CRX1/IRQ10	51
	TMCI3	TCK/FINEC/P27/CS7#/MTIOC2B/TMCI3/PO7/SCK1/RSPCKB	30
		TRST#/P34/MTIOC0A/TMCI3/PO12/POE2#/SCK6/SCK0/IRQ4	25
		PA6/A6/MTIC5V/MTCLKB/TIOCA2/TMCI3/PO22/POE2#/CTS5#/RTS5#/SS5#/MOSIA	89
	TMRI3	TDI/P30/MTIOC4B/TMRI3/PO8/RTCIC0/POE8#/RXD1/SMISO1/SSCL1/MISOB/IRQ0-DS	29
		P33/MTIOC0D/TIOCD0/TMRI3/PO11/POE3#/RXD6/RXD0/SMISO6/SMISO0/SSCL6/SSCL0/CRX0/IRQ3-DS	26
SCI0	RXD0 SSCL0 SMISO0	P21/MTIOC1B/TIOCA3/TMCI0/PO1/RXD0/SMISO0/SSCL0/SCL1/IRQ9	36
		P33/MTIOC0D/TIOCD0/TMRI3/PO11/POE3#/RXD6/RXD0/SMISO6/SMISO0/SSCL6/SSCL0/CRX0/IRQ3-DS	26
	TXD0 SSDA0 SMOSI0	P20/MTIOC1A/TIOCB3/TMRI0/PO0/TXD0/SMOSI0/SSDA0/SDA1/IRQ8	37
		P32/MTIOC0C/TIOCC0/TMO3/PO10/RTCOUT/RTCIC2/TXD6/TXD0/SMOSI6/SMOSI0/SSDA6/SSDA0/CTX0/IRQ2-DS	27
	SCK0	P22/MTIOC3B/MTCLKC/TIOCC3/TMO0/PO2/SCK0	35
		TRST#/P34/MTIOC0A/TMCI3/PO12/POE2#/SCK6/SCK0/IRQ4	25
	CTS0# RTS0# SS0#	P23/MTIOC3D/MTCLKD/TIOCD3/PO3/TXD3/CTS0#/RTS0#/SMOSI3/SS0#/SSDA3	34
		PJ3/MTIOC3C/CTS6#/RTS6#/CTS0#/RTS0#/SS6#/SS0#	13
SCI1	RXD1 SSCL1 SMISO1	P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SCK3/SMISO1/SSCL1/CRX1-DS/IRQ5	42
		TDI/P30/MTIOC4B/TMRI3/PO8/RTCIC0/POE8#/RXD1/SMISO1/SSCL1/MISOB/IRQ0-DS	29
	TXD1 SSDA1 SMOSI1	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	40
		TDO/P26/CS6#/MTIOC2A/TMO1/PO6/TXD1/CTS3#/RTS3#/SMOSI1/SS3#/SSDA1/MOSIB	31
	SCK1	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	38
		TCK/FINEC/P27/CS7#/MTIOC2B/TMCI3/PO7/SCK1/RSPCKB	30
	CTS1# RTS1# SS1#	P14/MTIOC3A/MTCLKA/TIOCB5/TCLKA/TMRI2/PO15/CTS1#/RTS1#/SS1#/CTX1/USB0_DPUPE/IRQ4	43
		TMS/P31/MTIOC4D/TMCI2/PO9/RTCIC1/CTS1#/RTS1#/SS1#/SSLB0/IRQ1-DS	28
SCI2	RXD2 SSCL2 SMISO2	P12/TMCI1/RXD2/SMISO2/SSCL2/SCL0[FM+]/IRQ2	45
		P52/RD#/RXD2/SMISO2/SSCL2/SSLB3	54
	TXD2 SSDA2 SMOSI2	P13/MTIOC0B/TIOCA5/TMO3/PO13/TXD2/SMOSI2/SSDA2/SDA0[FM+]/IRQ3/ADTRG#	44
		P50/WR0#/WR#/TXD2/SMOSI2/SSDA2/SSLB1	56
SCI3	RXD3 SSCL3 SMISO3	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	40

		P25/CS5#/MTIOC4C/MTCLKB/TIOCA4/PO5/RXD3/SMISO3/SSCL3/ADTRG0#	32
	TXD3 SSDA3 SMOSI3	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	38
		P23/MTIOC3D/MTCLKD/TIOCD3/PO3/TXD3/CTS0#/RTS0#/SMOSI3/SS0#/SSDA3	34
	SCK3	P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SCK3/SMISO1/SSCL1/CRX1-DS/IRQ5	42
		P24/CS4#/MTIOC4A/MTCLKA/TIOCB4/TMRI1/PO4/SCK3	33
SCI4	RXD4 SSCL4 SMISO4	PB0/A8/MTIC5W/TIOCA3/PO24/RXD4/RXD6/SMISO4/SMISO6/SSCL4/SSCL6/RSPCKA/IRQ12	87
		PK4/RXD4/SMISO4/SSCL4	105
	TXD4 SSDA4 SMOSI4	PB1/A9/MTIOC0C/MTIOC4C/TIOCB3/TMCI0/PO25/TXD4/TXD6/SMOSI4/SMOSI6/SSDA4/SSDA6/IRQ4-DS	84
		PK5/TXD4/SMOSI4/SSDA4	103
	SCK4	P70/SCK4	104
		PB3/A11/MTIOC0A/MTIOC4A/TIOCD3/TCLKD/TMO0/PO27/POE3#/SCK4/SCK6	82
	CTS4# RTS4# SS4#	PB2/A10/TIOCC3/TCLKC/PO26/CTS4#/RTS4#/CTS6#/RTS6#/SS4#/SS6#	83
		PE6/D14[A14/D14]/TIOCA11/CTS4#/RTS4#/SS4#/MOSIB/IRQ6/AN4	102
SCI5	RXD5 SSCL5 SMISO5	PA2/A2/PO18/RXD5/SMISO5/SSCL5/SSLA3	95
		PA3/A3/MTIOC0D/MTCLKD/TIOCD0/TCLKB/PO19/RXD5/SMISO5/SSCL5/IRQ6-DS	94
		PC2/A18/MTIOC4B/TCLKA/PO21/RXD5/SMISO5/SSCL5/SSLA3/IERXD	70
	TXD5 SSDA5 SMOSI5	PA4/A4/MTIC5U/MTCLKA/TIOCA1/TMRI0/PO20/TXD5/SMOSI5/SSDA5/SSLA0/IRQ5-DS	92
		PC3/A19/MTIOC4D/TCLKB/PO24/TXD5/SMOSI5/SSDA5/IETXD	67
	SCK5	PA1/A1/MTIOC0B/MTCLKC/TIOCB0/PO17/SCK5/SSLA2/IRQ11	96
		PC1/A17/MTIOC3A/TCLKD/PO18/SCK5/SSLA2/SDA3/IRQ12	73
		PC4/A20/CS3#/MTIOC3D/MTCLKC/TIOCC6/TCLKE/TMCI1/PO25/POE0#/SCK5/CTS8#/RTS8#/SS8#/SSLA0	66
	CTS5# RTS5# SS5#	PA6/A6/MTIC5V/MTCLKB/TIOCA2/TMCI3/PO22/POE2#/CTS5#/RTS5#/SS5#/MOSIA	89
		PC0/A16/MTIOC3C/TCLKC/PO17/CTS5#/RTS5#/SS5#/SSLA1/SCL3/IRQ14	75
SCI6	RXD6 SSCL6 SMISO6	P01/TMCI0/RXD6/SMISO6/SSCL6/IRQ9/AN019	7
		P33/MTIOC0D/TIOCD0/TMRI3/PO11/POE3#/RXD6/RXD0/SMISO6/SMISO0/SSCL6/SSCL0/CRX0/IRQ3-DS	26
		PB0/A8/MTIC5W/TIOCA3/PO24/RXD4/RXD6/SMISO4/SMISO6/SSCL4/SSCL6/RSPCKA/IRQ12	87
	TXD6 SSDA6 SMOSI6	P00/TMRI0/TXD6/SMOSI6/SSDA6/IRQ8/AN018	8
		P32/MTIOC0C/TIOCC0/TMO3/PO10/RTCOUT/RTCIC2/TXD6/TXD0/SMOSI6/SMOSI0/SSDA6/SSDA0/CTX0/IRQ2-DS	27
		PB1/A9/MTIOC0C/MTIOC4C/TIOCB3/TMCI0/PO25/TXD4/TXD6/SMOSI4/SMOSI6/SSDA4/SSDA6/IRQ4-DS	84

	SCK6	P02/TMCI1/SCK6/IRQ10/AN020	6
		TRST#/P34/MTIOC0A/TMCI3/PO12/POE2#/SCK6/SCK0/IRQ4	25
		PB3/A11/MTIOC0A/MTIOC4A/TIOCD3/TCLKD/TMO0/PO27/POE3#/SCK4/SCK6	82
	CTS6# RTS6# SS6#	PB2/A10/TIOCC3/TCLKC/PO26/CTS4#/RTS4#/CTS6#/RTS6#/SS4#/SS6#	83
		PJ3/MTIOC3C/CTS6#/RTS6#/CTS0#/RTS0#/SS6#/SS0#	13
SCI9	RXD9 SSCL9 SMISO9	PB6/A14/MTIOC3D/TIOCA5/PO30/RXD9/SMISO9/SSCL9	79
		PK3/RXD9/SMISO9/SSCL9	116
	TXD9 SSDA9 SMOSI9	PB7/A15/MTIOC3B/TIOCB5/PO31/TXD9/SMOSI9/SSDA9	78
		PK2/TXD9/SMOSI9/SSDA9	118
	SCK9	P60/CS0#/SCK9	117
		PB5/A13/MTIOC2A/MTIOC1B/TIOCB4/TMRI1/PO29/POE1#/SCK9	80
	CTS9# RTS9# SS9#	P61/CS1#/CTS9#/RTS9#/SS9#	115
		PB4/A12/TIOCA4/PO28/CTS9#/RTS9#/SS9#	81
RSPI0	RSPCKA	PA5/A5/TIOCB1/PO21/RSPCKA	90
		PB0/A8/MTIC5W/TIOCA3/PO24/RXD4/RXD6/SMISO4/SMISO6/SSCL4/SSCL6/RSPCKA/IRQ12	87
		PC5/A21/CS2#/WAIT#/MTIOC3B/MTCLKD/TIOCD6/TCLKF/TMRI2/PO29/SCK8/RSPCKA	62
	MOSIA	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	40
		PA6/A6/MTIC5V/MTCLKB/TIOCA2/TMCI3/PO22/POE2#/CTS5#/RTS5#/SS5#/MOSIA	89
		PC6/A22/CS1#/MTIOC3C/MTCLKA/TIOCA6/TMCI2/PO30/RXD8/SMISO8/SSCL8/MOSIA/IRQ13	61
	MISOA	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	38
		PA7/A7/TIOCB2/PO23/MISOA	88
		PC7/A23/CS0#/MTIOC3A/MTCLKB/TIOCB6/TMO2/PO31/TXD8/SMOSI8/SSDA8/MISOA/IRQ14	60
	SSLA0	PA4/A4/MTIC5U/MTCLKA/TIOCA1/TMRI0/PO20/TXD5/SMOSI5/SSDA5/SSLA0/IRQ5-DS	92
		PC4/A20/CS3#/MTIOC3D/MTCLKC/TIOCC6/TCLKE/TMCI1/PO25/POE0#/SCK5/CTS8#/RTS8#/SS8#/SSLA0	66
	SSLA1	PA0/A0/BC0#/MTIOC4A/TIOCA0/PO16/SSLA1	97
		PC0/A16/MTIOC3C/TCLKC/PO17/CTS5#/RTS5#/SS5#/SSLA1/SCL3/IRQ14	75
	SSLA2	PA1/A1/MTIOC0B/MTCLKC/TIOCB0/PO17/SCK5/SSLA2/IRQ11	96
		PC1/A17/MTIOC3A/TCLKD/PO18/SCK5/SSLA2/SDA3/IRQ12	73
	SSLA3	PA2/A2/PO18/RXD5/SMISO5/SSCL5/SSLA3	95
		PC2/A18/MTIOC4B/TCLKA/PO21/RXD5/SMISO5/SSCL5/SSLA3/IERXD	70

RSPI1	RSPCKB	TCK/FINEC/P27/CS7#/MTIOC2B/TMCI3/PO7/SCK1/RSPCKB	30
		PE1/D9[A9/D9]/MTIOC4C/TIOCD9/PO18/TXD12/SMOSI12/SSDA12/TXDX12/SIOX12/SSLB2/RSPCKB/ANEX1	110
		PE5/D13[A13/D13]/MTIOC4C/MTIOC2B/TIOCB10/RSPCKB/IRQ5/AN3	106
	MOSIB	TDO/P26/CS6#/MTIOC2A/TMO1/PO6/TXD1/CTS3#/RTS3#/SMOSI1/SS3#/SSDA1/MOSIB	31
		PE2/D10[A10/D10]/MTIOC4A/TIOCA9/PO23/RXD12/SMISO12/SSCL12/RXDX12/SSLB3/MOSIB/IRQ7-DS/AN0	109
		PE6/D14[A14/D14]/TIOCA11/CTS4#/RTS4#/SS4#/MOSIB/IRQ6/AN4	102
	MISOB	TDI/P30/MTIOC4B/TMRI3/PO8/RTCIC0/POE8#/RXD1/SMISO1/SSCL1/MISOB/IRQ0-DS	29
		PE3/D11[A11/D11]/MTIOC4B/TIOCB9/PO26/POE8#/CTS12#/RTS12#/SS12#/MISOB/AN1	108
		PE7/D15[A15/D15]/TIOCB11/MISOB/IRQ7/AN5	101
	SSLB0	TMS/P31/MTIOC4D/TMCI2/PO9/RTCIC1/CTS1#/RTS1#/SS1#/SSLB0/IRQ1-DS	28
		PE4/D12[A12/D12]/MTIOC4D/MTIOC1A/TIOCA10/PO28/SSLB0/AN2	107
	SSLB1	P50/WR0#/WR#/TXD2/SMOSI2/SSDA2/SSLB1	56
		PE0/D8[A8/D8]/TIOCC9/SCK12/SSLB1/ANEX0	111
	SSLB2	P51/WR1#/BC1#/WAIT#/SCK2/SSLB2	55
		PE1/D9[A9/D9]/MTIOC4C/TIOCD9/PO18/TXD12/SMOSI12/SSDA12/TXDX12/SIOX12/SSLB2/RSPCKB/ANEX1	110
	SSLB3	P52/RD#/RXD2/SMISO2/SSCL2/SSLB3	54
		PE2/D10[A10/D10]/MTIOC4A/TIOCA9/PO23/RXD12/SMISO12/SSCL12/RXDX12/SSLB3/MOSIB/IRQ7-DS/AN0	109
IEB0	IERXD	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	40
		PC2/A18/MTIOC4B/TCLKA/PO21/RXD5/SMISO5/SSCL5/SSLA3/IERXD	70
	IETXD	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	38
		PC3/A19/MTIOC4D/TCLKB/PO24/TXD5/SMOSI5/SSDA5/IETXD	67
RTCA	RTCOUT	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	40
		P32/MTIOC0C/TIOCC0/TMO3/PO10/RTCOUT/RTCIC2/TXD6/TXD0/SMOSI6/SMOSI0/SSDA6/SSDA0/CTX0/IRQ2-DS	27
S12AD	ADTRG0#	P07/IRQ15/ADTRG0#	144
		P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	40
		P25/CS5#/MTIOC4C/MTCLKB/TIOCA4/PO5/RXD3/SMISO3/SSCL3/ADTRG0#	32
ADA0	ADTRG#	P13/MTIOC0B/TIOCA5/TMO3/PO13/TXD2/SMOSI2/SSDA2/SDA0[FM+]/IRQ3/ADTRG#	44
		P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	38

Table a-1.5 100-pin TFLGA (the Upper Row of Each Pair is the Default Selection)

Peripheral module	Pin function	Selection of assignment	Pin No.
Bus control	WAIT#	P55/WAIT#/MTIOC4D/TMO3/CRX1/IRQ10	H5
		PC5/A21/CS2#/WAIT#/MTIOC3B/MTCLKD/TMRI2/PO29/SCK8/RSPCKA	K8
		P51/WR1#/BC1#/WAIT#/SCK2/SSLB2	K7
Interrupts	IRQ0	TDI/P30/MTIOC4B/TMRI3/PO8/RTCIC0/POE8#/RXD1/SMISO1/SSCL1/MISOB/IRQ0-DS	G3
		PD0/D0[A0/D0]/IRQ0/AN008	A6
	IRQ1	TMS/P31/MTIOC4D/TMCI2/PO9/RTCIC1/CTS1#/RTS1#/SS1#/SSLB0/IRQ1-DS	G2
		PD1/D1[A1/D1]/MTIOC4B/CTX0/IRQ1/AN009	B6
	IRQ2	P32/MTIOC0C/TIOCC0/TMO3/PO10/RTCOUT/RTCIC2/TXD6/TXD0/SMOSI6/SMOSI0/SSDA6/SSDA0/CTX0/IRQ2-DS	F4
		P12/TMCI1/RXD2/SMISO2/SSCL2/SCL0[FM+]/IRQ2	F5
		PD2/D2[A2/D2]/MTIOC4D/CRX0/IRQ2/AN010	C7
	IRQ3	P33/MTIOC0D/TIOCD0/TMRI3/PO11/POE3#/RXD6/RXD0/SMISO6/SMISO0/SSCL6/SSCL0/CRX0/IRQ3-DS	G1
		P13/MTIOC0B/TIOCA5/TMO3/PO13/TXD2/SMOSI2/SSDA2/SDA0[FM+]/IRQ3/ADTRG#	J4
		PD3/D3[A3/D3]/POE8#/IRQ3/AN011	B7
	IRQ4	PB1/A9/MTIOC0C/MTIOC4C/TIOCB3/TMCI0/PO25/TXD6/SMOSI6/SSDA6/IRQ4-DS	G9
		P14/MTIOC3A/MTCLKA/TIOCB5/TCLKA/TMRI2/PO15/CTS1#/RTS1#/SS1#/CTX1/USB0_DPUPE/IRQ4	K4
		TRST#/P34/MTIOC0A/TMCI3/PO12/POE2#/SCK6/SCK0/IRQ4	E4
		PD4/D4[A4/D4]/POE3#/IRQ4/AN012	A7
	IRQ5	PA4/A4/MTIC5U/MTCLKA/TIOCA1/TMRI0/PO20/TXD5/SMOSI5/SSDA5/SSLA0/IRQ5-DS	E8
		P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SCK3/SMISO1/SSCL1/CRX1-DS/IRQ5	H4
		PD5/D5[A5/D5]/MTIC5W/POE2#/IRQ5/AN013	C8
		PE5/D13[A13/D13]/MTIOC4C/MTIOC2B/RSPCKB/IRQ5/AN3	C9
	IRQ6	PA3/A3/MTIOC0D/MTCLKD/TIOCD0/TCLKB/PO19/RXD5/SMISO5/SSCL5/IRQ6-DS	E10
		P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	H3
		PD6/D6[A6/D6]/MTIC5V/POE1#/IRQ6/AN6	B8
		PE6/D14[A14/D14]/MOSIB/IRQ6/AN4	D7
	IRQ7	PE2/D10[A10/D10]/MTIOC4A/PO23/RXD12/SMISO12/SSCL12/RXDX12/SSLB3/MOSIB/IRQ7-DS/AN0	A10
		P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	J3
		PD7/D7[A7/D7]/MTIC5U/POE0#/IRQ7/AN7	B9

		PE7/D15[A15/D15]/MISOB/IRQ7/AN5	D8
	IRQ8	P40/IRQ8-DS/AN000	B4
		P20/MTIOC1A/TIOCB3/TMRI0/PO0/TXD0/SMOSI0/SSDA0/IRQ8	K3
	IRQ9	P41/IRQ9-DS/AN001	E5
		P21/MTIOC1B/TIOCA3/TMCI0/PO1/RXD0/SMISO0/SSCL0/IRQ9	J2
	IRQ10	P42/IRQ10-DS/AN002	C5
		P55/WAIT#/MTIOC4D/TMO3/CRX1/IRQ10	H5
	IRQ11	P43/IRQ11-DS/AN003	A5
		PA1/A1/MTIOC0B/MTCLKC/TIOCB0/PO17/SCK5/SSLA2/IRQ11	D9
	IRQ12	P44/IRQ12-DS/AN004	B5
		PB0/A8/MTIC5W/TIOCA3/PO24/RXD6/SMISO6/SSCL6/RSPCKA/IRQ12	F8
		PC1/A17/MTIOC3A/TCLKD/PO18/SCK5/SSLA2/IRQ12	J10
	IRQ13	P45/IRQ13-DS/AN005	D5
		P05/IRQ13/DA1	A1
		PC6/A22/CS1#/MTIOC3C/MTCLKA/TMCI2/PO30/RXD8/SMISO8/SSCL8/MOSIA/IRQ13	H8
	IRQ14	P46/IRQ14-DS/AN006	D6
		PC0/A16/MTIOC3C/TCLKC/PO17/CTS5#/RTS5#/SS5#/SSLA1/IRQ14	J9
		PC7/A23/CS0#/MTIOC3A/MTCLKB/TMO2/PO31/TXD8/SMOSI8/SSDA8/MISOA/IRQ14	H7
	IRQ15	P47/IRQ15-DS/AN007	C6
		P07/IRQ15/ADTRG0#	A3
MTU0-5	MTCLKA	P14/MTIOC3A/MTCLKA/TIOCB5/TCLKA/TMRI2/PO15/CTS1#/RTS1#/SS1#/CTX1/USB0_DPUPE/IRQ4	K4
		P24/CS4#/MTIOC4A/MTCLKA/TIOCB4/TMRI1/PO4/SCK3	J1
		PA4/A4/MTIC5U/MTCLKA/TIOCA1/TMRI0/PO20/TXD5/SMOSI5/SSDA5/SSLA0/IRQ5-DS	E8
		PC6/A22/CS1#/MTIOC3C/MTCLKA/TMCI2/PO30/RXD8/SMISO8/SSCL8/MOSIA/IRQ13	H8
	MTCLKB	P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SCK3/SMISO1/SSCL1/CRX1-DS/IRQ5	H4
		P25/CS5#/MTIOC4C/MTCLKB/TIOCA4/PO5/RXD3/SMISO3/SSCL3/ADTRG0#	H2
		PA6/A6/MTIC5V/MTCLKB/TIOCA2/TMCI3/PO22/POE2#/CTS5#/RTS5#/SS5#/MOSIA	E7
		PC7/A23/CS0#/MTIOC3A/MTCLKB/TMO2/PO31/TXD8/SMOSI8/SSDA8/MISOA/IRQ14	H7
	MTCLKC	P22/MTIOC3B/MTCLKC/TIOCC3/TMO0/PO2/SCK0	K2
		PA1/A1/MTIOC0B/MTCLKC/TIOCB0/PO17/SCK5/SSLA2/IRQ11	D9
		PC4/A20/CS3#/MTIOC3D/MTCLKC/TMCI1/PO25/POE0#/SCK5/CTS8#/RTS8#/SS8#/SSLA0	J8
	MTCLKD	P23/MTIOC3D/MTCLKD/TIOCD3/PO3/TXD3/CTS0#/RTS0#/SMOSI3/SS0#/SSDA3	K1
		PA3/A3/MTIOC0D/MTCLKD/TIOCD0/TCLKB/PO19/RXD5/SMISO5/SSCL5/IRQ6-DS	E10
		PC5/A21/CS2#/WAIT#/MTIOC3B/MTCLKD/TMRI2/PO29/SCK8/RSPCKA	K8

MTU0	MTIOC0A	TRST#/P34/MTIOC0A/TMCI3/PO12/POE2#/SCK6/SCK0/IRQ4	E4
		PB3/A11/MTIOC0A/MTIOC4A/TIOCD3/TCLKD/TMO0/PO27/POE3#/SCK6	F6
	MTIOC0B	P13/MTIOC0B/TIOCA5/TMO3/PO13/TXD2/SMOSI2/SSDA2/SDA0[FM+]/IRQ3/ADTRG#	J4
		P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SCK3/SMISO1/SSCL1/CRX1-DS/IRQ5	H4
		PA1/A1/MTIOC0B/MTCLKC/TIOCB0/PO17/SCK5/SSLA2/IRQ11	D9
	MTIOC0C	P32/MTIOC0C/TIOCC0/TMO3/PO10/RTCOUT/RTCIC2/TXD6/TXD0/SMOSI6/SMOSI0/SSDA6/SSDA0/CTX0/IRQ2-DS	F4
		PB1/A9/MTIOC0C/MTIOC4C/TIOCB3/TMCI0/PO25/TXD6/SMOSI6/SSDA6/IRQ4-DS	G9
	MTIOC0D	P33/MTIOC0D/TIOCD0/TMRI3/PO11/POE3#/RXD6/RXD0/SMISO6/SMISO0/SSCL6/SSCL0/CRX0/IRQ3-DS	G1
		PA3/A3/MTIOC0D/MTCLKD/TIOCD0/TCLKB/PO19/RXD5/SMISO5/SSCL5/IRQ6-DS	E10
MTU1	MTIOC1A	P20/MTIOC1A/TIOCB3/TMRI0/PO0/TXD0/SMOSI0/SSDA0/IRQ8	K3
		PE4/D12[A12/D12]/MTIOC4D/MTIOC1A/PO28/SSLB0/AN2	C10
	MTIOC1B	P21/MTIOC1B/TIOCA3/TMCI0/PO1/RXD0/SMISO0/SSCL0/IRQ9	J2
		PB5/A13/MTIOC2A/MTIOC1B/TIOCB4/TMRI1/PO29/POE1#/SCK9	G7
MTU2	MTIOC2A	TDO/P26/CS6#/MTIOC2A/TMO1/PO6/TXD1/CTS3#/RTS3#/SMOSI1/SS3#/SSDA1/MOSIB	H1
		PB5/A13/MTIOC2A/MTIOC1B/TIOCB4/TMRI1/PO29/POE1#/SCK9	G7
	MTIOC2B	TCK/FINEC/P27/CS7#/MTIOC2B/TMCI3/PO7/SCK1/RSPCKB	G4
		PE5/D13[A13/D13]/MTIOC4C/MTIOC2B/RSPCKB/IRQ5/AN3	C9
MTU3	MTIOC3A	P14/MTIOC3A/MTCLKA/TIOCB5/TCLKA/TMRI2/PO15/CTS1#/RTS1#/SS1#/CTX1/USB0_DPUPE/IRQ4	K4
		P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	J3
		PC1/A17/MTIOC3A/TCLKD/PO18/SCK5/SSLA2/IRQ12	J10
		PC7/A23/CS0#/MTIOC3A/MTCLKB/TMO2/PO31/TXD8/SMOSI8/SSDA8/MISOA/IRQ14	H7
	MTIOC3B	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	J3
		P22/MTIOC3B/MTCLKC/TIOCC3/TMO0/PO2/SCK0	K2
		PB7/A15/MTIOC3B/TIOCB5/PO31/TXD9/SMOSI9/SSDA9	H10
		PC5/A21/CS2#/WAIT#/MTIOC3B/MTCLKD/TMRI2/PO29/SCK8/RSPCKA	K8
	MTIOC3C	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	H3
		PC0/A16/MTIOC3C/TCLKC/PO17/CTS5#/RTS5#/SS5#/SSLA1/IRQ14	J9
		PC6/A22/CS1#/MTIOC3C/MTCLKA/TMCI2/PO30/RXD8/SMISO8/SSCL8/MOSIA/IRQ13	H8
		PJ3/MTIOC3C/CTS6#/RTS6#/CTS0#/RTS0#/SS6#/SS0#	C3
	MTIOC3D	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	H3

		P23/MTIOC3D/MTCLKD/TIOCD3/PO3/TXD3/CTS0#/RTS0#/SMOSI3/SS0#/SSDA3	K1
		PB6/A14/MTIOC3D/TIOCA5/PO30/RXD9/SMISO9/SSCL9	H9
		PC4/A20/CS3#/MTIOC3D/MTCLKC/TMCI1/PO25/POE0#/SCK5/CTS8#/RTS8#/SS8#/SSLA0	J8
MTU4	MTIOC4A	P24/CS4#/MTIOC4A/MTCLKA/TIOCB4/TMRI1/PO4/SCK3	J1
		PA0/A0/BC0#/MTIOC4A/TIOCA0/PO16/SSLA1	D10
		PB3/A11/MTIOC0A/MTIOC4A/TIOCD3/TCLKD/TMO0/PO27/POE3#/SCK6	F6
		PE2/D10[A10/D10]/MTIOC4A/PO23/RXD12/SMISO12/SSCL12/RXDX12/SSLB3/MOSIB/IRQ7-DS/AN0	A10
	MTIOC4B	TDI/P30/MTIOC4B/TMRI3/PO8/RTCIC0/POE8#/RXD1/SMISO1/SSCL1/MISOB/IRQ0-DS	G3
		P54/ALE/MTIOC4B/TMCI1/CTS2#/RTS2#/SS2#/CTX1	H6
		PC2/A18/MTIOC4B/TCLKA/PO21/RXD5/SMISO5/SSCL5/SSLA3/IERXD	K10
		PD1/D1[A1/D1]/MTIOC4B/CTX0/IRQ1/AN009	B6
		PE3/D11[A11/D11]/MTIOC4B/PO26/POE8#/CTS12#/RTS12#/SS12#/MISOB/AN1	B10
	MTIOC4C	P25/CS5#/MTIOC4C/MTCLKB/TIOCA4/PO5/RXD3/SMISO3/SSCL3/ADTRG0#	H2
		PB1/A9/MTIOC0C/MTIOC4C/TIOCB3/TMCI0/PO25/TXD6/SMOSI6/SSDA6/IRQ4-DS	G9
		PE1/D9[A9/D9]/MTIOC4C/PO18/TXD12/SMOSI12/SSDA12/TXDX12/SIOX12/SSLB2/RSPCKB/ANEX1	A9
		PE5/D13[A13/D13]/MTIOC4C/MTIOC2B/RSPCKB/IRQ5/AN3	C9
	MTIOC4D	TMS/P31/MTIOC4D/TMCI2/PO9/RTCIC1/CTS1#/RTS1#/SS1#/SSLB0/IRQ1-DS	G2
		P55/WAIT#/MTIOC4D/TMO3/CRX1/IRQ10	H5
		PC3/A19/MTIOC4D/TCLKB/PO24/TXD5/SMOSI5/SSDA5/IETXD	K9
		PD2/D2[A2/D2]/MTIOC4D/CRX0/IRQ2/AN010	C7
		PE4/D12[A12/D12]/MTIOC4D/MTIOC1A/PO28/SSLB0/AN2	C10
MTU5	MTIC5U	PA4/A4/MTIC5U/MTCLKA/TIOCA1/TMRI0/PO20/TXD5/SMOSI5/SSDA5/SSLA0/IRQ5-DS	E8
		PD7/D7[A7/D7]/MTIC5U/POE0#/IRQ7/AN7	B9
	MTIC5V	PA6/A6/MTIC5V/MTCLKB/TIOCA2/TMCI3/PO22/POE2#/CTS5#/RTS5#/SS5#/MOSIA	E7
		PD6/D6[A6/D6]/MTIC5V/POE1#/IRQ6/AN6	B8
	MTIC5W	PB0/A8/MTIC5W/TIOCA3/PO24/RXD6/SMISO6/SSCL6/RSPCKA/IRQ12	F8
		PD5/D5[A5/D5]/MTIC5W/POE2#/IRQ5/AN013	C8
POE	POE0#	PC4/A20/CS3#/MTIOC3D/MTCLKC/TMCI1/PO25/POE0#/SCK5/CTS8#/RTS8#/SS8#/SSLA0	J8
		PD7/D7[A7/D7]/MTIC5U/POE0#/IRQ7/AN7	B9
	POE1#	PB5/A13/MTIOC2A/MTIOC1B/TIOCB4/TMRI1/PO29/POE1#/SCK9	G7
		PD6/D6[A6/D6]/MTIC5V/POE1#/IRQ6/AN6	B8
	POE2#	TRST#/P34/MTIOC0A/TMCI3/PO12/POE2#/SCK6/SCK0/IRQ4	E4
		PA6/A6/MTIC5V/MTCLKB/TIOCA2/TMCI3/PO22/POE2#/CTS5#/RTS5#/SS5#/MOSIA	E7

		PD5/D5[A5/D5]/MTIC5W/POE2#/IRQ5/AN013	C8
	POE3#	P33/MTIOC0D/TIOCD0/TMRI3/PO11/POE3#/RXD6/RXD0/SMISO6/SMISO0/SSCL6/SSCL0/CRX0/IRQ3-DS	G1
		PB3/A11/MTIOC0A/MTIOC4A/TIOCD3/TCLKD/TMO0/PO27/POE3#/SCK6	F6
		PD4/D4[A4/D4]/POE3#/IRQ4/AN012	A7
	POE8#	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	J3
		TDI/P30/MTIOC4B/TMRI3/PO8/RTCIC0/POE8#/RXD1/SMISO1/SSCL1/MISOB/IRQ0-DS	G3
		PD3/D3[A3/D3]/POE8#/IRQ3/AN011	B7
		PE3/D11[A11/D11]/MTIOC4B/PO26/POE8#/CTS12#/RTS12#/SS12#/MISOB/AN1	B10
TPU0-5	TCLKA	P14/MTIOC3A/MTCLKA/TIOCB5/TCLKA/TMRI2/PO15/CTS1#/RTS1#/SS1#/CTX1/USB0_DPUPE/IRQ4	K4
		PC2/A18/MTIOC4B/TCLKA/PO21/RXD5/SMISO5/SSCL5/SSLA3/IERXD	K10
	TCLKB	P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SCK3/SMISO1/SSCL1/CRX1-DS/IRQ5	H4
		PA3/A3/MTIOC0D/MTCLKD/TIOCD0/TCLKB/PO19/RXD5/SMISO5/SSCL5/IRQ6-DS	E10
		PC3/A19/MTIOC4D/TCLKB/PO24/TXD5/SMOSI5/SSDA5/IETXD	K9
	TCLKC	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	H3
		PB2/A10/TIOCC3/TCLKC/PO26/CTS6#/RTS6#/SS6#	F7
		PC0/A16/MTIOC3C/TCLKC/PO17/CTS5#/RTS5#/SS5#/SSLA1/IRQ14	J9
	TCLKD	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	J3
		PB3/A11/MTIOC0A/MTIOC4A/TIOCD3/TCLKD/TMO0/PO27/POE3#/SCK6	F6
		PC1/A17/MTIOC3A/TCLKD/PO18/SCK5/SSLA2/IRQ12	J10
TPU0	TIOCB0	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	J3
		PA1/A1/MTIOC0B/MTCLKC/TIOCB0/PO17/SCK5/SSLA2/IRQ11	D9
	TIOCD0	P33/MTIOC0D/TIOCD0/TMRI3/PO11/POE3#/RXD6/RXD0/SMISO6/SMISO0/SSCL6/SSCL0/CRX0/IRQ3-DS	G1
		PA3/A3/MTIOC0D/MTCLKD/TIOCD0/TCLKB/PO19/RXD5/SMISO5/SSCL5/IRQ6-DS	E10
TPU1	TIOCB1	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	H3
		PA5/A5/TIOCB1/PO21/RSPCKA	E9
TPU2	TIOCB2	P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SCK3/SMISO1/SSCL1/CRX1-DS/IRQ5	H4
		PA7/A7/TIOCB2/PO23/MISOA	F9
TPU3	TIOCA3	P21/MTIOC1B/TIOCA3/TMCI0/PO1/RXD0/SMISO0/SSCL0/IRQ9	J2
		PB0/A8/MTIC5W/TIOCA3/PO24/RXD6/SMISO6/SSCL6/RSPCKA/IRQ12	F8

	TIOCB3	P20/MTIOC1A/TIOCB3/TMRI0/PO0/TXD0/SMOSI0/SSDA0/IRQ8	K3
		PB1/A9/MTIOC0C/MTIOC4C/TIOCB3/TMCI0/PO25/TXD6/SMOSI6/SSDA6/IRQ4-DS	G9
	TIOCC3	P22/MTIOC3B/MTCLKC/TIOCC3/TMO0/PO2/SCK0	K2
		PB2/A10/TIOCC3/TCLKC/PO26/CTS6#/RTS6#/SS6#	F7
	TIOCD3	P23/MTIOC3D/MTCLKD/TIOCD3/PO3/TXD3/CTS0#/RTS0#/SMOSI3/SS0#/SSDA3	K1
		PB3/A11/MTIOC0A/MTIOC4A/TIOCD3/TCLKD/TMO0/PO27/POE3#/SCK6	F6
TPU4	TIOCA4	P25/CS5#/MTIOC4C/MTCLKB/TIOCA4/PO5/RXD3/SMISO3/SSCL3/ADTRG0#	H2
		PB4/A12/TIOCA4/PO28/CTS9#/RTS9#/SS9#	G8
	TIOCB4	P24/CS4#/MTIOC4A/MTCLKA/TIOCB4/TMRI1/PO4/SCK3	J1
		PB5/A13/MTIOC2A/MTIOC1B/TIOCB4/TMRI1/PO29/POE1#/SCK9	G7
TPU5	TIOCA5	P13/MTIOC0B/TIOCA5/TMO3/PO13/TXD2/SMOSI2/SSDA2/SDA0[FM+]/IRQ3/ADTRG#	J4
		PB6/A14/MTIOC3D/TIOCA5/PO30/RXD9/SMISO9/SSCL9	H9
	TIOCB5	P14/MTIOC3A/MTCLKA/TIOCB5/TCLKA/TMRI2/PO15/CTS1#/RTS1#/SS1#/CTX1/USB0_DPUPE/IRQ4	K4
		PB7/A15/MTIOC3B/TIOCB5/PO31/TXD9/SMOSI9/SSDA9	H10
PPG Group3	PO13	P13/MTIOC0B/TIOCA5/TMO3/PO13/TXD2/SMOSI2/SSDA2/SDA0[FM+]/IRQ3/ADTRG#	J4
		P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SCK3/SMISO1/SSCL1/CRX1-DS/IRQ5	H4
	PO15	P14/MTIOC3A/MTCLKA/TIOCB5/TCLKA/TMRI2/PO15/CTS1#/RTS1#/SS1#/CTX1/USB0_DPUPE/IRQ4	K4
		P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	J3
PPG Group4	PO17	PA1/A1/MTIOC0B/MTCLKC/TIOCB0/PO17/SCK5/SSLA2/IRQ11	D9
		PC0/A16/MTIOC3C/TCLKC/PO17/CTS5#/RTS5#/SS5#/SSLA1/IRQ14	J9
	PO18	PA2/A2/PO18/RXD5/SMISO5/SSCL5/SSLA3	E6
		PC1/A17/MTIOC3A/TCLKD/PO18/SCK5/SSLA2/IRQ12	J10
		PE1/D9[A9/D9]/MTIOC4C/PO18/TXD12/SMOSI12/SSDA12/TXDX12/SIOX12/SSLB2/RSPCKB/ANEX1	A9
PPG Group5	PO21	PA5/A5/TIOCB1/PO21/RSPCKA	E9
		PC2/A18/MTIOC4B/TCLKA/PO21/RXD5/SMISO5/SSCL5/SSLA3/IERXD	K10
	PO23	PA7/A7/TIOCB2/PO23/MISOA	F9
		PE2/D10[A10/D10]/MTIOC4A/PO23/RXD12/SMISO12/SSCL12/RXDX12/SSLB3/MOSIB/IRQ7-DS/AN0	A10
PPG Group6	PO24	PB0/A8/MTIC5W/TIOCA3/PO24/RXD6/SMISO6/SSCL6/RSPCKA/IRQ12	F8
		PC3/A19/MTIOC4D/TCLKB/PO24/TXD5/SMOSI5/SSDA5/IETXD	K9
	PO25	PB1/A9/MTIOC0C/MTIOC4C/TIOCB3/TMCI0/PO25/TXD6/SMOSI6/SSDA6/IRQ4-DS	G9
		PC4/A20/CS3#/MTIOC3D/MTCLKC/TMCI1/PO25/POE0#/SCK5/CTS8#/RTS8#/SS8#/SSLA0	J8
	PO26	PB2/A10/TIOCC3/TCLKC/PO26/CTS6#/RTS6#/SS6#	F7
		PE3/D11[A11/D11]/MTIOC4B/PO26/POE8#/CTS12#/RTS12#/SS12#/MI	B10

		SOB/AN1	
PPG Group7	PO28	PB4/A12/TIOCA4/PO28/CTS9#/RTS9#/SS9#	G8
		PE4/D12[A12/D12]/MTIOC4D/MTIOC1A/PO28/SSLB0/AN2	C10
	PO29	PB5/A13/MTIOC2A/MTIOC1B/TIOCB4/TMRI1/PO29/POE1#/SCK9	G7
		PC5/A21/CS2#/WAIT#/MTIOC3B/MTCLKD/TMRI2/PO29/SCK8/RSPCKA	K8
	PO30	PB6/A14/MTIOC3D/TIOCA5/PO30/RXD9/SMISO9/SSCL9	H9
		PC6/A22/CS1#/MTIOC3C/MTCLKA/TMCI2/PO30/RXD8/SMISO8/SSCL8/MOSIA/IRQ13	H8
	PO31	PB7/A15/MTIOC3B/TIOCB5/PO31/TXD9/SMOSI9/SSDA9	H10
		PC7/A23/CS0#/MTIOC3A/MTCLKB/TMO2/PO31/TXD8/SMOSI8/SSDA8/MISOA/IRQ14	H7
TMR0	TMO0	P22/MTIOC3B/MTCLKC/TIOCC3/TMO0/PO2/SCK0	K2
		PB3/A11/MTIOC0A/MTIOC4A/TIOCD3/TCLKD/TMO0/PO27/POE3#/SCK6	F6
	TMCI0	P21/MTIOC1B/TIOCA3/TMCI0/PO1/RXD0/SMISO0/SSCL0/IRQ9	J2
		PB1/A9/MTIOC0C/MTIOC4C/TIOCB3/TMCI0/PO25/TXD6/SMOSI6/SSDA6/IRQ4-DS	G9
	TMRI0	P20/MTIOC1A/TIOCB3/TMRI0/PO0/TXD0/SMOSI0/SSDA0/IRQ8	K3
		PA4/A4/MTIC5U/MTCLKA/TIOCA1/TMRI0/PO20/TXD5/SMOSI5/SSDA5/SSLA0/IRQ5-DS	E8
TMR1	TMO1	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	J3
		TDO/P26/CS6#/MTIOC2A/TMO1/PO6/TXD1/CTS3#/RTS3#/SMOSI1/SS3#/SSDA1/MOSIB	H1
	TMCI1	P12/TMCI1/RXD2/SMISO2/SSCL2/SCL0[FM+]/IRQ2	F5
		P54/ALE/MTIOC4B/TMCI1/CTS2#/RTS2#/SS2#/CTX1	H6
		PC4/A20/CS3#/MTIOC3D/MTCLKC/TMCI1/PO25/POE0#/SCK5/CTS8#/RTS8#/SS8#/SSLA0	J8
	TMRI1	P24/CS4#/MTIOC4A/MTCLKA/TIOCB4/TMRI1/PO4/SCK3	J1
		PB5/A13/MTIOC2A/MTIOC1B/TIOCB4/TMRI1/PO29/POE1#/SCK9	G7
TMR2	TMO2	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	H3
		PC7/A23/CS0#/MTIOC3A/MTCLKB/TMO2/PO31/TXD8/SMOSI8/SSDA8/MISOA/IRQ14	H7
	TMCI2	P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SCK3/SMISO1/SSCL1/CRX1-DS/IRQ5	H4
		TMS/P31/MTIOC4D/TMCI2/PO9/RTCIC1/CTS1#/RTS1#/SS1#/SSLB0/IRQ1-DS	G2
		PC6/A22/CS1#/MTIOC3C/MTCLKA/TMCI2/PO30/RXD8/SMISO8/SSCL8/MOSIA/IRQ13	H8
	TMRI2	P14/MTIOC3A/MTCLKA/TIOCB5/TCLKA/TMRI2/PO15/CTS1#/RTS1#/SS1#/CTX1/USB0_DPUPE/IRQ4	K4
		PC5/A21/CS2#/WAIT#/MTIOC3B/MTCLKD/TMRI2/PO29/SCK8/RSPCKA	K8
TMR3	TMO3	P13/MTIOC0B/TIOCA5/TMO3/PO13/TXD2/SMOSI2/SSDA2/SDA0[FM+]/IRQ3/ADTRG#	J4
		P32/MTIOC0C/TIOCC0/TMO3/PO10/RTCOUT/RTCIC2/TXD6/TXD0/SM	F4

		OSI6/SMOSI0/SSDA6/SSDA0/CTX0/IRQ2-DS	
		P55/WAIT#/MTIOC4D/TMO3/CRX1/IRQ10	H5
	TMCI3	TCK/FINEC/P27/CS7#/MTIOC2B/TMCI3/PO7/SCK1/RSPCKB	G4
		TRST#/P34/MTIOC0A/TMCI3/PO12/POE2#/SCK6/SCK0/IRQ4	E4
		PA6/A6/MTIC5V/MTCLKB/TIOCA2/TMCI3/PO22/POE2#/CTS5#/RTS5#/SS5#/MOSIA	E7
	TMRI3	TDI/P30/MTIOC4B/TMRI3/PO8/RTCIC0/POE8#/RXD1/SMISO1/SSCL1/MISOB/IRQ0-DS	G3
		P33/MTIOC0D/TIOCD0/TMRI3/PO11/POE3#/RXD6/RXD0/SMISO6/SMISO0/SSCL6/SSCL0/CRX0/IRQ3-DS	G1
SCI0	RXD0 SSCL0 SMISO0	P21/MTIOC1B/TIOCA3/TMCI0/PO1/RXD0/SMISO0/SSCL0/IRQ9	J2
		P33/MTIOC0D/TIOCD0/TMRI3/PO11/POE3#/RXD6/RXD0/SMISO6/SMISO0/SSCL6/SSCL0/CRX0/IRQ3-DS	G1
	TXD0 SSDA0 SMOSI0	P20/MTIOC1A/TIOCB3/TMRI0/PO0/TXD0/SMOSI0/SSDA0/IRQ8	K3
		P32/MTIOC0C/TIOCC0/TMO3/PO10/RTCOUT/RTCIC2/TXD6/TXD0/SMOSI6/SMOSI0/SSDA6/SSDA0/CTX0/IRQ2-DS	F4
	SCK0	P22/MTIOC3B/MTCLKC/TIOCC3/TMO0/PO2/SCK0	K2
		TRST#/P34/MTIOC0A/TMCI3/PO12/POE2#/SCK6/SCK0/IRQ4	E4
	CTS0# RTS0# SS0#	P23/MTIOC3D/MTCLKD/TIOCD3/PO3/TXD3/CTS0#/RTS0#/SMOSI3/SS0#/SSDA3	K1
		PJ3/MTIOC3C/CTS6#/RTS6#/CTS0#/RTS0#/SS6#/SS0#	C3
SCI1	RXD1 SSCL1 SMISO1	P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SCK3/SMISO1/SSCL1/CRX1-DS/IRQ5	H4
		TDI/P30/MTIOC4B/TMRI3/PO8/RTCIC0/POE8#/RXD1/SMISO1/SSCL1/MISOB/IRQ0-DS	G3
	TXD1 SSDA1 SMOSI1	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	H3
		TDO/P26/CS6#/MTIOC2A/TMO1/PO6/TXD1/CTS3#/RTS3#/SMOSI1/SS3#/SSDA1/MOSIB	H1
	SCK1	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	J3
		TCK/FINEC/P27/CS7#/MTIOC2B/TMCI3/PO7/SCK1/RSPCKB	G4
	CTS1# RTS1# SS1#	P14/MTIOC3A/MTCLKA/TIOCB5/TCLKA/TMRI2/PO15/CTS1#/RTS1#/SS1#/CTX1/USB0_DPUPE/IRQ4	K4
		TMS/P31/MTIOC4D/TMCI2/PO9/RTCIC1/CTS1#/RTS1#/SS1#/SSLB0/IRQ1-DS	G2
SCI2	RXD2 SSCL2 SMISO2	P12/TMCI1/RXD2/SMISO2/SSCL2/SCL0[FM+]/IRQ2	F5
		P52/RD#/RXD2/SMISO2/SSCL2/SSLB3	G6
	TXD2 SSDA2 SMOSI2	P50/WR0#/WR#/TXD2/SMOSI2/SSDA2/SSLB1	J7
		P13/MTIOC0B/TIOCA5/TMO3/PO13/TXD2/SMOSI2/SSDA2/SDA0[FM+]/IRQ3/ADTRG#	J4
SCI3	RXD3 SSCL3 SMISO3	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	H3

		P25/CS5#/MTIOC4C/MTCLKB/TIOCA4/PO5/RXD3/SMISO3/SSCL3/ADTRG0#	H2
	TXD3 SSDA3 SMOSI3	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	J3
		P23/MTIOC3D/MTCLKD/TIOCD3/PO3/TXD3/CTS0#/RTS0#/SMOSI3/SS0#/SSDA3	K1
	SCK3	P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SCK3/SMISO1/SSCL1/CRX1-DS/IRQ5	H4
		P24/CS4#/MTIOC4A/MTCLKA/TIOCB4/TMRI1/PO4/SCK3	J1
SCI5	RXD5 SSCL5 SMISO5	PA2/A2/PO18/RXD5/SMISO5/SSCL5/SSLA3	E6
		PA3/A3/MTIOC0D/MTCLKD/TIOCD0/TCLKB/PO19/RXD5/SMISO5/SSCL5/IRQ6-DS	E10
		PC2/A18/MTIOC4B/TCLKA/PO21/RXD5/SMISO5/SSCL5/SSLA3/IERXD	K10
	TXD5 SSDA5 SMOSI5	PA4/A4/MTIC5U/MTCLKA/TIOCA1/TMRI0/PO20/TXD5/SMOSI5/SSDA5/SSLA0/IRQ5-DS	E8
		PC3/A19/MTIOC4D/TCLKB/PO24/TXD5/SMOSI5/SSDA5/IETXD	K9
	SCK5	PA1/A1/MTIOC0B/MTCLKC/TIOCB0/PO17/SCK5/SSLA2/IRQ11	D9
		PC1/A17/MTIOC3A/TCLKD/PO18/SCK5/SSLA2/IRQ12	J10
		PC4/A20/CS3#/MTIOC3D/MTCLKC/TMCI1/PO25/POE0#/SCK5/CTS8#/RTS8#/SS8#/SSLA0	J8
	CTS5# RTS5# SS5#	PA6/A6/MTIC5V/MTCLKB/TIOCA2/TMCI3/PO22/POE2#/CTS5#/RTS5#/SS5#/MOSIA	E7
		PC0/A16/MTIOC3C/TCLKC/PO17/CTS5#/RTS5#/SS5#/SSLA1/IRQ14	J9
SCI6	RXD6 SSCL6 SMISO6	P33/MTIOC0D/TIOCD0/TMRI3/PO11/POE3#/RXD6/RXD0/SMISO6/SMISO0/SSCL6/SSCL0/CRX0/IRQ3-DS	G1
		PB0/A8/MTIC5W/TIOCA3/PO24/RXD6/SMISO6/SSCL6/RSPCKA/IRQ12	F8
	TXD6 SSDA6 SMOSI6	P32/MTIOC0C/TIOCC0/TMO3/PO10/RTCOUT/RTCIC2/TXD6/TXD0/SMOSI6/SMOSI0/SSDA6/SSDA0/CTX0/IRQ2-DS	F4
		PB1/A9/MTIOC0C/MTIOC4C/TIOCB3/TMCI0/PO25/TXD6/SMOSI6/SSDA6/IRQ4-DS	G9
	SCK6	TRST#/P34/MTIOC0A/TMCI3/PO12/POE2#/SCK6/SCK0/IRQ4	E4
		PB3/A11/MTIOC0A/MTIOC4A/TIOCD3/TCLKD/TMO0/PO27/POE3#/SCK6	F6
	CTS6# RTS6# SS6#	PB2/A10/TIOCC3/TCLKC/PO26/CTS6#/RTS6#/SS6#	F7
		PJ3/MTIOC3C/CTS6#/RTS6#/CTS0#/RTS0#/SS6#/SS0#	C3
RSPI0	RSPCKA	PA5/A5/TIOCB1/PO21/RSPCKA	E9
		PB0/A8/MTIC5W/TIOCA3/PO24/RXD6/SMISO6/SSCL6/RSPCKA/IRQ12	F8
		PC5/A21/CS2#/WAIT#/MTIOC3B/MTCLKD/TMRI2/PO29/SCK8/RSPCKA	K8
	MOSIA	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	H3
		PA6/A6/MTIC5V/MTCLKB/TIOCA2/TMCI3/PO22/POE2#/CTS5#/RTS5#/SS5#/MOSIA	E7
		PC6/A22/CS1#/MTIOC3C/MTCLKA/TMCI2/PO30/RXD8/SMISO8/SSCL8/MOSIA/IRQ13	H8

	MISOA	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	J3
		PA7/A7/TIOCB2/PO23/MISOA	F9
		PC7/A23/CS0#/MTIOC3A/MTCLKB/TMO2/PO31/TXD8/SMOSI8/SSDA8/MISOA/IRQ14	H7
	SSLA0	PA4/A4/MTIC5U/MTCLKA/TIOCA1/TMRI0/PO20/TXD5/SMOSI5/SSDA5/SSLA0/IRQ5-DS	E8
		PC4/A20/CS3#/MTIOC3D/MTCLKC/TMCI1/PO25/POE0#/SCK5/CTS8#/RTS8#/SS8#/SSLA0	J8
	SSLA1	PA0/A0/BC0#/MTIOC4A/TIOCA0/PO16/SSLA1	D10
		PC0/A16/MTIOC3C/TCLKC/PO17/CTS5#/RTS5#/SS5#/SSLA1/IRQ14	J9
	SSLA2	PA1/A1/MTIOC0B/MTCLKC/TIOCB0/PO17/SCK5/SSLA2/IRQ11	D9
		PC1/A17/MTIOC3A/TCLKD/PO18/SCK5/SSLA2/IRQ12	J10
	SSLA3	PA2/A2/PO18/RXD5/SMISO5/SSCL5/SSLA3	E6
		PC2/A18/MTIOC4B/TCLKA/PO21/RXD5/SMISO5/SSCL5/SSLA3/IERXD	K10
RSPI1	RSPCKB	TCK/FINEC/P27/CS7#/MTIOC2B/TMCI3/PO7/SCK1/RSPCKB	G4
		PE1/D9[A9/D9]/MTIOC4C/PO18/TXD12/SMOSI12/SSDA12/TXDX12/SIOX12/SSLB2/RSPCKB/ANEX1	A9
		PE5/D13[A13/D13]/MTIOC4C/MTIOC2B/RSPCKB/IRQ5/AN3	C9
	MOSIB	TDO/P26/CS6#/MTIOC2A/TMO1/PO6/TXD1/CTS3#/RTS3#/SMOSI1/SS3#/SSDA1/MOSIB	H1
		PE2/D10[A10/D10]/MTIOC4A/PO23/RXD12/SMISO12/SSCL12/RXDX12/SSLB3/MOSIB/IRQ7-DS/AN0	A10
		PE6/D14[A14/D14]/MOSIB/IRQ6/AN4	D7
	MISOB	TDI/P30/MTIOC4B/TMRI3/PO8/RTCIC0/POE8#/RXD1/SMISO1/SSCL1/MISOB/IRQ0-DS	G3
		PE3/D11[A11/D11]/MTIOC4B/PO26/POE8#/CTS12#/RTS12#/SS12#/MISOB/AN1	B10
		PE7/D15[A15/D15]/MISOB/IRQ7/AN5	D8
	SSLB0	TMS/P31/MTIOC4D/TMCI2/PO9/RTCIC1/CTS1#/RTS1#/SS1#/SSLB0/IRQ1-DS	G2
		PE4/D12[A12/D12]/MTIOC4D/MTIOC1A/PO28/SSLB0/AN2	C10
	SSLB1	P50/WR0#/WR#/TXD2/SMOSI2/SSDA2/SSLB1	J7
		PE0/D8[A8/D8]/SCK12/SSLB1/ANEX0	A8
	SSLB2	P51/WR1#/BC1#/WAIT#/SCK2/SSLB2	K7
		PE1/D9[A9/D9]/MTIOC4C/PO18/TXD12/SMOSI12/SSDA12/TXDX12/SIOX12/SSLB2/RSPCKB/ANEX1	A9
	SSLB3	P52/RD#/RXD2/SMISO2/SSCL2/SSLB3	G6
		PE2/D10[A10/D10]/MTIOC4A/PO23/RXD12/SMISO12/SSCL12/RXDX12/SSLB3/MOSIB/IRQ7-DS/AN0	A10
IEB0	IERXD	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	H3
		PC2/A18/MTIOC4B/TCLKA/PO21/RXD5/SMISO5/SSCL5/SSLA3/IERXD	K10
	IETXD	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	J3
		PC3/A19/MTIOC4D/TCLKB/PO24/TXD5/SMOSI5/SSDA5/IETXD	K9

RTCA	RTCOUT	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	H3
		P32/MTIOC0C/TIOCC0/TMO3/PO10/RTCOUT/RTCIC2/TXD6/TXD0/SMOSI6/SMOSI0/SSDA6/SSDA0/CTX0/IRQ2-DS	F4
S12AD	ADTRG0#	P07/IRQ15/ADTRG0#	A3
		P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	H3
		P25/CS5#/MTIOC4C/MTCLKB/TIOCA4/PO5/RXD3/SMISO3/SSCL3/ADTRG0#	H2
ADA0	ADTRG#	P13/MTIOC0B/TIOCA5/TMO3/PO13/TXD2/SMOSI2/SSDA2/SDA0[FM+]/IRQ3/ADTRG#	J4
		P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	J3

Table a-1.6 100-pin LQFP (the Upper Row of Each Pair is the Default Selection)

Peripheral module	Pin function	Selection of assignment	Pin No.
Bus control	WAIT#	P55/WAIT#/MTIOC4D/TMO3/CRX1/IRQ10	39
		PC5/A21/CS2#/WAIT#/MTIOC3B/MTCLKD/TMRI2/PO29/SCK8/RSPCKA	47
		P51/WR1#/BC1#/WAIT#/SCK2/SSLB2	43
Interrupts	IRQ0	TDI/P30/MTIOC4B/TMRI3/PO8/RTCIC0/POE8#/RXD1/SMISO1/SSCL1/MISOB/IRQ0-DS	20
		PD0/D0[A0/D0]/IRQ0/AN008	86
	IRQ1	TMS/P31/MTIOC4D/TMCI2/PO9/RTCIC1/CTS1#/RTS1#/SS1#/SSLB0/IRQ1-DS	19
		PD1/D1[A1/D1]/MTIOC4B/CTX0/IRQ1/AN009	85
	IRQ2	P32/MTIOC0C/TIOCC0/TMO3/PO10/RTCOUT/RTCIC2/TXD6/TXD0/SMOSI6/SMOSI0/SSDA6/SSDA0/CTX0/IRQ2-DS	18
		P12/TMCI1/RXD2/SMISO2/SSCL2/SCL0[FM+]/IRQ2	34
		PD2/D2[A2/D2]/MTIOC4D/CRX0/IRQ2/AN010	84
	IRQ3	P33/MTIOC0D/TIOCD0/TMRI3/PO11/POE3#/RXD6/RXD0/SMISO6/SMISO0/SSCL6/SSCL0/CRX0/IRQ3-DS	17
		P13/MTIOC0B/TIOCA5/TMO3/PO13/TXD2/SMOSI2/SSDA2/SDA0[FM+]/IRQ3/ADTRG#	33
		PD3/D3[A3/D3]/POE8#/IRQ3/AN011	83
	IRQ4	PB1/A9/MTIOC0C/MTIOC4C/TIOCB3/TMCI0/PO25/TXD6/SMOSI6/SSDA6/IRQ4-DS	59
		P14/MTIOC3A/MTCLKA/TIOCB5/TCLKA/TMRI2/PO15/CTS1#/RTS1#/SS1#/CTX1/USB0_DPUPE/IRQ4	32
		TRST#/P34/MTIOC0A/TMCI3/PO12/POE2#/SCK6/SCK0/IRQ4	16
		PD4/D4[A4/D4]/POE3#/IRQ4/AN012	82
	IRQ5	PA4/A4/MTIC5U/MTCLKA/TIOCA1/TMRI0/PO20/TXD5/SMOSI5/SSDA5/SSLA0/IRQ5-DS	66
		P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SCK3/SMISO1/SSCL1/CRX1-DS/IRQ5	31

		PD5/D5[A5/D5]/MTIC5W/POE2#/IRQ5/AN013	81
		PE5/D13[A13/D13]/MTIOC4C/MTIOC2B/RSPCKB/IRQ5/AN3	73
	IRQ6	PA3/A3/MTIOC0D/MTCLKD/TIOCD0/TCLKB/PO19/RXD5/SMISO5/SSCL5/IRQ6-DS	67
		P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	30
		PD6/D6[A6/D6]/MTIC5V/POE1#/IRQ6/AN6	80
		PE6/D14[A14/D14]/MOSIB/IRQ6/AN4	72
	IRQ7	PE2/D10[A10/D10]/MTIOC4A/PO23/RXD12/SMISO12/SSCL12/RXDX12/SSLB3/MOSIB/IRQ7-DS/AN0	76
		P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	29
		PD7/D7[A7/D7]/MTIC5U/POE0#/IRQ7/AN7	79
		PE7/D15[A15/D15]/MISOB/IRQ7/AN5	71
	IRQ8	P40/IRQ8-DS/AN000	95
		P20/MTIOC1A/TIOCB3/TMRI0/PO0/TXD0/SMOSI0/SSDA0/IRQ8	28
	IRQ9	P41/IRQ9-DS/AN001	93
		P21/MTIOC1B/TIOCA3/TMCI0/PO1/RXD0/SMISO0/SSCL0/IRQ9	27
	IRQ10	P42/IRQ10-DS/AN002	92
		P55/WAIT#/MTIOC4D/TMO3/CRX1/IRQ10	39
	IRQ11	P43/IRQ11-DS/AN003	91
		PA1/A1/MTIOC0B/MTCLKC/TIOCB0/PO17/SCK5/SSLA2/IRQ11	69
	IRQ12	P44/IRQ12-DS/AN004	90
		PB0/A8/MTIC5W/TIOCA3/PO24/RXD6/SMISO6/SSCL6/RSPCKA/IRQ12	61
		PC1/A17/MTIOC3A/TCLKD/PO18/SCK5/SSLA2/IRQ12	51
	IRQ13	P45/IRQ13-DS/AN005	89
		P05/IRQ13/DA1	100
		PC6/A22/CS1#/MTIOC3C/MTCLKA/TMCI2/PO30/RXD8/SMISO8/SSCL8/MOSIA/IRQ13	46
	IRQ14	P46/IRQ14-DS/AN006	88
		PC0/A16/MTIOC3C/TCLKC/PO17/CTS5#/RTS5#/SS5#/SSLA1/IRQ14	52
		PC7/A23/CS0#/MTIOC3A/MTCLKB/TMO2/PO31/TXD8/SMOSI8/SSDA8/MISOA/IRQ14	45
	IRQ15	P47/IRQ15-DS/AN007	87
		P07/IRQ15/ADTRG0#	98
MTU0-5	MTCLKA	P14/MTIOC3A/MTCLKA/TIOCB5/TCLKA/TMRI2/PO15/CTS1#/RTS1#/SS1#/CTX1/USB0_DPUPE/IRQ4	32
		P24/CS4#/MTIOC4A/MTCLKA/TIOCB4/TMRI1/PO4/SCK3	24
		PA4/A4/MTIC5U/MTCLKA/TIOCA1/TMRI0/PO20/TXD5/SMOSI5/SSDA5/SSLA0/IRQ5-DS	66
		PC6/A22/CS1#/MTIOC3C/MTCLKA/TMCI2/PO30/RXD8/SMISO8/SSCL8/MOSIA/IRQ13	46
	MTCLKB	P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SCK3/SMISO1/SSCL1/CRX1-DS/IRQ5	31
		P25/CS5#/MTIOC4C/MTCLKB/TIOCA4/PO5/RXD3/SMISO3/SSCL3/ADT	23

		RG0#	
		PA6/A6/MTIC5V/MTCLKB/TIOCA2/TMCI3/PO22/POE2#/CTS5#/RTS5#/SS5#/MOSIA	64
		PC7/A23/CS0#/MTIOC3A/MTCLKB/TMO2/PO31/TXD8/SMOSI8/SSDA8/MISOA/IRQ14	45
	MTCLKC	P22/MTIOC3B/MTCLKC/TIOCC3/TMO0/PO2/SCK0	26
		PA1/A1/MTIOC0B/MTCLKC/TIOCB0/PO17/SCK5/SSLA2/IRQ11	69
		PC4/A20/CS3#/MTIOC3D/MTCLKC/TMCI1/PO25/POE0#/SCK5/CTS8#/RTS8#/SS8#/SSLA0	48
	MTCLKD	P23/MTIOC3D/MTCLKD/TIOCD3/PO3/TXD3/CTS0#/RTS0#/SMOSI3/SS0#/SSDA3	25
		PA3/A3/MTIOC0D/MTCLKD/TIOCD0/TCLKB/PO19/RXD5/SMISO5/SSCL5/IRQ6-DS	67
		PC5/A21/CS2#/WAIT#/MTIOC3B/MTCLKD/TMRI2/PO29/SCK8/RSPCKA	47
MTU0	MTIOC0A	TRST#/P34/MTIOC0A/TMCI3/PO12/POE2#/SCK6/SCK0/IRQ4	16
		PB3/A11/MTIOC0A/MTIOC4A/TIOCD3/TCLKD/TMO0/PO27/POE3#/SCK6	57
	MTIOC0B	P13/MTIOC0B/TIOCA5/TMO3/PO13/TXD2/SMOSI2/SSDA2/SDA0[FM+]/IRQ3/ADTRG#	33
		P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SCK3/SMISO1/SSCL1/CRX1-DS/IRQ5	31
		PA1/A1/MTIOC0B/MTCLKC/TIOCB0/PO17/SCK5/SSLA2/IRQ11	69
	MTIOC0C	P32/MTIOC0C/TIOCC0/TMO3/PO10/RTCOUT/RTCIC2/TXD6/TXD0/SMOSI6/SMOSI0/SSDA6/SSDA0/CTX0/IRQ2-DS	18
		PB1/A9/MTIOC0C/MTIOC4C/TIOCB3/TMCI0/PO25/TXD6/SMOSI6/SSDA6/IRQ4-DS	59
	MTIOC0D	P33/MTIOC0D/TIOCD0/TMRI3/PO11/POE3#/RXD6/RXD0/SMISO6/SMISO0/SSCL6/SSCL0/CRX0/IRQ3-DS	17
		PA3/A3/MTIOC0D/MTCLKD/TIOCD0/TCLKB/PO19/RXD5/SMISO5/SSCL5/IRQ6-DS	67
MTU1	MTIOC1A	P20/MTIOC1A/TIOCB3/TMRI0/PO0/TXD0/SMOSI0/SSDA0/IRQ8	28
		PE4/D12[A12/D12]/MTIOC4D/MTIOC1A/PO28/SSLB0/AN2	74
	MTIOC1B	P21/MTIOC1B/TIOCA3/TMCI0/PO1/RXD0/SMISO0/SSCL0/IRQ9	27
		PB5/A13/MTIOC2A/MTIOC1B/TIOCB4/TMRI1/PO29/POE1#/SCK9	55
MTU2	MTIOC2A	TDO/P26/CS6#/MTIOC2A/TMO1/PO6/TXD1/CTS3#/RTS3#/SMOSI1/SS3#/SSDA1/MOSIB	22
		PB5/A13/MTIOC2A/MTIOC1B/TIOCB4/TMRI1/PO29/POE1#/SCK9	55
	MTIOC2B	TCK/FINEC/P27/CS7#/MTIOC2B/TMCI3/PO7/SCK1/RSPCKB	21
		PE5/D13[A13/D13]/MTIOC4C/MTIOC2B/RSPCKB/IRQ5/AN3	73
MTU3	MTIOC3A	P14/MTIOC3A/MTCLKA/TIOCB5/TCLKA/TMRI2/PO15/CTS1#/RTS1#/SS1#/CTX1/USB0_DPUPE/IRQ4	32
		P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	29
		PC1/A17/MTIOC3A/TCLKD/PO18/SCK5/SSLA2/IRQ12	51
		PC7/A23/CS0#/MTIOC3A/MTCLKB/TMO2/PO31/TXD8/SMOSI8/SSDA8/MISOA/IRQ14	45
	MTIOC3B	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TX	29

		D3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	
		P22/MTIOC3B/MTCLKC/TIOCC3/TMO0/PO2/SCK0	26
		PB7/A15/MTIOC3B/TIOCB5/PO31/TXD9/SMOSI9/SSDA9	53
		PC5/A21/CS2#/WAIT#/MTIOC3B/MTCLKD/TMRI2/PO29/SCK8/RSPCKA	47
	MTIOC3C	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	30
		PC0/A16/MTIOC3C/TCLKC/PO17/CTS5#/RTS5#/SS5#/SSLA1/IRQ14	52
		PC6/A22/CS1#/MTIOC3C/MTCLKA/TMCI2/PO30/RXD8/SMISO8/SSCL8/MOSIA/IRQ13	46
		PJ3/MTIOC3C/CTS6#/RTS6#/CTS0#/RTS0#/SS6#/SS0#	4
	MTIOC3D	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	30
		P23/MTIOC3D/MTCLKD/TIOCD3/PO3/TXD3/CTS0#/RTS0#/SMOSI3/SS0#/SSDA3	25
		PB6/A14/MTIOC3D/TIOCA5/PO30/RXD9/SMISO9/SSCL9	54
		PC4/A20/CS3#/MTIOC3D/MTCLKC/TMCI1/PO25/POE0#/SCK5/CTS8#/RTS8#/SS8#/SSLA0	48
MTU4	MTIOC4A	P24/CS4#/MTIOC4A/MTCLKA/TIOCB4/TMRI1/PO4/SCK3	24
		PA0/A0/BC0#/MTIOC4A/TIOCA0/PO16/SSLA1	70
		PB3/A11/MTIOC0A/MTIOC4A/TIOCD3/TCLKD/TMO0/PO27/POE3#/SCK6	57
		PE2/D10[A10/D10]/MTIOC4A/PO23/RXD12/SMISO12/SSCL12/RXDX12/SSLB3/MOSIB/IRQ7-DS/AN0	76
	MTIOC4B	TDI/P30/MTIOC4B/TMRI3/PO8/RTCIC0/POE8#/RXD1/SMISO1/SSCL1/MISOB/IRQ0-DS	20
		P54/ALE/MTIOC4B/TMCI1/CTS2#/RTS2#/SS2#/CTX1	40
		PC2/A18/MTIOC4B/TCLKA/PO21/RXD5/SMISO5/SSCL5/SSLA3/IERXD	50
		PD1/D1[A1/D1]/MTIOC4B/CTX0/IRQ1/AN009	85
		PE3/D11[A11/D11]/MTIOC4B/PO26/POE8#/CTS12#/RTS12#/SS12#/MISOB/AN1	75
	MTIOC4C	P25/CS5#/MTIOC4C/MTCLKB/TIOCA4/PO5/RXD3/SMISO3/SSCL3/ADTRG0#	23
		PB1/A9/MTIOC0C/MTIOC4C/TIOCB3/TMCI0/PO25/TXD6/SMOSI6/SSDA6/IRQ4-DS	59
		PE1/D9[A9/D9]/MTIOC4C/PO18/TXD12/SMOSI12/SSDA12/TXDX12/SIOX12/SSLB2/RSPCKB/ANEX1	77
		PE5/D13[A13/D13]/MTIOC4C/MTIOC2B/RSPCKB/IRQ5/AN3	73
	MTIOC4D	TMS/P31/MTIOC4D/TMCI2/PO9/RTCIC1/CTS1#/RTS1#/SS1#/SSLB0/IRQ1-DS	19
		P55/WAIT#/MTIOC4D/TMO3/CRX1/IRQ10	39
		PC3/A19/MTIOC4D/TCLKB/PO24/TXD5/SMOSI5/SSDA5/IETXD	49
		PD2/D2[A2/D2]/MTIOC4D/CRX0/IRQ2/AN010	84
		PE4/D12[A12/D12]/MTIOC4D/MTIOC1A/PO28/SSLB0/AN2	74
MTU5	MTIC5U	PA4/A4/MTIC5U/MTCLKA/TIOCA1/TMRI0/PO20/TXD5/SMOSI5/SSDA5/SSLA0/IRQ5-DS	66

		PD7/D7[A7/D7]/MTIC5U/POE0#/IRQ7/AN7	79
	MTIC5V	PA6/A6/MTIC5V/MTCLKB/TIOCA2/TMCI3/PO22/POE2#/CTS5#/RTS5#/SS5#/MOSIA	64
		PD6/D6[A6/D6]/MTIC5V/POE1#/IRQ6/AN6	80
	MTIC5W	PB0/A8/MTIC5W/TIOCA3/PO24/RXD6/SMISO6/SSCL6/RSPCKA/IRQ12	61
		PD5/D5[A5/D5]/MTIC5W/POE2#/IRQ5/AN013	81
POE	POE0#	PC4/A20/CS3#/MTIOC3D/MTCLKC/TMCI1/PO25/POE0#/SCK5/CTS8#/RTS8#/SS8#/SSLA0	48
		PD7/D7[A7/D7]/MTIC5U/POE0#/IRQ7/AN7	79
	POE1#	PB5/A13/MTIOC2A/MTIOC1B/TIOCB4/TMRI1/PO29/POE1#/SCK9	55
		PD6/D6[A6/D6]/MTIC5V/POE1#/IRQ6/AN6	80
	POE2#	TRST#/P34/MTIOC0A/TMCI3/PO12/POE2#/SCK6/SCK0/IRQ4	16
		PA6/A6/MTIC5V/MTCLKB/TIOCA2/TMCI3/PO22/POE2#/CTS5#/RTS5#/SS5#/MOSIA	64
		PD5/D5[A5/D5]/MTIC5W/POE2#/IRQ5/AN013	81
	POE3#	P33/MTIOC0D/TIOCD0/TMRI3/PO11/POE3#/RXD6/RXD0/SMISO6/SMISO0/SSCL6/SSCL0/CRX0/IRQ3-DS	17
		PB3/A11/MTIOC0A/MTIOC4A/TIOCD3/TCLKD/TMO0/PO27/POE3#/SCK6	57
		PD4/D4[A4/D4]/POE3#/IRQ4/AN012	82
	POE8#	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	29
		TDI/P30/MTIOC4B/TMRI3/PO8/RTCIC0/POE8#/RXD1/SMISO1/SSCL1/MISOB/IRQ0-DS	20
		PD3/D3[A3/D3]/POE8#/IRQ3/AN011	83
		PE3/D11[A11/D11]/MTIOC4B/PO26/POE8#/CTS12#/RTS12#/SS12#/MISOB/AN1	75
TPU0-5	TCLKA	P14/MTIOC3A/MTCLKA/TIOCB5/TCLKA/TMRI2/PO15/CTS1#/RTS1#/SS1#/CTX1/USB0_DPUPE/IRQ4	32
		PC2/A18/MTIOC4B/TCLKA/PO21/RXD5/SMISO5/SSCL5/SSLA3/IERXD	50
	TCLKB	P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SCK3/SMISO1/SSCL1/CRX1-DS/IRQ5	31
		PA3/A3/MTIOC0D/MTCLKD/TIOCD0/TCLKB/PO19/RXD5/SMISO5/SSCL5/IRQ6-DS	67
		PC3/A19/MTIOC4D/TCLKB/PO24/TXD5/SMOSI5/SSDA5/IETXD	49
	TCLKC	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	30
		PB2/A10/TIOCC3/TCLKC/PO26/CTS6#/RTS6#/SS6#	58
		PC0/A16/MTIOC3C/TCLKC/PO17/CTS5#/RTS5#/SS5#/SSLA1/IRQ14	52
	TCLKD	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	29
		PB3/A11/MTIOC0A/MTIOC4A/TIOCD3/TCLKD/TMO0/PO27/POE3#/SCK6	57
		PC1/A17/MTIOC3A/TCLKD/PO18/SCK5/SSLA2/IRQ12	51
TPU0	TIOCB0	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	29

		PA1/A1/MTIOC0B/MTCLKC/TIOCB0/PO17/SCK5/SSLA2/IRQ11	69
	TIOCD0	P33/MTIOC0D/TIOCD0/TMRI3/PO11/POE3#/RXD6/RXD0/SMISO6/SMISO0/SSCL6/SSCL0/CRX0/IRQ3-DS	17
		PA3/A3/MTIOC0D/MTCLKD/TIOCD0/TCLKB/PO19/RXD5/SMISO5/SSCL5/IRQ6-DS	67
TPU1	TIOCB1	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	30
		PA5/A5/TIOCB1/PO21/RSPCKA	65
TPU2	TIOCB2	P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SCK3/SMISO1/SSCL1/CRX1-DS/IRQ5	31
		PA7/A7/TIOCB2/PO23/MISOA	63
TPU3	TIOCA3	P21/MTIOC1B/TIOCA3/TMCI0/PO1/RXD0/SMISO0/SSCL0/IRQ9	27
		PB0/A8/MTIC5W/TIOCA3/PO24/RXD6/SMISO6/SSCL6/RSPCKA/IRQ12	61
	TIOCB3	P20/MTIOC1A/TIOCB3/TMRI0/PO0/TXD0/SMOSI0/SSDA0/IRQ8	28
		PB1/A9/MTIOC0C/MTIOC4C/TIOCB3/TMCI0/PO25/TXD6/SMOSI6/SSDA6/IRQ4-DS	59
	TIOCC3	P22/MTIOC3B/MTCLKC/TIOCC3/TMO0/PO2/SCK0	26
		PB2/A10/TIOCC3/TCLKC/PO26/CTS6#/RTS6#/SS6#	58
	TIOCD3	P23/MTIOC3D/MTCLKD/TIOCD3/PO3/TXD3/CTS0#/RTS0#/SMOSI3/SS0#/SSDA3	25
		PB3/A11/MTIOC0A/MTIOC4A/TIOCD3/TCLKD/TMO0/PO27/POE3#/SCK6	57
TPU4	TIOCA4	P25/CS5#/MTIOC4C/MTCLKB/TIOCA4/PO5/RXD3/SMISO3/SSCL3/ADTRG0#	23
		PB4/A12/TIOCA4/PO28/CTS9#/RTS9#/SS9#	56
	TIOCB4	P24/CS4#/MTIOC4A/MTCLKA/TIOCB4/TMRI1/PO4/SCK3	24
		PB5/A13/MTIOC2A/MTIOC1B/TIOCB4/TMRI1/PO29/POE1#/SCK9	55
TPU5	TIOCA5	P13/MTIOC0B/TIOCA5/TMO3/PO13/TXD2/SMOSI2/SSDA2/SDA0[FM+]/IRQ3/ADTRG#	33
		PB6/A14/MTIOC3D/TIOCA5/PO30/RXD9/SMISO9/SSCL9	54
	TIOCB5	P14/MTIOC3A/MTCLKA/TIOCB5/TCLKA/TMRI2/PO15/CTS1#/RTS1#/SS1#/CTX1/USB0_DPUPE/IRQ4	32
		PB7/A15/MTIOC3B/TIOCB5/PO31/TXD9/SMOSI9/SSDA9	53
PPG Group3	PO13	P13/MTIOC0B/TIOCA5/TMO3/PO13/TXD2/SMOSI2/SSDA2/SDA0[FM+]/IRQ3/ADTRG#	33
		P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SCK3/SMISO1/SSCL1/CRX1-DS/IRQ5	31
	PO15	P14/MTIOC3A/MTCLKA/TIOCB5/TCLKA/TMRI2/PO15/CTS1#/RTS1#/SS1#/CTX1/USB0_DPUPE/IRQ4	32
		P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	29
PPG Group4	PO17	PA1/A1/MTIOC0B/MTCLKC/TIOCB0/PO17/SCK5/SSLA2/IRQ11	69
		PC0/A16/MTIOC3C/TCLKC/PO17/CTS5#/RTS5#/SS5#/SSLA1/IRQ14	52
	PO18	PA2/A2/PO18/RXD5/SMISO5/SSCL5/SSLA3	68
		PC1/A17/MTIOC3A/TCLKD/PO18/SCK5/SSLA2/IRQ12	51
		PE1/D9[A9/D9]/MTIOC4C/PO18/TXD12/SMOSI12/SSDA12/TXDX12/SIO	77

		X12/SSLB2/RSPCKB/ANEX1	
PPG Group5	PO21	PA5/A5/TIOCB1/PO21/RSPCKA	65
		PC2/A18/MTIOC4B/TCLKA/PO21/RXD5/SMISO5/SSCL5/SSLA3/IERXD	50
	PO23	PA7/A7/TIOCB2/PO23/MISOA	63
		PE2/D10[A10/D10]/MTIOC4A/PO23/RXD12/SMISO12/SSCL12/RXDX12/SSLB3/MOSIB/IRQ7-DS/AN0	76
PPG Group6	PO24	PB0/A8/MTIC5W/TIOCA3/PO24/RXD6/SMISO6/SSCL6/RSPCKA/IRQ12	61
		PC3/A19/MTIOC4D/TCLKB/PO24/TXD5/SMOSI5/SSDA5/IETXD	49
	PO25	PB1/A9/MTIOC0C/MTIOC4C/TIOCB3/TMCI0/PO25/TXD6/SMOSI6/SSDA6/IRQ4-DS	59
		PC4/A20/CS3#/MTIOC3D/MTCLKC/TMCI1/PO25/POE0#/SCK5/CTS8#/RTS8#/SS8#/SSLA0	48
	PO26	PB2/A10/TIOCC3/TCLKC/PO26/CTS6#/RTS6#/SS6#	58
		PE3/D11[A11/D11]/MTIOC4B/PO26/POE8#/CTS12#/RTS12#/SS12#/MISOB/AN1	75
PPG Group7	PO28	PB4/A12/TIOCA4/PO28/CTS9#/RTS9#/SS9#	56
		PE4/D12[A12/D12]/MTIOC4D/MTIOC1A/PO28/SSLB0/AN2	74
	PO29	PB5/A13/MTIOC2A/MTIOC1B/TIOCB4/TMRI1/PO29/POE1#/SCK9	55
		PC5/A21/CS2#/WAIT#/MTIOC3B/MTCLKD/TMRI2/PO29/SCK8/RSPCKA	47
	PO30	PB6/A14/MTIOC3D/TIOCA5/PO30/RXD9/SMISO9/SSCL9	54
		PC6/A22/CS1#/MTIOC3C/MTCLKA/TMCI2/PO30/RXD8/SMISO8/SSCL8/MOSIA/IRQ13	46
	PO31	PB7/A15/MTIOC3B/TIOCB5/PO31/TXD9/SMOSI9/SSDA9	53
		PC7/A23/CS0#/MTIOC3A/MTCLKB/TMO2/PO31/TXD8/SMOSI8/SSDA8/MISOA/IRQ14	45
TMR0	TMO0	P22/MTIOC3B/MTCLKC/TIOCC3/TMO0/PO2/SCK0	26
		PB3/A11/MTIOC0A/MTIOC4A/TIOCD3/TCLKD/TMO0/PO27/POE3#/SCK6	57
	TMCI0	P21/MTIOC1B/TIOCA3/TMCI0/PO1/RXD0/SMISO0/SSCL0/IRQ9	27
		PB1/A9/MTIOC0C/MTIOC4C/TIOCB3/TMCI0/PO25/TXD6/SMOSI6/SSDA6/IRQ4-DS	59
	TMRI0	P20/MTIOC1A/TIOCB3/TMRI0/PO0/TXD0/SMOSI0/SSDA0/IRQ8	28
		PA4/A4/MTIC5U/MTCLKA/TIOCA1/TMRI0/PO20/TXD5/SMOSI5/SSDA5/SSLA0/IRQ5-DS	66
TMR1	TMO1	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	29
		TDO/P26/CS6#/MTIOC2A/TMO1/PO6/TXD1/CTS3#/RTS3#/SMOSI1/SS3#/SSDA1/MOSIB	22
	TMCI1	P12/TMCI1/RXD2/SMISO2/SSCL2/SCL0[FM+]/IRQ2	34
		P54/ALE/MTIOC4B/TMCI1/CTS2#/RTS2#/SS2#/CTX1	40
		PC4/A20/CS3#/MTIOC3D/MTCLKC/TMCI1/PO25/POE0#/SCK5/CTS8#/RTS8#/SS8#/SSLA0	48
	TMRI1	P24/CS4#/MTIOC4A/MTCLKA/TIOCB4/TMRI1/PO4/SCK3	24
		PB5/A13/MTIOC2A/MTIOC1B/TIOCB4/TMRI1/PO29/POE1#/SCK9	55
TMR2	TMO2	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	30

		PC7/A23/CS0#/MTIOC3A/MTCLKB/TMO2/PO31/TXD8/SMOSI8/SSDA8/MISOA/IRQ14	45
	TMCI2	P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SCK3/SMISO1/SSCL1/CRX1-DS/IRQ5	31
		TMS/P31/MTIOC4D/TMCI2/PO9/RTCIC1/CTS1#/RTS1#/SS1#/SSLB0/IRQ1-DS	19
		PC6/A22/CS1#/MTIOC3C/MTCLKA/TMCI2/PO30/RXD8/SMISO8/SSCL8/MOSIA/IRQ13	46
	TMRI2	P14/MTIOC3A/MTCLKA/TIOCB5/TCLKA/TMRI2/PO15/CTS1#/RTS1#/SS1#/CTX1/USB0_DPUPE/IRQ4	32
		PC5/A21/CS2#/WAIT#/MTIOC3B/MTCLKD/TMRI2/PO29/SCK8/RSPCKA	47
TMR3	TMO3	P13/MTIOC0B/TIOCA5/TMO3/PO13/TXD2/SMOSI2/SSDA2/SDA0[FM+]/IRQ3/ADTRG#	33
		P32/MTIOC0C/TIOCC0/TMO3/PO10/RTCOUT/RTCIC2/TXD6/TXD0/SMOSI6/SMOSI0/SSDA6/SSDA0/CTX0/IRQ2-DS	18
		P55/WAIT#/MTIOC4D/TMO3/CRX1/IRQ10	39
	TMCI3	TCK/FINEC/P27/CS7#/MTIOC2B/TMCI3/PO7/SCK1/RSPCKB	21
		TRST#/P34/MTIOC0A/TMCI3/PO12/POE2#/SCK6/SCK0/IRQ4	16
		PA6/A6/MTIC5V/MTCLKB/TIOCA2/TMCI3/PO22/POE2#/CTS5#/RTS5#/SS5#/MOSIA	64
	TMRI3	TDI/P30/MTIOC4B/TMRI3/PO8/RTCIC0/POE8#/RXD1/SMISO1/SSCL1/MISOB/IRQ0-DS	20
		P33/MTIOC0D/TIOCD0/TMRI3/PO11/POE3#/RXD6/RXD0/SMISO6/SMISO0/SSCL6/SSCL0/CRX0/IRQ3-DS	17
SCI0	RXD0 SSCL0 SMISO0	P21/MTIOC1B/TIOCA3/TMCI0/PO1/RXD0/SMISO0/SSCL0/IRQ9	27
		P33/MTIOC0D/TIOCD0/TMRI3/PO11/POE3#/RXD6/RXD0/SMISO6/SMISO0/SSCL6/SSCL0/CRX0/IRQ3-DS	17
	TXD0 SSDA0 SMOSI0	P20/MTIOC1A/TIOCB3/TMRI0/PO0/TXD0/SMOSI0/SSDA0/IRQ8	28
		P32/MTIOC0C/TIOCC0/TMO3/PO10/RTCOUT/RTCIC2/TXD6/TXD0/SMOSI6/SMOSI0/SSDA6/SSDA0/CTX0/IRQ2-DS	18
	SCK0	P22/MTIOC3B/MTCLKC/TIOCC3/TMO0/PO2/SCK0	26
		TRST#/P34/MTIOC0A/TMCI3/PO12/POE2#/SCK6/SCK0/IRQ4	16
	CTS0# RTS0# SS0#	P23/MTIOC3D/MTCLKD/TIOCD3/PO3/TXD3/CTS0#/RTS0#/SMOSI3/SS0#/SSDA3	25
		PJ3/MTIOC3C/CTS6#/RTS6#/CTS0#/RTS0#/SS6#/SS0#	4
SCI1	RXD1 SSCL1 SMISO1	P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SCK3/SMISO1/SSCL1/CRX1-DS/IRQ5	31
		TDI/P30/MTIOC4B/TMRI3/PO8/RTCIC0/POE8#/RXD1/SMISO1/SSCL1/MISOB/IRQ0-DS	20
	TXD1 SSDA1 SMOSI1	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	30
		TDO/P26/CS6#/MTIOC2A/TMO1/PO6/TXD1/CTS3#/RTS3#/SMOSI1/SS3#/SSDA1/MOSIB	22
	SCK1	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	29
		TCK/FINEC/P27/CS7#/MTIOC2B/TMCI3/PO7/SCK1/RSPCKB	21

	CTS1# RTS1# SS1#	P14/MTIOC3A/MTCLKA/TIOCB5/TCLKA/TMRI2/PO15/CTS1#/RTS1#/SS1#/CTX1/USB0_DPUPE/IRQ4	32
		TMS/P31/MTIOC4D/TMCI2/PO9/RTCIC1/CTS1#/RTS1#/SS1#/SSLB0/IRQ1-DS	19
SCI2	RXD2 SSCL2 SMISO2	P12/TMCI1/RXD2/SMISO2/SSCL2/SCL0[FM+]/IRQ2	34
		P52/RD#/RXD2/SMISO2/SSCL2/SSLB3	42
	TXD2 SSDA2 SMOSI2	P13/MTIOC0B/TIOCA5/TMO3/PO13/TXD2/SMOSI2/SSDA2/SDA0[FM+]/IRQ3/ADTRG#	33
		P50/WR0#/WR#/TXD2/SMOSI2/SSDA2/SSLB1	44
SCI3	RXD3 SSCL3 SMISO3	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	30
		P25/CS5#/MTIOC4C/MTCLKB/TIOCA4/PO5/RXD3/SMISO3/SSCL3/ADTRG0#	23
	TXD3 SSDA3 SMOSI3	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	29
		P23/MTIOC3D/MTCLKD/TIOCD3/PO3/TXD3/CTS0#/RTS0#/SMOSI3/SS0#/SSDA3	25
	SCK3	P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SCK3/SMISO1/SSCL1/CRX1-DS/IRQ5	31
		P24/CS4#/MTIOC4A/MTCLKA/TIOCB4/TMRI1/PO4/SCK3	24
SCI5	RXD5 SSCL5 SMISO5	PA2/A2/PO18/RXD5/SMISO5/SSCL5/SSLA3	68
		PA3/A3/MTIOC0D/MTCLKD/TIOCD0/TCLKB/PO19/RXD5/SMISO5/SSCL5/IRQ6-DS	67
		PC2/A18/MTIOC4B/TCLKA/PO21/RXD5/SMISO5/SSCL5/SSLA3/IERXD	50
	TXD5 SSDA5 SMOSI5	PA4/A4/MTIC5U/MTCLKA/TIOCA1/TMRI0/PO20/TXD5/SMOSI5/SSDA5/SSLA0/IRQ5-DS	66
		PC3/A19/MTIOC4D/TCLKB/PO24/TXD5/SMOSI5/SSDA5/IETXD	49
	SCK5	PA1/A1/MTIOC0B/MTCLKC/TIOCB0/PO17/SCK5/SSLA2/IRQ11	69
		PC1/A17/MTIOC3A/TCLKD/PO18/SCK5/SSLA2/IRQ12	51
		PC4/A20/CS3#/MTIOC3D/MTCLKC/TMCI1/PO25/POE0#/SCK5/CTS8#/RTS8#/SS8#/SSLA0	48
	CTS5# RTS5# SS5#	PA6/A6/MTIC5V/MTCLKB/TIOCA2/TMCI3/PO22/POE2#/CTS5#/RTS5#/SS5#/MOSIA	64
		PC0/A16/MTIOC3C/TCLKC/PO17/CTS5#/RTS5#/SS5#/SSLA1/IRQ14	52
SCI6	RXD6 SSCL6 SMISO6	P33/MTIOC0D/TIOCD0/TMRI3/PO11/POE3#/RXD6/RXD0/SMISO6/SMISO0/SSCL6/SSCL0/CRX0/IRQ3-DS	17
		PB0/A8/MTIC5W/TIOCA3/PO24/RXD6/SMISO6/SSCL6/RSPCKA/IRQ12	61
	TXD6 SSDA6 SMOSI6	P32/MTIOC0C/TIOCC0/TMO3/PO10/RTCOUT/RTCIC2/TXD6/TXD0/SMOSI6/SMOSI0/SSDA6/SSDA0/CTX0/IRQ2-DS	18
		PB1/A9/MTIOC0C/MTIOC4C/TIOCB3/TMCI0/PO25/TXD6/SMOSI6/SSDA6/IRQ4-DS	59
	SCK6	TRST#/P34/MTIOC0A/TMCI3/PO12/POE2#/SCK6/SCK0/IRQ4	16
		PB3/A11/MTIOC0A/MTIOC4A/TIOCD3/TCLKD/TMO0/PO27/POE3#/SCK	57

		6	
	CTS6# RTS6# SS6#	PB2/A10/TIOCC3/TCLKC/PO26/CTS6#/RTS6#/SS6#	58
		PJ3/MTIOC3C/CTS6#/RTS6#/CTS0#/RTS0#/SS6#/SS0#	4
RSPI0	RSPCKA	PA5/A5/TIOCB1/PO21/RSPCKA	65
		PB0/A8/MTIC5W/TIOCA3/PO24/RXD6/SMISO6/SSCL6/RSPCKA/IRQ12	61
		PC5/A21/CS2#/WAIT#/MTIOC3B/MTCLKD/TMRI2/PO29/SCK8/RSPCKA	47
	MOSIA	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	30
		PA6/A6/MTIC5V/MTCLKB/TIOCA2/TMCI3/PO22/POE2#/CTS5#/RTS5#/SS5#/MOSIA	64
		PC6/A22/CS1#/MTIOC3C/MTCLKA/TMCI2/PO30/RXD8/SMISO8/SSCL8/MOSIA/IRQ13	46
	MISOA	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	29
		PA7/A7/TIOCB2/PO23/MISOA	63
		PC7/A23/CS0#/MTIOC3A/MTCLKB/TMO2/PO31/TXD8/SMOSI8/SSDA8/MISOA/IRQ14	45
	SSLA0	PA4/A4/MTIC5U/MTCLKA/TIOCA1/TMRI0/PO20/TXD5/SMOSI5/SSDA5/SSLA0/IRQ5-DS	66
		PC4/A20/CS3#/MTIOC3D/MTCLKC/TMCI1/PO25/POE0#/SCK5/CTS8#/RTS8#/SS8#/SSLA0	48
	SSLA1	PA0/A0/BC0#/MTIOC4A/TIOCA0/PO16/SSLA1	70
		PC0/A16/MTIOC3C/TCLKC/PO17/CTS5#/RTS5#/SS5#/SSLA1/IRQ14	52
	SSLA2	PA1/A1/MTIOC0B/MTCLKC/TIOCB0/PO17/SCK5/SSLA2/IRQ11	69
		PC1/A17/MTIOC3A/TCLKD/PO18/SCK5/SSLA2/IRQ12	51
	SSLA3	PA2/A2/PO18/RXD5/SMISO5/SSCL5/SSLA3	68
		PC2/A18/MTIOC4B/TCLKA/PO21/RXD5/SMISO5/SSCL5/SSLA3/IERXD	50
RSPI1	RSPCKB	TCK/FINEC/P27/CS7#/MTIOC2B/TMCI3/PO7/SCK1/RSPCKB	21
		PE1/D9[A9/D9]/MTIOC4C/PO18/TXD12/SMOSI12/SSDA12/TXDX12/SIOX12/SSLB2/RSPCKB/ANEX1	77
		PE5/D13[A13/D13]/MTIOC4C/MTIOC2B/RSPCKB/IRQ5/AN3	73
	MOSIB	TDO/P26/CS6#/MTIOC2A/TMO1/PO6/TXD1/CTS3#/RTS3#/SMOSI1/SS3#/SSDA1/MOSIB	22
		PE2/D10[A10/D10]/MTIOC4A/PO23/RXD12/SMISO12/SSCL12/RXDX12/SSLB3/MOSIB/IRQ7-DS/AN0	76
		PE6/D14[A14/D14]/MOSIB/IRQ6/AN4	72
	MISOB	TDI/P30/MTIOC4B/TMRI3/PO8/RTCIC0/POE8#/RXD1/SMISO1/SSCL1/MISOB/IRQ0-DS	20
		PE3/D11[A11/D11]/MTIOC4B/PO26/POE8#/CTS12#/RTS12#/SS12#/MISOB/AN1	75
		PE7/D15[A15/D15]/MISOB/IRQ7/AN5	71
	SSLB0	TMS/P31/MTIOC4D/TMCI2/PO9/RTCIC1/CTS1#/RTS1#/SS1#/SSLB0/IRQ1-DS	19
		PE4/D12[A12/D12]/MTIOC4D/MTIOC1A/PO28/SSLB0/AN2	74
	SSLB1	P50/WR0#/WR#/TXD2/SMOSI2/SSDA2/SSLB1	44

		PE0/D8[A8/D8]/SCK12/SSLB1/ANEX0	78
	SSLB2	P51/WR1#/BC1#/WAIT#/SCK2/SSLB2	43
		PE1/D9[A9/D9]/MTIOC4C/PO18/TXD12/SMOSI12/SSDA12/TXDX12/SIOX12/SSLB2/RSPCKB/ANEX1	77
	SSLB3	P52/RD#/RXD2/SMISO2/SSCL2/SSLB3	42
		PE2/D10[A10/D10]/MTIOC4A/PO23/RXD12/SMISO12/SSCL12/RXDX12/SSLB3/MOSIB/IRQ7-DS/AN0	76
IEB0	IERXD	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	30
		PC2/A18/MTIOC4B/TCLKA/PO21/RXD5/SMISO5/SSCL5/SSLA3/IERXD	50
	IETXD	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	29
		PC3/A19/MTIOC4D/TCLKB/PO24/TXD5/SMOSI5/SSDA5/IETXD	49
RTCA	RTCOUT	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	30
		P32/MTIOC0C/TIOCC0/TMO3/PO10/RTCOUT/RTCIC2/TXD6/TXD0/SMOSI6/SMOSI0/SSDA6/SSDA0/CTX0/IRQ2-DS	18
S12AD	ADTRG0#	P07/IRQ15/ADTRG0#	98
		P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/RXD3/SMOSI1/SMISO3/SSDA1/SSCL3/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	30
		P25/CS5#/MTIOC4C/MTCLKB/TIOCA4/PO5/RXD3/SMISO3/SSCL3/ADTRG0#	23
ADA0	ADTRG#	P13/MTIOC0B/TIOCA5/TMO3/PO13/TXD2/SMOSI2/SSDA2/SDA0[FM+]/IRQ3/ADTRG#	33
		P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/TXD3/SMOSI3/SSDA3/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	29

Table a-1.7 80-pin LQFP (the Upper Row of Each Pair is the Default Selection)

Peripheral module	Pin function	Selection of assignment	Pin No.
Interrupts	IRQ0	TDI/P30/MTIOC4B/TMRI3/PO8/RTCIC0/POE8#/RXD1/SMISO1/SSCL1/MISOB/IRQ0-DS	18
		PD0/IRQ0/AN008	66
	IRQ1	TMS/P31/MTIOC4D/TMCI2/PO9/RTCIC1/CTS1#/RTS1#/SS1#/SSLB0/IRQ1-DS	17
		PD1/MTIOC4B/IRQ1/AN009	65
	IRQ2	P32/MTIOC0C/TIOCC0/TMO3/PO10/RTCOUT/RTCIC2/TXD6/SMOSI6/SSDA6/IRQ2-DS	16
		P12/TMCI1/SCL0[FM+]/IRQ2	28
		PD2/MTIOC4D/IRQ2/AN010	64
	IRQ4	PB1/MTIOC0C/MTIOC4C/TIOCB3/TMCI0/PO25/TXD6/SMOSI6/SSDA6/IRQ4-DS	47
		P14/MTIOC3A/MTCLKA/TIOCB5/TCLKA/TMRI2/PO15/CTS1#/RTS1#/SS1#/CTX1/USB0_DPUPE/IRQ4	26

		TRST#/P34/MTIOC0A/TMCI3/PO12/POE2#/SCK6/IRQ4	15
	IRQ5	PA4/MTIC5U/MTCLKA/TIOCA1/TMRI0/PO20/TXD5/SMOSI5/SSDA5/SSLA0/IRQ5-DS	53
		P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SMISO1/SSCL1/CRX1-DS/IRQ5	25
		PE5/MTIOC4C/MTIOC2B/RSPCKB/IRQ5/AN3	58
	IRQ6	PA3/MTIOC0D/MTCLKD/TIOCD0/TCLKB/PO19/RXD5/SMISO5/SSCL5/IRQ6-DS	54
		P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/SMOSI1/SSDA1/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	24
	IRQ7	PE2/MTIOC4A/PO23/RXD12/SMISO12/SSCL12/RXDX12/SSLB3/MOSIB/IRQ7-DS/AN0	61
		P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	23
	IRQ8	P40/IRQ8-DS/AN000	75
		P20/MTIOC1A/TIOCB3/TMRI0/PO0/IRQ8	22
	IRQ9	P41/IRQ9-DS/AN001	73
		P21/MTIOC1B/TIOCA3/TMCI0/PO1/IRQ9	21
	IRQ10	P42/IRQ10-DS/AN002	72
		P55/MTIOC4D/TMO3/CRX1/IRQ10	33
	IRQ11	P43/IRQ11-DS/AN003	71
		PA1/MTIOC0B/MTCLKC/TIOCB0/PO17/SCK5/SSLA2/IRQ11	56
	IRQ12	P44/IRQ12-DS/AN004	70
		PB0/MTIC5W/TIOCA3/PO24/RXD6/SMISO6/SSCL6/RSPCKA/IRQ12	49
	IRQ13	P45/IRQ13-DS/AN005	69
		P05/IRQ13/DA1	80
		PC6/MTIOC3C/MTCLKA/TMCI2/PO30/RXD8/SMISO8/SSCL8/MOSIA/IRQ13	36
	IRQ14	P46/IRQ14-DS/AN006	68
		PC7/MTIOC3A/MTCLKB/TMO2/PO31/TXD8/SMOSI8/SSDA8/MISOA/IRQ14	35
	IRQ15	P47/IRQ15-DS/AN007	67
		P07/IRQ15/ADTRG0#	78
MTU0-5	MTCLKA	P14/MTIOC3A/MTCLKA/TIOCB5/TCLKA/TMRI2/PO15/CTS1#/RTS1#/SS1#/CTX1/USB0_DPUPE/IRQ4	26
		PA4/MTIC5U/MTCLKA/TIOCA1/TMRI0/PO20/TXD5/SMOSI5/SSDA5/SSLA0/IRQ5-DS	53
		PC6/MTIOC3C/MTCLKA/TMCI2/PO30/RXD8/SMISO8/SSCL8/MOSIA/IRQ13	36
	MTCLKB	P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SMISO1/SSCL1/CRX1-DS/IRQ5	25
		PA6/MTIC5V/MTCLKB/TIOCA2/TMCI3/PO22/POE2#/CTS5#/RTS5#/SS5#/MOSIA	51
		PC7/MTIOC3A/MTCLKB/TMO2/PO31/TXD8/SMOSI8/SSDA8/MISOA/IRQ14	35
	MTCLKC	PA1/MTIOC0B/MTCLKC/TIOCB0/PO17/SCK5/SSLA2/IRQ11	56
		PC4/MTIOC3D/MTCLKC/TMCI1/PO25/POE0#/SCK5/CTS8#/RTS8#/SS8	38

		#/SSLA0	
	MTCLKD	PA3/MTIOC0D/MTCLKD/TIOCD0/TCLKB/PO19/RXD5/SMISO5/SSCL5/IRQ6-DS	54
		PC5/MTIOC3B/MTCLKD/TMRI2/PO29/SCK8/RSPCKA	37
MTU0	MTIOC0A	TRST#/P34/MTIOC0A/TMCI3/PO12/POE2#/SCK6/IRQ4	15
		PB3/MTIOC0A/MTIOC4A/TIOCD3/TCLKD/TMO0/PO27/POE3#/SCK6	45
	MTIOC0B	P13/MTIOC0B/TIOCA5/TMO3/PO13/SDA0[FM+]/IRQ3/ADTRG#	27
		P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SMISO1/SSCL1/CRX1-DS/IRQ5	25
		PA1/MTIOC0B/MTCLKC/TIOCB0/PO17/SCK5/SSLA2/IRQ11	56
	MTIOC0C	P32/MTIOC0C/TIOCC0/TMO3/PO10/RTCOUT/RTCIC2/TXD6/SMOSI6/SSDA6/IRQ2-DS	16
		PB1/MTIOC0C/MTIOC4C/TIOCB3/TMCI0/PO25/TXD6/SMOSI6/SSDA6/IRQ4-DS	47
MTU1	MTIOC1A	P20/MTIOC1A/TIOCB3/TMRI0/PO0/IRQ8	22
		PE4/MTIOC4D/MTIOC1A/PO28/SSLB0/AN2	59
	MTIOC1B	P21/MTIOC1B/TIOCA3/TMCI0/PO1/IRQ9	21
		PB5/MTIOC2A/MTIOC1B/TIOCB4/TMRI1/PO29/POE1#/SCK9	43
MTU2	MTIOC2A	TDO/P26/MTIOC2A/TMO1/PO6/TXD1/SMOSI1/SSDA1/MOSIB	20
		PB5/MTIOC2A/MTIOC1B/TIOCB4/TMRI1/PO29/POE1#/SCK9	43
	MTIOC2B	TCK/FINEC/P27/MTIOC2B/TMCI3/PO7/SCK1/RSPCKB	19
		PE5/MTIOC4C/MTIOC2B/RSPCKB/IRQ5/AN3	58
MTU3	MTIOC3A	P14/MTIOC3A/MTCLKA/TIOCB5/TCLKA/TMRI2/PO15/CTS1#/RTS1#/SS1#/CTX1/USB0_DPUPE/IRQ4	26
		P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	23
		PC7/MTIOC3A/MTCLKB/TMO2/PO31/TXD8/SMOSI8/SSDA8/MISOA/IRQ14	35
	MTIOC3B	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	23
		PB7/MTIOC3B/TIOCB5/PO31/TXD9/SMOSI9/SSDA9	41
		PC5/MTIOC3B/MTCLKD/TMRI2/PO29/SCK8/RSPCKA	37
	MTIOC3C	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/SMOSI1/SSDA1/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	24
		PC6/MTIOC3C/MTCLKA/TMCI2/PO30/RXD8/SMISO8/SSCL8/MOSIA/IRQ13	36
	MTIOC3D	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/SMOSI1/SSDA1/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	24
		PB6/MTIOC3D/TIOCA5/PO30/RXD9/SMISO9/SSCL9	42
		PC4/MTIOC3D/MTCLKC/TMCI1/PO25/POE0#/SCK5/CTS8#/RTS8#/SS8#/SSLA0	38
MTU4	MTIOC4A	PA0/MTIOC4A/TIOCA0/PO16/SSLA1	57
		PB3/MTIOC0A/MTIOC4A/TIOCD3/TCLKD/TMO0/PO27/POE3#/SCK6	45
		PE2/MTIOC4A/PO23/RXD12/SMISO12/SSCL12/RXDX12/SSLB3/MOSIB/IRQ7-DS/AN0	61
	MTIOC4B	TDI/P30/MTIOC4B/TMRI3/PO8/RTCIC0/POE8#/RXD1/SMISO1/SSCL1/MISOB/IRQ0-DS	18

		P54/MTIOC4B/TMCI1/CTX1	34
		PC2/MTIOC4B/TCLKA/PO21/RXD5/SMISO5/SSCL5/SSLA3/IERXD	40
		PD1/MTIOC4B/IRQ1/AN009	65
		PE3/MTIOC4B/PO26/POE8#/CTS12#/RTS12#/SS12#/MISOB/AN1	60
	MTIOC4C	PB1/MTIOC0C/MTIOC4C/TIOCB3/TMCI0/PO25/TXD6/SMOSI6/SSDA6/IRQ4-DS	47
		PE1/MTIOC4C/PO18/TXD12/SMOSI12/SSDA12/TXDX12/SIOX12/SSLB2/RSPCKB/ANEX1	62
		PE5/MTIOC4C/MTIOC2B/RSPCKB/IRQ5/AN3	58
	MTIOC4D	TMS/P31/MTIOC4D/TMCI2/PO9/RTCIC1/CTS1#/RTS1#/SS1#/SSLB0/IRQ1-DS	17
		P55/MTIOC4D/TMO3/CRX1/IRQ10	33
		PC3/MTIOC4D/TCLKB/PO24/TXD5/SMOSI5/SSDA5/IETXD	39
		PD2/MTIOC4D/IRQ2/AN010	64
		PE4/MTIOC4D/MTIOC1A/PO28/SSLB0/AN2	59
POE	POE2#	TRST#/P34/MTIOC0A/TMCI3/PO12/POE2#/SCK6/IRQ4	15
		PA6/MTIC5V/MTCLKB/TIOCA2/TMCI3/PO22/POE2#/CTS5#/RTS5#/SS5#/MOSIA	51
	POE8#	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	23
		TDI/P30/MTIOC4B/TMRI3/PO8/RTCIC0/POE8#/RXD1/SMISO1/SSCL1/MISOB/IRQ0-DS	18
		PE3/MTIOC4B/PO26/POE8#/CTS12#/RTS12#/SS12#/MISOB/AN1	60
TPU0-5	TCLKA	P14/MTIOC3A/MTCLKA/TIOCB5/TCLKA/TMRI2/PO15/CTS1#/RTS1#/SS1#/CTX1/USB0_DPUPE/IRQ4	26
		PC2/MTIOC4B/TCLKA/PO21/RXD5/SMISO5/SSCL5/SSLA3/IERXD	40
	TCLKB	P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SMISO1/SSCL1/CRX1-DS/IRQ5	25
		PA3/MTIOC0D/MTCLKD/TIOCD0/TCLKB/PO19/RXD5/SMISO5/SSCL5/IRQ6-DS	54
		PC3/MTIOC4D/TCLKB/PO24/TXD5/SMOSI5/SSDA5/IETXD	39
	TCLKC	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/SMOSI1/SSDA1/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	24
		PB2/TIOCC3/TCLKC/PO26/CTS6#/RTS6#/SS6#	46
	TCLKD	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	23
		PB3/MTIOC0A/MTIOC4A/TIOCD3/TCLKD/TMO0/PO27/POE3#/SCK6	45
TPU0	TIOCB0	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	23
		PA1/MTIOC0B/MTCLKC/TIOCB0/PO17/SCK5/SSLA2/IRQ11	56
TPU1	TIOCB1	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/SMOSI1/SSDA1/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	24
		PA5/TIOCB1/PO21/RSPCKA	52
TPU3	TIOCA3	P21/MTIOC1B/TIOCA3/TMCI0/PO1/IRQ9	21
		PB0/MTIC5W/TIOCA3/PO24/RXD6/SMISO6/SSCL6/RSPCKA/IRQ12	49
	TIOCB3	P20/MTIOC1A/TIOCB3/TMRI0/PO0/IRQ8	22

		PB1/MTIOC0C/MTIOC4C/TIOCB3/TMCI0/PO25/TXD6/SMOSI6/SSDA6/IRQ4-DS	47
TPU5	TIOCA5	P13/MTIOC0B/TIOCA5/TMO3/PO13/SDA0[FM+]/IRQ3/ADTRG#	27
		PB6/MTIOC3D/TIOCA5/PO30/RXD9/SMISO9/SSCL9	42
	TIOCB5	P14/MTIOC3A/MTCLKA/TIOCB5/TCLKA/TMRI2/PO15/CTS1#/RTS1#/SS1#/CTX1/USB0_DPUPE/IRQ4	26
		PB7/MTIOC3B/TIOCB5/PO31/TXD9/SMOSI9/SSDA9	41
PPG Group3	PO13	P13/MTIOC0B/TIOCA5/TMO3/PO13/SDA0[FM+]/IRQ3/ADTRG#	27
		P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SMISO1/SSCL1/CRX1-DS/IRQ5	25
	PO15	P14/MTIOC3A/MTCLKA/TIOCB5/TCLKA/TMRI2/PO15/CTS1#/RTS1#/SS1#/CTX1/USB0_DPUPE/IRQ4	26
		P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	23
PPG Group4	PO18	PA2/PO18/RXD5/SMISO5/SSCL5/SSLA3	55
		PE1/MTIOC4C/PO18/TXD12/SMOSI12/SSDA12/TXDX12/SIOX12/SSLB2/RSPCKB/ANEX1	62
PPG Group5	PO21	PA5/TIOCB1/PO21/RSPCKA	52
		PC2/MTIOC4B/TCLKA/PO21/RXD5/SMISO5/SSCL5/SSLA3/IERXD	40
PPG Group6	PO24	PB0/MTIC5W/TIOCA3/PO24/RXD6/SMISO6/SSCL6/RSPCKA/IRQ12	49
		PC3/MTIOC4D/TCLKB/PO24/TXD5/SMOSI5/SSDA5/IETXD	39
	PO25	PB1/MTIOC0C/MTIOC4C/TIOCB3/TMCI0/PO25/TXD6/SMOSI6/SSDA6/IRQ4-DS	47
		PC4/MTIOC3D/MTCLKC/TMCI1/PO25/POE0#/SCK5/CTS8#/RTS8#/SS8#/SSLA0	38
	PO26	PB2/TIOCC3/TCLKC/PO26/CTS6#/RTS6#/SS6#	46
		PE3/MTIOC4B/PO26/POE8#/CTS12#/RTS12#/SS12#/MISOB/AN1	60
PPG Group7	PO28	PB4/TIOCA4/PO28/CTS9#/RTS9#/SS9#	44
		PE4/MTIOC4D/MTIOC1A/PO28/SSLB0/AN2	59
	PO29	PB5/MTIOC2A/MTIOC1B/TIOCB4/TMRI1/PO29/POE1#/SCK9	43
		PC5/MTIOC3B/MTCLKD/TMRI2/PO29/SCK8/RSPCKA	37
	PO30	PB6/MTIOC3D/TIOCA5/PO30/RXD9/SMISO9/SSCL9	42
		PC6/MTIOC3C/MTCLKA/TMCI2/PO30/RXD8/SMISO8/SSCL8/MOSIA/IRQ13	36
	PO31	PB7/MTIOC3B/TIOCB5/PO31/TXD9/SMOSI9/SSDA9	41
		PC7/MTIOC3A/MTCLKB/TMO2/PO31/TXD8/SMOSI8/SSDA8/MISOA/IRQ14	35
TMR0	TMCI0	P21/MTIOC1B/TIOCA3/TMCI0/PO1/IRQ9	21
		PB1/MTIOC0C/MTIOC4C/TIOCB3/TMCI0/PO25/TXD6/SMOSI6/SSDA6/IRQ4-DS	47
	TMRI0	P20/MTIOC1A/TIOCB3/TMRI0/PO0/IRQ8	22
		PA4/MTIC5U/MTCLKA/TIOCA1/TMRI0/PO20/TXD5/SMOSI5/SSDA5/SSLA0/IRQ5-DS	53
TMR1	TMO1	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	23
		TDO/P26/MTIOC2A/TMO1/PO6/TXD1/SMOSI1/SSDA1/MOSIB	20

	TMCI1	P12/TMCI1/SCL0[FM+]/IRQ2	28
		P54/MTIOC4B/TMCI1/CTX1	34
		PC4/MTIOC3D/MTCLKC/TMCI1/PO25/POE0#/SCK5/CTS8#/RTS8#/SS8#/SSLA0	38
TMR2	TMO2	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/SMOSI1/SSDA1/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	24
		PC7/MTIOC3A/MTCLKB/TMO2/PO31/TXD8/SMOSI8/SSDA8/MISOA/IRQ14	35
	TMCI2	P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SMISO1/SSCL1/CRX1-DS/IRQ5	25
		TMS/P31/MTIOC4D/TMCI2/PO9/RTCIC1/CTS1#/RTS1#/SS1#/SSLB0/IRQ1-DS	17
		PC6/MTIOC3C/MTCLKA/TMCI2/PO30/RXD8/SMISO8/SSCL8/MOSIA/IRQ13	36
	TMRI2	P14/MTIOC3A/MTCLKA/TIOCB5/TCLKA/TMRI2/PO15/CTS1#/RTS1#/SS1#/CTX1/USB0_DPUPE/IRQ4	26
		PC5/MTIOC3B/MTCLKD/TMRI2/PO29/SCK8/RSPCKA	37
TMR3	TMO3	P13/MTIOC0B/TIOCA5/TMO3/PO13/SDA0[FM+]/IRQ3/ADTRG#	27
		P32/MTIOC0C/TIOCC0/TMO3/PO10/RTCOUT/RTCIC2/TXD6/SMOSI6/SSDA6/IRQ2-DS	16
		P55/MTIOC4D/TMO3/CRX1/IRQ10	33
	TMCI3	TCK/FINEC/P27/MTIOC2B/TMCI3/PO7/SCK1/RSPCKB	19
		TRST#/P34/MTIOC0A/TMCI3/PO12/POE2#/SCK6/IRQ4	15
		PA6/MTIC5V/MTCLKB/TIOCA2/TMCI3/PO22/POE2#/CTS5#/RTS5#/SS5#/MOSIA	51
SCI1	RXD1 SSCL1 SMISO1	P15/MTIOC0B/MTCLKB/TIOCB2/TCLKB/TMCI2/PO13/RXD1/SMISO1/SSCL1/CRX1-DS/IRQ5	25
		TDI/P30/MTIOC4B/TMRI3/PO8/RTCIC0/POE8#/RXD1/SMISO1/SSCL1/MISOB/IRQ0-DS	18
	TXD1 SSDA1 SMOSI1	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/SMOSI1/SSDA1/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	24
		TDO/P26/MTIOC2A/TMO1/PO6/TXD1/SMOSI1/SSDA1/MOSIB	20
	SCK1	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	23
		TCK/FINEC/P27/MTIOC2B/TMCI3/PO7/SCK1/RSPCKB	19
	CTS1# RTS1# SS1#	P14/MTIOC3A/MTCLKA/TIOCB5/TCLKA/TMRI2/PO15/CTS1#/RTS1#/SS1#/CTX1/USB0_DPUPE/IRQ4	26
		TMS/P31/MTIOC4D/TMCI2/PO9/RTCIC1/CTS1#/RTS1#/SS1#/SSLB0/IRQ1-DS	17
SCI5	RXD5 SSCL5 SMISO5	PA2/PO18/RXD5/SMISO5/SSCL5/SSLA3	55
		PA3/MTIOC0D/MTCLKD/TIOCD0/TCLKB/PO19/RXD5/SMISO5/SSCL5/IRQ6-DS	54
		PC2/MTIOC4B/TCLKA/PO21/RXD5/SMISO5/SSCL5/SSLA3/IERXD	40
	TXD5 SSDA5 SMOSI5	PA4/MTIC5U/MTCLKA/TIOCA1/TMRI0/PO20/TXD5/SMOSI5/SSDA5/SSLA0/IRQ5-DS	53
		PC3/MTIOC4D/TCLKB/PO24/TXD5/SMOSI5/SSDA5/IETXD	39
	SCK5	PA1/MTIOC0B/MTCLKC/TIOCB0/PO17/SCK5/SSLA2/IRQ11	56

		PC4/MTIOC3D/MTCLKC/TMCI1/PO25/POE0#/SCK5/CTS8#/RTS8#/SS8#/SSLA0	38
SCI6	TXD6 SSDA6 SMOSI6	P32/MTIOC0C/TIOCC0/TMO3/PO10/RTCOUT/RTCIC2/TXD6/SMOSI6/SSDA6/IRQ2-DS	16
		PB1/MTIOC0C/MTIOC4C/TIOCB3/TMCI0/PO25/TXD6/SMOSI6/SSDA6/IRQ4-DS	47
	SCK6	TRST#/P34/MTIOC0A/TMCI3/PO12/POE2#/SCK6/IRQ4	15
		PB3/MTIOC0A/MTIOC4A/TIOCD3/TCLKD/TMO0/PO27/POE3#/SCK6	45
RSPI0	RSPCKA	PA5/TIOCB1/PO21/RSPCKA	52
		PB0/MTIC5W/TIOCA3/PO24/RXD6/SMISO6/SSCL6/RSPCKA/IRQ12	49
		PC5/MTIOC3B/MTCLKD/TMRI2/PO29/SCK8/RSPCKA	37
	MOSIA	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/SMOSI1/SSDA1/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	24
		PA6/MTIC5V/MTCLKB/TIOCA2/TMCI3/PO22/POE2#/CTS5#/RTS5#/SS5#/MOSIA	51
		PC6/MTIOC3C/MTCLKA/TMCI2/PO30/RXD8/SMISO8/SSCL8/MOSIA/IRQ13	36
	MISOA	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	23
		PC7/MTIOC3A/MTCLKB/TMO2/PO31/TXD8/SMOSI8/SSDA8/MISOA/IRQ14	35
	SSLA0	PA4/MTIC5U/MTCLKA/TIOCA1/TMRI0/PO20/TXD5/SMOSI5/SSDA5/SSLA0/IRQ5-DS	53
		PC4/MTIOC3D/MTCLKC/TMCI1/PO25/POE0#/SCK5/CTS8#/RTS8#/SS8#/SSLA0	38
	SSLA3	PA2/PO18/RXD5/SMISO5/SSCL5/SSLA3	55
		PC2/MTIOC4B/TCLKA/PO21/RXD5/SMISO5/SSCL5/SSLA3/IERXD	40
RSPI1	RSPCKB	TCK/FINEC/P27/MTIOC2B/TMCI3/PO7/SCK1/RSPCKB	19
		PE1/MTIOC4C/PO18/TXD12/SMOSI12/SSDA12/TXDX12/SIOX12/SSLB2/RSPCKB/ANEX1	62
		PE5/MTIOC4C/MTIOC2B/RSPCKB/IRQ5/AN3	58
	MOSIB	TDO/P26/MTIOC2A/TMO1/PO6/TXD1/SMOSI1/SSDA1/MOSIB	20
		PE2/MTIOC4A/PO23/RXD12/SMISO12/SSCL12/RXDX12/SSLB3/MOSIB/IRQ7-DS/AN0	61
	MISOB	TDI/P30/MTIOC4B/TMRI3/PO8/RTCIC0/POE8#/RXD1/SMISO1/SSCL1/MISOB/IRQ0-DS	18
		PE3/MTIOC4B/PO26/POE8#/CTS12#/RTS12#/SS12#/MISOB/AN1	60
	SSLB0	TMS/P31/MTIOC4D/TMCI2/PO9/RTCIC1/CTS1#/RTS1#/SS1#/SSLB0/IRQ1-DS	17
		PE4/MTIOC4D/MTIOC1A/PO28/SSLB0/AN2	59
IEB0	IERXD	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/SMOSI1/SSDA1/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	24
		PC2/MTIOC4B/TCLKA/PO21/RXD5/SMISO5/SSCL5/SSLA3/IERXD	40
	IETXD	P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	23
		PC3/MTIOC4D/TCLKB/PO24/TXD5/SMOSI5/SSDA5/IETXD	39
RTCA	RTCOUT	P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/	24

		SMOSI1/SSDA1/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	
		P32/MTIOC0C/TIOCC0/TMO3/PO10/RTCOUT/RTCIC2/TXD6/SMOSI6/SSDA6/IRQ2-DS	16
S12AD	ADTRG0#	P07/IRQ15/ADTRG0#	78
		P16/MTIOC3C/MTIOC3D/TIOCB1/TCLKC/TMO2/PO14/RTCOUT/TXD1/SMOSI1/SSDA1/MOSIA/SCL2-DS/IERXD/USB0_VBUS/IRQ6/ADTRG0#	24
ADA0	ADTRG#	P13/MTIOC0B/TIOCA5/TMO3/PO13/SDA0[FM+]/IRQ3/ADTRG#	27
		P17/MTIOC3A/MTIOC3B/TIOCB0/TCLKD/TMO1/PO15/POE8#/SCK1/MISOA/SDA2-DS/IETXD/IRQ7/ADTRG#	23

RX630 Group
Peripheral Driver Generator
Reference Manual

Publication Date: May 16, 2014 Rev.1.03

Published by: Renesas Electronics Corporation

Edited by: Microcomputer Tool Development Department 4
Renesas Solutions Corporation

SALES OFFICES Renesas Electronics Corporation http://www.renesas.com

Refer to "http://www.renesas.com/" for the latest and detailed information.

Renesas Electronics America Inc.
2801 Scott Boulevard Santa Clara, CA 95050-2549, U.S.A.
Tel: +1-408-588-6000, Fax: +1-408-588-6130

Renesas Electronics Canada Limited
1101 Nicholson Road, Newmarket, Ontario L3Y 9C3, Canada
Tel: +1-905-898-5441, Fax: +1-905-898-3220

Renesas Electronics Europe Limited
Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K
Tel: +44-1628-585-100, Fax: +44-1628-585-900

Renesas Electronics Europe GmbH
Arcadiastrasse 10, 40472 Düsseldorf, Germany
Tel: +49-211-6503-0, Fax: +49-211-6503-1327

Renesas Electronics (China) Co., Ltd.
Room 1709, Quantum Plaza, No.27 ZhiChunLu Haidian District, Beijing 100191, P.R.China
Tel: +86-10-8235-1155, Fax: +86-10-8235-7679

Renesas Electronics (Shanghai) Co., Ltd.
Unit 301, Tower A, Central Towers, 555 Langao Road, Putuo District, Shanghai, P. R. China 200333
Tel: +86-21-2226-0888, Fax: +86-21-2226-0999

Renesas Electronics Hong Kong Limited
Unit 1601-1613, 16/F., Tower 2, Grand Century Place, 193 Prince Edward Road West, Mongkok, Kowloon, Hong Kong
Tel: +852-2265-6688, Fax: +852 2886-9022/9044

Renesas Electronics Taiwan Co., Ltd.
13F, No. 363, Fu Shing North Road, Taipei 10543, Taiwan
Tel: +886-2-8175-9600, Fax: +886 2-8175-9670

Renesas Electronics Singapore Pte. Ltd.
80 Bendemeer Road, Unit #06-02 Hyflux Innovation Centre, Singapore 339949
Tel: +65-6213-0200, Fax: +65-6213-0300

Renesas Electronics Malaysia Sdn.Bhd.
Unit 906, Block B, Menara Amcorp, Amcorp Trade Centre, No. 18, Jln Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia
Tel: +60-3-7955-9390, Fax: +60-3-7955-9510

Renesas Electronics Korea Co., Ltd.
12F., 234 Teheran-ro, Gangnam-Ku, Seoul, 135-920, Korea
Tel: +82-2-558-3737, Fax: +82-2-558-5141

© 2014 Renesas Electronics Corporation and Renesas Solutions Corporation. All rights reserved.

Colophon 3.0

RX630 Group
Peripheral Driver Generator
Reference Manual

Renesas Electronics Corporation

R20UT2061EJ0103