Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

Send any inquiries to http://www.renesas.com/inquiry.

Mask ROM number

740 FAMILY MASK ROM CONFIRMATION FORM SINGLE-CHIP MICROCOMPUTER M38067MCDXXXFP RENESAS TECHNOLOGY

	Date:	
Receipt	Section head signature	Supervisor signature

Note : Please fill in all items marked *.

*	Customer	Company name		TEL ()	uance nature	Submitted by	Supervisor
414	oustonner	Date issued	Date:			lssu sigr		

* 1. Confirmation

Three EPROMs are required for each pattern if this order is performed by EPROMs. One floppy disk is required for each pattern if this order is performed by a floppy disk.

Ordering by EPROMs

If at least two of the three sets of EPROMs submitted contain identical data, we will produce masks based on this data. We shall assume the responsibility for errors only if the mask ROM data on the products we produce differs from this data. Thus, extreme care must be taken to verify the data in the submitted EPROMs.

Checksum code for entire EPROM

(hexadecimal notation)

EPROM type (indicate the type used)

□ 27512					
EPROM ac	ldress				
000016	Product name ASCII code :				
000F16	'M38067MCD'				
001016					
407F16					
408016	data				
FFFD16 FFFE16	ROM (48K-130) bytes				
FFFF16					

In the address space of the microcomputer, the internal ROM area is from address 408016 to FFFD16. The reset vector is stored in addresses FFFC16 and FFFD16.

(1) Set the data in the unused area (the shaded area of the diagram) to "FF16".

(2) The ASCII codes of the product name "M38067MCD" must be entered in addresses 000016 to 000816. And set the data "FF16" in addresses 000916 to 000F16. The ASCII codes and addresses are listed to the right in hexadecimal notation.

Address		Address	
000016	'M' = 4D16	000816	'D' = 4416
000116	' 3' = 3316	000916	FF16
000216	' 8' = 3816	000A16	FF16
000316	'0' = 3016	000B16	FF16
000416	'6' = 3616	000C16	FF16
000516	'7' = 3716	000D16	FF16
000616	'M' = 4D16	000E16	FF16
000716	'C' = 4316	000F16	FF16



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We recommend the use of the following pseudo-command to set the start address of the assembler source program because ASCII codes of the product name are written to addresses 000016 to 000816 of EPROM.

EPROM type	27512
The pseudo-command	*= △\$0000 .BYTE △'M38067MCD'

Note : If the name of the product written to the EPROMs does not match the name of the mask confirmation form, the ROM will not be processed.

Ordering by floppy disk

We will produce masks based on the mask files generated by the mask file generating utility. We shall assume the responsibility for errors only if the mask ROM data on the products we produce differs from this mask file. Thus, extreme care must be taken to verify the mask file in the submitted floppy disk.

The submitted floppy disk must be 3.5-inch 2HD type and DOS/V format. And the number of the mask files must be 1 in one floppy disk.

File code					(hexadecimal notation)
Mask file name					.MSK (equal or less than eight characters)

)

2. Mark specification

Mark specification must be submitted using the correct form for the package being ordered. Fill out the appropriate mark specification form (80P6N for M38067MCDXXXFP) and attach it to the mask ROM confirmation form.

* 3. Usage conditions

Please answer the following questions about usage for use in our product inspection :

(1) How	will you	use the	XIN-XOUT	oscillator?

Ceramic resonator	
External clock input	

At	what	frequency?
----	------	------------

Π	what	nequency:	

f(XIN) = MHz

Other (

Quartz crvstal

(2) In which operation mode will you use your microcomputer ?

- □ Single-chip mode
 - -chip mode \Box Memory expansion mode
- Microprocessor mode
- # 4. Comments

